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Copyright)); coerce\_it(VOIDP( coerce\_it(VOIDP(&(opts->pzFullVersion))); coerce\_it(VOIDP(&(opts->pzUsageTitle)));  
coerce\_it(VOIDP(&(opts->pzExplain))); coerce\_i  
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copyright note, \* depending on whether the layout is GNU standard form or not. \*/ if ( (od->fOptState & OPTST\_ARG\_OPTIONAL) && (od-  
>optArg.argString != NULL) && (od->optA  
copyright\n" "\t'n' - version and full copyright notice\n0"/ \* 1060 \*/ "%s error: the '%s' and '%s' options conflict\n0"/ \* 1107 \*/ "%s: The '%s'  
option has been disabled.\0"/ \* 1146  
copyright\n" "\t'n' - version and full copyright notice\n");#line 58 "../check.c" puts\_("'%s' error: the '%s' and '%s' options conflict\n");#line 187



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../find.c" puts(_("%s: The '%s' op
Copyright != NULL) fputs(opts->pzCopyright, fp);
Copyright != NULL) fputs(o->pzCopyright, fp);
Copyright */ puts(_("gensheloft 1\n\
copyright text
Copyright; ///< the full copyright notice char const * const pzCopyNotice; ///< a string with the program name, project name and version
char const * const pzFullVersio
autotools-dev 20120608.1 debian:autotools-dev/20120608.1/all
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  (C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
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 (C) tests whether C is a directory separator character. IS\_ABSOLUTE\_PATH(P) tests whether P is an absolute path. If it is not,  
 it may be concatenated to a direct  
 (C) tests whether C is a directory separator character. IS\_PATH\_WITH\_DIR(P) tests whether P contains a directory specification. \*/#if defined  
 \_\_WIN32 || defined \_\_WIN32\_\_ || defined \_\_E  
 Bash 5.0 opensuse:bash/5.0-6.1/armv6hl  
 (C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define  
 IS\_ABSOLUTE\_PATH(P) (ISSLASH ((P)[0]) || HAS\_DEVICE (  
 (C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define  
 IS\_PATH\_WITH\_DIR(P) \ (strchr (P, '/') != NULL ||

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(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISSLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)#endif
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISSLASH ((P)[0])#endif
(C) ((C) ==# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define FILESYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) == DIRECTORY_SEPARATOR)#endif
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it may be concatenated to a direct
(C) tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if defined
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IS\_ABSOLUTE\_PATH(P) (ISLASH ((P)[0]) || HAS\_DEVICE (P))  
(C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define  
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(C) ((C) ==# define IS\_ABSOLUTE\_PATH(P) ISLASH ((P)[0])#endif  
(C) ((C) ==# define IS\_PATH\_WITH\_DIR(P) (strchr (P, '/') != NULL)# define FILESYSTEM\_PREFIX\_LEN(P) 0#endif  
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(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C) tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if defined
__WIN32 || defined __WIN32__ || defined _E
Bash 5.0.18 opensuse:bash/5.0.18-2.1/armv6hl
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
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(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)#endif
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])#endif
(C) ((C) ==# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define FILESYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) == DIRECTORY_SEPARATOR)#endif
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 ISFUNC, r1\_insert }, /\* Not sign \*/ { ISFUNC, r1  
 (C) tests whether C is a directory separator character. IS\_ABSOLUTE\_PATH(P) tests whether P is an absolute path. If it is not,  
 it may be concatenated to a direct  
 (C) tests whether C is a directory separator character. IS\_PATH\_WITH\_DIR(P) tests whether P contains a directory specification. \*/#if defined  
 \_\_WIN32 || defined \_\_WIN32\_\_ || defined \_\_E  
 batch 0.1.0 packagist:yokai/batch:0.1.0

```

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Berkeley DB 5.3.28 debian:db5.3/5.3.28+dfsg2-2
(C) 2006, Sam Clegg <samo@debian.org> and
(C) 3 } else { set abandoned false }
(C) 3 process_msgs $envlist
(C) 3 process_msgs $envlist $envs(A) test force noarchive_timeout # Using small log file size, push into the second log file.
(C) 3 process_msgs $envlist error_check_good update.msg.sent \ $rep085_update_req_count [incr upd_before] error_check_good no.page.msg
$rep085_page_msg_count $pg_before if { !$env_private } {
(C) 3 proc_msgs_once $envlist
(C) 3 replclear 1 set succeeded [catch { process_msgs $envlist } ret] switch $result { internal_init { error_check_good
initd $succeeded 0
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need
for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
(C) AC_TRY_LINK( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS="$SAVE_CFLAGS" fi ;;sp
(C) "berkdb_env -create -txn $verbargs -home $clientdir -errpfx CLIENT -rep_client \ -rep_transport \[list 2 rep089_send\]" set
clientenv [eval $env_cmd(C)]
(C) "berkdb_env_noerr -create \ $c_logargs $envargs $verbargs -home $clientdir $repmemargs \ $privargs $c_txnargs -errpfx CLIENT -rep_client
\ -rep_transport \[list 2 replsend\]"
(C) "berkdb_env_noerr -create $c_logargs -errpfx ENV.C $verbargs $repmemargs \ -lock_detect default $privargs \ -
home $clientdir -rep_transport \[list 2 replsend\]" set env [eval
(C) "berkdb_env_noerr -create $c_logargs -home $clientdir $verbargs $repmemargs $privargs \ -rep_transport \[list 2 replsend\]"
(C) "berkdb_env_noerr -create $c_logargs -lock_detect default $verbargs $repmemargs \ $privargs \ -home $clientdir -rep_transport \[list
2 replsend\]"
(C) "berkdb_env_noerr -create $c_txnargs $c_logargs -home $clientdir -errpfx CLIENT $privargs \ -rep_client -rep_transport \[list 2
replsend\]"
(C) "berkdb_env_noerr -create \ $c_txnargs $c_logargs $verbargs -rep_client -errpfx CLIENT \ $privargs -home $clientdir $repmemargs \ -
rep_transport \[list 2 replsend\]"
(C) "berkdb_env_noerr -create -home $c_txnargs $c_logargs -rep_client $verbargs -errpfx CLIENT \ -rep_transport \[list 2 replsend\]"
(C) "berkdb_env_noerr -create -lock_max_objects $max_locks -lock_max_locks $max_locks \ -home $clientdir -errpfx CLIENT $verbargs
$repmemargs \ $privargs \ -rep_client -rep_transpo
(C) "berkdb_env_noerr -create -lock_max_objects $max_locks -lock_max_locks $max_locks \ -home $clientdir -errpfx CLIENT $verbargs
$repmemargs -rep -thread"
(C) "berkdb_env_noerr -create -log_max 1000000 $envargs $c_logargs $recargs $repmemargs \ -home $clientdir -errpfx CLIENT $c_txnargs
-rep_client \ -rep_transport \[list 2 replsend\]" s
(C) "berkdb_env_noerr -create -log_max 1000000 $envargs $c_logargs $recargs $verbargs \ -home $clientdir -errpfx CLIENT $c_txnargs -
rep_client \ -rep_transport \[list 2 replsend\]" set
(C) "berkdb_env_noerr -create -log_max 1000000 $envargs -home $clientdir $c_logargs \ -errfile /dev/stderr -errpfx CLIENT -txn -rep_client \
-rep_transport \[list 2 replsend\]" set env
(C) "berkdb_env_noerr -create -private -home -txn $c_logargs -errpfx CLIENT $verbargs $repmemargs \ -rep_client -rep_transport \[list 2
replsend\]"

```

```

(C) "berkdb_env_noerr -create \ $verbags $repmemargs $privargs \ -home $clntdir -errpfx CLIENT -txn nosync -rep_client \ -rep_transport
\[[list 2 replsend\]" set clientenv [eval $env
(C) "berkdb_env_noerr -home -errpfx CLIENT -lock -log -txn" set clientenv [eval $env_cmd(C)]
(C)/!b go :more \./!{ N s/\n# // b more
(C) case_operand(X) case_exprlist(Y) case_else(Z) END(E). A.pExpr = sqlite3PEExpr(pParse, TK_CASE, X, Z, 0); if (A.pExpr){ A.pExpr->x.pList = Y;
sqlite3ExprSetHeight(pParse, A.pExpr); }e
(C) (((c=C)>=0x42#endif
(C) (((c=C)>=0x42#endif#endif /* SQLITE_AMALGAMATION */
(C) (((c=C)#endif#ifdef SQLITE_EBCDICconst char sqlite3IsEbcidIdChar[] = {/* x0 x1 x2 x3 x4 x5 x6 x7 x8 x9 xA xB xC xD xE xF */ 0, 0, 1, 1, 1, 1,
1, 1, 1, 1, 0, 0, 0, 0, 0, 0, /* 4x */
(C) close $envs(A) close $envs(B) close
(C) close $envs(B) close $envs(A) close replclose $testdir/MSGQUEUEUDIR
(C) close $envs(B) close replclose $testdir/MSGQUEUEUDIR}
(C) close replclose $testdir/MSGQUEUEUDIR}
(C) close replclose $testdir/MSGQUEUEUDIR error_check_good envclose [$e close] 0
(C) close set envs(B) [eval $env_B_cmd -recover -rep_master] set envs(A) [eval $env_A_cmd -recover -rep_client]
(C) close set envs(C) [eval $env_C_cmd -rep_client -recover] if { !$env_private } {
(C) close set envs(C) [eval $env_C_cmd -rep_client -recover] } join_failure { error_check_bad no_autoinit
$succeeded 0 error_check_good join_fail \ [is_substr $ret DB_REP_JOIN_FAIL
(C) ((C)->txn#define NOWAIT_FLAG(txn) \ ((txn) != NULL && F_ISSET((txn), TXN_NOWAIT) ? DB_LOCK_NOWAIT : 0)#define
IS_REAL_TXN(txn) \ ((txn) != NULL && !F_ISSET(txn, TXN_FAMILY))#define IS_SU
(C) $envs(C) process_msgs $envlist rep_verify $dirs($master) $envs($master) \ $dirs($test_client) $envs($test_client) $init_test
(C) If you distribute any portion of the software, you must retain
(C) JOIN. { X = sqlite3JoinType(pParse,&A,&B,&C); }
(C) last if { $new_last_log > $last_client_log } { set stop 1 }
(C) log_flush exec $util_path/db_archive -d -h $dirC }
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code has been tested against that, and is
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"ProductVersion", "5.3.28\0" END
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copyright to this source code.*/ * First off, code is included that follows the "include" declaration** in the input grammar file. */ #include <stdio.h>
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copyright to this source code. * * This is an SQLite module implementing full-text search.
(C) $recargs error_check_good client_env [is_valid_env $cenv] TRUE
(C) $recargs error_check_good client_env [is_valid_env $clientenv] TRUE
(C) rep_config } puts "\tRep$num.f: Switch master to site B, try to sync client C" $envs(A) close set envs(B) [eval $env_B_cmd -rep_master]
(C) REP_F_NIMDBS_LOADED 0 } set upd_before $rep085_update_req_count set pg_before $rep085_page_msg_count
(C) REP_F_NIMDBS_LOADED 1 }
(C) rep_stat \ "Outdated conditions"] 0 } default { error "FAIL: unknown test result option $result"
(C) rep_stat "Pages received"
(C) rep_stat "Pages received"
(C) sortorder(Z). Expr *p = 0; if( C.n>0){ p = sqlite3Expr(pParse->db, TK_COLUMN, 0); sqlite3ExprSetCollByToken(pParse, p, &C); } A =
sqlite3ExprListAppend(pParse,X, p); sqlite3Expr
(C) sortorder(Z). Expr *p = 0; if( C.n>0){ p = sqlite3PEXPR(pParse, TK_COLUMN, 0, 0, 0); sqlite3ExprSetCollByToken(pParse, p, &C); } A =
sqlite3ExprListAppend(pParse,0, p); sqlite3E
(C) ((sqlite3CtypeMap#endef#ifdef SQLITE_EBCDICconst char sqlite3IsEbcidIdChar[] = { /* x0 x1 x2 x3 x4 x5 x6 x7 x8 x9 xA xB xC xD xE xF */ 0, 0,
1, 1, 1, 1, 1, 1, 1, 1, 0, 0, 0, 0, 0, 0, 0, 0, /*
(C) ((sqlite3CtypeMap#endef#ifdef SQLITE_EBCDICextern const char sqlite3IsEbcidIdChar[];
(C) t1.x=t2.y OR (t1.x=t2.z AND t1.y=15)** (D) x=expr1 OR (y>11 AND y<22 AND z LIKE '*hello*')** (E) (p.a=1 AND q.b=2 AND r.c=3)
OR (p.x=4 AND q.y=5 AND r.z=6)
(C) $testdir/SITE_C
(C) The content of every column in the row.
(C) trigger_event(D) ON fullname(E) foreach_clause when_clause(G). { sqlite3BeginTrigger(pParse, &B, &Z, C, D.a, D.b, E, G, T, NOERR);
A = (Z.n==0?B:Z);}
Berkeley DB 5.3.28 ubuntu:db5.3/5.3.28-11ubuntu0.1
(C) $envs(C) process_msgs $envlist rep_verify $dirs($master) $envs($master) \ $dirs($test_client) $envs($test_client) $init_test
(C) "berkdb_env_noerr -create -log_max 1000000 $envargs $c_logargs $recargs $verbargs \ -home $clientdir -errpfx CLIENT $c_txnargs -
rep_client \ -rep_transport \[list 2 replsend\]" set

```

```

Copyright 2007-2011 <chw@ch-werner.de>\0"      VALUE "LegalTrademarks", "\0"      VALUE "OriginalFilename",
"SQLITE3_MOD_BLOOTOXY.DLL\0"      VALUE "PrivateBuild", "\0"
(C) "berkdb_env_noerr -create      $c_logargs -home $clientdir $verbags $repmemargs $privargs \      -rep_transport \[list 2 replsend\]"
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(C) 2006, Sam Clegg <samo@debian.org> and
(C) JOIN.      { X = sqlite3JoinType(pParse,&A,&B,&C); }
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(C) "berkdb_env_noerr -create \      $verbags $repmemargs $privargs \      -home $clientdir -errpfx CLIENT -txn nosync -rep_client \      -rep_transport
\[list 2 replsend\]"      set clientenv [eval $env
(C) REP_F_NIMDBS_LOADED 1      }
(C) "berkdb_env_noerr -create -home      $c_txnargs $c_logargs -rep_client $verbags -errpfx CLIENT \      -rep_transport \[list 2 replsend\]"
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(C) ((sqlite3CtypeMap#endif#ifndef SQLITE_EBCDICconst char sqlite3IsEbcididChar[] = {/* x0 x1 x2 x3 x4 x5 x6 x7 x8 x9 xA xB xC xD xE xF */ 0, 0,
1, 1, 1, 1, 1, 1, 1, 0, 0, 0, 0, 0, 0, /*
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code has been tested against that, and is
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(C) sortorder(Z). Expr *p = 0; if( C.n>0 ){ p = sqlite3Expr(pParse->db, TK_COLUMN, 0); sqlite3ExprSetCollByToken(pParse, p, &C); } A =
sqlite3ExprListAppend(pParse,X, p); sqlite3Expr
Copyright (c) 1992, 1993 * The Regents of the University of California. All rights reserved
Copyright Oracle 1997, 2013\0"      VALUE "OriginalFilename", "libdb53.dll\0"      VALUE "ProductName", "Oracle libdb\0"      VALUE
"ProductVersion", "5.3.28\0"      END
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(C) (((c=C)>=0x42#endif
(C) "berkdb_env_noerr -create -private -home      -txn $c_logargs -errpfx CLIENT $verbags $repmemargs \      -rep_client -rep_transport \[list 2
replsend\]"
Copyright 2001-2011 <chw@ch-werner.de>\0"      VALUE "LegalTrademarks", "\0"#ifndef SQLITE_UTF8      VALUE "OriginalFilename",
"SQLITEODBCU.DLL\0"#else      VALUE "OriginalFile
copyright to this source code.
(C) (((c=C)#endif#ifndef SQLITE_EBCDICconst char sqlite3IsEbcididChar[] = {/* x0 x1 x2 x3 x4 x5 x6 x7 x8 x9 xA xB xC xD xE xF */ 0, 0, 1, 1, 1, 1,
1, 1, 1, 1, 0, 0, 0, 0, 0, /* 4x */
copyright to this source code. * * This is an SQLite module implementing full-text search.
(C) ((sqlite3CtypeMap#endif#ifndef SQLITE_EBCDICextern const char sqlite3IsEbcididChar[]);
Copyright 2004-2011 <chw@ch-werner.de>\0"      VALUE "LegalTrademarks", "\0"      VALUE "OriginalFilename", "SQLITE3ODBC.DLL\0"
VALUE "PrivateBuild", "\0"      VALUE
(C) "berkdb_env_noerr -create      $c_txnargs $c_logargs -home $clientdir -errpfx CLIENT $privargs \      -rep_client -rep_transport \[list 2
replsend\]"
(C) close set envs(C) [eval $env_C_cmd -rep_client -recover] if { !$env_private } {
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software; the Free Software Foundation# gives un
(C) REP_F_NIMDBS_LOADED 0      } set upd_before $rep085_update_req_count set pg_before $rep085_page_msg_count
Copyright (c) 1982, 1986, 1993 * The Regents of the University of California. All rights reserved
Copyright (c) 2004-2011 Christian Werner <chw@ch-werner.de>
Copyright (c) 1987, 1993, 1994 * The Regents of the University of California. All rights reserved
Copyright (C) 2005 Free Software Foundation, Inc." $echo "This is free software; see the source for copying conditions. There is NO" $echo
"warranty; not even for MERCHANTABILITY or FITNESS
Copyright 2006, Eric Woodruff, All rights reserved
(C) "berkdb_env_noerr -create      -lock_max_objects $max_locks -lock_max_locks $max_locks \      -home $clientdir -errpfx CLIENT $verbags
$repmemargs \      $privargs \      -rep_client -rep_transpo
Copyright (c) 1997-2011 Paul Marquess. All rights reserved
(C) $testdir/SITE_C
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Copyright (c) 2004 Lorne R. Sunley <lsunley@mb.sympatico.ca>
Copyright (c) 2007-2011 Christian Werner <chw@ch-werner.de>
Copyright (C) 1994 X Consortium
(C) rep_stat "Pages received"
Copyright (C) 2004, 2005, 2007 Free Software Foundation, Inc.#  Written by Scott James Remnant, 2004.

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(C) close $envs(B) close repclose $testdir/MSGQUEUEUDIR}
(C) ((C)->txn#define NOWAIT_FLAG(txn) \ ((txn) != NULL && F_ISSET((txn), TXN_NOWAIT) ? DB_LOCK_NOWAIT : 0)#define
IS_REAL_TXN(txn) \ ((txn) != NULL && !F_ISSET(txn, TXN_FAMILY))#define IS_SU
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distribute and modify it."
(C) 3 proc_msgs_once $envlist
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(C) rep_stat "Pages received"
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(C)/!b go :more \./!{ N s\n# // b more
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copyright to this source code.** First off, code is include which follows the "include" declaration** in the input file. */#include <stdio.h>
(C) "berkdb_env_noerr -create -log_max 1000000 $envargs $c_logargs $recargs $repmemargs \ -home $clientdir -errpfx CLIENT $c_txnargs
-rep_client \ -rep_transport \[list 2 replsend\]" s
(C) close repclose $testdir/MSGQUEUEUDIR}
(C) 3 process_msgs $envlist error_check_good update.msg.sent \ $rep085_update_req_count [incr upd_before] error_check_good no.page.msg
$rep085_page_msg_count $pg_before if { !$env_private } {
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(C) (((c=C)>=0x42#endif#endif /* SQLITE_AMALGAMATION */
(C) log_flush exec $util_path/db_archive -d -h $dirC }
(C) close set envs(C) [eval $env_C_cmd -rep_client -recover] } join_failure { error_check_bad no_autoint
$succeeded 0 error_check_good join_fail \ [is_substr $ret DB_REP_JOIN_FAIL
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(C) "berkdb_env_noerr -create -log_max 1000000 $envargs -home $clientdir $c_logargs \ -errfile /dev/stderr -errpfx CLIENT -txn -rep_client \
-rep_transport \[list 2 replsend\]" set env
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(C) close $envs(B) close $envs(A) close repclose $testdir/MSGQUEUEUDIR
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(C) "berkdb_env_noerr -create \ $c_txnargs $c_logargs $verbargs -rep_client -errpfx CLIENT \ $privargs -home $clientdir $repmemargs \ -
rep_transport \[list 2 replsend\]"
(C) 3 process_msgs $envlist $envs(A) test force noarchive_timeout # Using small log file size, push into the second log file.
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(C) 3 process_msgs $envlist
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(C) last if { $new_last_log > $last_client_log } { set stop 1 }
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<gord@gnu.ai.mit.edu>, 1996#### This file is free software; the Fr
(C) "berkdb_env_noerr -create $c_logargs -errpfx ENV.C $verbargs $repmemargs \ -lock_detect default $privargs \ -

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home $clientdir -rep_transport \[list 2 replsend\]"      set cenv [eval
(C) 3 } else {      set abandoned false }
(C) rep_config } puts "\tRep$num.f: Switch master to site B, try to sync client C" $envs(A) close set envs(B) [eval $env_B_cmd -rep_master]
(C) case_operand(X) case_explist(Y) case_else(Z) END(E). A.pExpr = sqlite3PEExpr(pParse, TK_CASE, X, Z, 0); if( A.pExpr ){ A.pExpr->x.pList = Y;
sqlite3ExprSetHeight(pParse, A.pExpr); }e
(C) t1.x=t2.y OR (t1.x=t2.z AND t1.y=15)** (D) x=expr1 OR (y>11 AND y<22 AND z LIKE '*hello')** (E) (p.a=1 AND q.b=2 AND r.c=3)
OR (p.x=4 AND q.y=5 AND r.z=6)
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Copyright (C) 2004, 2005, 2007, 2008 Free Software Foundation, Inc.# Written by Gary V. Vaughan, 2004
copyright to this source code.*/ First off, code is included that follows the "include" declaration** in the input grammar file. */#include <stdio.h>
(C) trigger_event(D) ON fullname(E) foreach_clause when_clause(G). { sqlite3BeginTrigger(pParse, &B, &Z, C, D.a, D.b, E, G, T, NOERR);
A = (Z.n==0?B:Z);}
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(C) "berkdb_env_noerr -create -lock_max_objects $max_locks -lock_max_locks $max_locks \ -home $clientdir -errpfx CLIENT $verbargs
$repmemargs -rep -thread"
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(C) AC_LINK_IFELSE( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need
for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
(C) "berkdb_env_noerr -home -errpfx CLIENT -lock -log -txn" set clientenv [eval $env_cmd(C)]
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(C) "berkdb_env_noerr -create \ $c_logargs $envargs $verbargs -home $clientdir $repmemargs \ $privargs $c_txnargs -errpfx CLIENT -rep_client
\
-rep_transport \[list 2 replsend\]"
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(C) $recargs error_check_good client_env [is_valid_env $clientenv] TRUE
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(C) The content of every column in the row.
(C) rep_stat \ "Outdated conditions"] 0 } default { error "FAIL: unknown test result option $result"
Copyright ? 2009, 2013 Oracle Corporation and/or its affiliates. All rights reserved
(C) close $envs(A) close $envs(B) close
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(C) AC_TRY_LINK( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS="$SAVE_CFLAGS" fi ;;sp
Copyright (C) 1997, 1999 Makoto Matsumoto and Takuji Nishimura./* Any feedback is very welcome. For any question, comments, */ see
http://www.math.keio.ac.jp/matsumoto/emt.html or email
(C) close set envs(B) [eval $env_B_cmd -recover -rep_master] set envs(A) [eval $env_A_cmd -recover -rep_client]
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(C) sortorder(Z). Expr *p = 0; if( C.n>0 ){ p = sqlite3PEExpr(pParse, TK_COLUMN, 0, 0, 0); sqlite3ExprSetCollByToken(pParse, p, &C); } A =
sqlite3ExprListAppend(pParse,0, p); sqlite3E
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(C) "berkdb_env_noerr -create      $c_logargs -lock_detect default $verbags $repmemargs \ $privargs \      -home $clientdir -rep_transport \[list
2 replsend\]"
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(C) $recargs      error_check_good client_env [is_valid_env $cenv] TRUE
(C) 3 replclear 1 set succeeded [catch { process_msgs $envlist } ret]      switch $result {      internal_init {      error_check_good
initiated $succeeded 0
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(C) close      replclose $testdir/MSGQUEUEDIR error_check_good envclose [$e close] 0
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(C) "berkdb_env -create -txn      $verbags -home $clientdir -errpfx CLIENT -rep_client \      -rep_transport \[list 2 rep089_send\]"      set
clientenv [eval $env_cmd(C)]
BIND9 (Berkeley Internet Name Domain) 9.11.22 suse:bind/9.11.22-3.22.1/s390x
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$!t_cv_cc_needs_belf"; then      # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;;*-so
(C)!/b go      :more      \./!{      N      s|\n# ||      b more
(C) * caps = BIND_SERVICE *      if ((T && C && P) || !T) *      caps += SETUID *      endif *      capset(caps) *      endif *      if (T && C && P && -u) *
setuid() * else if (T && -u) *      fail * --> start threads * i
(C) HAVE_SYS_CAPABILITY_H (or HAVE_LINUX_CAPABILITY_H) * (P) HAVE_SYS_PRCTL_H * The possible cases are: *      none:      setuid() normally
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Copyright 1991 by the Massachusetts Institute of Technology
Copyright 2000,2014"      VALUE "OriginalFilename", "BINDInstall.EXE"      VALUE "ProductName", "ISC BIND 9"      VALUE
"ProductVersion", "9.10.0"      END      END      BLOCK
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(C)/!b go :more /\./!{ N s|\n# | | b more

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belf CFLAGS=$SAVE_CFLAGS fi ;*-so
(C)!b go :more \./!{ N s|\n# || b more
(C) * caps = BIND_SERVICE * if ((T && C && P) || !T) * caps += SETUID * endif * capset(caps) * endif * if (T && C && P && -u) *
setuid() * else if (T && -u) * fail * --> start threads * i
(C) HAVE_SYS_CAPABILITY_H (or HAVE_LINUX_CAPABILITY_H) * (P) HAVE_SYS_PRCTL_H * The possible cases are: * none: setuid() normally
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(C) * caps = BIND_SERVICE * if ((T && C && P) || !T) * caps += SETUID * endif * capset(caps) * endif * if (T && C && P && -u) *
setuid() * else if (T && -u) * fail * --> start threads * i
(C) HAVE_SYS_CAPABILITY_H (or HAVE_LINUX_CAPABILITY_H) * (P) HAVE_SYS_PRCTL_H * The possible cases are: * none: setuid() normally
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(C) 1995-2017 Jean-loup Gailly END END BLOCK "VarFileInfo" BEGIN VALUE "Translation", 0x0409, 1252 END
(C) 1995-2017 Jean-loup Gailly VALUE "OriginalFilename", "zlib1.dll\0" VALUE "ProductName", "zlib\0" VALUE "ProductVersion",
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(C) BOOST\_MPL\_AUX\_VALUE\_WKND(C)#else  
(C) BOOST\_STATIC\_ASSERT(C)#endif  
(C) BOOST\_TEST(C)#else#include <boost/static\_assert.hpp>  
(C) C# define BOOST\_MPL\_AUX\_MSVC\_VALUE\_WKND(C) \ ::boost::mpl::aux::value\_wknd< C > \/\*\*/#endif  
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BOOST_SERIALIZATION_COLLECTION_TRAITS_HELPER(wchar_t, C) \
/**/#endif
(C) @f //! `Applicative` `F` is also a `Monad`, such functions taking normal //! values but returning monadic values are called `_monadic` functions.
//! To compose them, we obviously can't
(C) @f //! @f$ g : A \to M(B) @f$, the signature is //! @f$ //! \mathit{monadic\_compose} //! : (B \to M(C)) \times (A \to M(B)) \to (A \to M(C)) //! @f$. //! /
(C) @f //! must be satisfied:
(C) @f //! //! @param g //! A monadic function with signature @f$ A \to M(B) @f$.
(C) @f //! we could simply set //! @code //! monadic_compose(g, f)(x) = joker(transform(f(x), g)) //! @endcode //! //! and we would be happy. It turns out that `flatten` is
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)\n{\n" << "   return 0;\n}\n\n";}
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name="gentext"> <xsl:with-param name="
copyright</xsl:with-param>
Copyright : Your copyright notice// Description : Hello World in C++, Ansi-
style//=====
Copyright Zach Laine 2014
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(C) to respect the signature of
(C) volatile;typedef C (C::*cv_mem_func_ptr)() const volatile;typedef C C::*mem_ptr;
(C) * (y = 1. / sin(D));          y *= D * sin(lp_lat);          } else          x = y = 0.;          if (this-
>m_proj_parm.mode ==
boost_1_71_0-gnu-openmpi2-hpc 1.71.0 opensuse:boost_1_71_0-gnu-openmpi2-hpc/1.71.0-2.1/armv6hl
(C) 1995-2017 Jean-loup Gailly and Mark Adler
(C) 1995-2017 Jean-loup Gailly END END BLOCK "VarFileInfo" BEGIN VALUE "Translation", 0x0409, 1252 END
(C) 1995-2017 Jean-loup Gailly VALUE "OriginalFilename", "zlib1.dll\0" VALUE "ProductName", "zlib\0" VALUE "ProductVersion",
ZLIB_VERSION "\0" VALUE "Comments", "For more info
(C) 2006 Douglas Gregor <doug.gregor@gmail.com>
(C) 2013-2019 Niall Douglas <http://www.nedproductions.biz/> (14 commits)
(C) 2013-2019 Niall Douglas <http://www.nedproductions.biz/> (18 commits)
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(C) 2017-2019 Niall Douglas <http://www.nedproductions.biz/> (225 commits)

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(C) 2017-2019 Niall Douglas <<http://www.nedproductions.biz/>> (3 commits), Krzemienski <sg0897542@global.ad.sabre.com> (3 commits) and Andrzej Krzemienski <akrzemi1@gmail.com> (3 commits)  
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(C) 2017-2019 Niall Douglas <<http://www.nedproductions.biz/>> (4 commits)  
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(C) 2019 Niall Douglas <<http://www.nedproductions.biz/>> (1 commit)  
(C) ACM, 2011. <http://doi.acm.org/10.1145/1916461.1916469>  
(C)\* BOOST\_CONTRACT\_DETAIL\_NAME1(obj) \ BOOST\_CONTRACT\_DETAIL\_TVARIADIC\_COMMA(arity) \  
BOOST\_CONTRACT\_DETAIL\_TVARIADIC\_FPARAMS\_Z(z, arity, \ BOOST\_CONTRACT\_DETA  
(C) BOOST\_MPL\_AUX\_VALUE\_WKND(C)#else  
(C) BOOST\_STATIC\_ASSERT(C)#endif  
(C) BOOST\_TEST(C)#else#include <boost/static\_assert.hpp>  
(C) C# define BOOST\_MPL\_AUX\_MSVC\_VALUE\_WKND(C) \ ::boost::mpl::aux::value\_wknd< C > \V\*\*/#endif  
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(C) Copyright 2006-8 Anthony Williams  
(C) Copyright 2006 Boris Gubenko.// HP-UX has a restriction that for multi-thread applications, (i.e.// the ones compiled -mt) if argument to tmpnam is a NULL pointer, then,// citing the tmpnam(3S)  
(C) Copyright 2006 David Abrahams - <http://www.boost.org/>// implementation of class export functionality. This is an alternative to// "forward declaration" method to provoke instantiation of derive  
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(C) Copyright 2007-9 Anthony Williams  
(C) Copyright 2007-9 Anthony Williams  
(C) Copyright 2007 Andrew Sutton  
(C) Copyright 2007 Anthony Williams  
(C) Copyright 2007 Anthony Williams#ifndef THREAD\_HEAP\_ALLOC\_HPP#define THREAD\_HEAP\_ALLOC\_HPP#include <new>#include <boost/thread/detail/config.hpp>#include <boost/thread/win32/thread\_primitives.  
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(C) Copyright 2008-9 Anthony Williams  
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(C) Copyright 2008 Anthony Williams#ifndef THREAD\_HEAP\_ALLOC\_PTHREAD\_HPP#define THREAD\_HEAP\_ALLOC\_PTHREAD\_HPP  
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(C) Copyright 2010 Dean Michael Berris.// Instead of using the potentially dangrous tempnam function that's part// of the C standard library, on  
Unix/Linux we use the more portable and// "safe" uni  
(C) Copyright 2010 Dean Michael Berris. <mikhailberis@gmail.com> // Instead of using std::tmpnam, we use Boost.Filesystem's unique\_path  
boost::filesystem::path tmp\_path =  
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(C)     elements.push_back(el); // MAY THROW (C)     }
(C)#else #define BOOST_SERIALIZATION_COLLECTION_TRAITS_HELPER_WCHAR(C) \
BOOST_SERIALIZATION_COLLECTION_TRAITS_HELPER(wchar_t, C) \ /**/#endif
(C) @f //! `Applicative` `F` is also a `Monad`, such functions taking normal //! values but returning monadic values are called _monadic functions_.
//! To compose them, we obviously can't
(C) @f //! @f$ g : A \to M(B) @f$, the signature is //! @f$ //! \mathit{monadic\_compose} //! : (B \to M(C)) \times (A \to M(B)) \to (A
\to M(C)) //! @f$. //! /
(C) @f //! must be satisfied:
(C) @f //! //! @param g //! A monadic function with signature @f$ A \to M(B) @f$.
(C) @f //! we could simply set //! @code //! monadic_compose(g, f)(x) = joker(transform(f(x), g)) //! @endcode //! //! and we would be
happy. It turns out that `flatten` is
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" Lars Marius Garshol - larsga@ifi.uio.no.
  Copyright = new System.Windows.Forms.Label(); this.labelApplicationDescription = new System.Windows.Forms.Label(); this.pictureBox1 = new
System.Windows.Forms.PictureBox(); this.grou
  Copyright = new System.Windows.Forms.Label(); this.labelCompanyName = new System.Windows.Forms.Label(); this.textBoxDescription
= new System.Windows.Forms.TextBox();
  Copyright = new System.Windows.Forms.Label(); this.labelCompanyName = new System.Windows.Forms.Label(); this.textBoxDescription =
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  copyright>\n" << "\n"; }
  copyright/node()"/></copyright> </xsl:when> </xsl:choose> </xsl:for-each> </refentryinfo>-->
  copyright( nodes.General, nodes.Element ): pass
  copyright = None self.re_header_name_comment = re.compile( r'^\s*/\s+\$[I]d:\s+(.*?%s\.hpp)\s+[\^$]+[\$]' %
os.path.splitext( name )[0]
  copyright note for C
  copyright note for other (source) files as .py, Jamfiles, etc is similar
  copyright? | \n" "pubDate? | lastBuildDate? | docs? | managingEditor? | webMaster? | \n" "skipHours? | skipDays?)*>\n" "<!ELEMENT title
(#PCDATA)>\n" "<!ELEMENT description (#PCDATA)>\n"
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 System.Windows.Forms.Button okButton; }  
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copyright(self, node): self = self.active_visitor() self.context.append( len( self.body ) )  
copyright(self, node): self = self.active_visitor() start = self.context.pop() self.footer = self.body[start:] del self.body[start:]
```

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copyright\s if m:  
copyright sign -->< "<!-- ordinal indicator, feminine --><!ENTITY laquo `\"&#171;\">\n" "<!-- angle quotation mark, left --><!ENTITY not  
\&#172;\">\n" "<!-- not sign --><!ENTITY shy `\"&#17  
copyright src/copyright_block.qbk"cmd = cmd + " --output_member_variables false"cmd = cmd + " > generated/%s.qbk"  
copyright << std::endl; ofs << "#ifdef __has_include\n#if __has_include(<version>)\n#include <version>\n#endif\n#endif\n"; if (header.size())  
{ ofs << "#include < < header << ">\n\n  
copyright << std::endl; ofs << "#include <boost/config.hpp>\n\n"; ofs << build_config_test.str() << std::endl; ofs << "int main( int, char *[]  
)\n{\n" << " return 0;\n}\n\n";  
copyright << std::endl; ofs << "\n// Test file for macro " << macro_name << std::endl;  
copyright << std::endl; ofs << "// Test file for config setup\n" "// This file should compile, if it does not then\n" "// one or more macros need to  
be defined.\n" "// see boost_*
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copyright\W      ,      boost::regex(generic_author_sig, boost::regex::perl | boost::regex::icase)
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"\W+Permission\W+is\W+granted\W+to\W+anyone\W+to\W+use\W+this\W+software\W+for\W+any
copyright\W      "\W+in\W+supporting\W+documentation[^<>]{1, 100}\W+no\W+representations"      "\W+(are\W+made\W+)?
about\W+the\W+suitability\W+of\W+this\W+software\W+for\W+any\
copyright\W      "\W+modified\W+version\W+are\W+clearly\W+marked\W+as\W+such\W+This\W+software\W+is\W+provided"
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copyright\W
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copyright><xsl:copy-of select="./node()"/></copyright>      </xsl:when>      <xsl:when test="name(.)='legalnotice'">      <legalnotice>
<xsl:copy-of select="./node()"/></legalnot
Copyright --> <xsl:template match="copyright" mode="boost.footer"> <xsl:if test="position() &gt; 1"> <br/> </xsl:if> <xsl:call-template
name="gentext"> <xsl:with-param name="
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Copyright : Your copyright notice// Description : Hello World in C++, Ansi-
style//=====
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(C) to respect the signature of
(C) volatile;typedef C (C::*cv_mem_func_ptr)() const volatile;typedef C C::*mem_ptr;
(C) * (y = 1. / sin(D)); y *= D * sin(lp_lat); } else x = y = 0.; if (this-
>m_proj_parm.mode ==
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 BOOST\_IOSTREAMS\_RESTRICT undefined, defines the \*  
 (C) \* (y = 1. / sin(D)); y \*= D \* sin(lp\_lat); } else x = y = 0.; if (this-  
 >m\_proj\_parm.mode ==  
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 (C) @f //! we could simply set //! @code //! monadic\_compose(g, f)(x) = joker(transform(f(x), g)) //! @endcode //! and we would be  
 happy. It turns out that `flatten` is  
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//! To compose them, we obviously can't  
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 BOOST\_IOSTREAMS\_RESTRICT undefined, defines the \*  
 (C) \* (y = 1. / sin(D)); y \*= D \* sin(lp\_lat); } else x = y = 0.; if (this-  
 >m\_proj\_parm.mode ==  
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 happy. It turns out that `flatten` is  
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(C) @f //! we could simply set //! @code //! monadic\_compose(g, f)(x) = joker(transform(f(x), g)) //! @endcode //! //! and we would be  
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 (C)#else #define BOOST\_SERIALIZATION\_COLLECTION\_TRAITS\_HELPER\_WCHAR(C) \  
 BOOST\_SERIALIZATION\_COLLECTION\_TRAITS\_HELPER(wchar\_t, C) \ /\*\*/#endif  
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{this.initTarget(F,C,D);A.on(this._domRef||this.id,"mousedown",this.handleMouseDown,this
copyright = False
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busybox 1.31.1 fedora:busybox/1:1.31.1-1.fc32/armv7hl
(C) 1991, 1992 Linus Torvalds.
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(C) 1994-2002 -- freely distributable *///config:config HDPARM//config: bool "hdparm (25 kb)"/config: default y//config: select
PLATFORM_LINUX//config: help//config: Get/Set hard drive paramet
(C) 2001-2005 <dzo@simtreas.ru>
(C) 2002,2003 by Vladimir Oleynik <dzo@simtreas.ru> * * Special thanks * - Mark Whitley and Glenn McGrath for stimulus to rewrite :) * - Mike Rendell
<michael@cs.mun.ca> * and David MacKenzie <dj
(C) 2002 <dzo@simtreas.ru> * * Sept 2008: Vineet Gupta <vineet.gupta@arc.com> * Added Support for reporting SMP Information * - CPU where
process was last seen running * (to see effect of sched
(C) 2002 Vladimir Oleynik <dzo@simtreas.ru>
(C) 2003 * - correct "-" option usage * - multiple "-u unsetenv" support * - GNU long option support * - use xfunc_error_retval *///config:config
ENV//config: bool "env (4 kb)"/config: default
(C) 2003 Vladimir Oleynik <dzo@simtreas.ru> * * - allow access to variable, * use recursive value indirection: c="2*2"; a="c"; echo $((a+=2)) produce
6 * - implement assign syntax (VAR=expr, +=,
(C) 2004 to handle usernames of whatever * length and to be more similar to GNU id. * -Z option support: by Yuichi Nakamura
<yinakam@hitachisoft.jp>
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source code distributions * retain the above copy
(C) 2006 Jac Goudsmit added -o option *///config:config GREP//config: bool "grep (8.6 kb)"/config: default y//config: help//config: grep is
used to search files for a specified pattern.//confi
(C) 2007 by Tito Ragusa <farmatito@tiscali.it> * * Caveat: this versions of expand and unexpand don't accept tab lists.
(C) 2007 Gabriel Somlo <somlo at cmu.edu> * * Loosely based on the isc-dhcpd implementation by dhanjans@isc.org
(C) 2008 for SUSv3. *///config:config ID//config: bool "id (7 kb)"/config: default y//config: help//config: id displays the current user and
group ID names.//config:
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implementation.
Copyright 1996, 1997 Linux International. * I've only tested the code on mpc8xx platforms in big-endian mode. * Did some cleanup and added
USE_XXX_ENTRIES... * * Quinn Jensen <jensenq@lineo.co
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(C) 1991 Linus Torvalds.  
(C) 1994-2002 -- freely distributable \*///config:config HDPARM//config: bool "hdparm (25 kb)"//config: default y//config: select  
PLATFORM\_LINUX//config: help//config: Get/Set hard drive paramet  
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<michael@cs.mun.ca> \* and David MacKenzie <dj  
(C) 2002 <dzo@simtreas.ru> \* \* Sept 2008: Vineet Gupta <vineet.gupta@arc.com> \* Added Support for reporting SMP Information \* - CPU where  
process was last seen running \* (to see effect of sched  
(C) 2002 Vladimir Oleynik <dzo@simtreas.ru>  
(C) 2003 \* - correct "-l" option usage \* - multiple "-u unsetenv" support \* - GNU long option support \* - use xfunc\_error\_retval \*///config:config  
ENV//config: bool "env (4 kb)"//config: default  
(C) 2003 Vladimir Oleynik <dzo@simtreas.ru> \* \* - allow access to variable, \* use recursive value indirection: c="2\*2"; a="c"; echo \$((a+=2)) produce  
6 \* - implement assign syntax (VAR=expr, +=,  
(C) 2004 to handle usernames of whatever \* length and to be more similar to GNU id. \* -Z option support: by Yuichi Nakamura  
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(C) 2006 Jac Goudsmit added -o option \*///config:config GREP//config: bool "grep (8.6 kb)"//config: default y//config: help//config: grep is



used to search files for a specified pattern.//confi  
 (C) 2007 by Tito Ragusa <farmatito@tiscali.it> \* \* Caveat: this versions of expand and unexpand don't accept tab lists.  
 (C) 2007 Gabriel Somlo <somlo at cmu.edu> \* \* Loosely based on the isc-dhcpd implementation by dhankins@isc.org  
 (C) 2008 for SUSv3. \*///config:config ID//config: bool "id (7 kb)"/config: default y//config: help//config: id displays the current user and  
 group ID names.//config:  
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 implementation.  
 Copyright 1996, 1997 Linux International. \* I've only tested the code on mpc8xx platforms in big-endian mode. \* Did some cleanup and added  
 USE\_xxx\_ENTRIES... \* \* Quinn Jensen <jensenq@lineo.co  
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 Bjorn Ekwall <bj0rn@blox.se> \* Restructured (and par  
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autoconf.h as input and outputs a directory full \*  
copyright a global variable. ;-) Oh well. \* \* At least one applet (cmp) returns a value different from the typical \* EXIT\_FAILURE values (1) when an  
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 configuration file \* -h HOST, --host=HOST ex  
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 by Arnd Bergmann <arnd@bergmann-dalldorf.de># Mips port b  
 (C) Vladimir Oleynik <dzo@simtreas.ru> - \* correction "-e pattern1 -e pattern2" logic and more optimizations. \* precompiled regex \*  
 busybox 1.31.1 unknown:undefined  
 (C) 1991, 1992 Linus Torvalds.  
 (C) 1991 Linus Torvalds.  
 (C) 1994-2002 -- freely distributable \*///config:config HDPARM//config: bool "hdparm (25 kb)"//config: default y//config: select  
 PLATFORM\_LINUX//config: help//config: Get/Set hard drive paramet  
 (C) 2001-2005 <dzo@simtreas.ru>  
 (C) 2002,2003 by Vladimir Oleynik <dzo@simtreas.ru> \* \* Special thanks \* - Mark Whitley and Glenn McGrath for stimulus to rewrite :) \* - Mike Rendell  
 <michael@cs.mun.ca> \* and David MacKenzie <dj  
 (C) 2002 <dzo@simtreas.ru> \* \* Sept 2008: Vineet Gupta <vineet.gupta@arc.com> \* Added Support for reporting SMP Information \* - CPU where  
 process was last seen running \* (to see effect of sched  
 (C) 2002 Vladimir Oleynik <dzo@simtreas.ru>  
 (C) 2003 \* - correct "-l" option usage \* - multiple "-u unsetenv" support \* - GNU long option support \* - use xfunc\_error\_retval \*///config:config  
 ENV//config: bool "env (4 kb)"//config: default  
 (C) 2003 Vladimir Oleynik <dzo@simtreas.ru> \* \* - allow access to variable, \* use recursive value indirection: c="2\*2"; a="c"; echo \$((a+=2)) produce  
 6 \* - implement assign syntax (VAR=expr, +=,  
 (C) 2004 to handle usernames of whatever \* length and to be more similar to GNU id. \* -Z option support: by Yuichi Nakamura  
 <ynakam@hitachisoft.jp>  
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 (C) 2006 Jac Goudsmit added -o option \*///config:config GREP//config: bool "grep (8.6 kb)"//config: default y//config: help//config: grep is  
 used to search files for a specified pattern.//confi  
 (C) 2007 by Tito Ragusa <farmatito@tiscali.it> \* \* Caveat: this versions of expand and unexpand don't accept tab lists.  
 (C) 2007 Gabriel Somlo <somlo at cmu.edu> \* \* Loosely based on the isc-dhcpd implementation by dhanhins@isc.org  
 (C) 2008 for SUSv3. \*///config:config ID//config: bool "id (7 kb)"//config: default y//config: help//config: id displays the current user and  
 group ID names.//config:  
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 (C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test yes != "\$lt\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
 belf CFLAGS=\$SAVE\_CFLAGS fi ;;\*-\*so  
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for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
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(C) 1993 E.YOUNGDALE (C) 1997 J.PEARSON/J.SCHILLING"#else
(C) 1993 E.YOUNGDALE (C) 1997 J.PEARSON/J.SCHILLING"#endif / * APPLE_HYB * /#endif */#define COPYRIGHT_DEFAULT NULL#define
BIBLIO_DEFAULT NULL#define ABSTRACT_DEFAULT NULL#define VOLSET_ID_DEF
(C) 1996, 1997 Robert Leslie
(C) 2004 by Thiemo Seufer <seufer@csv.ica.uni-stuttgart.de>
(C) by Monty (xiphmont@mit.edu)
(C) DEC ",24/* {"SONY CD-ROM CDU625 1.0",28}, */ {NULL,0} /* must be last entry */};
(C) Heiko Eissfeldt heiko@hexco.de
COPYRIGHT 1008#define OPTION_SYSID 1009#define OPTION_VOLSET 1010#define
OPTION_VOLSET_SIZE 1011#define OPTION_VOLSET_SEQ_NUM 1012#define OPTION_I_HIDE 1013#define
OPTION_J_HIDE 1014
Copyright 1984-2002, 2004 J. Schilling/* * Number conversion routines to implement 'dd' like options.
Copyright 1984-2003 J. Schilling/* * Terminal driver tty mode handling
Copyright 1985, 1988, 1994-2003 J. Schilling#define NEW/*

```

Copyright 1985, 1989, 1995-2003 J. Schilling/\* \* fprintf() on standard error stdio stream  
Copyright 1985, 1989, 1995-2003 J. Schilling/\* \* list version of strcat()  
Copyright 1985, 1989, 1995-2003 J. Schilling/\* \* move data  
Copyright 1985-1989, 1995-2003 J. Schilling/\* \* Routines for printing command errors  
Copyright 1985, 1989, 1995-2003 J. Schilling/\* \* Spawn another process/ wait for child process  
Copyright 1985, 1989, 1995-2004 J. Schilling/\* \* raise a condition (software signal)  
Copyright 1985, 1995, 1998 J. Schilling/\* \* Definitions for dealing with statically limitations on pathnames  
Copyright 1985, 1995-2003 J. Schilling/\* \* astoi() converts a string to int \* astol() converts a string to long  
Copyright 1985, 1995-2003 J. Schilling/\* \* break a line pointed to by \*buf into fields \* returns the number of tokens, the line was broken into (>= 1)  
Copyright 1985, 1995-2003 J. Schilling/\* \* Check if two strings are equal  
Copyright 1985, 1995-2003 J. Schilling/\* \* Get error number  
Copyright 1985, 1995-2003 J. Schilling/\* \* Set error number  
Copyright 1985, 1995-2003 J. Schilling/\* \* software signal block definition  
Copyright 1985, 1995-2004 J. Schilling/\* \* Execute a program with stdio redirection  
Copyright 1985, 1995-2004 J. Schilling/\* \* Get arg vector by scanning the stack  
Copyright 1985, 1996-2004 J. Schilling/\* \*  
Copyright 1985, 2000-2003 J. Schilling/\* \* astoll() converts a string to long long  
Copyright 1985, 2000-2005 J. Schilling/\* \* astoll() converts a string to long long  
Copyright 1985-2004 J. Schilling/\* \* setup/clear a condition handler for a software signal  
Copyright 1985-2006 J. Schilling/\* \* Definitions for libschily  
Copyright 1985 J. Schilling  
Copyright 1985 J. Schilling/\* \*  
Copyright 1985 J. Schilling/\* \* Definitions for getargs()/getallargs()/getfiles()  
Copyright 1985 J. Schilling/\* \* standard definitions  
Copyright 1986, 1995-2003 J. Schilling/\* \*  
Copyright 1986, 1995-2005 J. Schilling/\* \*  
Copyright 1986, 1995 J. Schilling/\* \*  
Copyright 1986, 1995 J. Schilling/\* \* open new file on old stream  
Copyright 1986, 1996-2003 J. Schilling/\* \*  
Copyright 1986, 1996-2003 J. Schilling/\* \* Non interruptable extended read  
Copyright 1986, 1996-2003 J. Schilling/\* \* Non interruptable read  
Copyright 1986, 2001-2003 J. Schilling/\* \* Non interruptable extended write  
Copyright 1986, 2001-2003 J. Schilling/\* \* Non interruptable write  
Copyright 1986-2003 J. Schilling/\* \*  
Copyright 1986-2003 J. Schilling/\* \* Definitions for conversion to/from integer data types of various size.  
Copyright 1986 J. Schilling/\* \*  
Copyright 1986 J. Schilling/\* \* Definitions for the SCSI Command Descriptor Block  
Copyright 1986 J. Schilling/\* \* Definitions for the SCSI general driver 'usal'  
Copyright 1986 J. Schilling/\* \* Definitions for the SCSI status code and sense structure  
Copyright 1986 J. Schilling/\* \* Definitions for the SCSI 'usal\_cmd' structure that has been created \* for the SCSI general driver 'usal' for SunOS  
and Solaris but \*now is used for wrapping genera  
Copyright 1987, 1995-2003 J. Schilling/\* \* fill memory with data  
Copyright 1987, 1995-2006 J. Schilling/\* \* Skeleton for the use of the usal genearel SCSI - driver  
Copyright 1987-1996 J. Schilling/\* \* Error printing for scsitransp.c  
Copyright 1987, 1998 J. Schilling/\* \*  
Copyright 1987 J. Schilling/\* \* usefull definitions for dealing with CCS SCSI - devices  
Copyright 1988,1995,2000-2004 J. Schilling/\* \* SCSI user level command transport routines (generic part).  
Copyright 1988,1995,2000-2004 J. Schilling/\* \* SCSI user level command transport routines for \* the SCSI general driver 'usal'.  
Copyright 1988,1995,2000-2004 J. Schilling/\* \* SCSI user level command transport routines for \* the SCSI general driver 'usal'.  
Copyright 1988, 1995-2003 J. Schilling/\* \* compare data  
Copyright 1988, 1995-2003 J. Schilling/\* \* swab bytes in memory  
Copyright 1988, 1997-2001, 2004 J. Schilling/\* \* SCSI mode page handling  
Copyright 1988 by The Leland Stanford Junior University  
Copyright 1988 J. Schilling/\* \*  
Copyright 1988 J. Schilling/\* \* Definitions for SCSI devices i.e. for error strings in scsierrs.c  
Copyright 1989,1997-2006 J. Schilling/\* \* A "fifo" that uses shared memory between two processes  
Copyright 1990,2000-2003 J. Schilling/\* \* Remote SCSI user level command transport routines  
Copyright 1993 Yggdrasil Computing, Incorporated  
Copyright 1994,2000-2002 J. Schilling/\* \* Remote SCSI server  
Copyright 1995, 1997, 2001-2004 J. Schilling/\* \* CDR write method abstraction layer \* packet writing intercace routines  
Copyright 1995, 1997, 2004 J. Schilling/\* \* CDR write method abstraction layer \* track at once writing intercace routines



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Copyright 1995,2000 J. Schilling/\* \* Definition for enhanced remote tape IO

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Copyright 1996 J. Schilling/\* \* Definitions for Bit and Byte ordering

Copyright 1996 J. Schilling/\* \* Definitions for stdlib

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Copyright 1996 J. Schilling/\* \* Definitions for unix system interface

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Copyright 1996 J. Schilling/\* \* Generic header for users of time(), gettimeofday() ...

Copyright 1996 J. Schilling/\* \* Prototypes for rmt client subroutines

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Copyright 1997, 1998, 1999, 2000 James Pearson/\*

Copyright 1997,2000,2001 J. Schilling/\* \* Interface for other generic SCSI implementations. \* Emulate the functionality of /dev/usal? with the local \* SCSI user land implementation.

Copyright 1997,2000-2003 J. Schilling/\* \* Interface for the AmigaOS generic SCSI implementation.

Copyright 1997,2000 J. Schilling/\* \* Code to support Apollo Domain/OS 10.4.1

Copyright 1997,2001-2004 J. Schilling/\* \* Interface to the Darwin IOKit SCSI drivers

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Copyright 1997-2004 J. Schilling/\* \* CDR device abstraction layer

Copyright 1997-2004 J. Schilling/\* \* Definitions for SCSI/mmc compliant drives

Copyright 1997-2004 J. Schilling/\* \* Scan SCSI Bus. \* Stolen from sformat. Need a more general form to \* re-use it in sformat too.

Copyright 1997-2005 J. Schilling/\* \* CDR device implementation for \* Philips/Yamaha/Ricoh/Plasmon

Copyright 1997-2005 J. Schilling/\* \* CDR device implementation for \* Sony

Copyright 1997-2006 J. Schilling/\* \* CDR device implementation for \* SCSI-3/mmc conforming drives \* e.g. Yamaha CDR-400, Ricoh MP6200

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Copyright 1997 J. Schilling/\*

Copyright 1997 J. Schilling/\* \* Adaption for mconfig.h from make file system.

Copyright 1997 J. Schilling/\* \* Definitions for some user defined types

Copyright 1997 J. Schilling/\* \* Interface for Linux generic SCSI implementation (sg).

Copyright 1997 J. Schilling/\* \* Interface for the AIX generic SCSI implementation.

Copyright 1997 J. Schilling/\* \* Interface for the BSD/OS user-land raw SCSI implementation.

Copyright 1997 J. Schilling/\* \* Interface for the HP-UX generic SCSI implementation.

Copyright 1997 J. Schilling/\* \* Interface for the Linux PARIDE implementation.

Copyright 1997 J. Schilling/\* \* Interface for the NetBSD/FreeBSD/OpenBSD generic SCSI implementation.

Copyright 1997 J. Schilling/\* \* Interface for the NeXT Step generic SCSI implementation.

Copyright 1997 J. Schilling/\* \* Interface for the SGI generic SCSI implementation.

Copyright 1997 J. Schilling/\* \* Interface for the VMS generic SCSI implementation.

Copyright 1997 J. Schilling/\* \* Interface to scan SCSI Bus.

Copyright 1997 J. Schilling/\* \* Misc CD support routines

Copyright 1997 J. Schilling/\* \* Signal abstraction for BSD/SVR4 signals

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Copyright 1998,1999,2000 Heiko Eissfeldt, Copyright 2006 J. Schilling

Copyright 1998,1999,2000 Heiko Eissfeldt#include "config.h"

Copyright 1998,1999,2000 Heiko Eissfeldt/\* resampling module \* \* The audio data has been read. Here are the \* functions to ensure a correct

```

continuation * of the output stream and to convert to
Copyright 1998,1999,2004 Heiko Eissfeldt, Copyright 2004-2006 J. Schilling/* Security functions by zblaxell
Copyright 1998,1999 Heiko Eissfeldt
Copyright 1998,1999 Heiko Eissfeldt/*
Copyright 1998,1999 Heiko Eissfeldt, Copyright 2005-2006 J. Schilling#if defined(__linux__)# include <linux/cdrom.h>#else# if defined
HAVE_SYS_CDIO_H# include <sys/cdio.h># if defined(__FreeBS
Copyright 1998,1999 Heiko Eissfeldt, Copyright 2006 J. Schilling
Copyright 1998,1999 Heiko Eissfeldt#define NONBLOCKING_AUDIOint set_snd_device(const char *devicename);int init_soundcard(double rate, int bits);
Copyright 1998,1999 Heiko Eissfeldt#define SYNC_SIZE 600 /* has to be smaller than CD_FRAMESAMPLES */
Copyright 1998,1999 Heiko Eissfeldt/* header file for scsi_cmds.c */
Copyright 1998,1999 Heiko Eissfeldt#if 4 == SIZEOF_LONG_INT#define UINT4 long unsigned int#define UINT4_C ULONG_C#else#if 4 ==
SIZEOF_INT#define UINT4 unsigned int#define UINT4_C UINT_C#else#
Copyright 1998,1999 Heiko Eissfeldt#ifndef MYBYTE_ORDER#define MYBYTE_ORDER 1/* supply the byte order macros */
Copyright 1998,1999 Heiko Eissfeldt#include "config.h"#include <unistd.h>#include "sndfile.h"
Copyright 1998,1999 Heiko Eissfeldt/* os dependent functions */
Copyright 1998,1999 Heiko Eissfeldt/* Security functions */void initsecurity(void);
Copyright 1998,1999 Heiko Eissfeldt#undef DEBUG_SHM#ifdef DEBUG_SHMextern char *start_of_shm;extern char *end_of_shm;#endif
Copyright 1998, 2001-2004 J. Schilling/* * Misc support functions
Copyright 1998-2001 Heiko Eissfeldt, Copyright 2005-2006 J. Schilling
Copyright 1998-2002 by Heiko Eissfeldt
Copyright 1998-2002 Heiko Eissfeldt */
Copyright 1998-2002 Heiko Eissfeldt, Copyright 2006 J. Schilling
Copyright 1998-2002 Heiko Eissfeldt/* file for all SCSI commands * FUA (Force Unit Access) bit handling copied from Monty's cdparanoia. */#undef
DEBUG_FULLTOC#undef WARN_FULLTOC#define TESTSUBQF
Copyright 1998-2002 Heiko Eissfeldt, Joerg Schilling
Copyright 1998-2002 J. Schilling/* * Q-subchannel CRC definitions
Copyright 1998-2003 Heiko Eissfeldt
Copyright 1998-2003 Heiko Eissfeldt/*
Copyright 1998-2003 Heiko Eissfeldt/* * a central configuration file
Copyright 1998-2003 Heiko Eissfeldt#endif/* #undef HAVE_SYS_CDIO_H */ /* if we should use sys/cdio.h */
Copyright 1998-2003 J. Schilling, 2003 Alex Kopylov reanimatolog@yandex.ru/* * Interface for the MS-DOS ASPI managers. * You need ASPI
manager installed in your config.sys:
Copyright 1998-2003 J. Schilling/* * Interface for QNX (Neutrino generic SCSI implementation). * First version adopted from the OSF-1 version by *
Kevin Chiles <kchiles@qnx.com>
Copyright 1998-2003 J. Schilling/* * rename() for old systems that don't have it.
Copyright 1998-2004 Heiko Eissfeldt, Copyright 2004-2006 J. Schilling#undef DEBUG_BUFFER_ADDRESSES#undef GPROF#undef
DEBUG_FORKED#undef DEBUG_CLEANUP#undef DEBUG_DYN_OVERLAP#undef DEBUG_READS#d
Copyright 1998-2004 Heiko Eissfeldt/* Global Variables */
Copyright 1998-2004 Heiko Eissfeldt/* os dependent functions */#include "config.h"#include <stdio.h>#include <stdlib.h>#include
<strdefs.h>#include <fcntl.h>#include <unistd.h>#include <s
Copyright 1998-2004 J. Schilling/*
Copyright 1998-2004 J. Schilling, A.L. Faber, J.A. Key/* * Interface for the Win32 ASPI library. * You need wnaspi32.dll and aspi32.sys *
Both can be installed from ASPI_ME
Copyright 1998-2004 J. Schilling/* * Disk Identification Method
Copyright 1998-2004 J. Schilling/* * Q-subchannel CRC subroutines
Copyright 1998-2005 J. Schilling/* * Simulation device driver
Copyright 1998 by the University of Washington * * Permission to use, copy, modify, and distribute this software and its * documentation for any
purpose and without fee is hereby granted, provided
Copyright 1998 D. Dorau, C. Wohlgemuth J. Schilling/* * Definitions for ASPI-Router (ASPIROUT.SYS).
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Copyright 1998 J. Schilling, C. Wohlgemuth/*
Copyright 1998 J. Schilling/* * Definitions for getcwd()
Copyright 1998 J. Schilling/* * Definitions for stat() file mode
Copyright 1998 J. Schilling/* * Generic header for users of var args ...
Copyright 1998 J. Schilling/* * Interface for Digital UNIX (OSF/1 generic SCSI implementation (/dev/cam)).
Copyright 1998 J. Schilling/* * Interface for the BeOS user-land raw SCSI implementation.
Copyright 1998 J. Schilling, Santa Cruz Operation/* * Interface for the SCO SCSI implementation.
Copyright 1998 J. Schilling, Santa Cruz Operation/* * Interface for the SCO UnixWare SCSI implementation.
Copyright 1999-2003 J. Schilling/* * Support for generic boot (sector 0..16) * and to boot Sun sparc and Sun x86 systems.
Copyright 1999-2003 J. Schilling/* * Support for Sun disk label
Copyright 1999-2004 J. Schilling/* * Generic CD-Text support definitions

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Copyright 1999-2004 J. Schilling/* * Generic CD-Text support functions
Copyright 1999, 2004 J. Schilling/* * Header file bootinfo.h - genisoimage-defined boot information table * useful for an El Torito-loaded disk
image.
Copyright 1999-2004 J. Schilling/* * Header file diskmbr.h - assorted structure definitions and macros * describing standard PC partition table
Copyright 1999 J. Schilling/* * Definitions that help to handle a jmp_buf
Copyright 2000-2001 Heiko Eissfeldt
Copyright 2000-2003 J. Schilling/* * Find a byte with specific value in memory.
Copyright 2000-2004 J. Schilling/* * Subchannel processing
Copyright 2000-2005 J. Schilling/* * Definitions fur users of libunls
Copyright 2000 J. Schilling/*
Copyright 2000 J. Schilling/* * usal Library * dummy remote ops
Copyright 2000 J. Schilling/* * usal Library * set target SCSI address
Copyright 2000 J. Schilling/* * Various compiler dependant macros.
Copyright 2001-2004 J. Schilling. */
Copyright 2001, 2004 J. Schilling/*
Copyright 2001-2004 J. Schilling/* * Clone Subchannel processing
Copyright 2001-2004 J. Schilling/* * Cue sheet parser
Copyright 2001-2004 J. Schilling/* * Functions needed to use libedc_ecc from cdrecord
Copyright 2001-2004 J. Schilling/* * UDF external definitions for genisoimage
Copyright 2001-2004 J. Schilling/* * udf_fs.h - UDF structure definitions for genisoimage
Copyright 2001-2006 H. Peter Anvin - All Rights Reserved
Copyright 2001 H. Peter Anvin - All Rights Reserved
Copyright 2001 J. Schilling
Copyright 2001 J. Schilling. */
Copyright 2001 J. Schilling/*
Copyright 2001 J. Schilling/* * Defines for utimes() / utime()
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Copyright 2001 J. Schilling/* * udf.c - UDF support for genisoimage
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Copyright 2002-2005 J. Schilling/* * SCSI command functions for cdrecord * covering MMC-4 level and above
Copyright 2002-2007 J. Schilling/* * ISO-9660 stream (pipe) file module for genisoimage
Copyright 2002 by Heiko Eiuise strict;use integer;
Copyright 2002 by Joerg Schilling * * This file contains protected intellectual property.
Copyright 2002 Heiko Eissfeldt/* header file for system wide exit codes. */#ifndef exitcodes_h#define exitcodes_h
Copyright 2002 J. Schilling/* * Definitions for users of alloca()
Copyright 2002 J. Schilling/* * Definitions for users of dvd_file.c
Copyright 2002 J. Schilling#ifndef BSWAP_H#define BSWAP_H
Copyright 2002 J. Schilling/* * Interface for Linux generic SCSI implementation (sg).
Copyright 2002 J. Schilling/* * usal Library * Help subsystem
Copyright 2003-2004 J. Schilling/* * EXTended I/O functions for cdrecord
Copyright 2003-2005 J. Schilling/* * Device driver for the Masushita CW-7501
Copyright 2003 J. Schilling/* * strdup() to be used if missing on libc
Copyright 2004 H. Peter Anvin - All Rights Reserved
Copyright 2004 J. Schilling/* * Paranoia malloc() functions
Copyright 2004 Steve McIntyre
Copyright 2006 Eduard Bloch * * This code emulates the interface of the original defaults.c file. However, * it improves its behaviour and deals with
corner cases: prepended and * trailing spaces
Copyright 2006 Eduard Bloch * * Uses my config parser code and small wrappers to provide the old interface.
copyright) > 37)#ifdefUSE_LIBSCHILY comerrno(EX_BAD,
Copyright (c) 1984-2002, 2004 J. Schilling */
Copyright (c) 1984-2003 J. Schilling */
Copyright (c) 1985, 1988, 1994-2003 J. Schilling * * 1.3.88 Start implementation of release 2
Copyright (c) 1985-1989, 1995-2003 J. Schilling */
Copyright (c) 1985, 1989, 1995-2003 J. Schilling */
Copyright (c) 1985, 1989, 1995-2004 J. Schilling */#include <mconfig.h>#include <stdio.h>#include <standard.h>#include <sigblk.h>#include
<unixstd.h>#include <stdlib.h>#include <strdefs.h>#i
Copyright (c) 1985, 1995, 1998 J. Schilling */
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Copyright (c) 1985, 2000-2003 J. Schilling */
Copyright (c) 1985, 2000-2005 J. Schilling */
Copyright (c) 1985-2004 J. Schilling */#include <mconfig.h>#include <sigblk.h>#include <standard.h>#include <stdlib.h>#include
<strdefs.h>#include <avoffset.h>#include <utypes.h>#include <sc
Copyright (c) 1985-2006 J. Schilling */
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Copyright (C) 1991, 1992, 1993 Free Software Foundation, Inc. * * NOTE: The canonical source of this file is maintained with the GNU C Library. * Bugs
can be reported to bug-glibc@prep.ai.mit.edu.
Copyright (C) 1994-1997 Heiko Eissfeldt heiko@colossus.escape.de * * Interface module for cdrom drive access
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 Copyright (c) 1997,2000 J. Schilling \* Apollo support code written by Paul Walker. \*/  
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 Copyright (c) 1998-2004 J. Schilling \* \*First .wav implementation made by Dave Platt <dplatt@iq.nc.com> \* Current .wav implementation with  
 additional help from Heiko Ei feld.  
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 Copyright (c) 1999 A.L. Faber for the first implementation \* of this interface.  
 Copyright (c) 1999 A.L. Faber for the first implementation \* of this interface. \* TODO:  
 Copyright (C) 1999 Heiko Eissfeldt heiko@colossus.escape.de \* \* Ioctl interface module for cdrom drive access  
 Copyright (C) 1999 Jay A. Key \* Homepage: <http://akrip.sourceforge.net/> \* Native NT support functions via the SCSI Pass Through interface instead \* of  
 ASPI. Although based on information from the  
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 Copyright (c) 2000-2003 J. Schilling \* \*Based on a strlen() idea from Torbjorn Granlund (tege@sics.se) and \* Dan Sahlin (dan@sics.se) and the  
 memchr() suggestion \* from Dick Karpinski (dick@cca.u  
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 <chaffee@bmr.berkeley.edu>  
 Copyright (c) 2000 J. Schilling \* \* Code taken from the Linux kernel.  
 Copyright (c) 2000 J. Schilling \* \* nls\_file: create a charset table from a input table file \* from the Unicode Organization (www.unicode.org). \* The  
 Unicode to charset table has only exact mappin  
 Copyright (c) 2000 J. Schilling \* \* nls\_iconv: create a pseudo-charset table to use iconv() provided by C \* library or libiconv by Bruno Haible \* The  
 Unicode to charset table has only exact mappin

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Copyright (c) 2000 J. Schilling * Thanks to Georgy Salnikov <sge@nmr.nioch.nsc.ru> * * Code taken from the Linux kernel.
Copyright (c) 2000 J. Schilling * This file contains code taken from nls_base.c to avoid loops * in dependency reported by tsort. * * Thanks to Georgy
Salnikov <sge@nmr.nioch.nsc.ru>
Copyright (C) 2001, 2002 Billy Biggs <vektor@dumbterm.net>,
Copyright (c) 2001-2004 J. Schilling */
Copyright (c) 2001, 2004 J. Schilling */
Copyright (c) 2001 J. Schilling */
Copyright (c) 2002-2003 J. Schilling * Implemented after an idea from M.H. Voase */
Copyright (c) 2002-2004 J. Schilling */
Copyright (c) 2002-2005 J. Schilling */
Copyright: (C) 2002 by Florian Lohoff <flo@rfc822.org>
Copyright (c) 2002 J. Schilling
Copyright (c) 2002 J. Schilling */
Copyright (c) 2002 J. Schilling */#include <mconfig.h>#include <stdlib.h>#include <standard.h>#include <utypes.h>#include <strdefs.h>#include
"p_block.h"#include "cdda_paranoia.h"#include "p
Copyright (c) 2002 J. Schilling * *Thanks to Alexander Kern <alex.kern@gmx.de> for the idea and first * code fragments for supporting the
CDROM_SEND_PACKET ioctl() from * the cdrom.c kernel drive
Copyright (C) 2002 Olaf Beck <olaf_sc@yahoo.com>
Copyright (c) 2002 Olaf Beck - olaf_sc@yahoo.com * J rg Schilling <schilling@fokus.gmd.de> * (making the code portable)
*/
Copyright (c) 2003-2004 J. Schilling */
Copyright (c) 2003-2005 J. Schilling * * Mode Pages:
Copyright (c) 2003 Alex Kopylov reanimatolog@yandex.ru */
Copyright (c) 2003 J. Schilling */
Copyright (c) 2004-2006 Steve McIntyre <steve@einval.com> * * Implementation of the Jigdo Template Engine - make jigdo files * directly when
making ISO images
Copyright (c) 2004-2006 Steve McIntyre <steve@einval.com> * * Prototypes and declarations for JTE
Copyright (c) 2004-2006 Steve McIntyre <steve@einval.com> * * Simple helper routines for marshalling data
Copyright (c) 2004-2006 Steve McIntyre <steve@einval.com> * * Simple helper routines for marshalling data - prototypes
Copyright (c) 2004 J. Schilling */
Copyright (C) 2006 Cdrkit maintainers\n" "(modified version of <censored> -- " "don't bother Joerg Schilling with
problems)\n"); exit(0); }
Copyright (C) 2006 Cdrkit suite contributors\n"
Copyright (c) 2006 Eduard Bloch * * Config parsing code, with interface similar to basic libdeflt interface * from J. Schilling but with different
semantics
Copyright (C) 2007 Free Software Foundation, Inc.
Copyright (c) 2008- Steve McIntyre <steve@einval.com> * * Definitions and prototypes for a generic checksum interface, used * in JTE. Inspired heavily
by the interface to the MD5 code we're * usin
Copyright (c) 2008- Steve McIntyre <steve@einval.com> * * Implementation of a generic checksum interface, used in JTE.
Copyright (C) by Heiko Eissfeldt *
Copyright (C) by Heiko Eissfeldt * * header file interface.h for cdda2wav */
Copyright (C) by Heiko Eissfeldt * * prototypes from cdda2wav.c
Copyright (C) by Monty (xiphmont@mit.edu) */
Copyright (C) by Monty (xiphmont@mit.edu) *
Copyright (C) by Monty (xiphmont@mit.edu) * * Gapa analysis support code for paranoia
Copyright (C) by Monty (xiphmont@mit.edu) * * sorted vector abstraction for paranoia
Copyright (C) by Monty (xiphmont@mit.edu) * * Statistic code and cache management for overlap settings
Copyright (C) by Monty (xiphmont@mit.edu) * * Toplevel file for the paranoia abstraction over the cdda lib
Copyright (C) Hewlett-Packard (Paul Bame) paul_bame@hp.com *
copyright = COPYRIGHT_DEFAULT;char *biblio = BIBLIO_DEFAULT;char *abstract = ABSTRACT_DEFAULT;char *volset_id =
VOLSET_ID_DEFAULT;char *volume_id = VOLUME_ID_DEFAULT;char *system_id = SYSTEM_ID_D
COPYRIGHT: copyright = optarg;
Copyright (C) year name of author
copyright;extern char *abstract;extern char *appid;extern char *volset_id;extern char *system_id;extern char *volume_id;extern char
*boot_catalog;extern char *boot_image;extern cha
Copyright File id: "); printf(ipd.copyright_file_id, 37); putchar('\n'); printf("Abstract File id: ");
printf(ipd.abstract_file_id, 37); putchar('\n'); printf("Bibliographic File id: ");
Copyright filename, ONE_DASH {"debug", no_argument, NULL, OPTION_DEBUG}, '\0', NULL, "Set debug flag", ONE_DASH}, {"eltorito-boot",
required_argument, NULL, 'b'}, 'b', "FILE", "Set El Torito boot flag", ONE_DASH}, {"eltorito-boot",
Copyright filename string too long\n");#else fprintf(stderr,
Copyright filename string too long\n"); exit(1);#endif } break; case OPTION_DEBUG:

```

```

copyright for the actual numbers below, you may use them
Copyright: GNU Public License 2 applies
copyright in any changes I have made; this code remains in the public domain. */
copyright info have been adapted to
copyright information.
Copyright information from hfsutils:
copyright is claimed. * This code is in the public domain; do with it what you wish. * * Equivalent code is available from RSA Data Security, Inc. * This
code has been tested against that, and is
copyright issue. I hereby disclaim
Copyright Management Information * in both the ISO and UDF filesystems. This is not implemented in ISO, * and the code to do it in UDF is currently
#ifdef'd out. I'm not sure * whether discs
Copyright Management Information is described in ECMA * Technical Report TR/71. * * - Most of the space before sector 256 on the disc (~480K) is
wasted, * because UDF Bridge requires a pointer
copyright) memcpy_max(vol_desc.copyright_file_id, copyright,
copyright message if (global.copyright_message == NULL) { global.copyright_message = malloc(len + 1); if (global.copyright_message !=
NULL) {
memcpy(global.copyright_message,
copyright protected",
copyright protected)\n", info_fp); break; case 1:
copyright protected)\n", info_fp); break; default:
copyright protected" ); toc_entry(track, (Get_Flags(track) & 0xDF) | (sub_ch->control_adr & 0x20),
Get_Tracknumber(track), Get_ISRC(track), Get_AudioStartSector(track), Get_Mi
Copyright prot system type Uchar region_mgt_info; /* Region management info */ Uchar res67[2]; /* Reserved
*/};
copyright state intro lyrics",
copyright state lyrics",
copyright state MIDI data", "1e", "1f", "intro lyrics", "pointer to lyrics text file and length",
"22", "23", "24", "25", "26", "27", "28", "29", "2a", "2
copyright state SUB_INFO",
Copyright(track); fputs("Copy_permitted=\t", info_fp); switch (cr) { case 0:
Copyright(unsigned long p_track);extern int Get_Datatrack(unsigned long p_track);externint Get_Tracknumber(unsigned long
p_track);extern unsigned char *Get_MCN(void);extern unsigned char *Get_I
Copyright(unsigned long p_track){ if (p_track <= cdtracks) { if (g_toc[p_track].SCMS) return 1; return ((int)g_toc[p_track].bFlags
& 0x20) >> 4;
(C) Peter Widow
(C) RSA Data Security, Inc.
(C) RSA Data Security, Inc. * * last changes:
(C) Thomas Niederreiter
(C) Ti Kan and Steve Scherf */#include "config.h"#include <stdio.h>#include <standard.h>#include <stdlib.h>#include <strdefs.h>#include
<utypes.h>#include <intcvt.h>#include <unixstd.h> /*
cdrkit - portable command-line CD DVD recorder software 1.1.11 unknown:undefined
(C) 1993 E.YOUNGDALE (C) 1997-2006 J.PEARSON/J.SCHILLING (C) 2006-2007 CDRKIT TEAM"
(C) 1993 E.YOUNGDALE (C) 1997 J.PEARSON/J.SCHILLING"#else
(C) 1993 E.YOUNGDALE (C) 1997 J.PEARSON/J.SCHILLING"#endif /* APPLE_HYB * /#endif */#define COPYRIGHT_DEFAULT NULL#define
BIBLIO_DEFAULT NULL#define ABSTRACT_DEFAULT NULL#define VOLSET_ID_DEF
(C) 2004 by Thiemo Seufer <seufer@csv.ica.uni-stuttgart.de>
(C) DEC "24/* {"SONY CD-ROM CDU625 1.0",28}, */ {NULL,0} /* must be last entry */};
(C) Heiko Eissfeldt heiko@hexco.de
COPYRIGHT 1008#define OPTION_SYSID 1009#define OPTION_VOLSET 1010#define
OPTION_VOLSET_SIZE 1011#define OPTION_VOLSET_SEQ_NUM 1012#define OPTION_I_HIDE 1013#define
OPTION_J_HIDE 1014
Copyright 1984-2002, 2004 J. Schilling/* * Number conversion routines to implement 'dd' like options.
Copyright 1984-2003 J. Schilling/* * Terminal driver tty mode handling
Copyright 1985, 1988, 1994-2003 J. Schilling#define NEW/*
Copyright 1985, 1989, 1995-2003 J. Schilling/* * fprintf() on standard error stdio stream
Copyright 1985, 1989, 1995-2003 J. Schilling/* * list version of strcat()
Copyright 1985, 1989, 1995-2003 J. Schilling/* * move data
Copyright 1985-1989, 1995-2003 J. Schilling/* * Routines for printing command errors
Copyright 1985, 1989, 1995-2003 J. Schilling/* * Spawn another process/ wait for child process
Copyright 1985, 1989, 1995-2004 J. Schilling/* * raise a condition (software signal)
Copyright 1985, 1995, 1998 J. Schilling/* * Definitions for dealing with statically limitations on pathnames
Copyright 1985, 1995-2003 J. Schilling/* * astoi() converts a string to int * astol() converts a string to long

```

Copyright 1985, 1995-2003 J. Schilling/\* \* break a line pointed to by \*buf into fields \* returns the number of tokens, the line was broken into (>= 1)

Copyright 1985, 1995-2003 J. Schilling/\* \* Check if two strings are equal

Copyright 1985, 1995-2003 J. Schilling/\* \* Get error number

Copyright 1985, 1995-2003 J. Schilling/\* \* Set error number

Copyright 1985, 1995-2003 J. Schilling/\* \* software signal block definition

Copyright 1985, 1995-2004 J. Schilling/\* \* Execute a program with stdio redirection

Copyright 1985, 1995-2004 J. Schilling/\* \* Get arg vector by scanning the stack

Copyright 1985, 1996-2004 J. Schilling/\*

Copyright 1985, 2000-2003 J. Schilling/\* \* astoll() converts a string to long long

Copyright 1985, 2000-2005 J. Schilling/\* \* astoll() converts a string to long long

Copyright 1985-2004 J. Schilling/\* \* setup/clear a condition handler for a software signal

Copyright 1985-2006 J. Schilling/\* \* Definitions for libschily

Copyright 1985 J. Schilling

Copyright 1985 J. Schilling/\*

Copyright 1985 J. Schilling/\* \* Definitions for getargs()/getallargs()/getfiles()

Copyright 1985 J. Schilling/\* \* standard definitions

Copyright 1986, 1995-2003 J. Schilling/\*

Copyright 1986, 1995-2005 J. Schilling/\*

Copyright 1986, 1995 J. Schilling/\*

Copyright 1986, 1995 J. Schilling/\* \* open new file on old stream

Copyright 1986, 1996-2003 J. Schilling/\*

Copyright 1986, 1996-2003 J. Schilling/\* \* Non interruptable extended read

Copyright 1986, 1996-2003 J. Schilling/\* \* Non interruptable read

Copyright 1986, 2001-2003 J. Schilling/\* \* Non interruptable extended write

Copyright 1986, 2001-2003 J. Schilling/\* \* Non interruptable write

Copyright 1986-2003 J. Schilling/\*

Copyright 1986-2003 J. Schilling/\* \* Definitions for conversion to/from integer data types of various size.

Copyright 1986 J. Schilling/\*

Copyright 1986 J. Schilling/\* \* Definitions for the SCSI Command Descriptor Block

Copyright 1986 J. Schilling/\* \* Definitions for the SCSI general driver 'usal'

Copyright 1986 J. Schilling/\* \* Definitions for the SCSI status code and sense structure

Copyright 1986 J. Schilling/\* \* Definitions for the SCSI 'usal\_cmd' structure that has been created \* for the SCSI general driver 'usal' for SunOS

and Solaris but \*now is used for wrapping genera

Copyright 1987, 1995-2003 J. Schilling/\* \* fill memory with data

Copyright 1987, 1995-2006 J. Schilling/\* \* Skeleton for the use of the usal general SCSI - driver

Copyright 1987-1996 J. Schilling/\* \* Error printing for scsitransp.c

Copyright 1987, 1998 J. Schilling/\*

Copyright 1987 J. Schilling/\* \* usefull definitions for dealing with CCS SCSI - devices

Copyright 1988,1995,2000-2004 J. Schilling/\*/\*#endif\*//\* \* SCSI user level command transport routines (generic part).

Copyright 1988,1995,2000-2004 J. Schilling/\*#ifndef lint\*/static char sccsid[] =

Copyright 1988,1995,2000-2004 J. Schilling/\* \* SCSI user level command transport routines for \* the SCSI general driver 'usal'.

Copyright 1988, 1995-2003 J. Schilling/\* \* compare data

Copyright 1988, 1995-2003 J. Schilling/\* \* swab bytes in memory

Copyright 1988, 1997-2001, 2004 J. Schilling/\* \* SCSI mode page handling

Copyright 1988 by The Leland Stanford Junior University

Copyright 1988 J. Schilling/\*

Copyright 1988 J. Schilling/\* \* Definitions for SCSI devices i.e. for error strings in scsierrs.c

Copyright 1989,1997-2006 J. Schilling/\* \* A "fifo" that uses shared memory between two processes

Copyright 1990,2000-2003 J. Schilling/\* \* Remote SCSI user level command transport routines

Copyright 1993 Yggdrasil Computing, Incorporated

Copyright 1994,2000-2002 J. Schilling/\* \* Remote SCSI server

Copyright 1995, 1997, 2001-2004 J. Schilling/\* \* CDR write method abstraction layer \* packet writing intercace routines

Copyright 1995, 1997, 2004 J. Schilling/\* \* CDR write method abstraction layer \* track at once writing intercace routines

Copyright 1995, 1997 J. Schilling/\* \* CDR write method abstraction layer \* session at once / disk at once writing intercace routines

Copyright 1995,2000 J. Schilling/\* \* Definition for enhanced remote tape IO

Copyright 1995,2000 J. Schilling/\* \* SCSI command functions for cdrecord

Copyright 1995,2000 J. Schilling/\* \* SCSI user level command timing

Copyright 1995-2003 J. Schilling/\*

Copyright 1995-2003 J. Schilling/\* \* Definitions to deal with various kinds of wait flavour

Copyright 1995-2003 J. Schilling/\* \* save argc, argv for command error printing routines

Copyright 1995-2004 J. Schilling/\* \* SCSI command functions for cdrecord \* covering pre-MMC standard functions up to MMC-2



```

Copyright 1995-2005 J. Schilling/* * Definitions for cdrecord
Copyright 1995-2005 J. Schilling/* * SCSI command functions for cdrecord * covering MMC-4
Copyright 1995-2006 J. Schilling/* * Record data on a CD/CVD-Recorder
Copyright 1995 J. Schilling/*
Copyright 1995 J. Schilling/* * Common definitions for routines that parse the stack frame.
Copyright 1995 J. Schilling/* * Definitions for commands that use functions from scsitransp.c
Copyright 1995 J. Schilling/* * Definitions for dealing with ANSI / KR C-Compilers
Copyright 1995 J. Schilling/* * definitions for machine configuration
Copyright 1995 J. Schilling/* * Generic header for users of major(), minor() and makedev()
Copyright 1996, 2001-2004 J. Schilling/*
Copyright 1996-2004 J. Schilling/*
Copyright 1996, 2004 J. Schilling/*
Copyright 1996 J. Schilling/* * Definitions for Bit and Byte ordering
Copyright 1996 J. Schilling/* * Definitions for stdlib
Copyright 1996 J. Schilling/* * Definitions for strings
Copyright 1996 J. Schilling/* * Definitions for unix system interface
Copyright 1996 J. Schilling/* * Generic header for users of open(), creat() and chmod()
Copyright 1996 J. Schilling/* * Generic header for users of time(), gettimeofday() ...
Copyright 1996 J. Schilling/* * Prototypes for rmt client subroutines
Copyright 1996 RedHat Software, Incorporated
Copyright 1997, 1998, 1999, 2000 James Pearson/*
Copyright 1997,2000,2001 J. Schilling/* * Interface for other generic SCSI implementations. * Emulate the functionality of /dev/usal? with the
local * SCSI user land implementation.
Copyright 1997,2000-2003 J. Schilling/* * Interface for the AmigaOS generic SCSI implementation.
Copyright 1997,2000 J. Schilling/* * Code to support Apollo Domain/OS 10.4.1
Copyright 1997,2001-2004 J. Schilling/* * Interface to the Darwin IOKit SCSI drivers
Copyright 1997-2004 J. Schilling/*
Copyright 1997-2004 J. Schilling/* * CDR device abstraction layer
Copyright 1997-2004 J. Schilling/* * Definitions for SCSI/mmc compliant drives
Copyright 1997-2004 J. Schilling/* * Scan SCSI Bus. * Stolen from sformat. Need a more general form to * re-use it in sformat too.
Copyright 1997-2005 J. Schilling/* * CDR device implementation for * Philips/Yamaha/Ricoh/Plasmon
Copyright 1997-2005 J. Schilling/* * CDR device implementation for * Sony
Copyright 1997-2006 J. Schilling/* * CDR device implementation for * SCSI-3/mmc conforming drives * e.g. Yamaha CDR-400, Ricoh MP6200
Copyright 1997 Eric Youngdale. * APPLE_HYB James Pearson j.pearson@ge.ucl.ac.uk 22/2/2000
Copyright 1997 J. Schilling/*
Copyright 1997 J. Schilling/* * Adaption for mconfig.h from make file system.
Copyright 1997 J. Schilling/* * Definitions for some user defined types
Copyright 1997 J. Schilling/* * Interface for Linux generic SCSI implementation (sg).
Copyright 1997 J. Schilling/* * Interface for the AIX generic SCSI implementation.
Copyright 1997 J. Schilling/* * Interface for the BSD/OS user-land raw SCSI implementation.
Copyright 1997 J. Schilling/* * Interface for the HP-UX generic SCSI implementation.
Copyright 1997 J. Schilling/* * Interface for the Linux PARIDE implementation.
Copyright 1997 J. Schilling/* * Interface for the NetBSD/FreeBSD/OpenBSD generic SCSI implementation.
Copyright 1997 J. Schilling/* * Interface for the NeXT Step generic SCSI implementation.
Copyright 1997 J. Schilling/* * Interface for the SGI generic SCSI implementation.
Copyright 1997 J. Schilling/* * Interface for the VMS generic SCSI implementation.
Copyright 1997 J. Schilling/* * Interface to scan SCSI Bus.
Copyright 1997 J. Schilling/* * Misc CD support routines
Copyright 1997 J. Schilling/* * Signal abstraction for BSD/SVR4 signals
Copyright 1998,1999,2000 Heiko Eissfeldt
Copyright 1998,1999,2000 Heiko Eissfeldt, Copyright 2006 J. Schilling
Copyright 1998,1999,2000 Heiko Eissfeldt#include "config.h"
Copyright 1998,1999,2000 Heiko Eissfeldt/* resampling module * * The audio data has been read. Here are the * functions to ensure a correct
continuation * of the output stream and to convert to
Copyright 1998,1999,2004 Heiko Eissfeldt, Copyright 2004-2006 J. Schilling/* Security functions by zblaxell
Copyright 1998,1999 Heiko Eissfeldt
Copyright 1998,1999 Heiko Eissfeldt/*
Copyright 1998,1999 Heiko Eissfeldt, Copyright 2005-2006 J. Schilling#if defined(__linux__)# include <linux/cdrom.h>#else# if defined
HAVE_SYS_CDIO_H# include <sys/cdio.h># if defined(__FreeBS
Copyright 1998,1999 Heiko Eissfeldt, Copyright 2006 J. Schilling
Copyright 1998,1999 Heiko Eissfeldt#define NONBLOCKING_AUDIOint set_snd_device(const char *devicename);int init_soundcard(double rate, int bits);

```

```

Copyright 1998,1999 Heiko Eissfeldt#define SYNC_SIZE 600 /* has to be smaller than CD_FRAMESAMPLES */
Copyright 1998,1999 Heiko Eissfeldt/* header file for scsi_cmds.c */
Copyright 1998,1999 Heiko Eissfeldt#if 4 == SIZEOF_LONG_INT#define UINT4 long unsigned int#define UINT4_C ULONG_C#else#if 4 ==
SIZEOF_INT#define UINT4 unsigned int#define UINT4_C UINT_C#else#
Copyright 1998,1999 Heiko Eissfeldt#ifndef MYBYTE_ORDER#define MYBYTE_ORDER 1/* supply the byte order macros */
Copyright 1998,1999 Heiko Eissfeldt#include "config.h"#include <unistd.h>#include "sndfile.h"
Copyright 1998,1999 Heiko Eissfeldt/* os dependent functions */
Copyright 1998,1999 Heiko Eissfeldt/* Security functions */void initsecurity(void);
Copyright 1998,1999 Heiko Eissfeldt#undef DEBUG_SHM#ifdef DEBUG_SHMextern char *start_of_shm;extern char *end_of_shm;#endif
Copyright 1998,2001-2004 J. Schilling/* * Misc support functions
Copyright 1998-2001 Heiko Eissfeldt, Copyright 2005-2006 J. Schilling
Copyright 1998-2002 by Heiko Eissfeldt
Copyright 1998-2002 Heiko Eissfeldt */
Copyright 1998-2002 Heiko Eissfeldt, Copyright 2006 J. Schilling
Copyright 1998-2002 Heiko Eissfeldt/* file for all SCSI commands * FUA (Force Unit Access) bit handling copied from Monty's cdparanoia. */#undef
DEBUG_FULLTOC#undef WARN_FULLTOC#define TESTSUBQF
Copyright 1998-2002 Heiko Eissfeldt, Joerg Schilling
Copyright 1998-2002 J. Schilling/* * Q-subchannel CRC definitions
Copyright 1998-2003 Heiko Eissfeldt/*
Copyright 1998-2003 Heiko Eissfeldt/* * a central configuration file
Copyright 1998-2003 Heiko Eissfeldt#endif/* #undef HAVE_SYS_CDIO_H */ /* if we should use sys/cdio.h */
Copyright 1998-2003 J. Schilling, 2003 Alex Kopylov reanimatolog@yandex.ru/* * Interface for the MS-DOS ASPI managers. * You need ASPI
manager installed in your config.sys:
Copyright 1998-2003 J. Schilling/* * Interface for QNX (Neutrino generic SCSI implementation). * First version adopted from the OSF-1 version by *
Kevin Chiles <kchiles@qnx.com>
Copyright 1998-2003 J. Schilling/* * rename() for old systems that don't have it.
Copyright 1998-2004 Heiko Eissfeldt, Copyright 2004-2006 J. Schilling#undef DEBUG_BUFFER_ADDRESSES#undef GPROF#undef
DEBUG_FORKED#undef DEBUG_CLEANUP#undef DEBUG_DYN_OVERLAP#undef DEBUG_READS#d
Copyright 1998-2004 Heiko Eissfeldt/* Global Variables */
Copyright 1998-2004 Heiko Eissfeldt/* os dependent functions */#include "config.h"#include <stdio.h>#include <stdlib.h>#include
<strdefs.h>#include <fctldefs.h>#include <unistd.h>#include <s
Copyright 1998-2004 J. Schilling/*
Copyright 1998-2004 J. Schilling, A.L. Faber, J.A. Key/* * Interface for the Win32 ASPI library. * You need wnaspi32.dll and aspi32.sys *
Both can be installed from ASPI_ME
Copyright 1998-2004 J. Schilling/* * Disk Identification Method
Copyright 1998-2004 J. Schilling/* * Q-subchannel CRC subroutines
Copyright 1998-2005 J. Schilling/* * Simulation device driver
Copyright 1998 by the University of Washington * * Permission to use, copy, modify, and distribute this software and its * documentation for any
purpose and without fee is hereby granted, provided
Copyright 1998 D. Dorau, C. Wohlgemuth J. Schilling/* * Definitions for ASPI-Router (ASPIROUT.SYS).
Copyright 1998 J. Schilling/*
Copyright 1998 J. Schilling, C. Wohlgemuth/*
Copyright 1998 J. Schilling/* * Definitions for getcwd()
Copyright 1998 J. Schilling/* * Definitions for stat() file mode
Copyright 1998 J. Schilling/* * Generic header for users of var args ...
Copyright 1998 J. Schilling/* * Interface for Digital UNIX (OSF/1 generic SCSI implementation (/dev/cam)).
Copyright 1998 J. Schilling/* * Interface for the BeOS user-land raw SCSI implementation.
Copyright 1998 J. Schilling, Santa Cruz Operation/* * Interface for the SCO SCSI implementation.
Copyright 1998 J. Schilling, Santa Cruz Operation/* * Interface for the SCO UnixWare SCSI implementation.
Copyright 1999-2003 J. Schilling/* * Support for generic boot (sector 0..16) * and to boot Sun sparc and Sun x86 systems.
Copyright 1999-2003 J. Schilling/* * Support for Sun disk label
Copyright 1999-2004 J. Schilling/* * Generic CD-Text support definitions
Copyright 1999-2004 J. Schilling/* * Generic CD-Text support functions
Copyright 1999, 2004 J. Schilling/* * Header file bootinfo.h - genisoimage-defined boot information table * useful for an El Torito-loaded disk
image.
Copyright 1999-2004 J. Schilling/* * Header file diskmbr.h - assorted structure definitions and macros * describing standard PC partition table
Copyright 1999 J. Schilling/* * Definitions that help to handle a jmp_buf
Copyright 2000-2001 Heiko Eissfeldt
Copyright 2000-2003 J. Schilling/* * Find a byte with specific value in memory.
Copyright 2000-2004 J. Schilling/* * Subchannel processing
Copyright 2000-2005 J. Schilling/* * Definitions fur users of libunls

```

```

Copyright 2000 J. Schilling/*
Copyright 2000 J. Schilling/* *   usal Library *   dummy remote ops
Copyright 2000 J. Schilling/* *   usal Library *   set target SCSI address
Copyright 2000 J. Schilling/* *   Various compiler dependant macros.
Copyright 2001-2004 J. Schilling. */
Copyright 2001, 2004 J. Schilling/*
Copyright 2001-2004 J. Schilling/* *   Clone Subchannel processing
Copyright 2001-2004 J. Schilling/* *   Cue sheet parser
Copyright 2001-2004 J. Schilling/* *   Functions needed to use libedc_ecc from cdrecord
Copyright 2001-2004 J. Schilling/* *   UDF external definitions for genisoimage
Copyright 2001-2004 J. Schilling/* *   udf_fs.h - UDF structure definitions for genisoimage
Copyright 2001-2006 H. Peter Anvin - All Rights Reserved
Copyright 2001 H. Peter Anvin - All Rights Reserved
Copyright 2001 J. Schilling
Copyright 2001 J. Schilling. */
Copyright 2001 J. Schilling/*
Copyright 2001 J. Schilling/* *   Defines for utimes() / utime()
Copyright 2001 J. Schilling/* *   Definitions to be used for mmap()
Copyright 2001 J. Schilling/* *   udf.c - UDF support for genisoimage
Copyright 2002-2003 J. Schilling
Copyright 2002-2004 J. Schilling/*
Copyright 2002-2005 J. Schilling/* *   SCSI command functions for cdrecord *   covering MMC-3 level and above
Copyright 2002-2005 J. Schilling/* *   SCSI command functions for cdrecord *   covering MMC-4 level and above
Copyright 2002-2007 J. Schilling/* *   ISO-9660 stream (pipe) file module for genisoimage
Copyright 2002 by Heiko Eiuse strict;use integer;
Copyright 2002 by Joerg Schilling * * This file contains protected intellectual property.
Copyright 2002 Heiko Eissfeldt/* header file for system wide exit codes. */#ifndef   exitcodes_h#define   exitcodes_h
Copyright 2002 J. Schilling/* *   Definitions for users of alloca()
Copyright 2002 J. Schilling/* *   Definitions for users of dvd_file.c
Copyright 2002 J. Schilling#ifndef _BSWAP_H#define _BSWAP_H
Copyright 2002 J. Schilling/* *   Interface for Linux generic SCSI implementation (sg).
Copyright 2002 J. Schilling/* *   usal Library *   Help subsystem
Copyright 2003-2004 J. Schilling/* *   Extended I/O functions for cdrecord
Copyright 2003-2005 J. Schilling/* *   Device driver for the Masushita CW-7501
Copyright 2003 J. Schilling/* *   strdup() to be used if missing on libc
Copyright 2004 H. Peter Anvin - All Rights Reserved
Copyright 2004 J. Schilling/* *   Paranoia malloc() functions
Copyright 2004 Steve McIntyre
Copyright 2006 Eduard Bloch * * This code emulates the interface of the original defaults.c file. However, * it improves its behaviour and deals with
corner cases: prepended and * trailing spaces
Copyright 2006 Eduard Bloch * * Uses my config parser code and small wrappers to provide the old interface.
copyright) > 37)#ifdefUSE_LIBSCHILY           comerrno(EX_BAD,
Copyright (c) 1984-2002, 2004 J. Schilling */
Copyright (c) 1984-2003 J. Schilling */
Copyright (c) 1985, 1988, 1994-2003 J. Schilling * *   1.3.88   Start implementation of release 2
Copyright (c) 1985-1989, 1995-2003 J. Schilling */
Copyright (c) 1985, 1989, 1995-2003 J. Schilling */
Copyright (c) 1985, 1989, 1995-2004 J. Schilling */#include <mconfig.h>#include <stdio.h>#include <standard.h>#include <sigblk.h>#include
<unixstd.h>#include <stdlib.h>#include <strdefs.h>#i
Copyright (c) 1985, 1995, 1998 J. Schilling */
Copyright (c) 1985, 1995-2003 J. Schilling */
Copyright (c) 1985, 1995-2004 J. Schilling */
Copyright (c) 1985,1995 J. Schilling */
Copyright (c) 1985, 1996-2004 J. Schilling */
Copyright (c) 1985, 2000-2003 J. Schilling */
Copyright (c) 1985, 2000-2005 J. Schilling */
Copyright (c) 1985-2004 J. Schilling */#include <mconfig.h>#include <sigblk.h>#include <standard.h>#include <stdlib.h>#include
<strdefs.h>#include <avoffset.h>#include <utypes.h>#include <sc
Copyright (c) 1985-2006 J. Schilling */
Copyright (c) 1985 J. Schilling */
Copyright (c) 1986, 1987, 1988 by The Trustees of Columbia University in the * City of New York. * * Edit History: *

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 Copyright (c) 1986, 1995-2003 J. Schilling \*/  
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"p_block.h"#include "cdda_paranoia.h"#include "p
Copyright (c) 2002 J. Schilling * *Thanks to Alexander Kern <alex.kern@gmx.de> for the idea and first * code fragments for supporting the
CDROM_SEND_PACKET ioctl() from * the cdrom.c kernel drive
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problems)\n"); exit(0); }
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Copyright File id: "); printf("Abstract File id: ");
printf(ipd.abstract_file_id, 37); putchar('\n'); printf("Bibliographic File i
Copyright filename", ONE_DASH {"debug", no_argument, NULL, OPTION_DEBUG}, '\0', NULL, "Set debug flag", ONE_DASH}, {"eltorito-boot",
required_argument, NULL, 'b'}, 'b', "FILE", "Set El Torito bo
Copyright filename string too long\n");#else fprintf(stderr,
Copyright filename string too long\n"); exit(1);#endif } break; case OPTION_DEBUG:
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Copyright Management Information * in both the ISO and UDF filesystems. This is not implemented in ISO, * and the code to do it in UDF is currently
#ifdef out. I'm not sure * whether discs
Copyright Management Information is described in ECMA * Technical Report TR/71. * * - Most of the space before sector 256 on the disc (~480K) is

```

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wasted, * because UDF Bridge requires a pointer
copyright) memcpy_max(vol_desc.copyright_file_id, copyright,
copyright message if (global.copyright_message == NULL) { global.copyright_message = malloc(len + 1); if (global.copyright_message !=
NULL) { memcpy(global.copyright_message,
copyright protected",
copyright protected)\n", info_fp); break; case 1:
copyright protected)\n", info_fp); break; default:
copyright protected" ); toc_entry(track, (Get_Flags(track) & 0xDF) | (sub_ch->control_adr & 0x20),
Get_Tracknumber(track), Get_ISRC(track), Get_AudioStartSector(track), Get_Mi
Copyright prot system type Uchar region_mgt_info; /* Region management info */ Uchar res67[2]; /* Reserved
*/};
copyright state intro lyrics",
copyright state lyrics",
copyright state MIDI data", "1e", "1f", "intro lyrics", "pointer to lyrics text file and length",
"22", "23", "24", "25", "26", "27", "28", "29", "2a", "2
copyright state SUB_INFO",
Copyright(track); fputs("Copy_permitted=\t", info_fp); switch (cr) { case 0:
Copyright(unsigned long p_track);extern int Get_Datatrack(unsigned long p_track);extern int Get_Tracknumber(unsigned long
p_track);extern unsigned char *Get_MCN(void);extern unsigned char *Get_I
Copyright(unsigned long p_track){ if (p_track <= cdtracks) { if (g_toc[p_track].SCMS) return 1; return ((int)g_toc[p_track].bFlags
& 0x20) >> 4;
(C) Peter Widow
(C) RSA Data Security, Inc. * * last changes:
(C) Thomas Niederreiter
(C) Ti Kan and Steve Scherf */#include "config.h"#include <stdio.h>#include <standard.h>#include <stdlib.h>#include <strdefs.h>#include
<utypes.h>#include <intcvt.h>#include <unixstd.h> /*
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(C) token bucketuint64_t tp; /* Number of bytes currently available in the peak (P) token bucket */ uint64_t cbs; /* Upper limit for C token
bucket */ uint64_t pbs; /* Upper limit for
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<inttypes.h>#include <errno.h>#include <string.h>#incl  
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version, andwe recommend the users to update t
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<rte_ethdev.h>#include <rte_eventdev.h>#include <rte_bus_v
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char req1[] = "GET / HTTP/1.1\r\n\r\n"; char req2[] = "
copyright\\W "\\W+permission\\W+notice\\W+appear\\W+in\\W+supporting\\W+documentation"
"\\W+None\\W+of\\W+the\\W+above\\W+authors\\W+nor.{1,100}make\\W+any"

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 (C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test x"\$lt\_cv\_cc\_needs\_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need  
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"1.2.3.4:1r",#if defined(USE\_IPV6) "[::1]:123", "[::]:8  
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 (C) \* (y = 1. / sin(D)); y \*= D \* sin(lp\_lat); } else x = y = 0.; if (this-  
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 (C) @f //! we could simply set //! @code //! monadic\_compose(g, f)(x) = joker(transform(f(x), g)) //! @endcode //! //! and we would be

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 copyright cm\_zlib\_deflate\_copyright#define gzclearerr cm\_zlib\_gzclearerr#define gzclose cm\_zlib\_gzclose#define gzdirect cm\_zlib\_gzdirect#define  
 gzdopen cm\_zlib\_gzdopen#define gzeof cm\_zlib\_gzeof  
 copyright cm\_zlib\_inflate\_copyright#define inflate\_table cm\_zlib\_inflate\_table#define \_dist\_code cm\_zlib\_\_dist\_code#define \_length\_code  
 cm\_zlib\_\_length\_code#define \_tr\_align cm\_zlib\_\_tr\_align#def  
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Copyright;${CPACK_RESOURCE_FILE_LICENSE
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COPYRIGHT          ERROR_QUIET)
Copyright File", 0, D_CHAR);   if (r != ARCHIVE_OK)      return (r); /* Abstract File Identifier */ r = set_file_identifier(bp, 740, 776, vdc, a, vdd,
&(iso9660->abstract_file_identifier), "Ab
copyright-file") == 0)      r = get_str_opt(a,          &(iso9660->copyright_file_identifier),          COPYRIGHT_FILE_SIZE,
key, value);               iso9660->opt.copyright_file = r == ARCHIVE_OK;          return (r);          }
Copyright File Identifier    r = set_file_identifier(bp, 703, 739, vdc, a, vdd,          &(iso9660->copyright_file_identifier),
copyright-file",           KEY_STR, iso9660->copyright_file_identifier.s); if (iso9660->opt.iso_level != OPT_ISO_LEVEL_DEFAULT)
set_option_info(&info, &opt, "iso-level",          KEY_INT, iso9660->opt.iso
Copyright Filename. * This file shall be described in the Root Directory * and containing a copyright statement. */ unsigned int
copyright_file:1;#define OPT_COPYRIGHT_FILE_DEFAULT 0 /* Not s
copyright file="Setup.Configuration.h" company="Microsoft Corporation">
copyright-file=<value> * Type : string, max 37 bytes * Default: Not specified
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Copyright line of expected format endif(else() message(STATUS "SKIPPED: CMAKE_VERSION does not know the year: ${CMAKE_VERSION}")
COPYRIGHT MATCHES "ActiveState") set (${_PYTHON_PREFIX}_INTERPRETER_ID "ActivePython") endif() endif() endif() else()
set (${_PYTHON_PREFIX}_INTERPRETER_I
copyright\nAbout" ID_APP_EXIT "Quit the application; prompts to save documents\nExit"ID_FILE_MRU_FILE1 "Open this document"
ID_FILE_MRU_FILE2 "Open this document" ID_FILE_
copyright Niels Provos. Two clause BSD license.
copyright no longer supported"); return true; } else if ((strcmp(argv[i], "--version") == 0) || (strcmp(argv[i], "--version") == 0) ||
(strcmp(argv[i], "/V") ==
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"ampersand &"; }, { "bad-byte \x80", "bad-
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 Copyright File", 0, D\_CHAR); if (r != ARCHIVE\_OK) return (r); /\* Abstract File Identifier \*/ r = set\_file\_identifer(bp, 740, 776, vdc, a, vdd,  
 &(iso9660->abstract\_file\_identifer), "Ab  
 copyright-file") == 0) r = get\_str\_opt(a, &(iso9660->copyright\_file\_identifer), COPYRIGHT\_FILE\_SIZE,  
 key, value); iso9660->opt.copyright\_file = r == ARCHIVE\_OK; return (r); }  
 Copyright File Identifier r = set\_file\_identifer(bp, 703, 739, vdc, a, vdd, &(iso9660->copyright\_file\_identifer),  
 copyright-file", KEY\_STR, iso9660->copyright\_file\_identifer.s); if (iso9660->opt.iso\_level != OPT\_ISO\_LEVEL\_DEFAULT)  
 set\_option\_info(&info, &opt, "iso-level", KEY\_INT, iso9660->opt.iso  
 Copyright Filename. \* This file shall be described in the Root Directory \* and containing a copyright statement. \*/ unsigned int  
 copyright\_file:1;#define OPT\_COPYRIGHT\_FILE\_DEFAULT 0 /\* Not s  
 copyright-file=<value> \* Type : string, max 37 bytes \* Default: Not specified  
 copyright is \* claimed, and the software is hereby placed in the public domain. \* In case this attempt to disclaim copyright and place the software in the  
 \* public domain is deemed null and void, t  
 copyright no longer supported"); return true; } else if ((strcmp(argv[i], "--version") == 0) || (strcmp(argv[i], "--version") == 0) ||  
 (strcmp(argv[i], "/V") ==  
 copyright <value> \*  
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Copyright File", 0, D_CHAR); if (r != ARCHIVE_OK) return (r); /* Abstract File Identifier */ r = set_file_identifier(bp, 740, 776, vdc, a, vdd,
&(iso9660->abstract_file_identifier), "Ab
copyright-file") == 0) r = get_str_opt(a, &(iso9660->copyright_file_identifier), COPYRIGHT_FILE_SIZE,
key, value); iso9660->opt.copyright_file = r == ARCHIVE_OK; return (r); }
Copyright File Identifier r = set_file_identifier(bp, 703, 739, vdc, a, vdd, &(iso9660->copyright_file_identifier),
copyright-file", KEY_STR, iso9660->copyright_file_identifier.s); if (iso9660->opt.iso_level != OPT_ISO_LEVEL_DEFAULT)
set_option_info(&info, &opt, "iso-level", KEY_INT, iso9660->opt.iso
Copyright Filename. * This file shall be described in the Root Directory * and containing a copyright statement. */ unsigned int
copyright_file:1;#define OPT_COPYRIGHT_FILE_DEFAULT 0 /* Not s
copyright-file=<value> * Type : string, max 37 bytes * Default: Not specified
copyright no longer supported"); return true; } else if ((strcmp(argv[i], "--version") == 0) || (strcmp(argv[i], "--version") == 0) ||
(strcmp(argv[i], "/V") ==
copyright <value> *
cmake-debugsource 3.18.2 redhat:cmake-debugsource/3.18.2-9.el8/i686
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copyright no longer supported"); return true; } else if ((strcmp(argv[i], "--version") == 0) || (strcmp(argv[i], "--version") == 0) ||
(strcmp(argv[i], "/V") ==
cmd2 0.9.16 fedora:python-cmd2/0.9.16-2.fc32/noarch
copyright = '2010-2019, cmd2 contributorsauthor = 'cmd2 contributors'
Copyright (c) 2008-2019 Catherine Devlin and others
cmd2 0.9.23 pypi:cmd2/0.9.23
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colorama 0.4.1 fedora:python-colorama/0.4.1-2.fc31/noarch
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colorama import Fore, Back, Stylefrom random import
Copyright Jonathan Hartley 2013. BSD 3-Clause license, see LICENSE file.from . import win32
Copyright Jonathan Hartley 2013. BSD 3-Clause license, see LICENSE file.from .initialise import init, deinit, reinital, colorama_textfrom .ansi import Fore,
Back, Style, Cursorfrom .ansitowin32 impor
Copyright Jonathan Hartley 2013. BSD 3-Clause license, see LICENSE file.import atexitimport contextlibimport sys
Copyright Jonathan Hartley 2013. BSD 3-Clause license, see LICENSE file.import reimport sysimport os
Copyright Jonathan Hartley 2013. BSD 3-Clause license, see LICENSE file."This module generates ANSI character codes to printing colors to
terminals.See: http://en.wikipedia.org/wiki/ANSI_escape_c
colorama 0.4.1 pypi:colorama/0.4.1
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com.tagtraum:libz 4.0.0 maven:com.tagtraum:libz:4.0.0
&copy);      assert(ret == Z_OK);      err = 9;          /* don't care next time around */      have += strm.avail_in;      strm.avail_in =
step > have ? have : step;
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 t") == 0) { test = 1;  
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 compiler distribution. On that \* machine, I fou  
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copyright to help \* - Send help to stdout instead of stderr \* - Add some preemptive typecasts \* - Add L to constants in lseek() calls

```
&copy, &strm); assert(ret == Z_MEM_ERROR); mem_limit(&strm, 0); ret = inflateUndermine(&strm, 1); assert(ret ==
Z_DATA_ERROR); (void)inflateMark(&strm); ret = infl
&copy, &strm); assert(ret == Z_OK);
```

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(C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test x"\$lt\_cv\_cc\_needs\_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;  
(C)!b go :more /\.!{ N s/\n# // b more

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 for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;  
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 for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;  
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(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;; *-so
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 (C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \* \* Based on SandyBridge monitor. Implements the new package C-states \* (PC8,  
 PC9, PC10) coming with a specific Haswell (family 0  
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 git repo): subcommand builtins and param parsin  
 (C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \* \* Miscellaneous helpers which do not fit or are worth \* to put into separate  
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 (C) 2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \*/  
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 (C) is the return code for exiting with status C. - RX\_MAKE\_STOPPED (S) is the return code for stopping on signal S. \*/#define RX\_MAKE\_STEPPED()  
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(C) tests whether C is a directory separator character. IS\_PATH\_WITH\_DIR(P) tests whether P contains a directory specification. \*/#if defined  
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 (C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 2/0 - ADDF3, MPYF3 Alias: i,  
 i3\*/#define SC\_CLASS\_INSN(name, opcode,  
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 (C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 1/0 - SUBF3 Alias: i, i3\*/#define  
 S\_CLASS\_INSN(name, opcode, level) \  
 copyright string. len = obj\_som\_copyright\_hdr (abfd)->header\_id.length - 4; obj\_som\_file\_hdr (abfd)->aux\_header\_size += len;  
 current\_offset += len; if (bfd\_bwrite ((void \*) obj  
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belf CFLAGS=$SAVE_CFLAGS fi ;;*-so
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curl 7.69.1 fedora:curl/7.69.1-1.fc32/armv7hl  
(C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test yes != "\$lt\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
belf CFLAGS=\$SAVE\_CFLAGS fi ;\*-\*so  
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copyright = sort
Copyright symbol
copyright year checking on all # files would cause hundreds of errors. Instead we only look at files # which are tracked in the Git repo and
edited in the workdir, or # committ
copyright year checks against the year # the file was last committed (and thus edited to some degree). my $commityear = undef;
copyright year incorrect );
Copyright year out of date, should be "is $copyright[0]{year}", 1); }
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copyright=(();  checksrc_clear();  accept_violations();
copyright)) {      checkwarn("COPYRIGHT", 1, 0, }
copyright) &&      $copyright      checkwarn("COPYRIGHTYEAR", $copyright[0]{line}, $copyright[0]{col},          $file, $copyright[0]
{code},
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ignore was not used',  'OPENCOMMENT'      => 'f
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copyright is * claimed, and the software is hereby placed in the public domain. * In case this attempt to disclaim copyright and place the software in the
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COPYRIGHT LIBCURL_COPYRIGHT#define CURL_VERSION "7.69.1"#define CURL_VERSION_MAJOR LIBCURL_VERSION_MAJOR#define
CURL_VERSION_MINOR LIBCURL_VERSION_MINOR#define CURL_VERSION_PATCH LIBCURL_VERSION_P
COPYRIGHT LIBCURL_COPYRIGHT#define CURL_VERSION LIBCURL_VERSION#define CURL_VERSION_MAJOR LIBCURL_VERSION_MAJOR#define
CURL_VERSION_MINOR LIBCURL_VERSION_MINOR#define CURL_VERSION_PATCH LIBCURL_VE
copyright = sort
Copyright symbol
copyright year checking on all      # files would cause hundreds of errors. Instead we only look at files      # which are tracked in the Git repo and
edited in the workdir, or      # committ
copyright year checks against the year      # the file was last committed (and thus edited to some degree).      my $commityear = undef;
copyright year incorrect  );
Copyright year out of date, should be      "is $copyright[0]{year}", 1);  }
copyright years are
curl 7.69.1 opensuse:curl/7.69.1-1.4/s390x
(C) AC_LINK_IFELSE( AC_LANG_POP)) if test yes != "$lt_cv_cc_needs_belf"; then      # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;;*-*so
(C)!b go      :more      \./!{      N      s|\n# ||      b more
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need to rebuild service program yet.MODULES

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copyright=(); checksrc_clear(); accept_violations();
copyright) { checkwarn("COPYRIGHT", 1, 0, }
copyright) && $copyright checkwarn("COPYRIGHTYEAR", $copyright[0]{line}, $copyright[0]{col},
```

\$file, \$copyright[0]

{code},

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CURL_VERSION_MINOR LIBCURL_VERSION_MINOR#define CURL_VERSION_PATCH LIBCURL_VERSION_P
copyright = sort
Copyright symbol
copyright year checking on all # files would cause hundreds of errors. Instead we only look at files # which are tracked in the Git repo and
edited in the workdir, or # committ
copyright year checks against the year # the file was last committed (and thus edited to some degree). my $commityear = undef;
copyright year incorrect );
Copyright year out of date, should be "is $copyright[0]{year}", 1); }
copyright years are
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```
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define  
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (  
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)#endif  
(C) ((C) == DIRECTORY_SEPARATOR)#endif
```

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copyright information\n");

Silently build keeper file\n");

Show this message\n");

printf(\_("-q, --quiet

(C) tests whether C is a directory separator character. IS\_ABSOLUTE\_PATH(P) tests whether P is an absolute path. If it is not,

it may be concatenated to a direct

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IS\_ABSOLUTE\_PATH(P) (ISLASH ((P)[0]) || HAS\_DEVICE (P))  
(C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define  
IS\_PATH\_WITH\_DIR(P) \ (strchr (P, '/') != NULL ||  
(C) ((C) ==# define IS\_ABSOLUTE\_PATH(P) ISLASH ((P)[0])# define IS\_PATH\_WITH\_DIR(P) (strchr (P, '/') != NULL)#endif  
(C) ((C) ==# define IS\_ABSOLUTE\_PATH(P) ISLASH ((P)[0])#endif  
(C) ((C) ==# define IS\_PATH\_WITH\_DIR(P) (strchr (P, '/') != NULL)# define FILESYSTEM\_PREFIX\_LEN(P) 0#endif  
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Optional thanks list (with LFs) * 19: Bu
Copyright String auf stderr ausgeben * 1: Kurzusage auf stderr ausgeben und beenden * 2: Langusage auf stdout ausgeben und beenden * 11:
name of program * 12: optional name of packag
copyright string if( (s=strusage(14)) ) printf("%s\n", s); /* copying conditions */ if( (s=strusage(15)) ) fputs(s, stdout); /* thanks */
if( (s=strusage(18)) ) fputs(s, stdout)
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C) tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if defined
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NULL }, { 'W', ARG_DUP, "<name>,<target>,...", gettext
COPYRIGHT); printf(_("Compile time options:
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copyright': u 'dedication': u'Dedikation', 'abstract': u'Sammanfattning', 'attention': u'Observera!', 'caution': u'Akta!', # 'Varning'
already used for 'warning'
copyright': u 'dedication': u'献辞', 'abstract': u'概要', 'attention': u'注目!', 'caution': u'注意!', 'danger': u'危険!', 'error': u'エラー',
'hint': u'ヒント', 'impor
Copyright: © 2011 G
Copyright: © 2015 G
copyright': 'copyright 'toewijding': 'dedication', 'samenvatting': 'abstract'}""Dutch (lowcased) to canonical name mapping for bibliographic
fields.""

```



```
copyright': u 'dedication': 'Dedikacija', 'abstract': 'Santrauka', 'attention': u'D emesio!', 'caution': 'Atsargiai!', 'danger': '!PAVOJINGA!',
'error': 'Klaida',
  COPYRIGHT SIGN 0x00aa: ur'\textordfeminine{', #   FEMININE ORDINAL INDICATOR 0x00ac: ur'\textlnot{', #   NOT SIGN
0x00ae: ur'\textregistered{', #   REGISTERE
copyright': 'copyright 'velt jums': 'dedication', 'atrefer jums': 'abstract'}''''English (lowcased) to canonical name mapping for bibliographic fields.'''''
Copyright (c) 1991 - 1995, Stichting Mathematisch Centrum Amsterdam, The Netherlands. All rights reserved
copyright': u u'dedication': u'D u00e9dicace', u'abstract': u'R u00e9sum u00e9', u'attention': u'Attention!', u'caution': u'Avertissement!',
u'danger': u'!DANGER!',
Copyright:   2010 G# Based on rst2mathml.py from the latex_math sandbox project
Copyright (c) 2001 Mark Pilgrim
Copyright   Alex Fern
Copyright: This document/module/DTD/stylesheets/file/etc. has been placed in the public domain.
copyright': 'copyright 'dedica': 'dedication', 'riassunto': 'abstract'}''''Italian (lowcased) to canonical name mapping for bibliographic fields.'''''
copyright': u 'dedication': u' u984c u737b', # '   ', 'abstract': u' u6458 u8981', # '   ', 'attention': u' u6ce8 u610f uff01', # '   !',
'caution': u' u5c0f u5fc3 uff01', # '   !',
Copyright (C) 2009-2011 Alex Fern
copyright sign.. [BogusMegaCorp (TM)] unicode:: BogusMegaCorp U+2122 .. with trademark sign''''u''''\<document source="test data">
<paragraph> Testing comments and extra text. <p
copyright info for rst.el).
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bibliographic fields.'''''
copyright': 'copyright 'dedication': 'dedication', 'abstract': 'abstract'}''''English (lowcased) to canonical name mapping for bibliographic fields.'''''
copyright': 'dummy Copyright 'dedication': 'dummy Dedication', 'abstract': 'dummy Abstract', 'attention': 'dummy Attention!', 'caution':
'dummy Caution!', 'danger': 'dummy
Copyright (C) 2009,2010 Alex Fern
copyright': 'Copyright 'dedication': 'Velt jums', 'abstract': 'Atrefer jums', 'attention': 'Uzman bu!', 'caution': 'Piesardz bu!', 'danger':
'!B STAMI!', 'error':
copyright': u 'dedication': u'Dedikation', 'abstract': u'Resum ', 'attention': u'Giv agt!', 'caution': u'Pas p !', 'danger': u'!FARE!', 'error':
u'Fej!',
'hin
copyright') self.set_current_element(el)
copyright': u 'dedication': u'Dedykacja', 'abstract': u'Streszczenie', 'attention': u'Uwaga!', 'caution': u'Ostro u017cnie!', 'danger':
u'!Niebezpiecze u0144stwo!',
COPYRIGHT n % self._docinfo['copyright']) self.body.append(self.comment('Generated by docutils manpage writer.))
copyright': u 'dedication': u'V u011Bnov u00E1n u00ED', 'abstract': u'Abstrakt', 'attention': u'Pozor!', 'caution': u'Opatrn u011B!',
'danger': u'!NEBEZPE u010C u00CD!',
copyright' in title_words:
copyright(self, node):
copyright': u 'dedication': u'Venovanie', 'abstract': u'Abstraktne', 'attention': u'Pozor!', 'caution': u'Opatrne!', 'danger':
u'!NEBEZPE u010cENSTVO!', 'error': u'
COPYRIGHTpublic domain.\'' Generated by docutils manpage writer..
copyright': u 'dedication': u'Dedi u0109o', 'abstract': u'Resumo', 'attention': u'Atentu!', 'caution': u'Zorgu!', 'danger': u'DAN u011cERO!',
'error': u'Eraro',
copyright': 'copyright u'dedicatoria': 'dedication', u'resumen': 'abstract'}''''Spanish (lowcased) to canonical name mapping for bibliographic
fields.'''''
Copyright: public domain:Version: 0.1:Manual section: 1:Manual group: text processing:Arbitrary field: some text
copyright': nodes.copyright, 'dedication': nodes.topic, 'abstract': nodes.topic} ''''Canonical field name (lowcased) to node class name
mapping for bibliographic fields (fie
copyright = None while i >= 0 and isinstance(doc[i], nodes.section):
Copyright:   2010 G
copyright symbol (  u00a9 ), a non-breaking space (  u00a0 ), a backwards-not-equals ( =  u20e5 ), and a captial
omega (  u03a9
copyright': 'copyright 'dummy dedication': 'dedication', 'dummy abstract': 'abstract'}''''English (lowcased) to canonical name mapping for
bibliographic fields.'''''
Copyright: This file has been placed in the public domain.
copyright symbol (space (|nbsp|), a backwards-not-equals (|bne|), and a captial omega (|Omega|).
copyright: # Put the new "References" section before "Copyright": doc.insert(copyright, refsect) else:
copyright': u u'dedication': u'    ', u'abstract': u'    ', u'attention': u'   ', u'caution': u'    ', u'danger': u'   ', u'error':
u'   ', u'hint': u
Copyright: This script has been placed in the public domain.
copyright': u u'dedication': u'Omistuskirjoitus', u'abstract': u'Tiivistelm u00e4', u'attention': u'Huomio!', u'caution': u'Varo!', u'danger':
u'!VAARA!', u'error':
```

```

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'error': 'Errore'
Copyright |copy
copyright = i i -= 1 if not refsect:
copyright sign <row> <entry> <paragraph> \xae <entry>
<paragraph>
Docutils 0.14 centos:python-docutils/0.14-12.module_el8+762+77bd8591/noarch
Copyright: © 2005, 2009, 2015 G
Copyright: © 2010 G
Copyright: © 2010 G# Based on rst2mathml.py from the latex_math sandbox project
Copyright: © 2011, 2017 G
Copyright: © 2011 G
Copyright: © 2015 G
Copyright: © 2016 David Goodger, G
copyright: add a copyright notice at the bottom Trace.error(' Deprecated options:') Trace.error(' --toc: (deprecated) create a
table of contents') Trace.err
copyright(Bibliographic, TextElement): pass
Copyright (c) 2001 Mark Pilgrim
Copyright (C) 2003-2017 Free Software Foundation, Inc.
Copyright (C) 2009,2010 Alex Fern
Copyright (C) 2009-2011 Alex Fern
Copyright (C) 2010-2012 Free Software Foundation, Inc.
Copyright |copy
copyright: 'Copyright' 'abstract': 'Zusammenfassung', 'attention': 'Achtung!', 'caution': 'Vorsicht!', 'danger': '!GEFAHR!', 'error': 'Fehler', 'hint':
'Hinweis', 'importan
copyright: 'copyright' 'dedica': 'dedication', 'riassunto': 'abstract'}''''Italian (lowcased) to canonical name mapping for bibliographic fields.'''''
copyright: 'Copyright' 'dedication': 'Dedica', 'abstract': 'Riassunto', 'attention': 'Attenzione!', 'caution': 'Cautela!', 'danger': '!PERICOLO!',
'error': 'Errore'
copyright: 'Copyright' 'dedication': 'Dedication', 'abstract': 'Abstract', 'attention': 'Attention!', 'caution': 'Caution!', 'danger': '!DANGER!',
'error': 'Error',
copyright: 'copyright' 'dedication': 'dedication', 'abstract': 'abstract'}''''English (lowcased) to canonical name mapping for bibliographic fields.'''''
copyright: 'Copyright' 'dedication': 'Toewijding', 'abstract': 'Samenvatting', 'attention': 'Attentie!', 'caution': 'Let op!', 'danger':
'!GEVAAR!', 'error': 'Fout'
copyright: 'Copyright' 'dedication': 'Veltjums', 'abstract': 'Atreferējums', 'attention': 'Uzmanību!', 'caution': 'Piesardzību!', 'danger':
'!BĪSTAMI!', 'error': '
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bibliographic fields.'''''
copyright: 'copyright' 'toewijding': 'dedication', 'samenvatting': 'abstract'}''''Dutch (lowcased) to canonical name mapping for bibliographic
fields.'''''
copyright: 'copyright' 'u'dedicatoria': 'dedication', 'u'resumen': 'abstract'}''''Spanish (lowcased) to canonical name mapping for bibliographic
fields.'''''
copyright: 'copyright' 'u'dedicat\u00F2ria': 'dedication', 'u'resum': 'abstract'}''''Catalan (lowcased) to canonical name mapping for bibliographic
fields.'''''
copyright: 'copyright' 'u'dedicat\u00F3ria': 'dedication', 'u'resumo': 'abstract'}''''Brazilian Portuguese (lowcased) to canonical name mapping for
bibliographic fields.'''''
copyright: 'copyright' 'u'dedikation': 'dedication', 'u'resume': 'abstract', 'u'resumé': 'abstract'}''''Danish (lowcased) to canonical name mapping
for bibliographic fields.'''''
copyright: 'copyright' 'u'dedikation': 'dedication', 'u'sammanfattning': 'abstract'}''''Swedish (lowcased) to canonical name mapping for
bibliographic fields.'''''
copyright: 'copyright' 'u'dedykacja': 'dedication', 'u'streszczenie': 'abstract'}''''Polish (lowcased) to canonical name mapping for bibliographic
fields.'''''
copyright: 'copyright' 'u'venovanie': 'dedication', 'u'abstraktne': 'abstract'}''''Slovak (lowcased) to canonical name mapping for bibliographic
fields.'''''
copyright: 'copyright' 'u'\u011Bnov\u00E1n\u00ED': 'dedication', 'u'abstrakt': 'abstract'}''''Czech (lowcased) to canonical name mapping for
bibliographic fields.'''''
copyright: 'copyright' 'veltjums': 'dedication', 'atreferējums': 'abstract'}''''English (lowcased) to canonical name mapping for bibliographic fields.'''''
copyright: 'copyright' 'widmung': 'dedication', 'zusammenfassung': 'abstract'}''''German (lowcased) to canonical name mapping for bibliographic
fields.'''''
copyright danger date decoration definition definition_list definition_list_item description docinfo doctest_block document emphasis entry
enumerated_list error field field_body fi
copyright: 'dummy Copyright' 'dedication': 'dummy Dedication', 'abstract': 'dummy Abstract', 'attention': 'dummy Attention!', 'caution':

```

```

'dummy Caution!', 'danger': 'dumm
copyright = False debug = False quiet = False version = False hardversion = False versiondate = False html = False help = False showlines = True
unicode = False iso885915 = False
copyright = ignore_node visit_date = ignore_node visit_organization = ignore_node visit_status = ignore_node visit_version = visit_list_item
copyright = i i -= 1 if not refsect:
copyright' in title_words:
copyright': 'Kopiereg 'dedication': 'Opdrag', 'abstract': 'Opsomming', 'attention': 'Aandag!', 'caution': 'Wees versigtig!', 'danger':
'!GEVAAR!', 'error': 'Fout',
copyright': nodes.copyright, 'dedication': nodes.topic, 'abstract': nodes.topic} ""Canonical field name (lowcased) to node class name
mapping for bibliographic fields (fie
copyright = None while i >= 0 and isinstance(doc[i], nodes.section):
COPYRIGHT\n % self._docinfo['copyright']) self.body.append(self.comment('Generated by docutils manpage writer.))
Copyright \n\ \u00a9 2003, \n\ BogusMegaCorp \u2122 . <substitution_definition names="copy"> \u00a9
<substitution_definition names="BogusMegaCo
COPYRIGHTpublic domain.\" Generated by docutils manpage writer..
Copyright: public domain:Version: 0.1:Manual section: 1:Manual group: text processing:Arbitrary field: some text
copyright: # Put the new "References" section before "Copyright": doc.insert(copyright, refsect) else:
copyright(self, node):
copyright(self, node): self.depart_docinfo_item()
copyright(self, node): self.set_to_parent()
copyright(self, node): self.visit_docinfo_item(node, 'copyright', meta=False)
copyright) self.set_current_element(el)
© COPYRIGHT SIGN 0x00aa: ur'\textordfeminine{', # ^ FEMININE ORDINAL INDICATOR 0x00ac: ur'\textlnot{', # ¬ NOT SIGN
0x00ae: ur'\textregistered{', # ® REGISTERE
copyright sign.. |BogusMegaCorp (TM)| unicode:: BogusMegaCorp U+2122 .. with trademark sign""",u"""\<document source="test data">
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© copyright sign® registered sign« left pointing guillemet» right pointing guillemet– en-dash— em-dash' single turned comma quotation mark ' single
comma quotation mark , low single comma quot
copyright sign <row> <entry> <paragraph> \xae <entry>
<paragraph>
Copyright <substitution_reference refname="copy"> copy 2003, <substitution_reference refname="BogusMegaCorp (TM)">
BogusMegaCorp (TM) . <
copyright symbol (space (|nbsp|), a backwards-not-equals (|bne|), and a captial omega (|Omega|).
copyright symbol ( <substitution_reference refname="copy"> copy ), a non-breaking space ( <substitution_reference
refname="nbsp"> nbsp )
copyright symbol ( \u00a9 ), a non-breaking space ( \u00a0 ), a backwards-not-equals ( = \u20e5 ), and a captial
omega ( \u03a9
Copyright): & This document has been placed in the public domain. Youmay do with it as you wish. You may copy, modify,redistribute, reattribute,
sell, buy, rent, lease,destroy, or improve it, quot
Copyright: This file has been placed in the public domain.
Copyright: This file has been placed in the public domain.print("")print('# This is a mapping of Unicode characters to LaTeX equivalents.)print('# The
information has been extracted from')print('
Copyright: This module has been placed in the public domain.
Copyright: This module is put into the public domain.
Copyright: This script has been placed in the public domain.
copyright: This stylesheet has been placed in the public domain.
Copyright: This stylesheet has been placed in the public domain.
copyright (translation required) 'dedication (translation required)': 'dedication', 'abstract (translation required)': 'abstract'}""Traditional Chinese to
canonical name mapping for bibl
copyright': u 'dedication': 'Dedikacija', 'abstract': 'Santrauka', 'attention': u'Demesio!', 'caution': 'Atsargiai!', 'danger': '!PAVOJINGA!',
'error': 'Klaida',
copyright': u 'dedication': u'Dedicatoria', 'abstract': u'Abstract', 'attention': u'Atenci\u00f3n!', 'caution': u'Advertencia!', 'danger':
u'PERIGO!', 'error': u'Er
copyright': u 'dedication': u'Dedicatoria', 'abstract': u'Resumen', 'attention': u'\u00a1Atenci\u00f3n!', 'caution': u'\u00a1Precauci\u00f3n!',
'danger': u'\u00a1PELIGRO!
copyright': u 'dedication': u'Dedicat\u00f3ria', 'abstract': u'Resum', 'attention': u'Atenci\u00f3n!', 'caution': u'Compte!', 'danger':
u'PERILL!', 'error': u'Error'
copyright': u 'dedication': u'Dedicat\u00f3ria', 'abstract': u'Resumo', 'attention': u'Aten\u00e7\u00e3o!', 'caution': u'Cuidado!', 'danger':
u'PERIGO!', 'error': u
copyright': u 'dedication': u'Dedikation', 'abstract': u'Resum\u00e9', 'attention': u'Giv agt!', 'caution': u'Pas p\u00e5!', 'danger': u'!FARE!', 'error':
u'Fejl', 'hin

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copyright': u 'dedication': u'Dedikation', 'abstract': u'Sammanfattning', 'attention': u'Observera!', 'caution': u'Akta!', # 'Varning'  
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 'hint': u'ヒント', 'impor  
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 u'important': u'Важно', u'note':  
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 u'danger': u'!DANGER!',  
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 u'!VAARA!', u'error':  
 copyright': u 'dedication': u'تخصیص', u'abstract': u'چکیده', u'attention': u'توجه!', u'caution': u'احتیاط!', u'danger': u'خطر!', u'error':  
 u'خطا', u'hint': u'  
 copyright': u 'dedication': u'd\u000e9dicace', u'r\u000e9sum\u000e9: u'abstract'}''''French (lowcased) to canonical name mapping for bibliographic  
 fields.'''''

Docutils 0.14 unknown:undefined

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Copyright: © 2011, 2017 G

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copyright: add a copyright notice at the bottom Trace.error(' Deprecated options:') Trace.error(' --toc: (deprecated) create a

table of contents') Trace.err

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Copyright |copy

copyright': 'Copyright 'abstract': 'Zusammenfassung', 'attention': 'Achtung!', 'caution': 'Vorsicht!', 'danger': '!GEFAHR!', 'error': 'Fehler', 'hint':  
 'Hinweis', 'importan

copyright': 'copyright 'dedica': 'dedication', 'riassunto': 'abstract'}''''Italian (lowcased) to canonical name mapping for bibliographic fields.'''''

copyright': 'Copyright 'dedication': 'Dedica', 'abstract': 'Riassunto', 'attention': 'Attenzione!', 'caution': 'Cautela!', 'danger': '!PERICOLO!',  
 'error': 'Errore'

copyright': 'Copyright 'dedication': 'Dedication', 'abstract': 'Abstract', 'attention': 'Attention!', 'caution': 'Caution!', 'danger': '!DANGER!',  
 'error': 'Error',

copyright': 'copyright 'dedication': 'dedication', 'abstract': 'abstract'}''''English (lowcased) to canonical name mapping for bibliographic fields.'''''

copyright': 'Copyright 'dedication': 'Toewijding', 'abstract': 'Samenvatting', 'attention': 'Attentie!', 'caution': 'Let op!', 'danger':  
 '!GEVAAR!', 'error': 'Fout'

```

copyright: 'Copyright 'dedication': 'Veltījums', 'abstract': 'Atreferējums', 'attention': 'Uzmanību!', 'caution': 'Piesardzību!', 'danger':
'!BĪSTAMI!', 'error': '
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bibliographic fields.""
copyright: 'copyright 'toewijding': 'dedication', 'samenvatting': 'abstract'}""Dutch (lowcased) to canonical name mapping for bibliographic
fields.""
copyright: 'copyright u'dedicatoria': 'dedication', u'resumen': 'abstract'}""Spanish (lowcased) to canonical name mapping for bibliographic
fields.""
copyright: 'copyright u'dedicat\u00F2ria': 'dedication', u'resum': 'abstract'}""Catalan (lowcased) to canonical name mapping for bibliographic
fields.""
copyright: 'copyright u'dedicat\u00F3ria': 'dedication', u'resumo': 'abstract'}""Brazilian Portuguese (lowcased) to canonical name mapping for
bibliographic fields.""
copyright: 'copyright u'dedikation': 'dedication', u'resume': 'abstract', u'resumé': 'abstract'}""Danish (lowcased) to canonical name mapping
for bibliographic fields.""
copyright: 'copyright u'dedikation': 'dedication', u'sammanfattning': 'abstract'}""Swedish (lowcased) to canonical name mapping for
bibliographic fields.""
copyright: 'copyright u'dedykacja': 'dedication', u'streszczenie': 'abstract'}""Polish (lowcased) to canonical name mapping for bibliographic
fields.""
copyright: 'copyright u'venovanie': 'dedication', u'abstraktne': 'abstract'}""Slovak (lowcased) to canonical name mapping for bibliographic
fields.""
copyright: 'copyright u'v\u011Bnov\u00E1n\u00ED': 'dedication', u'abstrakt': 'abstract'}""Czech (lowcased) to canonical name mapping for
bibliographic fields.""
copyright: 'copyright 'veltījums': 'dedication', 'atreferējums': 'abstract'}""English (lowcased) to canonical name mapping for bibliographic fields.""
copyright: 'copyright 'widmung': 'dedication', 'zusammenfassung': 'abstract'}""German (lowcased) to canonical name mapping for bibliographic
fields.""
copyright danger date decoration definition definition_list definition_list_item description docinfo doctest_block document emphasis entry
enumerated_list error field field_body fi
copyright details for non-public-domain files (most are PD).
copyright: 'dummy Copyright 'dedication': 'dummy Dedication', 'abstract': 'dummy Abstract', 'attention': 'dummy Attention!', 'caution':
'dummy Caution!', 'danger': 'dumm
copyright = False debug = False quiet = False version = False hardversion = False versiondate = False html = False help = False showlines = True
unicode = False iso885915 = False
copyright = ignore_node visit_date = ignore_node visit_organization = ignore_node visit_status = ignore_node visit_version = visit_list_item
copyright = i i -= 1 if not refsect:
copyright info for rst.el).
copyright' in title_words:
copyright: 'Kopiereg 'dedication': 'Opdrag', 'abstract': 'Opsomming', 'attention': 'Aandag!', 'caution': 'Wees versigtig!', 'danger':
'!GEVAAR!', 'error': 'Fout',
copyright: nodes.copyright, 'dedication': nodes.topic, 'abstract': nodes.topic} ""Canonical field name (lowcased) to node class name
mapping for bibliographic fields (fie
copyright = None while i >= 0 and isinstance(doc[i], nodes.section):
COPYRIGHT\n % self.docinfo['copyright']) self.body.append(self.comment('Generated by docutils manpage writer.))
Copyright \n\ \u00a9 2003, \n\ BogusMegaCorp \u2122 . <substitution_definition names="copy"> \u00a9
<substitution_definition names="BogusMegaCo
COPYRIGHTpublic domain.\" Generated by docutils manpage writer..
Copyright: public domain:Version: 0.1:Manual section: 1:Manual group: text processing:Arbitrary field: some text
copyright: # Put the new "References" section before "Copyright": doc.insert(copyright, refsect) else:
copyright(self, node):
copyright(self, node): self.depart_docinfo_item()
copyright(self, node): self.set_to_parent()
copyright(self, node): self.visit_docinfo_item(node, 'copyright', meta=False)
copyright') self.set_current_element(el)
© COPYRIGHT SIGN 0x00aa: ur'\textordfeminine{', # ª FEMININE ORDINAL INDICATOR 0x00ac: ur'\textlnot{', # ¬ NOT SIGN
0x00ae: ur'\textregistered{', # ® REGISTERE
copyright sign.. [BogusMegaCorp (TM)] unicode:: BogusMegaCorp U+2122 .. with trademark sign""",u""<document source="test data">
<paragraph> Testing comments and extra text. <p
© copyright sign® registered sign« left pointing guillemet» right pointing guillemet– en-dash— em-dash` single turned comma quotation mark ´ single
comma quotation mark , low single comma quot
copyright sign <row> <entry> <paragraph> \xae <entry>
<paragraph>
Copyright <substitution_reference refname="copy"> copy 2003, <substitution_reference refname="BogusMegaCorp (TM)">

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Copyright(c) 2016 Cavium, Inc \*/  
Copyright 2016, 2019 NXP \*/  
Copyright(c) 2015-2018 Intel Corporation \*/#ifndef \_ICP\_QAT\_HW\_H\_#define \_ICP\_QAT\_HW\_H\_  
Copyright(c) 2015 - 2016 CESNET \*/  
Copyright(c) 2010-2014 Intel Corporation \*/#include <stdint.h>#include <string.h>#include <stdlib.h>#include <stdarg.h>#include  
<inttypes.h>#include <stdio.h>#include <termios.h>#include <err  
Copyright(c) 2016-2017 Intel Corporation \*/#include <stdio.h>#include <string.h>#include <stdint.h>#include <inttypes.h>#include  
<errno.h>#include <stdarg.h>#include <sys/queue.h>  
Copyright 2008-2013 Freescale Semiconductor, Inc.  
Copyright(c) 2018 Intel Corporation \*/#ifndef \_QAT\_QP\_H\_#define \_QAT\_QP\_H\_

```

Copyright(c) 2010-2014 Intel Corporation. * All rights reserved
Copyright(c) 2010-2014 Intel Corporation */#include <stdint.h>#include <stddef.h>#include <stdlib.h>#include <stdio.h>#include
<stdarg.h>#include <errno.h>#include <sys/queue.h>
(C) token bucketuint64_t te; /* Number of bytes currently available in the excess (E) token bucket */};
Copyright(c) 2020 Intel Corporation */#include <sys/types.h>#include <sys/stat.h>#include <pthread.h>#include <unistd.h>
Copyright 2010-2012 Freescale Semiconductor, Inc. * All rights reserved
Copyright 2020 Mellanox Technologies, Ltd */#include <rte_windows.h>#include <rte_errno.h>#include <rte_log.h>#include <rte_eal.h>
Copyright(c) 2016 Ethan Zhuang <zhuangwj@gmail.com>.
Copyright (C) 2014-2016 Freescale Semiconductor, Inc. *
Copyright (C) 1991, 1999 Free Software Foundation, Inc.
Copyright (c) 2017-2019 Pensando Systems, Inc. All rights reserved
Copyright (c) 2015-2016 Freescale Semiconductor, Inc. All rights reserved
Copyright (c) 2020 Dmitry Kozlyuk */
Copyright 2020 Mellanox Technologies, Ltd */
Copyright 2017,2019 NXP */
Copyright(c) 2015 Neil Horman <nhorman@tuxdriver.com>. * All rights reserved
Copyright 2019 Mellanox Technologies, Ltd */#include <netinet/in.h>
Copyright 2020 NXP */#ifndef _FSL_QBMAN_DEBUG_H#define _FSL_QBMAN_DEBUG_H
Copyright(c) 2010-2016 Intel Corporation */#include <stdint.h>#include <string.h>
Copyright (c) 2007,2008 Kip Macy kmacy@freebsd.org
Copyright(c) 2014-2017 Wind River Systems, Inc. */
Copyright(c) 2007-2014 Intel Corporation. */
Copyright(c) 2017 Brocade Communications Systems, Inc. * Author: Jan Blunck <jblunck@infradead.org> */
Copyright 2016 Neil Horman <nhorman@tuxdriver.com> * Based in part on modpost.c from the linux kernel */
Copyright (C) 2020 Marvell International Ltd. */#include <rte_acl.h>#include <rte_event_eth_tx_adapter.h>#include <rte_lpm.h>#include
<rte_lpm6.h>
Copyright(c) 2016 Intel Corporation. */
Copyright 2016-2019 NXP *
Copyright 2019 Mellanox Technologies, Ltd */#include <unistd.h>#include <stdint.h>#include <fcntl.h>#include <sys/eventfd.h>
Copyright(c) 2018 Aquantia Corporation */#ifndef ATL_LOGS_H#define ATL_LOGS_H
(C) token bucketuint64_t tp; /**< Number of bytes currently available in the peak(P) token bucket */};
Copyright(C) 2019 Marvell International Ltd. */#ifndef _OTX2_EP_VF_H#define _OTX2_EP_VF_H_
Copyright(c) 2017 Wind River Systems, Inc. */
Copyright(c) 2017 Intel Corporation */#include <rte_string_fns.h>#include <rte_reorder.h>#include <rte_cryptodev.h>#include
<rte_cryptodev_pmd.h>#include <rte_malloc.h>
Copyright(c) 2010-2015 Intel Corporation */#include <stdint.h>
Copyright (C) 2015 Freescale Semiconductor, Inc.
Copyright (c) 2015-2019 Amazon.com, Inc. or its affiliates. * All rights reserved
Copyright: 2016, Neil Horman <nhorman@tuxdriver.com>
Copyright (C) Cavium, Inc. 2015.
Copyright(c) 2015-2019 Vladimir Medvedkin <medvedkinv@gmail.com> */
Copyright 2012 Hasan Alayli <halayli@gmail.com> */
Copyright(c) 2016-2020 Intel Corporation */#include <sys/types.h>#include <netinet/in.h>#include <netinet/ip.h>
Copyright 2017-2018 NXP */
Copyright(c) 2019 Intel Corporation */
Copyright (c) 2015-2020 Amazon.com, Inc. or its affiliates. * All rights reserved
Copyright(c) 2018 Intel Corporation */#ifndef _QAT_COMMON_H#define _QAT_COMMON_H_
Copyright(c) 2018-2020 Intel Corporation */
Copyright 2019-2020 NXP *//* qbman_sys_decl.h and qbman_sys.h are the two platform-specific files in the * driver. They are only included via
qbman_private.h, which is itself a * platform-independ
Copyright 2012 Hasan Alayli <halayli@gmail.com> */#ifndef LTHREAD_H#define LTHREAD_H_
Copyright(c) 2012,2013 Intel Corporation */
Copyright 2015-2016 Intel Corporation. */
Copyright 2017 Mellanox Technologies, Ltd */
Copyright 2017 NXP.
Copyright(c) 2010-2014 Intel Corporation */#include <sys/cdefs.h>__FBSDID("$FreeBSD$");
Copyright(c) 2019 Intel Corporation * * Derived do_macswap implementation from app/test-pmd/macswap_sse.h
Copyright(c) 2017 Intel Corporation */#include <unistd.h>
Copyright(c) 2018 HUAWEI TECHNOLOGIES CO., LTD. */#include <rte_cryptodev_pmd.h>
Copyright (c) 2008-2016 Freescale Semiconductor, Inc.
Copyright (C) Mellanox 2020. */

```

Copyright (c) 2018 Microsoft Corp. \* All rights reserved  
Copyright 2020 Mellanox Technologies, Ltd \* \* This file contain the application main file \* This application provides the user the ability to test the \*  
insertion rate for specific rte\_flow rule u  
Copyright(c) 2010-2014 Intel Corporation \*/#include <sys/types.h>#include <sys/sysctl.h>#include <sys/mman.h>#include <string.h>  
Copyright(c) 2017 Cavium, Inc \*/  
Copyright 2018 6WIND S.A.  
Copyright (C) 2006 Bob Jenkins (bob\_jenkins@burtleburtle.net) \* \* http://burtleburtle.net/bob/hash/  
Copyright 2017 6WIND S.A.  
Copyright (c) 2020 Marvell Semiconductor Inc. \* All rights reserved  
Copyright 2020 Mellanox Technologies, Ltd \* \* This file contains the functions definitions to \* generate each supported action.  
Copyright(c) 2012-2014 6WIND S.A. \*/  
Copyright(c) 2019 Ericsson AB \*/  
Copyright 2008-2012 Freescale Semiconductor, Inc  
Copyright: 2007-2020 Intel Corporation.  
Copyright 2007 Nuova Systems, Inc. All rights reserved  
Copyright(c) 2010-2014 Intel Corporation \*/#include <stdlib.h>#include <stdio.h>#include <errno.h>#include <stdint.h>#include  
<rte\_hexdump.h>#include <rte\_string\_fns.h>  
Copyright(c) 2010 - 2015 Intel Corporation  
Copyright(c) 2018 Intel Corporation. \*/#include <rte\_spinlock.h>#include <rte\_service\_component.h>#include <rte\_ethdev.h>  
Copyright 2019 Mellanox Technologies, Ltd \*/  
Copyright(c) 2018 Synopsys, Inc. All rights reserved  
Copyright(c) 2019 Intel Corporation. \*/  
Copyright(c) 2019 Arm Limited \*/  
Copyright(C) 2019 Marvell International Ltd. \*/#include <string.h>#include <unistd.h>  
Copyright (C) IGEL Co.,Ltd. \* All rights reserved  
Copyright 2016 6WIND S.A.  
Copyright(c) 2018 Cavium, Inc \*/#ifndef \_OTX\_CRYPTODEV\_HW\_ACCESS\_H\_#define \_OTX\_CRYPTODEV\_HW\_ACCESS\_H\_  
Copyright(c) 2016-2017 Intel Corporation \*/#ifndef RTE\_EXEC\_ENV\_FREEBSD #define \_WITH\_GETLINE#endif#include <stdio.h>  
Copyright (c) 2019 Red Hat, Inc.  
Copyright(c) 2010-2016 Intel Corporation \*/#include <stdio.h>#include <string.h>#include <stdint.h>#include <rte\_string\_fns.h>#include  
<rte\_version.h>#include <rte\_ethdev.h>#include <rte\_ether  
Copyright(c) 2017-2018 Intel Corporation \*/#include <rte\_malloc.h>#include <rte\_hash.h>#include <rte\_jhash.h>#include <rte\_mbuf.h>#include  
<rte\_cryptodev.h>  
Copyright(c) 2010-2017 Intel Corporation \*/#include <stdlib.h>#include <stdbool.h>#include <netinet/in.h>  
Copyright(c) 2018 Netronome Systems, Inc. \* All rights reserved  
Copyright 2020 Mellanox Technologies, Ltd  
Copyright (c) 2012 NetApp Inc. \* All rights reserved  
Copyright(c) 2009-2019 Solarflare Communications Inc. \*/  
Copyright (c) 2016 - 2018 Cavium Inc. \* All rights reserved  
Copyright 2016 Intel Corporation.  
Copyright(c) 2020 Intel Corporation \*/#include <string.h>#include <stdlib.h>#include <stdio.h>#include <stdint.h>  
Copyright 2019 Mellanox Technologies, Ltd  
Copyright (c) 2014, 2015 Netronome Systems, Inc. \* All rights reserved  
Copyright 2017 NXP \*  
Copyright(c) 2015-2017 Intel Corporation \*/  
Copyright(c) 2016-2020 Intel Corporation \*/  
Copyright 2017 Intel Corporation  
Copyright(c) 2010-2014 Intel Corporation \*/#ifndef \_GUEST\_CHANNEL\_H#define \_GUEST\_CHANNEL\_H  
Copyright(c) 2010-2017 Intel Corporation \*/  
Copyright(c) 2015 Intel Corporation \*/  
Copyright(c) 2014-2019 Solarflare Communications Inc. \*  
Copyright(c) 2010-2020 Intel Corporation \*/  
Copyright(c) 2016-2017 Cavium, Inc \*/  
Copyright (C) IBM Corporation 2016. \*/  
Copyright(c) 2020 Intel Corporation \*/#ifndef \_\_INCLUDE\_RTE\_SWX\_PORT\_SOURCE\_SINK\_H\_\_#define  
\_\_INCLUDE\_RTE\_SWX\_PORT\_SOURCE\_SINK\_H\_\_  
Copyright 2016-2017 NXP \*  
Copyright 2015-2020 NXP \*  
Copyright(c) 2016 Intel Corporation. \*/#include <string.h>  
Copyright(c) 2017-2018 Broadcom \* All rights reserved  
Copyright(c) 2018 CESNET \*/

Copyright (c) 2009-2012,2016 Microsoft Corp.  
Copyright(C) 2020 Broadcom. \* All rights reserved  
Copyright: 2008-2018 Cisco Systems, Inc.  
Copyright(C) 2020 Marvell International Ltd. \*/  
Copyright 2018 Ga \*/  
Copyright(c) 2018 Intel Corporation  
Copyright (c) 2001 David E. O  
Copyright(c) 2020 Intel Corporation \*/#ifndef \_\_INCLUDE\_RTE\_SWX\_PORT\_H\_\_#define \_\_INCLUDE\_RTE\_SWX\_PORT\_H\_\_  
Copyright(c) 2010-2014 Intel Corporation \*/#include <inttypes.h>#include <stdint.h>#include <stddef.h>#include <stdio.h>#include  
<string.h>#include <unistd.h>#include <sys/queue.h>  
Copyright(c) 2010-2018 Intel Corporation.  
Copyright(c) 2016-2019 Intel Corporation \*/  
Copyright (c) 2002 Todd C. Miller <Todd.Miller@courtesan.com> \* \* Sponsored in part by the Defense Advanced Research Projects \* Agency (DARPA)  
and Air Force Research Laboratory, Air Force \* Materi  
Copyright(c) 2018 Intel Corporation \*/  
Copyright(c) 2006-2019 Solarflare Communications Inc.  
Copyright(c) 2015-2018 Intel Corporation \*/#ifndef ADF\_TRANSPORT\_ACCESS\_MACROS\_H#define ADF\_TRANSPORT\_ACCESS\_MACROS\_H  
Copyright(c) 2016 RehiveTech. All rights reserved  
Copyright(c) 2019 Intel Corporation  
Copyright (C) 2020 Marvell International Ltd. \*/  
Copyright 2016 NXP \* All rights reserved  
Copyright 2008-2012 Freescale Semiconductor Inc.  
Copyright 2016 NXP \*  
Copyright(C) 2019 IBM Corporation \*/  
Copyright (c) 2008 Marcel Moolenaar  
Copyright(c) 2017 Intel Corporation  
Copyright (c) 2017 Cavium Inc. \* All rights reserved  
Copyright(c) 2018 Solarflare Communications Inc. \*/  
Copyright (c) 1991, 1993 \* The Regents of the University of California. All rights reserved  
Copyright (c) 2019 Cavium Inc. \* \* All rights reserved  
Copyright(c) 2020 Intel Corporation \*/#ifndef \_\_INCLUDE\_RTE\_SWX\_CTL\_H\_\_#define \_\_INCLUDE\_RTE\_SWX\_CTL\_H\_\_  
Copyright(C) 2020 Marvell International Ltd. \*/#ifndef \_\_INCLUDE\_ETHDEV\_TX\_PRIV\_H\_\_#define \_\_INCLUDE\_ETHDEV\_TX\_PRIV\_H\_\_  
Copyright 2013 6WIND S.A.  
Copyright(c) 2010-2016 Intel Corporation  
Copyright (C) 2019 Marvell International Ltd. \*/#include <rte\_cryptodev.h>  
Copyright 2018 Mellanox Technologies, Ltd  
Copyright(C) 2020 Marvell International Ltd. \*/#include <rte\_trace\_point.h>  
Copyright(c) 2015-2018 Intel Corporation \*/#ifndef \_\_ICP\_QAT\_FW\_COMP\_H\_\_#define \_\_ICP\_QAT\_FW\_COMP\_H\_\_  
Copyright(c) 2019 Solarflare Communications Inc. \* \* This software was jointly developed between OKTET Labs (under contract \* for Solarflare) and  
Solarflare Communications, Inc.  
Copyright 2011-2016 Freescale Semiconductor Inc.  
Copyright 2017 Cavium \*/  
Copyright 2015-2020 Mellanox Technologies, Ltd  
Copyright(c) 2018 Semihalf. \* All rights reserved  
Copyright 2020 Mellanox Technologies, Ltd \* \* This file contains the items, actions and attributes \* definition. And the methods to prepare and fill items,  
\* actions and attributes to generate rte  
Copyright (c) 2015-2018 Atomic Rules LLC \*/  
Copyright(c) 2012-2019 Solarflare Communications Inc. \*/  
Copyright(c) 2015 Cavium, Inc  
Copyright (C) 2020 Marvell. \*/#ifndef \_\_INCLUDE\_PKT\_CLS\_PRIV\_H\_\_#define \_\_INCLUDE\_PKT\_CLS\_PRIV\_H\_\_  
Copyright 2014 6WIND S.A. \*/  
Copyright 2014-2020 Mellanox Technologies, Ltd \*/  
Copyright(c) 2017 Huawei Technologies Co., Ltd \*/#include<rte\_bus\_pci.h>  
Copyright 2017 Cavium, Inc. \*/  
Copyright(c) 2018-2019 Ericsson AB \*/  
Copyright (C) 1996, 1997, 1998 Theodore Ts \*/  
Copyright 2013-2015 Freescale Semiconductor Inc.  
Copyright (c) 2017-2018 Solarflare Communications Inc. \* All rights reserved  
Copyright(c) 2020 Hisilicon Limited. \*/  
Copyright 2013-2014 6WIND S.A. \*/  
Copyright(c) 2016 6WIND S.A. \*/

```

Copyright(c) 2018 Intel Corporation */#include <stdint.h>#include <stdlib.h>#include <string.h>
Copyright (c) 2000 The NetBSD Foundation, Inc. * All rights reserved
Copyright (c) 1989, 1993, 1994 * The Regents of the University of California. All rights reserved
Copyright (c) 2012 NetApp Inc.
Copyright(c) 2010-2014 Intel Corporation */#include <string.h>#include <stdio.h>#include <unistd.h>#include <inttypes.h>#include
<sys/types.h>#include <sys/sysctl.h>#include <errno.h>
Copyright(c) 2013 6WIND S.A. */
Copyright 2019 Mellanox Technologies, Ltd */#include <stdlib.h>
Copyright(C) 2019 Marvell International Ltd.
Copyright(c) 2018 Cavium Networks */
Copyright 2017 Mellanox Technologies, Ltd
Copyright (c) 2020 Marvell. * All rights reserved
Copyright (c) 1998 Doug Rabson
Copyright(c) 2010-2014 Intel Corporation. */
Copyright(c) 2007-2019 Solarflare Communications Inc. * * Acknowledgement to Fen Systems Ltd.
Copyright(c) 2015 Cavium, Inc */
Copyright 2018-2019 Cisco Systems, Inc. All rights reserved
Copyright(c) 2019 Advanced Micro Devices, Inc. All rights reserved
Copyright(c) 2013-2019 Solarflare Communications Inc. */
Copyright (c) 2020 Arm Limited */
Copyright(c) 2017 6WIND S.A. */
Copyright 2013-2016 Freescale Semiconductor Inc.
Copyright (C) IBM Corporation 2014. */
Copyright(c) 2016 Intel Corporation
Copyright(c) 2015-2018 Intel Corporation */#ifndef _ICP_QAT_FW_H_#define _ICP_QAT_FW_H_#include <sys/types.h>#include "icp_qat_hw.h"
Copyright (c) 2014-2016 Freescale Semiconductor, Inc. All rights reserved
Copyright 2010-2011 Dmitry Vyukov */
Copyright 2012 Mellanox Technologies, Ltd */
Copyright 2006 Bob Jenkins * * Derived from public domain source, see * <http://burtleburtle.net/bob/c/lookup3.c>: *
Copyright 2018 NXP *
Copyright(c) 2016 Intel Corporation */#include <string.h>#include <stdint.h>#include <unistd.h>
Copyright(c) 2017 Cavium, Inc
Copyright 2019 Mellanox Technologies, Ltd */#include <unistd.h>#include <net/if.h>#include <sys/socket.h>#include <sys/ioctl.h>#include
<fcntl.h>#include <netinet/in.h>
Copyright(c) 2015-2017 Intel Corporation */#ifndef TEST_CRYPTODEV_H_#define TEST_CRYPTODEV_H_
Copyright(c) 2014-2019 Chelsio Communications. * All rights reserved
Copyright(c) 2017 Cavium, Inc. All rights reserved
Copyright (c) 2009-2018 Microsoft Corp.
Copyright(c) 2010-2014 Intel Corporation */#include <string.h>#include <stdint.h>
Copyright(c) 2014 John W. Linville <linville@tuxdriver.com> * Originally based upon librt_pmd_pcap code:
Copyright(C) 2019 Marvell International Ltd. */
Copyright(c) 2015-2019 Intel Corporation */#ifndef _QAT_SYM_SESSION_H_#define _QAT_SYM_SESSION_H_
Copyright(c) 2015-2020 Intel Corporation
Copyright (C) 2019 Marvell International Ltd. */
Copyright(c) 2014 6WIND S.A. */
Copyright 2017-2018 NXP *
Copyright(c) 2014-2019 Broadcom * All rights reserved
Copyright(c) 2010-2014 Intel Corporation */#include <sys/mman.h>#include <unistd.h>#include <sys/types.h>#include <sys/sysctl.h>#include
<inttypes.h>#include <errno.h>#include <string.h>#incl
Copyright(c) 2020 Intel Corporation */#ifndef __INCLUDE_RTE_SWX_TABLE_EM_H__#define __INCLUDE_RTE_SWX_TABLE_EM_H__
Copyright(c) 2015-2016 Intel Corporation */
Copyright(c) 2020 Red Hat Inc. */
Copyright(c) 2017 IBM Corporation. * All rights reserved
Copyright(c) 2010-2016 Intel Corporation */
Copyright 2017-2020 NXP */
Copyright(c) 2019-2020 Broadcom * All rights reserved
Copyright(c) 2018 HUAWEI TECHNOLOGIES CO., LTD. */
Copyright(c) 2010-2014 Intel Corporation */#include <stdio.h>#include <stdint.h>#include <signal.h>#include <errno.h>#include <string.h>#include
<sys/queue.h>#include <sys/time.h>#include <sy
Copyright(c) 2012-2013 6WIND S.A. */
Copyright(c) 2016 Intel Corporation */#ifndef __RTE_IPSEC_XFORM_ESP_H__#define __RTE_IPSEC_XFORM_ESP_H__

```

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Copyright 2017 NXP */#include <rte_common.h>#include <rte_mbuf.h>#include <rte_malloc.h>#include <rte_memcpy.h>#include
<rte_dev.h>#include <rte_rawdev.h>#include <rte_bus_vdev.h>
Copyright(c) Hannes Frederic Sowa * All rights reserved
Copyright (c) 2020 Microsoft Corporation""Script to query and setup huge pages for DPDK applications.""
Copyright(c) 2014-2018 Chelsio Communications. * All rights reserved
Copyright(c) 2017 IBM Corporation. */
Copyright 2018 NXP */
Copyright(c) 2016 Intel Corporation */#include <arpa/inet.h>#include <sys/socket.h>
Copyright (c) 2017,2018 HXT-semitech Corporation.
Copyright(c) 2010-2014 Intel Corporation.
Copyright(c) 2017 Intel Corporation. * All rights reserved
Copyright (c) 2018, Microsoft Corporation. * All Rights Reserved
Copyright (c) 2020 Atomic Rules LLC */
Copyright(c) 2018-2019 Pensando Systems, Inc. All rights reserved
Copyright (c) 2007-2009 Kip Macy kmacy@freebsd.org
Copyright(c) 2010-2014 Intel Corporation */#ifndef _RTE_LPM6_H_#define _RTE_LPM6_H_
Copyright 2018 Arnon Warshavsky <arnon@qwilt.com>
Copyright (c) 1998 Doug Rabson * All rights reserved
Copyright (c) 2019-2020 Arm Limited */
Copyright(c) 2020 Intel Corporation */#ifndef __INCLUDE_RTE_SWX_PORT_ETHDEV_H__#define __INCLUDE_RTE_SWX_PORT_ETHDEV_H__
Copyright 2008-2015 Cisco Systems, Inc"
Copyright 2009-2012 Freescale Semiconductor Inc.
Copyright (c) 2018-2020 Arm Limited */
Copyright 2019 Mellanox Technologies, Ltd */#include <rte_malloc.h>#include <rte_errno.h>
Copyright(c) 2017 Intel Corporation */#include <string.h>#include <rte_common.h>#include <rte_mempool.h>#include <rte_mbuf.h>#include
<rte_ethdev.h>#include <rte_eventdev.h>#include <rte_bus_v
Copyright (c) 2020 Red Hat, Inc. */
Copyright(c) 2013 - 2015 Intel Corporation */
Copyright(c) 2015-2018 Intel Corporation
Copyright(c) 2015-2019 Solarflare Communications Inc. */
Copyright(C) 2020 Marvell International Ltd. */#ifndef __INCLUDE_IP4_REWRITE_PRIV_H__#define __INCLUDE_IP4_REWRITE_PRIV_H__
Copyright(c) 2018 Advanced Micro Devices, Inc. All rights reserved
Copyright(c) 2018 Intel Corporation */#include <string.h>
Copyright (c) 2012 Citrix Inc. * All rights reserved
Copyright(c) 2013 6WIND S.A. * All rights reserved
Copyright(c) 2015-2020 */
Copyright(c) 2020 Arm Limited */
Copyright(c) 2019-2020 Xilinx, Inc.
Copyright 2010-2016 Freescale Semiconductor Inc.
Copyright(c) 2019 Xilinx, Inc. All rights reserved
Copyright 2020 Mellanox Technologies, Ltd */#include <rte_eal_memconfig.h>#include <rte_errno.h>#include <rte_mempool.h>#include
<rte_malloc.h>#include <rte_rwlock.h>
Copyright 2016,2019-2020 NXP *
Copyright 2017,2019-2020 NXP
Copyright(c) 2016-2019 Solarflare Communications Inc. * * This software was jointly developed between OKTET Labs (under contract * for Solarflare)
and Solarflare Communications, Inc.
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Copyright (c) 2018 Arm Limited */
Copyright 2011 Freescale Semiconductor, Inc. * All rights reserved
Copyright 2016,2018-2019 NXP *
Copyright (c) 2019 Intel Corporation * All rights reserved
Copyright(c) 2017 Intel Corporation */#include <rte_common.h>#include <rte_hexdump.h>#include <rte_cryptodev.h>#include
<rte_cryptodev_pmd.h>#include <rte_bus_vdev.h>#include <rte_malloc.h>#in
Copyright 2020 Mellanox Technologies, Ltd * * This file contain the implementations of the items * related methods. Each Item have a method to
prepare * the item and add it into items array in giv
Copyright(c) 2018 Arm Limited */
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```



```

Copyright(c) 2017 Intel Corporation */
Copyright (c) 2016 - 2020 Cavium Inc. * All rights reserved
Copyright(c) 2010-2014 Intel Corporation */#include <string.h>
Copyright(c) 2010-2015 Intel Corporation. */
Copyright(C) 2020 Marvell International Ltd. */#include <inttypes.h>#include <signal.h>#include <stdio.h>#include <unistd.h>
Copyright 2016 Freescale Semiconductor, Inc. All rights reserved
Copyright(c) 2017 Semihalf. * All rights reserved
Copyright (C) IBM Corporation 2019. */
Copyright 2011 Freescale Semiconductor, Inc. *
Copyright(c) 2017-2018 Intel Corporation. */
Copyright(c) 2014-2020 Broadcom * All rights reserved
Copyright(c) 2020 Intel Corporation */#ifndef __INCLUDE_RTE_SWX_PIPELINE_H__#define __INCLUDE_RTE_SWX_PIPELINE_H__
Copyright(c) 2020 Dmitry Kozlyuk */
Copyright(c) 2017 Cavium, Inc. */
Copyright(c) 2018 Intel Corporation */#include <isa-l.h>
Copyright (C) 2014 Freescale Semiconductor, Inc.
Copyright(c) 2019-2020 Intel Corporation */
Copyright(c) 2018-2019 Solarflare Communications Inc. */
Copyright(c) 2008-2019 Solarflare Communications Inc. */
Copyright(C) 2020 Marvell International Ltd. */#ifndef __INCLUDE_ETHDEV_RX_PRIV_H__#define __INCLUDE_ETHDEV_RX_PRIV_H__
Copyright(c) 2017-2018 Intel Corporation. * All rights reserved
Copyright(c) 2010-2014 Intel Corporation */#include <errno.h>#include <string.h>
Copyright(c) 2017-2018 Intel Corporation */
Copyright (c) 2019 Intel Corporation */
Copyright(c) 2010-2014 Intel Corporation */#include <string.h>#include <stdint.h>#include <errno.h>#include <stdarg.h>#include <stdio.h>#include
<sys/queue.h>
Copyright(c) 2019 Solarflare Communications Inc. */
Copyright(c) 2017 Cavium, Inc
Copyright(c) 2017 Intel Corporation
Copyright(c) 2016 6WIND S.A.
Copyright 2008-2017 Cisco Systems, Inc. All rights reserved
Copyright 2008-2016 Freescale Semiconductor Inc.
Copyright 2017 NXP */
Copyright (c) 2010-2012 Citrix Inc.
Copyright 2016-2020 NXP *
Copyright(c) 2020 Intel Corporation */#ifndef __INCLUDE_RTE_SWX_TABLE_H__#define __INCLUDE_RTE_SWX_TABLE_H__
(C) token bucket uint64_t te; /**< Number of bytes currently available in the excess(E) token bucket */);
Copyright (c) 2010, Keith Wiles <keith.wiles@windriver.com> * All rights reserved
Copyright 2015 6WIND S.A.
Copyright (c) 2020 Samsung Electronics Co., Ltd All Rights Reserved
Copyright 2008-2012 Freescale Semiconductor, Inc. *
Copyright: 2015-2020 Canonical Ltd.
Copyright(c) 2020 Intel Corporation */#include <stdint.h>#include <stdlib.h>#include <stdio.h>#include <string.h>#include <errno.h>
Copyright(c) 2015 Intel Corporation */#ifndef LTHREAD_DIAG_API_H__#define LTHREAD_DIAG_API_H__
Copyright: 2010-2020, Intel Corporation.
Copyright(c) 2016-2018 Microsoft Corporation
Copyright(c) 2018 HUAWEI TECHNOLOGIES CO., LTD. */#include <stdbool.h>#include <unistd.h>
Copyright 2019 NXP */#include <fsl_mc_sys.h>#ifndef _FSL_DPRTC_CMD_H#define _FSL_DPRTC_CMD_H
Copyright(c) 2019 Arm Limited *
Copyright 2009-2016 Freescale Semiconductor Inc.
Copyright 2020 Microsoft Corporation
Copyright(c) 2010-2013 Intel Corporation.
Copyright(c) 2017-2019 Solarflare Communications Inc. */
Copyright (c) 2010 Fabien Romano <fabien@openbsd.org>
(C) The END item is met. */static intflow_item_skip_disabled_protos(const struct rte_flow_item **item, uint64_t proto_mask, size_t *length, struct
rte_flow_error *error)
Copyright 2019 6WIND S.A.
Copyright 2020 Mellanox Technologies, Ltd * * The file contains the implementations of the method to * fill items, actions & attributes in their
corresponding * arrays, and then generate rte_flow
Copyright(c) 2010-2016 Intel Corporation */#ifndef _TEST_LPM_ROUTES_H__#define _TEST_LPM_ROUTES_H__
Copyright(c) 2019-2020 Intel Corporation */#ifndef _RTE_IOAT_RAWDEV_FNS_H__#define _RTE_IOAT_RAWDEV_FNS_H__

```

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Copyright(c) 2018 Aquantia Corporation \*/  
Copyright(c) 2019 Netcope Technologies, a.s. <info@netcope.com> \* All rights reserved  
Copyright(c) 2016 Cavium, Inc.  
Copyright(c) 2010-2018 Intel Corporation \*/#include <stdlib.h>#include <string.h>  
Copyright 2015 6WIND S.A.  
Copyright(c) 2010-2015 Intel Corporation  
Copyright(c) 2010-2015 Intel Corporation \*/  
Copyright 2012 6WIND S.A.  
Copyright (C) 2014-2016 Freescale Semiconductor, Inc.  
Copyright 2016 NXP \*/  
Copyright (c) 2019 Arm Limited  
Copyright 2020 Intel Corporation \*/  
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Copyright(c) 2020 Intel Corporation. \*/  
Copyright(c) 2017 Huawei Technologies Co., Ltd \*/  
Copyright(c) 2019-2020 Broadcom All rights reserved  
Copyright 2020 Mellanox Technologies, Ltd \*/#include <fcntl.h>#include <stdint.h>  
Copyright 2019 NXP \*/#include <fsl\_mc\_sys.h>#include <fsl\_mc\_cmd.h>#include <fsl\_dprtc.h>#include <fsl\_dprtc\_cmd.h>  
Copyright(c) 2016-2018 Intel Corporation.  
copyright = Falsehighlight\_language = 'none'  
Copyright (c) 2009, Olivier MATZ <zer0@droids-corp.org> \* All rights reserved  
Copyright (C) 1996, 1997 Theodore Ts \*/  
Copyright 2020 Mellanox Technologies, Ltd  
Copyright(c) 2020 Red Hat, Inc. \*/  
Copyright(c) 2016-2017 Intel Corporation  
Copyright(c) 2010-2014 Intel Corporation \*/#include <stdint.h>  
Copyright(c) 2001 - 2015 Intel Corporation \*/\*\$FreeBSD\$\*/  
Copyright 2019-2020 NXP \*/  
Copyright 2020 Mellanox Technologies Ltd \*/  
Copyright 2020 Mellanox Technologies, Ltd \* \* The file contains the implementations of actions generators. \* Each generator is responsible for preparing  
it's action instance \* and initializing it  
Copyright (C) 2015 Freescale Semiconductor, Inc. \*/  
Copyright(c) 2018 Red Hat Corp. \*/  
Copyright 2017-2019 NXP \*/  
Copyright(c) 2019 Intel Corporation. \*/#include <stdint.h>#include <stdio.h>#include <string.h>#include <errno.h>  
Copyright 2019 Mellanox Technologies, Ltd \*/#include <string.h>#include <unistd.h>#include <sys/mman.h>  
Copyright (C) 1989, 1991 Free Software Foundation, Inc.,  
Copyright 2017,2020 NXP \*/  
Copyright(c) 2016-2018 Intel Corporation \*/#include <unistd.h>#include <pthread.h>#include <stdbool.h>#include <sys/epoll.h>  
Copyright(c) 2016 Neil Horman <nhorman@tuxdriver.com>  
Copyright(c) 2018 David Marchand <david.marchand@redhat.com>  
Copyright (C) 2006-2012 Toni Ronkko \* <https://github.com/tronkko/dirent> \*/  
Copyright(c) 2010-2015 Intel Corporation.  
Copyright(c) 2018 Marvell International Ltd.  
Copyright 2012 Hasan Alayli <halayli@gmail.com> \*/#ifndef LTHREAD\_INT\_H#define LTHREAD\_INT\_H  
Copyright 2018 Mellanox Technologies, Ltd \*/  
Copyright(c) 2015 Intel Corporation \*/#ifndef \_IGB\_REGS\_H\_#define \_IGB\_REGS\_H\_  
Copyright(c) 2017-2018 Linaro Limited \*/  
Copyright(c) 2018-2019 Hisilicon Limited. \*/  
Copyright (c) 2016 Brocade Communications Systems, Inc.  
Copyright 2010-2016 Freescale Semiconductor, Inc.  
Copyright 2017-2020 NXP \*/  
Copyright 2019 6WIND S.A. \*/  
Copyright(c) Cavium, Inc. 2017. \* All rights reserved  
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Copyright (C) 2014-2017 aQuantia Corporation.  
Copyright(c) 2019 Cesnet  
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```
Copyright (C) 2020 Marvell. */
Copyright(c) 2018 Cavium, Inc */#include <assert.h>#include <string.h>#include <unistd.h>
Copyright(c) 2015-2020 Intel Corporation */
Copyright(c) 2007-2019 Solarflare Communications Inc. */
Copyright(c) 2014-2018 Broadcom * All rights reserved
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Copyright(c) 2015-2019 Intel Corporation */
Copyright (c) 2015-2018 Cavium Inc. * All rights reserved
Copyright (c) 2020 Arm Limited
Copyright(c) 2020 Intel Corporation */#include <stdlib.h>#include <string.h>#include <stdio.h>#include <errno.h>
Copyright(c) 2016 IGEL Co., Ltd.
Copyright(c) 2015-2020 Intel Corporation. */
Copyright(c) 2018 Cavium Networks
Copyright(c) 2017-2020 Intel Corporation. */
Copyright(c) 2020 Intel Corporation */
Copyright(c) 2020 Xilinx, Inc. */
Copyright(c) 2018 Aquantia Corporation */#ifndef ATL_TYPES_H#define ATL_TYPES_H
Copyright (C) 2007 VMware, Inc. All rights reserved
Copyright(C) 2020 Marvell International Ltd. */#include <assert.h>#include <inttypes.h>#include <signal.h>#include <stdio.h>#include
<string.h>#include <unistd.h>
Copyright 2019 NXP */
Copyright (C) 2015-2016 Freescale Semiconductor, Inc.
Copyright(c) 2017-2019 Intel Corporation */
Copyright 2020 Mellanox Technologies, Ltd * * This file contains the items related methods
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Copyright(c) 2015 Intel Corporation
Copyright(c) 2001-2020 Intel Corporation */
Copyright 2020 NXP *
Copyright 2018 Luca Boccassi <bluca@debian.org>
Copyright(c) 2018 Marvell International Ltd. */
Copyright(c) 2020 Intel Corporation */#include <stdlib.h>#include <string.h>#include <stdio.h>#include <sys/queue.h>#include <unistd.h>
Copyright(c) 2018 Vladimir Medvedkin <medvedkinv@gmail.com>
Copyright(c) 2015 Cavium, Inc.
Copyright (c) 2007-2013 Broadcom Corporation. * * Eric Davis <edavis@broadcom.com> * David Christensen <davidch@broadcom.com> * Gary
Zambrano <zambrano@broadcom.com>
Copyright(c) 2020 Mellanox Technologies, Ltd */#include <string.h>
Copyright (c) 2016 Freescale Semiconductor, Inc. All rights reserved
Copyright(c) 2006-2019 Solarflare Communications Inc. */
Copyright(c) 2016-2018, Linaro Limited. */
Copyright(c) 2016-2019 Solarflare Communications Inc. */
Copyright(c) 2020 Intel Corporation */#ifndef __INCLUDE_RTE_SWX_EXTERN_H__#define __INCLUDE_RTE_SWX_EXTERN_H__
Copyright 2016,2019 NXP *
copyright-format/1.0/Upstream-Name: dpdkSource: https://dpdk.org
Copyright (c) 2013-2015 Brocade Communications Systems, Inc.
Copyright (C) 2019 Marvell International Ltd. */#include <rte_cryptodev.h>#include <rte_ethdev.h>
Copyright(c) 2017 Intel Corporation. */
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Copyright(c) 2013-2016 Intel Corporation */
Copyright(c) 2018 Chelsio Communications. * All rights reserved
Copyright(c) 2015-2019 Intel Corporation */#ifndef _ICP_QAT_FW_LA_H_#define _ICP_QAT_FW_LA_H_#include "icp_qat_fw.h"
Copyright(c) 2014 6WIND S.A. * All rights reserved
Copyright (c) 2018 Microsoft Corp.
Copyright 2018 Mellanox Technologies, Ltd */#include <math.h>
Copyright(c) 2019 Intel Corporation. */#include <stdint.h>#include <stdio.h>#include <errno.h>
```

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 Copyright (c) 2010-2017 Intel Corporation  
 Copyright(c) 2018 Cavium, Inc \*/  
 Copyright 2016,2020 NXP \*  
 Copyright (C) 2020 Marvell International Ltd. \*/#ifndef \_IPSEC\_WORKER\_H\_#define \_IPSEC\_WORKER\_H\_  
 Copyright (c) 2010 Michael Knudsen <mk@openbsd.org> \* All rights reserved  
 Copyright (c) 2014-2018 Netronome Systems, Inc. \* All rights reserved  
 Copyright (c) 2010-2015 Intel Corporation  
 Copyright 2018-2019 NXP \*  
 Copyright(c) 2017-2018 Linaro Limited. \*/  
 DPDK 20.11.1 debian:dpdk/20.11.1-1  
 Copyright: 2007-2020, Intel Corporation  
 Copyright(c) 2019 Broadcom \* All rights reserved  
 Copyright (c) 1987, 1991, 1993 \* The Regents of the University of California. All rights reserved  
 Copyright (c) 2010 Jonathan Armani <armani@openbsd.org>  
 Copyright 2017-2019 NXP \*/  
 Copyright(c) 2017 Cavium.  
 Copyright(c) 2020 Chelsio Communications. \* All rights reserved  
 Copyright(c) 2020 Intel Corporation \*/#include <stdint.h>#include <stdlib.h>#include <string.h>#ifdef RTE\_PORT\_PCAP#include  
 <pcap.h>#endif#include <sys/time.h>  
 Copyright 2020 Mellanox Technologies, Ltd \*/#include <mlx5\_prm.h>#include <rte\_malloc.h>#include <rte\_cycles.h>  
 Copyright 2015-2019 Mellanox Technologies, Ltd \*/  
 Copyright(c) 2018 Intel Corporation \*/#include <stdio.h>#include <unistd.h>#include <stdint.h>#include <limits.h>  
 Copyright(c) 2015 Akamai Technologies. \* All rights reserved  
 Copyright 2008-2012 Freescale Semiconductor, Inc.  
 Copyright(c) 2010-2014 Intel Corporation \*/  
 Copyright(c) 2017 Intel Corporation \*/#include <string.h>  
 Copyright(c) 2013-2016 Brocade Communications Systems, Inc. \* All rights reserved  
 Copyright(c) 2010-2014 Intel Corporation  
 Copyright(c) 2019-2020 Intel Corporation. \*/#include <unistd.h>#include <errno.h>#include <stdlib.h>#include <string.h>#include <poll.h>#include  
 <netinet/in.h>#include <net/if.h>#include <sys  
 Copyright(c) 2016-2018 Intel Corporation \*/#ifndef \_RTE\_ETH\_VHOST\_H\_#define \_RTE\_ETH\_VHOST\_H\_  
 Copyright(c) 2018 - 2019 Intel Corporation \*/#include <string.h>#include <zlib.h>#include <math.h>#include <stdlib.h>#include <unistd.h>#include  
 <stdio.h>  
 Copyright 2016,2019 NXP \*/  
 Copyright (c) 2017 Red Hat, Inc. \*/  
 Copyright(c) 1982, 1986, 1990, 1993 \* The Regents of the University of California.  
 Copyright(c) 2010-2014 Intel Corporation  
 Copyright(c) 2018-2019 Intel Corporation \*/  
 Copyright (c) 2001 Benno Rice  
 Copyright(c) 2010-2015 Intel Corporation  
 Copyright 2019 NXP \*/#ifndef \_\_FSL\_DPRTC\_H#define \_\_FSL\_DPRTC\_H  
 Copyright(c) 2010-2015 Intel Corporation \*/#include "test.h"#include <string.h>  
 Copyright(c) 2017-2020 Intel Corporation \*/  
 Copyright(c) 2015 Intel Corporation \*/#ifndef LTHREAD\_OBJCACHE\_H\_#define LTHREAD\_OBJCACHE\_H\_  
 Copyright (c) 2014-2018 Cavium Inc. \* All rights reserved  
 Copyright(c) 2010-2016 Intel Corporation.  
 Copyright(c) 2017 Marvell International Ltd.  
 Copyright(c) 2018 Neil Horman <nhorman@tuxdriver.com>  
 Copyright 2010-2011 Freescale Semiconductor, Inc. \* All rights reserved  
 Copyright(c) 2010-2021 Intel Corporation \*/#ifndef RTE\_POWER\_GUEST\_CHANNEL\_H#define RTE\_POWER\_GUEST\_CHANNEL\_H  
 Copyright(c) 2015 Intel Corporation \*/#ifndef \_IXGBE\_REGS\_H\_#define \_IXGBE\_REGS\_H\_  
 Copyright(c) Intel Corporation \*/  
 Copyright(c) 2016-2017 Intel Corporation \*/  
 Copyright(c) 2018 Ga \*/  
 Copyright(c) 2016 Intel Corporation \*/  
 Copyright(c) 2018 Intel Corporation \*/#ifndef \_QAT\_DEVICE\_H\_#define \_QAT\_DEVICE\_H\_  
 Copyright 2014-2016 6WIND S.A. \*/  
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Copyright 2018-2019 NXP */
Copyright 2019-2020 NXP *
Copyright(c) 2015 Intel Corporation.
Copyright(c) 2020 Intel Corporation */#include <stdlib.h>#include <string.h>#include <stdio.h>#include <errno.h>#include <inttypes.h>#include
<sys/queue.h>#include <arpa/inet.h>
Copyright 2008-2018 Cisco Systems, Inc. All rights reserved
Copyright 2018 Red Hat, Inc. */
Copyright (C) 2020 Marvell International Ltd. */#include <rte_bitmap.h>#include <rte_ethdev.h>#include <rte_eventdev.h>#include
<rte_event_eth_rx_adapter.h>#include <rte_event_eth_tx_adapter.h>
Copyright 2015 Intel Corporation.
Copyright(c) 2013-2017 Wind River Systems, Inc. */
Copyright(c) 2018 Intel Corporation. * All rights reserved
Copyright(c) 2010-2017 Intel Corporation */#include <string.h>#include <stdio.h>
Copyright(c) 2016 Intel Corporation.
Copyright (C) 2020 Marvell International Ltd. */#ifndef _EVENT_HELPER_H_#define _EVENT_HELPER_H_
Copyright(c) 2013-2015 Intel Corporation */
Copyright (C) 2020 Marvell International Ltd. */#ifndef _IPSEC_SECGW_H_#define _IPSEC_SECGW_H_
Copyright(c) 2018-2019 Arm Limited */
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Copyright (C) 2012, Hasan Alayli <halayli@gmail.com> * Portions derived from: https://github.com/halayli/lthread * With permissions from Hasan Alayli
to use them as BSD-3-Clause */
Copyright(c) 2020 Broadcom * All rights reserved
Copyright(c) 2020 Intel Corporation
Copyright(c) 2019 Intel Corporation */#include <stdint.h>#include <stdio.h>#include <inttypes.h>#include <unistd.h>#include <signal.h>#include
<string.h>#include <getopt.h>
Copyright 2015 Mellanox Technologies, Ltd */
Copyright(c) 2016 Cavium, Inc */
Copyright 2016, 2019 NXP *
Copyright(c) 2015-2018 Intel Corporation */#ifndef _ICP_QAT_HW_H_#define _ICP_QAT_HW_H_
Copyright(c) 2015 - 2016 CESNET */
Copyright(c) 2010-2014 Intel Corporation */#include <stdint.h>#include <string.h>#include <stdlib.h>#include <stdarg.h>#include
<inttypes.h>#include <stdio.h>#include <termios.h>#include <err
Copyright(c) 2016-2017 Intel Corporation */#include <stdio.h>#include <string.h>#include <stdint.h>#include <inttypes.h>#include
<errno.h>#include <stdarg.h>#include <sys/queue.h>
Copyright 2008-2013 Freescale Semiconductor, Inc.
Copyright(c) 2018 Intel Corporation */#ifndef _QAT_QP_H_#define _QAT_QP_H_
Copyright(c) 2010-2014 Intel Corporation. * All rights reserved
Copyright(c) 2010-2014 Intel Corporation */#include <stdint.h>#include <stddef.h>#include <stdlib.h>#include <stdio.h>#include
<stdarg.h>#include <errno.h>#include <sys/queue.h>
(C) token bucketuint64_t te; /* Number of bytes currently available in the excess (E) token bucket */};
Copyright(c) 2020 Intel Corporation */#include <sys/types.h>#include <sys/stat.h>#include <pthread.h>#include <unistd.h>
Copyright 2010-2012 Freescale Semiconductor, Inc. * All rights reserved
Copyright 2020 Mellanox Technologies, Ltd */#include <rte_windows.h>#include <rte_errno.h>#include <rte_log.h>#include <rte_eal.h>
Copyright(c) 2016 Ethan Zhuang <zhuangwj@gmail.com>.
Copyright (C) 2014-2016 Freescale Semiconductor, Inc. *
Copyright (C) 1991, 1999 Free Software Foundation, Inc.
Copyright (c) 2017-2019 Pensando Systems, Inc. All rights reserved
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Copyright (c) 2020 Dmitry Kozlyuk */
Copyright 2020 Mellanox Technologies, Ltd */
Copyright 2017,2019 NXP */
Copyright(c) 2015 Neil Horman <nhorman@tuxdriver.com>. * All rights reserved
Copyright 2019 Mellanox Technologies, Ltd */#include <netinet/in.h>
Copyright 2020 NXP */#ifndef _FSL_QBMAN_DEBUG_H_#define _FSL_QBMAN_DEBUG_H_
Copyright(c) 2010-2016 Intel Corporation */#include <stdint.h>#include <string.h>
Copyright (c) 2007,2008 Kip Macy kmacy@freebsd.org
Copyright(c) 2014-2017 Wind River Systems, Inc. */
Copyright(c) 2007-2014 Intel Corporation. */
Copyright(c) 2017 Brocade Communications Systems, Inc. * Author: Jan Blunck <jblunck@infradead.org> */
Copyright 2016 Neil Horman <nhorman@tuxdriver.com> * Based in part on modpost.c from the linux kernel */
Copyright (C) 2020 Marvell International Ltd. */#include <rte_acl.h>#include <rte_event_eth_tx_adapter.h>#include <rte_lpm.h>#include

```

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<rte_lpm6.h>
    Copyright(c) 2016 Intel Corporation. */
    Copyright 2016-2019 NXP *
    Copyright 2019 Mellanox Technologies, Ltd */#include <unistd.h>#include <stdint.h>#include <fcntl.h>#include <sys/eventfd.h>
    Copyright(c) 2018 Aquantia Corporation */#ifndef ATL_LOGS_H#define ATL_LOGS_H
    (C) token bucket uint64_t tp; /**< Number of bytes currently available in the peak(P) token bucket */);
    Copyright(C) 2019 Marvell International Ltd. */#ifndef _OTX2_EP_VF_H_#define _OTX2_EP_VF_H_
    Copyright(c) 2017 Wind River Systems, Inc. */
    Copyright(c) 2017 Intel Corporation */#include <rte_string_fns.h>#include <rte_reorder.h>#include <rte_cryptodev.h>#include
<rte_cryptodev_pmd.h>#include <rte_malloc.h>
    Copyright(c) 2010-2015 Intel Corporation */#include <stdint.h>
    Copyright (C) 2015 Freescale Semiconductor, Inc.
    Copyright (c) 2015-2019 Amazon.com, Inc. or its affiliates. * All rights reserved
    Copyright: 2016, Neil Horman <nhorman@tuxdriver.com>
    Copyright (C) Cavium, Inc. 2015.
    Copyright(c) 2015-2019 Vladimir Medvedkin <medvedkinv@gmail.com> */
    Copyright 2012 Hasan Alayli <halayli@gmail.com> */
    Copyright(c) 2016-2020 Intel Corporation */#include <sys/types.h>#include <netinet/in.h>#include <netinet/ip.h>
    Copyright 2017-2018 NXP */
    Copyright(c) 2019 Intel Corporation */
    Copyright (c) 2015-2020 Amazon.com, Inc. or its affiliates. * All rights reserved
    Copyright(c) 2018 Intel Corporation */#ifndef _QAT_COMMON_H_#define _QAT_COMMON_H_
    Copyright(c) 2018-2020 Intel Corporation */
    Copyright 2019-2020 NXP *//* qbman_sys_decl.h and qbman_sys.h are the two platform-specific files in the * driver. They are only included via
qbman_private.h, which is itself a * platform-independ
    Copyright 2012 Hasan Alayli <halayli@gmail.com> */#ifndef LTHREAD_H_#define LTHREAD_H_
    Copyright(c) 2012,2013 Intel Corporation */
    Copyright 2015-2016 Intel Corporation. */
    Copyright 2017 Mellanox Technologies, Ltd */
    Copyright 2017 NXP.
    Copyright(c) 2010-2014 Intel Corporation */#include <sys/cdefs.h>__FBSDID("$FreeBSD$");
    Copyright(c) 2019 Intel Corporation * * Derived do_macswap implementation from app/test-pmd/macswap_sse.h
    Copyright(c) 2017 Intel Corporation */#include <unistd.h>
    Copyright(c) 2018 HUAWEI TECHNOLOGIES CO., LTD. */#include <rte_cryptodev_pmd.h>
    Copyright (c) 2008-2016 Freescale Semiconductor, Inc.
    Copyright (C) Mellanox 2020. */
    Copyright (c) 2018 Microsoft Corp. * All rights reserved
    Copyright 2020 Mellanox Technologies, Ltd * * This file contain the application main file * This application provides the user the ability to test the *
insertion rate for specific rte_flow rule u
    Copyright(c) 2010-2014 Intel Corporation */#include <sys/types.h>#include <sys/sysctl.h>#include <sys/mman.h>#include <string.h>
    Copyright(c) 2017 Cavium, Inc */
    Copyright 2018 6WIND S.A.
    Copyright (C) 2006 Bob Jenkins (bob_jenkins@burtleburtle.net) * * http://burtleburtle.net/bob/hash/
    Copyright 2017 6WIND S.A.
    Copyright (c) 2020 Marvell Semiconductor Inc. * All rights reserved
    Copyright 2020 Mellanox Technologies, Ltd * * This file contains the functions definitions to * generate each supported action.
    Copyright(c) 2012-2014 6WIND S.A. */
    Copyright(c) 2019 Ericsson AB */
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    Copyright: 2007-2020 Intel Corporation.
    Copyright 2007 Nuova Systems, Inc. All rights reserved
    Copyright(c) 2010-2014 Intel Corporation */#include <stdlib.h>#include <stdio.h>#include <errno.h>#include <stdint.h>#include
<rte_hexdump.h>#include <rte_string_fns.h>
    Copyright(c) 2010 - 2015 Intel Corporation
    Copyright(c) 2018 Intel Corporation. */#include <rte_spinlock.h>#include <rte_service_component.h>#include <rte_ethdev.h>
    Copyright 2019 Mellanox Technologies, Ltd */
    Copyright(c) 2018 Synopsys, Inc. All rights reserved
    Copyright(c) 2019 Intel Corporation. */
    Copyright(c) 2019 Arm Limited */
    Copyright(C) 2019 Marvell International Ltd. */#include <string.h>#include <unistd.h>
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Copyright(c) 2018 Cavium, Inc */#ifndef _OTX_CRYPTODEV_HW_ACCESS_H_#define _OTX_CRYPTODEV_HW_ACCESS_H_
Copyright(c) 2016-2017 Intel Corporation */#ifndef RTE_EXEC_ENV_FREEBSD #define _WITH_GETLINE#endif#include <stdio.h>
Copyright (c) 2019 Red Hat, Inc.
Copyright(c) 2010-2016 Intel Corporation */#include <stdio.h>#include <string.h>#include <stdint.h>#include <rte_string_fns.h>#include
<rte_version.h>#include <rte_ethdev.h>#include <rte_ether
Copyright(c) 2017-2018 Intel Corporation */#include <rte_malloc.h>#include <rte_hash.h>#include <rte_jhash.h>#include <rte_mbuf.h>#include
<rte_cryptodev.h>
Copyright(c) 2010-2017 Intel Corporation */#include <stdlib.h>#include <stdbool.h>#include <netinet/in.h>
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Copyright (c) 2012 NetApp Inc. * All rights reserved
Copyright(c) 2009-2019 Solarflare Communications Inc. */
Copyright (c) 2016 - 2018 Cavium Inc. * All rights reserved
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Copyright(c) 2020 Intel Corporation */#include <string.h>#include <stdlib.h>#include <stdio.h>#include <stdint.h>
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Copyright (c) 2014, 2015 Netronome Systems, Inc. * All rights reserved
Copyright 2017 NXP *
Copyright(c) 2015-2017 Intel Corporation */
Copyright(c) 2016-2020 Intel Corporation */
Copyright 2017 Intel Corporation
Copyright(c) 2010-2014 Intel Corporation */#ifndef _GUEST_CHANNEL_H#define _GUEST_CHANNEL_H
Copyright(c) 2010-2017 Intel Corporation */
Copyright(c) 2015 Intel Corporation */
Copyright(c) 2014-2019 Solarflare Communications Inc. *
Copyright(c) 2010-2020 Intel Corporation */
Copyright(c) 2016-2017 Cavium, Inc */
Copyright (C) IBM Corporation 2016. */
Copyright(c) 2020 Intel Corporation */#ifndef __INCLUDE_RTE_SWX_PORT_SOURCE_SINK_H__#define
__INCLUDE_RTE_SWX_PORT_SOURCE_SINK_H__
Copyright 2016-2017 NXP *
Copyright 2015-2020 NXP *
Copyright(c) 2016 Intel Corporation. */#include <string.h>
Copyright(c) 2017-2018 Broadcom * All rights reserved
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Copyright(C) 2020 Marvell International Ltd. */
Copyright 2018 Ga */
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Copyright (c) 2001 David E. O
Copyright(c) 2020 Intel Corporation */#ifndef __INCLUDE_RTE_SWX_PORT_H__#define __INCLUDE_RTE_SWX_PORT_H__
Copyright(c) 2010-2014 Intel Corporation */#include <inttypes.h>#include <stdint.h>#include <stddef.h>#include <stdio.h>#include
<string.h>#include <unistd.h>#include <sys/queue.h>
Copyright(c) 2010-2018 Intel Corporation.
Copyright(c) 2016-2019 Intel Corporation */
Copyright (c) 2002 Todd C. Miller <Todd.Miller@courtesan.com> * * Sponsored in part by the Defense Advanced Research Projects * Agency (DARPA)
and Air Force Research Laboratory, Air Force * Materl
Copyright(c) 2018 Intel Corporation */
Copyright(c) 2006-2019 Solarflare Communications Inc.
Copyright(c) 2015-2018 Intel Corporation */#ifndef ADF_TRANSPORT_ACCESS_MACROS_H#define ADF_TRANSPORT_ACCESS_MACROS_H
Copyright(c) 2016 RehiveTech. All rights reserved
Copyright(c) 2019 Intel Corporation
Copyright (C) 2020 Marvell International Ltd. */
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```

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Copyright (c) 1991, 1993 \* The Regents of the University of California. All rights reserved  
Copyright (c) 2019 Cavium Inc. \* \* All rights reserved  
Copyright(c) 2020 Intel Corporation \*/#ifndef \_\_INCLUDE\_RTE\_SWX\_CTL\_H\_\_#define \_\_INCLUDE\_RTE\_SWX\_CTL\_H\_\_  
Copyright(C) 2020 Marvell International Ltd. \*/#ifndef \_\_INCLUDE\_ETHDEV\_TX\_PRIV\_H\_\_#define \_\_INCLUDE\_ETHDEV\_TX\_PRIV\_H\_\_  
Copyright 2013 6WIND S.A.  
Copyright(c) 2010-2016 Intel Corporation  
Copyright (C) 2019 Marvell International Ltd. \*/#include <rte\_cryptodev.h>  
Copyright 2018 Mellanox Technologies, Ltd  
Copyright(C) 2020 Marvell International Ltd. \*/#include <rte\_trace\_point.h>  
Copyright(c) 2015-2018 Intel Corporation \*/#ifndef \_\_ICP\_QAT\_FW\_COMP\_H\_\_#define \_\_ICP\_QAT\_FW\_COMP\_H\_\_  
Copyright(c) 2019 Solarflare Communications Inc. \* \* This software was jointly developed between OKTET Labs (under contract \* for Solarflare) and Solarflare Communications, Inc.  
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Copyright 2017 Cavium \*/  
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Copyright(c) 2018 Semihalf. \* All rights reserved  
Copyright 2020 Mellanox Technologies, Ltd \* \* This file contains the items, actions and attributes \* definition. And the methods to prepare and fill items, \* actions and attributes to generate rte  
Copyright (c) 2015-2018 Atomic Rules LLC \*/  
Copyright(c) 2012-2019 Solarflare Communications Inc. \*/  
Copyright(c) 2015 Cavium, Inc  
Copyright (C) 2020 Marvell. \*/#ifndef \_\_INCLUDE\_PKT\_CLS\_PRIV\_H\_\_#define \_\_INCLUDE\_PKT\_CLS\_PRIV\_H\_\_  
Copyright 2014 6WIND S.A. \*/  
Copyright 2014-2020 Mellanox Technologies, Ltd \*/  
Copyright(c) 2017 Huawei Technologies Co., Ltd \*/#include<rte\_bus\_pci.h>  
Copyright 2017 Cavium, Inc. \*/  
Copyright(c) 2018-2019 Ericsson AB \*/  
Copyright (C) 1996, 1997, 1998 Theodore Ts \*/  
Copyright 2013-2015 Freescale Semiconductor Inc.  
Copyright (c) 2017-2018 Solarflare Communications Inc. \* All rights reserved  
Copyright(c) 2020 Hisilicon Limited. \*/  
Copyright 2013-2014 6WIND S.A. \*/  
Copyright(c) 2016 6WIND S.A. \*/  
Copyright(c) 2018 Intel Corporation \*/#include <stdint.h>#include <stdlib.h>#include <string.h>  
Copyright (c) 2000 The NetBSD Foundation, Inc. \* All rights reserved  
Copyright (c) 1989, 1993, 1994 \* The Regents of the University of California. All rights reserved  
Copyright (c) 2012 NetApp Inc.  
Copyright(c) 2010-2014 Intel Corporation \*/#include <string.h>#include <stdio.h>#include <unistd.h>#include <inttypes.h>#include <sys/types.h>#include <sys/sysctl.h>#include <errno.h>  
Copyright(c) 2013 6WIND S.A. \*/  
Copyright 2019 Mellanox Technologies, Ltd \*/#include <stdlib.h>  
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Copyright (C) IBM Corporation 2014. \*/  
Copyright(c) 2016 Intel Corporation  
Copyright(c) 2015-2018 Intel Corporation \*/#ifndef \_\_ICP\_QAT\_FW\_H\_\_#define \_\_ICP\_QAT\_FW\_H\_\_#include <sys/types.h>#include "icp\_qat\_hw.h"



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 Copyright 2006 Bob Jenkins \* \* Derived from public domain source, see \* <<http://burtleburtle.net/bob/c/lookup3.c>>: \*  
 Copyright 2018 NXP \*  
 Copyright(c) 2016 Intel Corporation \*/#include <string.h>#include <stdint.h>#include <unistd.h>  
 Copyright(c) 2017 Cavium, Inc  
 Copyright 2019 Mellanox Technologies, Ltd \*/#include <unistd.h>#include <net/if.h>#include <sys/socket.h>#include <sys/ioctl.h>#include  
 <fcntl.h>#include <netinet/in.h>  
 Copyright(c) 2015-2017 Intel Corporation \*/#ifndef TEST\_CRYPTODEV\_H\_#define TEST\_CRYPTODEV\_H\_  
 Copyright(c) 2014-2019 Chelsio Communications. \* All rights reserved  
 Copyright(c) 2017 Cavium, Inc. All rights reserved  
 Copyright (c) 2009-2018 Microsoft Corp.  
 Copyright(c) 2010-2014 Intel Corporation \*/#include <string.h>#include <stdint.h>  
 Copyright(c) 2014 John W. Linville <linville@tuxdriver.com> \* Originally based upon librt\_e\_pmd\_pcap code:  
 Copyright(C) 2019 Marvell International Ltd. \*/  
 Copyright(c) 2015-2019 Intel Corporation \*/#ifndef \_QAT\_SYM\_SESSION\_H\_#define \_QAT\_SYM\_SESSION\_H\_  
 Copyright(c) 2015-2020 Intel Corporation  
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 Copyright(c) 2014 6WIND S.A. \*/  
 Copyright 2017-2018 NXP \*  
 Copyright(c) 2014-2019 Broadcom \* All rights reserved  
 Copyright(c) 2010-2014 Intel Corporation \*/#include <sys/mman.h>#include <unistd.h>#include <sys/types.h>#include <sys/sysctl.h>#include  
 <inttypes.h>#include <errno.h>#include <string.h>#incl  
 Copyright(c) 2020 Intel Corporation \*/#ifndef \_\_INCLUDE\_RTE\_SWX\_TABLE\_EM\_H\_\_#define \_\_INCLUDE\_RTE\_SWX\_TABLE\_EM\_H\_\_  
 Copyright(c) 2015-2016 Intel Corporation \*/  
 Copyright(c) 2020 Red Hat Inc. \*/  
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 Copyright(c) 2019-2020 Broadcom \* All rights reserved  
 Copyright(c) 2018 HUAWEI TECHNOLOGIES CO., LTD. \*/  
 Copyright(c) 2010-2014 Intel Corporation \*/#include <stdio.h>#include <stdint.h>#include <signal.h>#include <errno.h>#include <string.h>#include  
 <sys/queue.h>#include <sys/time.h>#include <sy  
 Copyright(c) 2012-2013 6WIND S.A. \*/  
 Copyright(c) 2016 Intel Corporation \*/#ifndef \_\_RTE\_IPSEC\_XFORM\_ESP\_H\_\_#define \_\_RTE\_IPSEC\_XFORM\_ESP\_H\_\_  
 Copyright 2017 NXP \*/#include <rte\_common.h>#include <rte\_mbuf.h>#include <rte\_malloc.h>#include <rte\_memcpy.h>#include  
 <rte\_dev.h>#include <rte\_rawdev.h>#include <rte\_bus\_vdev.h>  
 Copyright(c) Hannes Frederic Sowa \* All rights reserved  
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 Copyright(c) 2014-2018 Chelsio Communications. \* All rights reserved  
 Copyright(c) 2017 IBM Corporation. \*/  
 Copyright 2018 NXP \*/  
 Copyright(c) 2016 Intel Corporation \*/#include <arpa/inet.h>#include <sys/socket.h>  
 Copyright (c) 2017,2018 HXT-semitech Corporation.  
 Copyright(c) 2010-2014 Intel Corporation.  
 Copyright(c) 2017 Intel Corporation. \* All rights reserved  
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 Copyright (c) 2007-2009 Kip Macy kmacy@freebsd.org  
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 Copyright 2018 Arnon Warshavsky <arnon@qwilt.com>  
 Copyright (c) 1998 Doug Rabson \* All rights reserved  
 Copyright (c) 2019-2020 Arm Limited \*/  
 Copyright(c) 2020 Intel Corporation \*/#ifndef \_\_INCLUDE\_RTE\_SWX\_PORT\_ETHDEV\_H\_\_#define \_\_INCLUDE\_RTE\_SWX\_PORT\_ETHDEV\_H\_\_  
 Copyright 2008-2015 Cisco Systems, Inc"  
 Copyright 2009-2012 Freescale Semiconductor Inc.  
 Copyright (c) 2018-2020 Arm Limited \*/  
 Copyright 2019 Mellanox Technologies, Ltd \*/#include <rte\_malloc.h>#include <rte\_errno.h>  
 Copyright(c) 2017 Intel Corporation \*/#include <string.h>#include <rte\_common.h>#include <rte\_mempool.h>#include <rte\_mbuf.h>#include

```

<rte_ethdev.h>#include <rte_eventdev.h>#include <rte_bus_v
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  Copyright(c) 2013 - 2015 Intel Corporation */
  Copyright(c) 2015-2018 Intel Corporation
  Copyright(c) 2015-2019 Solarflare Communications Inc. */
  Copyright(C) 2020 Marvell International Ltd.*/#ifndef __INCLUDE_IP4_REWRITE_PRIV_H__#define __INCLUDE_IP4_REWRITE_PRIV_H__
  Copyright(c) 2018 Advanced Micro Devices, Inc. All rights reserved
  Copyright(c) 2018 Intel Corporation */#include <string.h>
  Copyright (c) 2012 Citrix Inc. * All rights reserved
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  Copyright(c) 2019-2020 Xilinx, Inc.
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  Copyright(c) 2019 Xilinx, Inc. All rights reserved
  Copyright 2020 Mellanox Technologies, Ltd */#include <rte_eal_memconfig.h>#include <rte_errno.h>#include <rte_mempool.h>#include
<rte_malloc.h>#include <rte_rwlock.h>
  Copyright 2016,2019-2020 NXP *
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  Copyright(c) 2017 Intel Corporation */#include <rte_common.h>#include <rte_hexdump.h>#include <rte_cryptodev.h>#include
<rte_cryptodev_pmd.h>#include <rte_bus_vdev.h>#include <rte_malloc.h>#in
  Copyright 2020 Mellanox Technologies, Ltd * * This file contain the implementations of the items * related methods. Each Item have a method to
prepare * the item and add it into items array in giv
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  Copyright 2016 Mellanox Technologies, Ltd */
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  Copyright(c) 2017 Intel Corporation.
  Copyright(c) 2010-2019 Intel Corporation */
  Copyright(c) 2017 Intel Corporation */
  Copyright (c) 2016 - 2020 Cavium Inc. * All rights reserved
  Copyright(c) 2010-2014 Intel Corporation */#include <string.h>
  Copyright(c) 2010-2015 Intel Corporation. */
  Copyright(C) 2020 Marvell International Ltd.*/#include <inttypes.h>#include <signal.h>#include <stdio.h>#include <unistd.h>
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  Copyright(c) 2017-2018 Intel Corporation. */
  Copyright(c) 2014-2020 Broadcom * All rights reserved
  Copyright(c) 2020 Intel Corporation */#ifndef __INCLUDE_RTE_SWX_PIPELINE_H__#define __INCLUDE_RTE_SWX_PIPELINE_H__
  Copyright(c) 2020 Dmitry Kozlyuk */
  Copyright(c) 2017 Cavium, Inc. */
  Copyright(c) 2018 Intel Corporation */#include <isa-l.h>
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  Copyright(c) 2018-2019 Solarflare Communications Inc. */
  Copyright(c) 2008-2019 Solarflare Communications Inc. */
  Copyright(C) 2020 Marvell International Ltd.*/#ifndef __INCLUDE_ETHDEV_RX_PRIV_H__#define __INCLUDE_ETHDEV_RX_PRIV_H__
  Copyright(c) 2017-2018 Intel Corporation. * All rights reserved
  Copyright(c) 2010-2014 Intel Corporation */#include <errno.h>#include <string.h>
  Copyright(c) 2017-2018 Intel Corporation */
  Copyright (c) 2019 Intel Corporation */
  Copyright(c) 2010-2014 Intel Corporation */#include <string.h>#include <stdint.h>#include <errno.h>#include <stdarg.h>#include <stdio.h>#include

```

```

<sys/queue.h>
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Copyright 2017 NXP */
Copyright (c) 2010-2012 Citrix Inc.
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Copyright(c) 2020 Intel Corporation */#ifndef __INCLUDE_RTE_SWX_TABLE_H__#define __INCLUDE_RTE_SWX_TABLE_H__
(C) token bucketuint64_t te; /*< Number of bytes currently available in the excess(E) token bucket */};
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Copyright: 2015-2020 Canonical Ltd.
Copyright(c) 2020 Intel Corporation */#include <stdint.h>#include <stdlib.h>#include <stdio.h>#include <string.h>#include <errno.h>
Copyright(c) 2015 Intel Corporation */#ifndef LTHREAD_DIAG_API_H_#define LTHREAD_DIAG_API_H_
Copyright: 2010-2020, Intel Corporation.
Copyright(c) 2016-2018 Microsoft Corporation
Copyright(c) 2018 HUAWEI TECHNOLOGIES CO., LTD. */#include <stdbool.h>#include <unistd.h>
Copyright 2019 NXP */#include <fsl_mc_sys.h>#ifndef _FSL_DPRTC_CMD_H#define _FSL_DPRTC_CMD_H
Copyright(c) 2019 Arm Limited *
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Copyright(c) 2017-2019 Solarflare Communications Inc. */
Copyright (c) 2010 Fabien Romano <fabien@openbsd.org>
(C) The END item is met. */static intflow_item_skip_disabled_protos(const struct rte_flow_item **item, uint64_t proto_mask, size_t *length, struct
rte_flow_error *error)
Copyright 2019 6WIND S.A.
Copyright 2020 Mellanox Technologies, Ltd * * The file contains the implementations of the method to * fill items, actions & attributes in their
corresponding * arrays, and then generate rte_flow
Copyright(c) 2010-2016 Intel Corporation */#ifndef _TEST_LPM_ROUTES_H_#define _TEST_LPM_ROUTES_H_
Copyright(c) 2019-2020 Intel Corporation */#ifndef _RTE_IOAT_RAWDEV_FNS_H_#define _RTE_IOAT_RAWDEV_FNS_H_
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Copyright(c) 2019 Netcope Technologies, a.s. <info@netcope.com> * All rights reserved
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Copyright(c) 2017 Huawei Technologies Co., Ltd */
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Copyright 2020 Mellanox Technologies, Ltd */#include <fcntl.h>#include <stdint.h>
Copyright 2019 NXP */#include <fsl_mc_sys.h>#include <fsl_mc_cmd.h>#include <fsl_dprtc.h>#include <fsl_dprtc_cmd.h>
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copyright = Falsehighlight_language = 'none'
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 Copyright(c) 2001 - 2015 Intel Corporation \*/\*\$FreeBSD\$\*/  
 Copyright 2019-2020 NXP \*/  
 Copyright 2020 Mellanox Technologies Ltd \*/  
 Copyright 2020 Mellanox Technologies, Ltd \* \* The file contains the implementations of actions generators. \* Each generator is responsible for preparing  
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 Copyright (C) 2015 Freescale Semiconductor, Inc. \*/  
 Copyright(c) 2018 Red Hat Corp. \*/  
 Copyright 2017-2019 NXP \*  
 Copyright(c) 2019 Intel Corporation. \*/#include <stdint.h>#include <stdio.h>#include <string.h>#include <errno.h>  
 Copyright 2019 Mellanox Technologies, Ltd \*/#include <string.h>#include <unistd.h>#include <sys/mman.h>  
 Copyright (C) 1989, 1991 Free Software Foundation, Inc.,  
 Copyright 2017,2020 NXP \*  
 Copyright(c) 2016-2018 Intel Corporation \*/#include <unistd.h>#include <pthread.h>#include <stdbool.h>#include <sys/epoll.h>  
 Copyright(c) 2016 Neil Horman <nhorman@tuxdriver.com>  
 Copyright(c) 2018 David Marchand <david.marchand@redhat.com>  
 Copyright (C) 2006-2012 Toni Ronkko \* <https://github.com/tronkko/dirent> \*/  
 Copyright(c) 2010-2015 Intel Corporation.  
 Copyright(c) 2018 Marvell International Ltd.  
 Copyright 2012 Hasan Alayli <halayli@gmail.com> \*/#ifndef LTHREAD\_INT\_H#define LTHREAD\_INT\_H  
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 Copyright(c) 2018 Cavium, Inc \*/#include <assert.h>#include <string.h>#include <unistd.h>  
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 Copyright (c) 2015-2018 Cavium Inc. \* All rights reserved  
 Copyright (c) 2020 Arm Limited  
 Copyright(c) 2020 Intel Corporation \*/#include <stdlib.h>#include <string.h>#include <stdio.h>#include <errno.h>  
 Copyright(c) 2016 IGEL Co., Ltd.  
 Copyright(c) 2015-2020 Intel Corporation. \*/  
 Copyright(c) 2018 Cavium Networks  
 Copyright(c) 2017-2020 Intel Corporation. \*/  
 Copyright(c) 2020 Intel Corporation \*/  
 Copyright(c) 2020 Xilinx, Inc. \*/  
 Copyright(c) 2018 Aquantia Corporation \*/#ifndef ATL\_TYPES\_H#define ATL\_TYPES\_H  
 Copyright (C) 2007 VMware, Inc. All rights reserved  
 Copyright(C) 2020 Marvell International Ltd. \*/#include <assert.h>#include <inttypes.h>#include <signal.h>#include <stdio.h>#include  
 <string.h>#include <unistd.h>  
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Copyright 2018 Luca Boccassi <bluca@debian.org>  
Copyright(c) 2018 Marvell International Ltd. \*/  
Copyright(c) 2020 Intel Corporation \*/#include <stdlib.h>#include <string.h>#include <stdio.h>#include <sys/queue.h>#include <unistd.h>  
Copyright(c) 2018 Vladimir Medvedkin <medvedkinv@gmail.com>  
Copyright(c) 2015 Cavium, Inc.  
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Zambrano <zambrano@broadcom.com>  
Copyright(c) 2020 Mellanox Technologies, Ltd \*/#include <string.h>  
Copyright (c) 2016 Freescale Semiconductor, Inc. All rights reserved  
Copyright(c) 2006-2019 Solarflare Communications Inc. \*/  
Copyright(c) 2016-2018, Linaro Limited. \*/  
Copyright(c) 2016-2019 Solarflare Communications Inc. \*/  
Copyright(c) 2020 Intel Corporation \*/#ifndef \_\_INCLUDE\_RTE\_SWX\_EXTERN\_H\_\_#define \_\_INCLUDE\_RTE\_SWX\_EXTERN\_H\_\_  
Copyright 2016,2019 NXP \*  
copyright-format/1.0/Upstream-Name: dpdkSource: https://dpdk.org  
Copyright (c) 2013-2015 Brocade Communications Systems, Inc.  
Copyright (C) 2019 Marvell International Ltd. \*/#include <rte\_cryptodev.h>#include <rte\_ethdev.h>  
Copyright(c) 2017 Intel Corporation. \*/  
Copyright(c) 2018-2019 Solarflare Communications Inc. \* \* This software was jointly developed between OKTET Labs (under contract \* for Solarflare)  
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Copyright(c) 2013-2016 Intel Corporation \*/  
Copyright(c) 2018 Chelsio Communications. \* All rights reserved  
Copyright(c) 2015-2019 Intel Corporation \*/#ifndef \_ICP\_QAT\_FW\_LA\_H\_#define \_ICP\_QAT\_FW\_LA\_H\_#include "icp\_qat\_fw.h"  
Copyright(c) 2014 6WIND S.A. \* All rights reserved  
Copyright (c) 2018 Microsoft Corp.  
Copyright 2018 Mellanox Technologies, Ltd \*/#include <math.h>  
Copyright(c) 2019 Intel Corporation. \*/#include <stdint.h>#include <stdio.h>#include <errno.h>  
Copyright 2018-2020 NXP \*  
Copyright (c) 2010-2017 Intel Corporation  
Copyright(c) 2018 Cavium, Inc \*/  
Copyright 2016,2020 NXP \*  
Copyright (C) 2020 Marvell International Ltd. \*/#ifndef \_IPSEC\_WORKER\_H\_#define \_IPSEC\_WORKER\_H\_  
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Copyright (c) 2014-2018 Netronome Systems, Inc. \* All rights reserved  
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dtc 1.5.1 openseuse:dtc/1.5.1-lp152.2.3.1/aarch64  
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(C) Copyright David Gibson <dwg@au1.ibm.com>, IBM Corporation. 2005, 2008. \*/  
(C) Copyright David Gibson <dwg@au1.ibm.com>, IBM Corporation. 2006. \*/#include <stdio.h>#include <stdlib.h>#include <unistd.h>#include  
<fcntl.h>#include <stdint.h>  
(C) Copyright David Gibson <dwg@au1.ibm.com>, IBM Corporation. 2007. \*/  
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gmail.com>  
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Copyright (C) 2006 David Gibson, IBM Corporation. */#include <fdt.h>
Copyright (C) 2006 David Gibson, IBM Corporation. */#include "libfdt_env.h"
Copyright (C) 2006 David Gibson, IBM Corporation. */#include <stdbool.h>#include <stdlib.h>#include <stdio.h>#include <string.h>#include
<stdint.h>
Copyright (C) 2006 David Gibson, IBM Corporation. */#include <stdlib.h>#include <stdio.h>#include <string.h>#include <stdint.h>
Copyright (C) 2006 David Gibson, IBM Corporation. */#include <stdlib.h>#include <stdio.h>#include <string.h>#include <stdint.h>#include <errno.h>
Copyright (C) 2006 David Gibson, IBM Corporation. */#include <stdlib.h>#include <stdio.h>#include <string.h>#include <stdint.h>#include
<stdarg.h>
Copyright (C) 2007 David Gibson, IBM Corporation. */
Copyright (C) 2008 David Gibson, IBM Corporation. */
Copyright (C) 2009 David Gibson, IBM Corporation. */#include <stdlib.h>#include <stdio.h>#include <string.h>#include <stdint.h>
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Copyright (C) 2011 The Chromium Authors. All rights reserved
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Copyright (C) 2014 David Gibson <david@gibson.dropbear.id.au>
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Copyright (C) 2016 Free Electrons
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Copyright (C) 2017 Google, Inc. * Written by Simon Glass <sjg@chromium.org> */
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Copyright (c) 2017 Konsulko Group Inc. All rights reserved
Copyright (C) 2018 AKASHI Takahiro, Linaro Limited */#include <stdlib.h>#include <stdio.h>#include <string.h>#include <stdint.h>
Copyright (C) 2018 David Gibson */
Copyright (C) 2018 David Gibson, IBM Corporation. */
Copyright (C) 2018 David Gibson, Red Hat Inc. */
Copyright (C) 2018 embedded brains GmbH */#include "libfdt_env.h"
eBtables 2.0.10.4+snapshot20181205 debian:eBtables/2.0.10.4+snapshot20181205-3/mips
Copyright: 1999 Paul
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copyright-format/1.0/Upstream-Name: eBtablesSource: https://www.netfilter.org/
eCryptfs 111 unknown:undefined
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;:*-*so
(C)!b go :more \./!{ N s|\n# || b more
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Copyright (C) 1995-2003, 2005-2006, 2008-2014 Free Software Foundation,
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 Johansson.%%=====%% Filename :  
 hipe\_rtl\_arch.erl%% History : \* 2001-04-10 Erik Johansson (happi@it.uu.  
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 (C) C = immediate()%% @type immediate(I) = I%% I = term()%% @end  
 (C) of true -> true; false -> any\_catchall(Cs) end;any\_catchall([]) -> false.  
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copyright(Es) -> case get_content(copyright, Es) of [] -> []; Es1 ->
copyright -doc -vsn -import -include -include_lib -compile -define -else -endif -file -behaviour -behavior -spec",c:
copyright 2000 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @doc Predefined Core Erlang primitive operations
used by HiPE.
(C) andalso mnesia_monitor:use_dir() of true -> if is_record(C, commit) -> append/latest_log, strip_snmp(C)); true -
> %% Either a commit record as binary %% or some
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(C) of false -> true; true -> check_unique_tags(S, C, []) end; _ -> check_unique_tags(S, C, [])
end.
Copyright (C) 2004-2018 Bootstrap Authors
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(C) of true -> match_var(Us, Cs, Def, St); false -> match_con(Us, Cs, Def, St) end.
(C) andalso C >= 1)).%% Line numbers less than zero have always been allowed:
Copyright: 2010 Torbjorn Tornkvist
Copyright Ericsson 2017-2018. All Rights Reserved
(C) when C := 16 C := 16#2E; C := 16#5F; 16#30 =< C, C =< 16#39; 16#41 =< C, C =< 16#5A; 16#61 =< C, C =< 16#7A -
> true;is_url_char(_) -> false.
copyright(Ts) -> get_pdata_tag(copyright, Ts).
Copyright Ericsson AB 2013-2018. All Rights Reserved
(C) bor (D) bor (E) bor (F) band (bnot 16#ff) := 0).
Copyright Ericsson AB 2002-2020. All Rights Reserved
Copyright Ericsson AB 2005-2015. All Rights Reserved
Copyright Ericsson AB 2017-2020. All Rights Reserved
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copyright 2012 Richard Carlsson%% @doc Trivial Lisp interpreter in Erlang.
(C) of '$end_of_table' -> Acc; {New, Cont} -> select_1({New ++ Acc, Cont}) end.
(C) of ws -> only_ws(Cs, A) end.
Copyright Ericsson AB 2009-2020. All Rights Reserved
(C) when is_integer(C) -> C.
copyright(HeaderTags) ++ get_deprecated(HeaderTags) ++ sees(HeaderTags, Env) ++ references(HeaderTags) ++
todos(HeaderTags, Opts) ++ [{typedcls, types(AllTags, Env)}, {func
Copyright &#169; </xsl:variable>
Copyright Ericsson AB 2006-2019. All Rights Reserved
(C) Function block ErtsAlcType t type; /* (C) */
Copyright Ericsson AB 1996-2016. All Rights Reserved
Copyright (c) 2010, Google Inc.// All rights reserved
copyright 2000-2006 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @doc Core Erlang pattern matching
compiler.
copyright { padding: 20px 0px 0px 0px; font-family: "Courier New", monospace; font-size: .7em; color: #000000;}
Copyright (C) 1995-1998 Eric Young (eay@cryptsoft.com) * All rights reserved
(C) whenget_hex_digit(_) -> none.
(C) when C==repeat_n; C==duration; C==until -> dupl(4, start_logging() ++ all_succ() ++ stop_logging());
copyright> <year>2007</year><year>2016</year> <holder>Ericsson AB. All Rights Reserved
(C) when is_integer(C), 16 C + 32;
Copyright (C) 2014 Free Software Foundation, Inc.
(C) when C >= C + 32;tolower(C) -> C.
copyright(Es) ++ deprecated(Es, "module") ++ [?NL] ++ version(Es) ++ since(Es) ++ behaviours(Es, Name, Opts) ++
authors(Es) ++ references(Es) ++ sees(Es) +
Copyright (c) 2000 by Erik Johansson. All Rights Reserved
Copyright (C) 1991-2, RSA Data Security, Inc. Created 1991. All
Copyright 2008, Ericsson AB. All Rights * Reserved." * * $Id$
Copyright \251 " end.
Copyright (c) 2001-2016 Richard Carlsson. Parts written by Ericsson
(C) bsl 8) bor (D)).-define(encode(N), [((N) bsr 24) band 16#FF), (((N) bsr 16) band 16#FF), (((N) bsr 8) band 16#FF), ((N) band
16#FF)]. -define(check_server(), case whereis(uds_server)
copyright 1999-2002 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @doc Basic functions on Core Erlang abstract
syntax trees.%%<p>Syntax trees are defined in the mo
(C) when is_integer(C), C >= [$$string_char(unicode_as_unicode, C, -1, [])].

```

```

(C) of k_binary -> select_types(Cs, [C | Bin], BinCon, Cons, Tuple, Map, Atom, Float, Int, Nil); k_bin_seg -> select_types(Cs, Bin, [C |
BinCon], Cons, Tuple, Map, Atom, Float, Int, Nil);
Copyright Scott Lystig Fritchie 2011-2016.
copyright 1999-2014 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @end%%
=====
(C) when not is_list(C), not is_tuple(C) -> {?ATOM_TYPE, C};make_element(T) when is_tuple(T) -> make_tuple(tuple_to_list(T), [],
[]);make_element(L) when is_list(L) -> create(L, ?ANYTYPE
(C) end), Loop2 = fun(____,0) -> {failure, timeout}; (L2,{MA,MB},F2,N) -> case F2() of MA -> MA;
MB -> MB; Other -> io:format("~p~n",[Other]), receive after 100
(C) when C==duration_force_stop; C==until_force_stop -> dupl(2, start_logging() ++ all_succ() ++ stop_logging() ++
start_logging() ++ all_succ(r1_SUITE) ++ stop_loggi
copyright>%% <year>2005-2008</year>%% <holder>Ericsson AB, All Rights Reserved
(C) of C1 when C1 > $\000 -> {C1,Cs,Pos}; _ -> {error, undefined_escape_sequence} end;scan_escape([], _Pos) -> {error, truncated_char}.
(C) of true -> coverage_trim_2(R, I - 1, [C | L]); false -> {I,Bin,lists:reverse(L)} end.
(C) or else has_nl(Cs);has_nl([]) -> false.
copyright(Tags) ++ get_version(Tags) ++ get_since(Tags) ++ sees(Tags, Env) ++ references(Tags) ++ todos(Tags, Opts)) }.
copyright 2012-2015 Richard Carlsson%% @doc Parse transform for merl. Enables the use of automatic metavariables%% and using quasi-quotes in
matches and case switches. Also optimizes calls%% to fun
Copyright (C), 1998, Ericsson%% Telecom AB. All Rights Reserved
Copyright Ericsson AB 1996-2015. All Rights Reserved
Copyright (C) 2003-2005, 2009 Mikael Petterson%% Notes:
Copyright Ericsson AB 2016-2020. All Rights Reserved
copyright 2012 Richard Carlsson%% @doc Trivial Basic interpreter in Erlang
Copyright(c) 2009-2019 Zoltan HerczegAll rights reserved
(C) when is_digit(_) -> false.
Copyright Ericsson AB 1997-2015. All Rights Reserved
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(C) when C >= C + $A - $a;tu(C) -> C.
(C) end end, filelib:fold_files("/cores", "^core.*$",
false, fun (C,Cs) -> [C|Cs] end, []) end after catch file:set_cwd(OrgCWD), catch c
Copyright: 2004-2009, Richard Carlsson, Eric Merritt
copyright 1999-2002 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @doc Core Erlang inliner.
Copyright Ericsson AB 2018-2020. All Rights Reserved
(C)Copyright 1981-1995 Microsoft Corp Licensed Material - Property of Microsoft All rights reserved
Copyright Ericsson AB 1996-2017. All Rights Reserved
(C) < ?MINFORJUMPTABLE of true -> gen_small_switch_val(Arg,C,Fail,VarMap,ConstTab,Options); _ ->
gen_jump_table(Arg,Fail,IcodeFail,VarMap,ConstTab,C,Min) end;find_cluster([Dens
Copyright (C) 2001-2005 by Ericsson Telecom AB%% SE-126 25 STOCKHOLM%% SWEDEN, tel int + 46 8 719 0000%% The program may
be used and/or copied only with the written%% permission from Eric
(C) | hh_field(Cs)hh_field([C|Cs]) -> [C|hh_field(Cs)];hh_field([]) -> [].
(C) when (C) -> C end, Label).
(C) ((char) (C))
Copyright Ericsson AB 2012-2019. All Rights Reserved
Copyright Ericsson AB 2005-2019. All Rights Reserved
Copyright (C) 2004, 2011-2018 Free Software Foundation, Inc.# Written by Scott James Remnant, 2004
Copyright Ericsson AB 1998-2019. All Rights Reserved
Copyright Ericsson AB 2013-2015. All Rights Reserved
(C) bor (D) bor (E) bor (F) bor (G) bor (H) band (bnot 16#ffff) == 0).
(C) when is_list(C) -> test_alias_tclocal(newalias);test_alias_tclocal(Alias) when is_atom(Alias) -> A = [{a,a_value},{b,b_value},{c,[{d,d_value}]}] =
ct:get_config(Alias), A = ct:get_con
Copyright (c) 2001-2005 Bjorn Gustavsson
Copyright Ericsson AB 2014-2017. All Rights Reserved
copyright 2009 Micka%% @see eunit%% @doc Surefire reports for EUnit (Format used by Maven and Atlassian%% Bamboo for example to integrate
test results). Based on initial code%% from Paul Guyot.%%
(C) of alias -> C; var -> C; _ -> cerl:set_ann(C, [compiler_generated]) end end, cerl_trees:map(F,
Core).
Copyright Ericsson AB 1998-2017. All Rights Reserved
(C) of true -> name_chars(Cs); false -> false end;name_chars([]) -> true.
(C) ((Ulong) ALC_TEST1(0x00b, (C)))#define SBC2BLK(A, C) ((Block_t *) ALC_TEST2(0x00c, (A), (C)))#define
BLK_TO_SBC(A, B) ((Carrier_t *) ALC_TEST2(0x00d, (A), (B)))#define MBC_TO_FIRST_BLK(A, C)
Copyright (c) 1997-2019 University of CambridgeAll rights reserved

```

```

copyright 2004-2009 Micka%% @author Mickaël Rémond <mickael.remond@process-one.net>%% [http://www.process-one.net/]%% @author
Richard Carlsson <carlsson.richard@gmail.com>%% @version {@version},
Copyright Ericsson AB 2004-2017. All Rights Reserved
copyright 2012 Richard Carlsson%% @doc Lisp compiler in Erlang.
(C) andalso C >= 1)).
(C) part of the net interface *          This is a module of miscellaneous functions. * *          We (try to) avoid name clashes by prefixing "all" internal *
function names with e
Copyright (c) 2005, Google Inc.// All rights reserved
copyright 2006 Daniel Luna%% @doc
(C) of          Result ->          ct:pal("gen_tcp:close(C) -> ~p~n", [Result]),          ok = Result          catch
Class
(C) when byte_size(<<C/utf16>>) /= 42 -> ok;
Copyright Ericsson AB 1997-2017. All Rights Reserved
Copyright Ericsson AB 2012-2017. All Rights Reserved
Copyright (C) 1999-2001 Richard Carlsson%% An implementation of ordered sets using Prof. Arne Andersson's%% General Balanced Trees. This
can be much more efficient than using%% ordered lists, fo
Copyright(c) 2010-2019 Zoltan HerczegAll rights reserved
Copyright 1995-2017 Mark Adler "/* If you use the zlib library in a product, an acknowledgment is welcome in the documentation of your product. If
for some reason you cannot include such an ac
Copyright Ericsson AB 2000-2019. All Rights Reserved
copyright 2006-2009 Richard Carlsson%% @private%% @see eunit%% @doc Text-based frontend for EUnit
Copyright (C) 1995-2016 Jean-loup Gailly, Mark Adler * For conditions of distribution and use, see copyright notice in zlib.h */
Copyright 1999, Ericsson Utvecklings%% AB. All Rights Reserved
Copyright Ericsson AB 2006-2017. All Rights Reserved
Copyright Ericsson AB 2013-2017. All Rights Reserved
(C) of          true ->          {true,          wrap_encode(          false          end
filename:join(Dirname, N),          C));          false ->          end
end          end          end,
copyright 1999-2002 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @doc Core Erlang prettyprinter.%%
<p>This module is a front end to the pretty-printing library modu
copyright>%% <year>2004-2008</year>%% <holder>Ericsson AB, All Rights Reserved
Copyright Ericsson AB 2005-2017. All Rights Reserved
Copyright Ericsson AB 2007-2017. All Rights Reserved
Copyright Ericsson AB 2010-2012. All Rights Reserved
Copyright: Ericsson AB 2012-2016
Copyright Ericsson AB 1999-2020. All Rights Reserved
(C) Auxiliary files:
(C) of          true -> over_word2(Cs, [C|Stack], N+gc_len(C)); false -> {[C|Cs],Stack,N} end;over_word2([], Stack, N) when is_integer(N) ->
{[],Stack,N}.
copyright/year          <xsl:text>-</xsl:text>          <xsl:value-of select="substring-after(normalize-space(substring-after($gendate, ' '), ' '))"/>
<xsl:text> </xsl:te
copyright 2001-2003 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @see edoc%% @end %%
=====
(C)          %%?pp_debug("Parameters are E=          F = emend(E,DST,DEF),          check_definition(F,Rest,BlockLabel,Cfg,XsiGraph) %% Continue
the search end end; #move{} ->          %% It's
Copyright Ericsson AB 2011-2018. All Rights Reserved
Copyright is defined in the accompanying file%% COPYRIGHT. However, the resultant scanner generated by leex is the%% property of the creator of
the scanner and is not covered by that%% Copyright.
Copyright Ericsson AB 2004-2020. All Rights Reserved
Copyright Ericsson AB 2003-2015. All Rights Reserved
Copyright Ericsson AB 2000-2017. All Rights Reserved
(C) Table name (static, for debugging)          int size_mask;          /* (RW) Number of slots - 1 */          SafeHashBucket** tab;          /* (RW) Vector of bucket
pointers (objects) */          int grow_limit;
(C) when is_integer(C),          C - 32;
(C)          ((Ulong)          ALC_TEST1(0x007, (C)))#define IS_SBC_BLK(B)          ((Ulong)          ALC_TEST1(0x008, (B)))
Copyright © 2012-2015 Dan Nicholson <dbn.lists@gmail.com>
Copyright &#169; <xsl:value-of select="/book/header/copyright/year
Copyright (C) 2000-2003 Richard Carlsson%%
Copyright (c) 2002 by Erik Johansson.%%
=====%% Filename : hipe_rtl_arith_32.erl%%
Module : hipe_rtl_arith_32%% Purpose : To

```

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Copyright Ericsson AB 2019. All Rights Reserved
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Copyright Ericsson AB 2004-2019. All Rights Reserved
(C) andalso (C >= 0 andalso C < 16#D800 orelse C > 16#DFFF andalso C < 16#FFFE orelse C > 16#FFFF andalso C =<
16#10FFFF)).
Copyright Ericsson AB 2007-2018. All Rights Reserved
Copyright Ericsson AB 1999-2019. All Rights Reserved
copyright 2001-2002 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @doc Type analysis of Core Erlang programs.
(C) when C:=46; C>47 andalso C<58; C>64 andalso C<91; C:=95; C>96 andalso C<123 -> C;llvm_id(C) -> io_lib:format("_~2.16.0B_",
[C]).
Copyright Ericsson AB 2017. All Rights Reserved
(C) end, case S of [$,$,\|Cs] -> "\\|" ++ Cs; [$$|Cs] -> Cs end;quote(maxchar, _File) ->
(C) end, List) of true -> {list, codepoints}; false -> case [deep || L <- List, Deep(L)] of [] -> {list,
{list, codepoints}}
Copyright &copy; ", year(), " <a href="http://www.erlang.org">", "Open Telecom Platform</a><br>\n", "Updated:
<!--date-->", current_time(), <!--/date-->", "<br>\n</f
Copyright Dustin Sallings, Michal Ptaszek, Scott Lystig Fritchie 2011-2017.
Copyright (C) 2001-2005 Richard Carlsson
Copyright (C) 2006 Richard Carlsson <carlsson.richard@gmail.com>
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;*-*so
copyright 2001-2006 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @end%%
=====
copyright(Es) ++ version(Es) ++ since(Es) ++ authors(Es) ++ references(Es) ++ sees(Es) ++ todos(Es) ++ FullDesc
++ [?NL, hr] ++ navigation("bottom") +
Copyright 2008, Ericsson Utvecklings AB. All * Rights Reserved." * * $Id$
Copyright 2013-2013 Tiler Corporation(jiwang@tilera.com). All rights reserved
Copyright Ericsson AB 2012-2020. All Rights Reserved
(C) when is_record(C, config) -> C;args_to_config(Args) when is_list(Args) -> do_args_to_config(Args, []).
(C) of none -> {none, [], []}; G -> vann(G, Env1) end, {Es,
{Bound2
(C) when C==duration_force_stop_skip_rest; C==until_force_stop_skip_rest -> dupl(2, start_logging()) ++ skip_first_tc1(r1_SUITE)
++ stop_logging());
Copyright (C) 1995-2016 Mark Adler * For conditions of distribution and use, see copyright notice in zlib.h */
Copyright (C) 1994 X Consortium
Copyright Ericsson AB 2015-2018. All Rights Reserved
copyright 1998-2014 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @end%%
=====
Copyright (C) 2000-2004 Mikael Pettersson
(C) ((Carrier_t
(C) of B when is_integer(B) -> %% More valid data, skip the eq as invalid case B3 of eq -> mime_decode_list(Cs, A, B1,
B2, B); -> mime
Copyright Ericsson AB 2018-2019. All Rights Reserved
Copyright: 2002-2018 Ericsson AB
Copyright Ericsson AB 2008-2020. All Rights Reserved
(C) end, OrigCs) andalso all(fun is_compiler_generated/1, OptCs) of true -> %% The original list of clauses did contain at least one
%% user-specified clause, but none of them will match.
(C) when byte_size(<<C/utf8>>) /= 42 -> ok;
Copyright Ericsson AB 2016. All Rights Reserved
Copyright (C) 1991, Ellementel Telecommunications Systems Laboratories%% File : ordsets.erl%% Author : Robert Virding%% Purpose : Functions for
manipulating sets as ordered lists.
Copyright (C) 2000-2005 by Ericsson Telecom AB%% SE-126 25 STOCKHOLM%% SWEDEN, tel int + 46 8 719 0000%% The program may
be used and/or copied only with the written%% permission from Eric
Copyright (C) 2000-2005 Mikael Pettersson%% This is the syntax of x86 r/m operands:%%
Copyright (C) 2004-2005, 2007-2009, 2011-2018 Free Software# Foundation, Inc.# Written by Gary V. Vaughan, 2004
Copyright (c) 2001-2008 Bjorn Gustavsson.
Copyright Ericsson AB 2011-2015. All Rights Reserved
Copyright (C) 1995-2017 Jean-loup Gailly and Mark Adler * For conditions of distribution and use, see copyright notice in zlib.h */
(C) of true -> scan_name(Cs, [C|Ncs]); false -> {Ncs,[C|Cs]} % Must rebuild here, sigh! end;scan_name([], Ncs) -> {Ncs,[]}.
(C) when ( ) -> false end, Cs).
Copyright (C) 2002-2018 Free Software Foundation, Inc.

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Copyright Ericsson AB 20016-2018. All Rights Reserved
(C) end end, ?eval_debug_fun({?MODULE, commit_participant, vote_yes},
[{tid, Tid}]), reply(Coord,
{vote_yes, Tid, self()}),
Copyright (C) 2006-2016 Richard Carlsson and Ericsson AB%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @author Dan
Gudmundsson <dgud@erix.ericsson.se>
Copyright &copy; ", year(), " <a href=\"http://www.erlang.org\">Open Telecom Platform</a>", xhtml("<br>\n", "<br />\n"), "Updated: <!--
date-->", current_time(), "<!--/date-->",
Copyright (c) 2002 by Erik Johansson.%%
=====%% Module : hipec_jit%% Purpose : %%
Notes : %% History : * 2002-03-14 Erik
(C) lstring_gen(Cs)
(C) of true -> scan_comment(Cs, St, Line, Col, Toks, [C|Ncs]); false -> Ncol = incr_column(Col, length(Ncs)+1),
scan_error({illegal, character}, Line
(C) ((unsigned char) (C)) #define INT(I) ((int) (I)) #define UINT(U) ((unsigned int) (U)) #define LONG(L) ((long) (L)) #define ULONG(L) ((unsigned
long) (L)) #define SZT(I) ((size_t) (I)) #def
(C) of P when is_pid(P) -> add_prim(C, Paren, DB2); _ -> DB2 end end, DB, CList);
(C) of {ok, {Source, Msg}} -> if (#client.verbose == true) -> i("received ~w bytes of
data~s",
(C) end, List) of true -> {list, binary}; false -> mixed end end; type({Number, String}) when is_number(Number) ->
%% to_integer or to_float typ
Copyright Ericsson AB 2008-2019. All Rights Reserved
(C) when C >= C + ($a - $A); to_lower1(C) -> C.
Copyright Ericsson AB 2000-2016. All Rights Reserved
Copyright (C) 1999-2001 Sven-Olof Nystr%% An efficient implementation of Prof. Arne Andersson's General%% Balanced Trees. These have no
storage overhead compared to plain%% unbalanced binary tre
(C) < ?MINFORINTSEARCHTREE of true -> gen_small_switch_val(Arg, C, Fail, VarMap, ConstTab, Options); _ ->
gen_search_switch_val(Arg, C, Fail, VarMap, ConstTab, Options) end; find_cluster([
(C) when is_record(C, config) -> ?d("~n", []), ?d("Populate database...~n", []), create_tables(C), Populate = fun() ->
populate_subscriber(write, C), populate_group
(C) when is_record(C, config) -> MonPid = spawn_link(?MODULE, monitor_init, [C, self()]), receive {'EXIT', MonPid, Reason} ->
exit(Reason); {monitor_done, MonPid, Res} -> Res e
Copyright (C) 1996-2018 Free Software Foundation, Inc.
Copyright (c) 2010 Torbjorn Tornkvist
Copyright Ericsson AB 2006-2016. All Rights Reserved
(C) when byte_size(<<C/utf32>>) /= 42 -> ok; do_guard(_) -> error.
copyright 2012 Richard Carlsson%% @doc Basic compiler in Erlang.
(C)' is a nonempty list.%% @see fun_expr/1%% @see fun_expr_clauses/1%% @see clause/3%% @see clause_patterns/1
Copyright (C) 1996, Ellemtel Telecommunications Systems Laboratories%% Author: Hakan Mattsson hakan@erix.ericsson.se%% Purpose: Evil
usage of the API%% Invoke all functions in the API and
Copyright (c) 1998-2011 The OpenSSL Project. All rights reserved
copyright/year <xsl:text>-</xsl:text>
(C) when is_integer(C) -> to_lower_char(C).
Copyright (C) 2004-2006 Micka
Copyright Ericsson AB 2001-2018. All Rights Reserved
Copyright (c) 1997-2014 University of Cambridge
Copyright Ericsson AB 1999-2016. All Rights Reserved
Copyright 2000, Ericsson Utvecklings%% AB. All Rights Reserved
(C) ((Ulong) ALC_TEST1(0x00a, (C)))
(C)%% takes a list of constraints as input and%% returns a list with the intersection of all extension roots%% and only the extension of the last
constraint kept if any%% extension in the last con
Copyright Ericsson AB 2004-2016. All Rights Reserved
Copyright 2002, Mobile Arts AB%% All Rights Reserved
(C) andalso mnesia_monitor:use_dir() of true -> if is_record(C, commit) -> sappend(latest_log, strip_snmp(C)); true -
> %% Either a commit record as binary %% or some
(C) of true -> {[C|Cs], Stack, N}; false -> over_non_word(Cs, [C|Stack], N+gc_len(C)) end; over_non_word([], Stack, N) -> {[], Stack, N}.
copyright 2002 Richard Carlsson, 2006 Richard Carlsson, Tobias Lindahl%% and Kostis Sagonas%% @author Richard Carlsson
<carlsson.richard@gmail.com>%% @author Tobias Lindahl <tobias.lindahl@gmail.com
Copyright Ericsson AB 2010-2016. All Rights Reserved
Copyright Ericsson AB 1997-2016. All Rights Reserved
(C)' is a nonempty list.%% @see function/2%% @see function_clauses/1%% @see clause/3%% @see clause_patterns/1
Copyright: 1995-2013, Mark Adler

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Copyright Ericsson AB 2020. All Rights Reserved
(C) of%% true ->%% ok;%% false ->%% wrong_type(errorCode, C, C)%% end;%% chk_ErrorCode(C1, C2) ->%% case
(is_ErrorCode(C1) andalso is_ErrorCode(C2)) of%% true ->%% n
copyright 2003-2006 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @see edoc%% @end%%
=====
Copyright Ericsson AB 2013. All Rights Reserved
(C) when is_integer(C), 16 C - 32;to_upper_char(C) -> C.
Copyright (C) 2006-2018 Free Software Foundation, Inc.
Copyright (C) 2011 Free Software Foundation, Inc.This config.lt script is free software; the Free Software Foundationgives unlimited permission to copy,
distribute and modify it."
Copyright Ericsson AB 2012-2016. All Rights Reserved
Copyright (c) 2008,2009 Robert Virding. All rights reserved
Copyright Ericsson AB 1998-2018. All Rights Reserved
Copyright Ericsson AB 2000-2020. All Rights Reserved
Copyright: 1995-2013, Jean-loup Gailly.
(C) when to_lower_ascii(C) -> C.
Copyright (C) 2004-2005, 2007-2008, 2011-2018 Free Software# Foundation, Inc.# Written by Gary V. Vaughan, 2004
(C) of true -> [P] = cerl:clause_pats(C), case F(P) of true -> is_switch(Cs, F, N + 1); false ->
%% The final clause may be a catch-all. Cs := [] andalso N > 1 andals
Copyright Ericsson AB 2005-2018. All Rights Reserved
(C) of {L,C2} -> F(F,C2,A+length(L)); '$end_of_table' -> A end end, H=fun({L,C}) ->
G(G,C,length(L)) end,
copyright 2006 Richard Carlsson%% @private%% @see eunit%% @doc Parse transform for automatic exporting of test functions.
(C) or else C ==
(C) ((Carrier_t#define ABLK_HDR_SZ ((Ulong) ALC_TEST0(0x016))#define MIN_BLK_SZ(A) ((Ulong) ALC_TEST1(0x017,
(A)))#define NXT_BLK(B) ((Block_t *) ALC_TEST1(0x018, (B)))#define PREV_BLK(B) ((B
Copyright Ericsson AB 2014-2016. All Rights Reserved
Copyright Ericsson AB 2008-2016. All Rights Reserved
Copyright: Ericsson AB 2011-2016
Copyright (C) 1996, Ericsson Telecommunications# Author: Lars Thorsen# -----SUFFIXES: .hrl .erl .jam .beam
Copyright (c) 2007-2012, Google Inc.All rights reserved
Copyright Dustin Sallings, Michal Ptaszek, Scott Lystig Fritchie 2011-2018.
(C) when is_hex_digit(_) -> false.
Copyright Ericsson AB 1996-2018. All Rights Reserved
(C) ((void) ALC_TEST1(0xf0e, (C)))#define THR_COND_WAIT(C, M) ((void) ALC_TEST2(0xf0f, (C), (M)))#define
THR_CREATE(F, A) ((erts_thread) ALC_TEST2(0xf10, (F), (A)))#define THR_JOIN(T) ((void) A
Copyright (c) 1997-2017 University of Cambridge
Copyright (C) 1996-2018 Free Software Foundation, Inc.
Copyright (c) 1997-2004 University of Cambridge AngelsHolocaust 4-11-04 updated to use version v5.0 (INFO: this is regex-directed, NFA) AH: 9-11-
04 - pcre_free: removed var, pcre already g
(C) of undefined -> erlang:error(badarg, [CCs,Acc]); Opt -> Type = type_ifopt(Opt), {Val,Rest} = dec_value(Type, Cs),
build_ifaddrs_opts(Rest, [{Opt,Val}|Acc]) end. buil
copyright 2010-2015 Richard Carlsson%% @doc Metaprogramming in Erlang.%% Merl is a more user friendly interface to the `erl_syntax' module,
making%% it easy both to build new ASTs from scratch a
(C) when C - $a + 10.
Copyright &copy; ", year(), " <a href="http://www.erlang.org\">
"Open Telecom Platform</a><br />\n", "Updated: <!--
date-->", current_time(), "<!--/date-->", "<br />\n</div>\n"], CS
Copyright Ericsson AB 2003-2018. All Rights Reserved
Copyright (C) 2010-2018 Bootstrap Authors
Copyright: 1994, X Consortium
copyright 2001-2005 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @see edoc%% @end%%
=====
(C) end, List) of true ->
copyright (C) 2015, Simon Cornish%% @doc%% Provide manipulatable TCP-level relaying for testing SSH%% @end%% Created : 7 May 2015
by Simon Cornish <simon@cali.coffee>%% -----
copyright 2000-2006 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @end%%
=====
Copyright remains Eric Young * the code are not to be removed. * If this package is used in a product, Eric Young should be given attribution * as the
author of the parts of the library used. * Th
Copyright (C) 1999-2018 Free Software Foundation, Inc.
Copyright (C) 1996-2001, 2003-2018 Free Software Foundation, Inc.# Written by Gordon Matzigkeit, 1996

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Copyright (C) 1995-2003, 2010 Mark Adler * For conditions of distribution and use, see copyright notice in zlib.h */
Copyright Ericsson AB 2009-2018. All Rights Reserved
Copyright Ericsson AB 2002-2019. All Rights Reserved
(C)|opt_guard_try_list(Cs)opt_guard_try_list([]) -> [].
(C) when C ==word_char([_]) -> true; %% Is graphemeword_char(_) -> false.
Copyright Ericsson AB 2016-2016. All Rights Reserved
Copyright Ericsson AB 2016-2017. All Rights Reserved
copyright 1999-2002 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @doc Utility functions for Core Erlang abstract
syntax trees.%%<p>Syntax trees are defined in the
(C) of true -> ok; false -> wrong_type(errorCode, C, C) end;chk_ErrorCode(C1, C2) -> case (is_ErrorCode(C1) andalso
is_ErrorCode(C2)) of true -> not_equal(errorCode, C1, C2)
Copyright (c) 2001, Alexander Tokarev All rights reserved
Copyright Ericsson AB 2009-2017. All Rights Reserved
(C) of true -> all_vars(Cs); false -> false end;all_vars([]) -> true.
Copyright (C) 2000, 2004, 2007 Mikael Pettersson
Copyright Ericsson AB 1997-2020. All Rights Reserved
copyright 2004 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @doc Provides a better way to start Dialyzer from a
script.
Copyright for zlib itself see copyright notice in zlib.h
Copyright (c) 2010-2012
Copyright Ericsson AB 2002-2017. All Rights Reserved
Copyright (C) 1995-2011, 2016 Mark Adler * For conditions of distribution and use, see copyright notice in zlib.h */
Copyright (c) 2010-2013
Copyright: 1992-2015, Free Software Foundation, Inc.
Copyright Ericsson AB 2017-2019. All Rights Reserved
Copyright (C)% 2007 S.G. Consulting srl. All Rights Reserved
Copyright Ericsson AB 2001-2016. All Rights Reserved
Copyright (C) 1995-2017 Mark Adler * For conditions of distribution and use, see copyright notice in zlib.h */
Copyright Ericsson AB 2001-2015. All Rights Reserved
Copyright: 1997-2017, University of Cambridge
(C) of true -> {Head, [], []};false -> {NewC, MaxInserts, PerKey} = dets_utils:reset_cache(C), %% MaxNoInsertedKeys is an upper limit
on the number of new keys. MaxNoInsertedKeys = e
Copyright (C) 2009-2018 Free Software Foundation, Inc.
(C) > 2 -> ok;csemi7(_, _, _) -> error.
Copyright Ericsson AB 2016-2018. All Rights Reserved
copyright 2007 Richard Carlsson%% @private%% @see eunit%% @doc External tests for eunit.erl
Copyright Ericsson AB 2002-2016. All Rights Reserved
(C) when C >= 0, C <= 255 -> [$%, hex_digit(C bsr 4), hex_digit(C band 15)].
Copyright Ericsson AB 2007-2020 All Rights Reserved
Copyright (C) 2007-2008 Mikael Pettersson
(C) of [] ->
(C) when ($\n) -> true; (_) -> false end, L).
Copyright (C) 1999-2002 Richard Carlsson.
Copyright Ericsson AB 2009-2019. All Rights Reserved
Copyright Ericsson AB 2014-2020. All Rights Reserved
copyright 2006 Richard Carlsson, Eric Merritt%% @private%% @see eunit%% @doc Parse transform for stripping EUnit test functions.
Copyright Ericsson AB 2006-2020. All Rights Reserved
Copyright Ericsson AB 2013-2020. All Rights Reserved
copyright 2000-2003 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @doc Support functions for property
lists.%%<p>Property lists are ordinary lists containing entrie
(C) AC_COMPILE_IFELSE( #define SLJIT_CONFIG_AUTO 1 #include "sljit/sljitConfigInternal.h" #if (defined SLJIT_CONFIG_UNSUPPORTED &&
SLJIT_CONFIG_UNSUPPORTED) #error unsupported #endif]],
copyright 2011-2014 Yiannis Tsiouris <gtsiour@softlab.ntua.gr>,%%% Chris Stavrakakis <hydralisk.r@gmail.com>%% @author
Yiannis Tsiouris <gtsiour@softlab.ntua.gr>%% [http:/
Copyright Ericsson AB 2020-2020. All Rights Reserved
copyright 2006 Richard Carlsson%% @private%% @see eunit%% @doc Interpretation of symbolic test representation
Copyright Ericsson AB 2017-2018. All Rights Reserved
(C) >< ~p: [Class,Reason,Stacktrace]), erlang:raise(Class, Reason, Stacktrace) end end,
[link,monitor]), receive
Copyright (c) 1991-1994 The Regents of the University of California.
Copyright Ericsson AB 2010-2017. All Rights Reserved

```

```

(C) when list(C) -> filter_extensions(C, []).
copyright/year      <xsl:text> </xsl:text>      <xsl:value-of select="header/copyright/holder"/>      </p>      </div>
(C) when is_integer(C), 16 C - 32;
Copyright Ericsson AB 2015-2015. All Rights Reserved
(C) when is_integer(C) -> to_upper_char(C).
Copyright (c) 2008 Steven G. Johnson <stevenj@alum.mit.edu>
Copyright (C) 1995-2003, 2010, 2014, 2016 Jean-loup Gailly, Mark Adler * For conditions of distribution and use, see copyright notice in zlib.h */
(C) was encoded wrong-- when C was larger than 16. There was also an error when encoding-- in compact_bit_string mode.
Copyright (c) 2003 by Erik Stenman.%%
=====%% Filename : hipe_icode_pp.erl%%
Module : hipe_icode_pp%% Purpose : Pretty-printe
copyright 2000-2004 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @doc Closure conversion of Core Erlang
modules. This is done as a%% step in the translation from Core
copyright 2011-2014 Yiannis Tsiouris <gtsiour@softlab.ntua.gr>,%%% Chris Stavrakakis <hydralisk.r@gmail.com>,%%%
Kostis Sagonas <kostis@cs.ntua.gr>%%%% @aut
Copyright 2003 and onwards Google Inc.// Author: Sanjay Ghemawat
copyright 2004-2007 Micka%% @author Mickaël Rémond <mickael.remond@process-one.net>%% [http://www.process-one.net/]%% @author
Richard Carlsson <carlsson.richard@gmail.com>%% @private%% @see eun
Copyright Ericsson AB 2003-2019. All Rights Reserved
(C) when $a =< C, C =< $z -> true;is_alpha(_) -> false.
(C) when C C#commit.disc_only_copies == [], C#commit.schema_ops == [] -> ignore;log(C) -> case mnesia_monitor:use_dir()
of true -> if record(C, comm
(C) | expand_comments(Cs)expand_comments([]) -> [].
copyright 2012 Richard Carlsson%% @doc Tests. For including in another module.
(C) of [_P] -> G = cerl:clause_guard(C), case cerl_clauses:eval_guard(G) of {value, true} -> true; _ -> false end;
_ -> false end.
copyright 1999-2002 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @doc Core Erlang abstract syntax
trees.%%%%<p> This module defines an abstract data type for represe
copyright> <year>2003</year><year>2016</year> <holder>Ericsson AB. All Rights Reserved
Copyright Ericsson AB 2001-2017. All Rights Reserved
Copyright Zoltan Herczeg (hzmester@freemail.hu). All rights reserved
(C) of true -> auth:set_cookie(Node, C); false -> erlang:error(badarg) end.
Copyright (c) 1998 by Erik Johansson. All Rights Reserved
copyright 2000-2006 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @doc Translation from Core Erlang to HiPE
Icode.
copyright 2009 Richard Carlsson%% @private%% @see eunit%% @doc Generic listener process for eunit.
Copyright Ericsson AB 2008-2017. All Rights Reserved
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Copyright (C) 1995-2005, 2010 Mark Adler * For conditions of distribution and use, see copyright notice in zlib.h */
copyright>%%<legalnotice>
(C) when is_integer(C) and (0 =< C) and (C =< 65535) -> C;cre_ErrorCode(C) -> exit({invalid_ErrorCode, C}).
copyright 2001-2002 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @doc Closure analysis of Core Erlang
programs.
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(C) when C >=upper(C) -> C.
Copyright (c) 1997-2016 University of Cambridge
Copyright 2007, Ericsson AB. * All Rights Reserved
Copyright (C) 1995-2005, 2014, 2016 Jean-loup Gailly, Mark Adler * For conditions of distribution and use, see copyright notice in zlib.h */
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Copyright Ericsson AB 2007-2015. All Rights Reserved
Copyright Ericsson AB 2019-2020. All Rights Reserved
Copyright 1995-2017 Jean-loup Gailly and Mark Adler "/* If you use the zlib library in a product, an acknowledgment is welcome in the documentation
of your product. If for some reason you cannot
copyright 2001-2003 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @see edoc%% @end%%
=====
(C) of true -> C; false -> none end end.
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(C)#define DEBUG_CHECK_ALIGNMENT(P)#endif

```



```

Copyright: 1995-2013, Jean-loup Gailly and Mark Adler
Copyright (c) 1994-1996 Sun Microsystems, Inc.
(C) of B when is_integer(B) -> %% More valid data, skip the eq as invalid case B3 of eq ->
mime_decode_list_to_string(Cs, B1, B2, B);
(C) or else is_binary(C) end, L); (L) -> false end,
(C) when integer(C), 0 =< C, C =< 65535 -> %% C; %% cre_ErrorCode(C) -> %% exit({invalid_ErrorCode, C}).
Copyright (C) 2005, 2008, 2010-2011 Free Software Foundation, Inc.dnl This file is free software; the Free Software Foundationdnl gives unlimited
permission to copy and/or distribute it,dnl with or
(C) when is_integer(C) andalso (0 =< C) andalso (C =< 65535) -> C; cre_ErrorCode(C) -> exit({invalid_ErrorCode, C}).
Copyright (c) 2001 by Erik Johansson. All Rights Reserved
(C) of ws -> only_ws_binary(Cs, A) end.
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Copyright Ericsson AB 1999-2017. All Rights Reserved
Copyright Ericsson AB 2001-2019. All Rights Reserved
(C) of true -> skip_comment(Cs, St, Line, Col, Toks, N+1); false -> Ncol = incr_column(Col, N+1),
scan_error({illegal,character}, Line, Col, Line, N
copyright 2006 Richard Carlsson%% @private%% @see eunit%% @doc Test running functionality
Copyright Ericsson AB 2018. All Rights Reserved
copyright 2000-2003 Richard Carlsson, 2006-2009 Tobias Lindahl%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @author Tobias
Lindahl <tobias.lindahl@gmail.com>%% @author Kostis Sagonas <
(C) of true -> %% The fail label will not actually be used in this case. clause(C, F, Vs, Fail, Next, Ctxt, Env, S); false -> %% The
previous 'Fail' is not used here. Fail1 = new
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Copyright Ericsson AB 1996-2018
Copyright Ericsson AB 2011-2017. All Rights Reserved
Copyright (C) 1995-1996 Jean-loup Gailly. * For conditions of distribution and use, see copyright notice in zlib.h */ * %ExternalCopyright% */
Copyright (C) 2004 Daniel Luna%% This is the syntax of amd64 r/m operands:%%
Copyright (C) 2005%% by ERICSSON TELECOM AB%% S - 125 26 STOCKHOLM%% SWEDEN, tel int + 46 8 719 0000%% The program
may be used and/or copied only with the written%% permission from ERIC
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Copyright Ericsson AB 1998-2016. All Rights Reserved
(C) when is_integer(C), C + 32;
(C) or else is_upper(C) or else is_digit(C).
Copyright (c) 1997-2018 University of Cambridge
Copyright Ericsson AB 2003-2016. All Rights Reserved
copyright 1999-2004 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @doc Abstract environments, supporting self-
referential bindings and%% automatic new-key generation.
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Copyright: 2001-2007 Richard Carlsson
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(C)' is a nonempty list.%% @see named_fun_expr/2%% @see named_fun_expr_clauses/1%% @see clause/3%% @see clause_patterns/1
copyright 2006 Richard Carlsson%% @private%% @see eunit%% @doc Test runner process tree functions
Copyright Ericsson AB 2011-2016. All Rights Reserved
copyright 2012 Richard Carlsson%% @doc Making it simple to build a module with merl
copyright 2001-2007 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @version {@version}%% @end%%
=====
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Copyright (C) 2004-2005, 2007, 2009, 2011-2018 Free Software# Foundation, Inc.# Written by Scott James Remnant, 2004.
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(C) bor (D)) band (bnot 16
copyright>%% <year>2000-2008</year>%% <holder>Ericsson AB, All Rights Reserved
Copyright Ericsson AB 2015. All Rights Reserved
Copyright: 1991-1992 RSA Data Security, Inc.
(C) of true -> [{ignore, C}|Cs]; _ -> [C|Cs] end end.
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```

```

(C) end, class => undoc_user_option },
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Copyright: 2011-2014, Yiannis Tsiouris <gtsiour@softlab.ntua.gr>
Copyright 1999-2000, Ericsson%% Utvecklings AB. All Rights Reserved
Copyright (c) 2002 by Niklas Andersson, Andreas Lundin, and Erik Johansson.%%
=====%% Module :
hipe_spillmin_scan%% Purpose
Copyright Ericsson AB 2011-2020. All Rights Reserved
Copyright Ericsson 2015-2016. All Rights Reserved
Copyright /# 4. Immediately followed by any lines between the previous matches,# except lines preceding the intervening completely blank line.#
For example, see the header comments of this f
(C) end, Ec = #iclude{anno=#a{anno=[compiler_generated|LA]}, pats=[c_tuple(Evs)],guard=[#c_literal{val=true}], body=
[#iprimop{anno=#a{}, %Must have an #a{}} name=#c_literal{va
Copyright Ericsson AB 2007-2020. All Rights Reserved
(C) when is_integer(C), 16 C + 32;to_lower_char(C) -> C.
Copyright &copy; ", year(), " <a href="http://www.erlang.org">Open Telecom Platform</a>", xhtml("<br>\n", "<br />\n"), "Updated: <!--
date-->", current_time(), "<!--/date-->", xht
Copyright Ericsson AB 2015-2019. All Rights Reserved
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Copyright Ericsson AB 2000-2020 All Rights Reserved
copyright 2001-2006 Richard Carlsson%% @see edoc%% @end%%
=====
(C) ((Ulong) ALC_TEST1(0x009, (C)))
(C) ((void) ALC_TEST1(0xf0d, (C)))
(C) of true -> C; false -> generate_automatic_tags1(C,TagNo) end.
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(C) of true -> expr_receive_2(C, E, F, Ctxt, Env, S); false -> error_msg("receive-
expression clause " "must be a catch-all."), throw(error) end; ->
copyright 1997-2006 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @end%%
=====
(C) of [P] -> case cerl:is_c_binary(P) of true -> is_binary_switch1(Cs, N + 1); false -> %% The final
clause may be a catch-all. Cs =:= [] andalso N > 0 andalso cerl:type(
Copyright (C) 1995-2017 Jean-loup Gailly * For conditions of distribution and use, see copyright notice in zlib.h */
copyright 1999-2002 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @doc Utility functions for Core Erlang
case/receive clauses.%%>>>Syntax trees are defined in the m
(C) > 2 end, Cs), FD, Skip)
(C) when is_record(C, config) -> ?d("~n", []), ?d("Actual configuration...~n", []), ?d("~n", []), Fields = record_info(fields, config), [config |
Values] = tuple_to_list(C), di
(C) of true -> scan_name(Cs, [C|Ncs]); false -> {Ncs,[C|Cs]} %Must rebuild here, sigh! end;scan_name([], Ncs) -> {Ncs,[]}.
(C) | hh_field(Cs)
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Brown <rbrown64@csc.com.au> for his contribu
copyright 2012-2015 Richard Carlsson%% @doc Unit tests for merl.%% @private
(C) of B when is_integer(B) -> %% More valid data, skip the eq as invalid case B3 of eq -> mime_decode_binary(Cs, A,
B1, B2, B); -> mi
copyright bla bla bla" \%% @private" \%% foo" \%% @author X.Y.\".
Copyright Ericsson AB 2011-2013. All Rights Reserved
(C) of [] -> %% No active catch. Assume cont./fail labels are correct as is. {I, Is, State0}; L -> %% Only update the fail label if the call
*can* fail. case hipe_icode
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Prepares Core Erlang code%% for translation to ICode.%% @see
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(C) andalso is_boolean(cerl:atom_val(C)), yes_no(B) end.
(C) of binary -> C; map -> C; _ ->
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```

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copyright>%%<year>1996-2007</year>%%<holder>Ericsson AB, All Rights Reserved
(C) when C      C#commit.disc_only_copies == [],      C#commit.schema_ops == [] -> ignore; slog(C) -> case mnesia_monitor:use_dir()
of true -> if record(C, com
(C) < (void ASSERT((void *) (P) \      < (void *) (((char *) (C)) + (A)->chunks_mem_size)); \}
while (0)#else#define ERTS_SPPA_DBG_CHK_IN_CHNK(A, C, P)#endif
Copyright (C)%%>> 2007 S.G. Consulting srl. All Rights Reserved
Copyright (C) 2011-2018 Free Software Foundation, Inc.
Copyright Ericsson Telecom AB",-1,40,25,      119,8 DEFPUSHBUTTON "OK",IDOK,178,7,32,14,WS_GROUPEND
(C) C#else# define RBT_ASSERT(x)
(C) end), ListData = lists:duplicate(1000, $a), F = fun() -> receive {tcp, _Sock, _Data} -> inet:setopts(A,
[{active, once}], Res = gen_tcp:send(A, ListData), Res; Err
Copyright (C) Tobias Lindahl <tobiasl@it.uu.se>\n" " Kostis Sagonas <kostis@it.uu.se>\n\n", output_sms(State, "About Dialyzer",
Message, info), gui_loop(State); %% -
Copyright Ericsson AB 2006-2018. All Rights Reserved
copyright-format/1.0/Files-Excluded: lib/diameter/doc/standard lib/megaco/doc/standard lib/ftp/doc/archive lib/inets/doc/archive
lib/common_test/priv/jquery-latest.js lib/common_test/priv/jquery
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copyright/author)%% - @history (never properly updated; use version control etc.)%% - @category (useless; superseded by keywords or free text
search)
Copyright Ericsson AB 1999-2018. All Rights Reserved
copyright 2006 Richard Carlsson%% @private%% @see eunit%% @doc Event serializing and multiplexing process, to be used as the%% main
"supervisor" process for an EUnit test runner. See eunit_proc%%
(C) when (C) -> C end, String).
(C) of _ -> Ok catch Cls:Exc -> ct:pal("~p::~p ~p", [Cls, Exc, C]),
copyright 2003 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @see edoc%% @end%%
=====
Copyright Ericsson AB 2001-2020. All Rights Reserved
(C) < ?MINFORJUMPTABLE, length(D) < R = {sparse, Min, Max, C ++ D}, cluster_merge([R|T]); cluster_merge({{dense, Min, _, D}, {sparse, _, Max, C}|T})
when length(D) < ?MINFORJUMPTABLE -> R = {sparse, Min, Ma
(C) of false -> percent_encode_binary(C); true -> <<C>> end.
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Copyright 1999, Ericsson Utvecklings * AB. All Rights Reserved
copyright> <legalnotice>
copyright string in the executable. */
(C) when C==duration_force_stop_skip_rest_group; C==until_force_stop_skip_rest_group -> dupl(2, start_logging()) ++
skip_tc1_in_group(r1_SUITE) ++ stop_logging().
Copyright (C) 1995-2017 Jean-loup Gailly * detect_data_type() function provided freely by Cosmin Truta, 2006 * For conditions of distribution and use,
see copyright notice in zlib.h */
Copyright (C) 2005 Mikael Pettersson%% Implementation Notes:
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(C) andalso (C >= $000 andalso C = < $s orelse C >= $200 andalso C = < $240).-define(DIGIT(C), C >= $0, C = < $9).-define(CHAR(C),
is_integer(C), C >= 0).-define(UNICODE(C),
Copyright © 2004 Scott James Remnant <scott@netsplit.com>.
Copyright Ericsson AB 1997-2018. All Rights Reserved
copyright (#PCDATA)>%%<!ELEMENT deprecated (description)>%%<!ELEMENT see (#PCDATA)>%%<!ATTLIST see%% name CDATA
#REQUIRED%% href CDATA #IMPLIED>%%<!ELEMENT reference (#PCDATA)>%%<!ELEM
Copyright Ericsson 2017-2020. All Rights Reserved
Copyright Scott Lystig Fritchie 2011-2016. All Rights Reserved
(C) of [] -> false; Es -> E = lists:last(Es), case erl_syntax:type(E) of tuple -> Ts =
erl_syntax:tuple_elements
(C) when tuple_size(C) == 4 andalso element(4, C) == PRF -> true; (_) -> false end, ssl:cipher_suites().
(C) | Nodes L < Min -> %% The comment line should be above this node. %% (This duplicates what insert/5 would have done.)
[node_add_precomment(C, N) | Ns]; Col = < 1, L = <

```

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(C) of none -> ?PADDING; P1 -> P1 end,
beside(text(spaces(P)), D); false -> D end, case Cs of [] -> D1; % done _ -> above(D1, stack_comm
Copyright Ericsson AB 2002-2018. All Rights Reserved
(C) when C - $A + 10;
Copyright (c) 2005, Google Inc.All rights reserved
(C) when C >=name_char($_) -> true;name_char($@) -> true;name_char(_) -> false.
copyright 2006 Richard Carlsson%% @private%% @see eunit%% @doc EUnit server process
(C) of {L,C2} -> F(F,C2,lists:reverse(L)++A); 'end_of_table' -> lists:reverse(A) end end,
K = fun({L,C}) -> J(J,C,lists:reverse(L)) end, M = fun(_,_, 0) -> ok;
Copyright Ericsson AB 2007-2016. All Rights Reserved
Copyright (c) 2015-2016 Kenji Rikitake%% expop (xoroshiro116+) added, statistical distribution%% improvements and uniform_real added by the
Erlang/OTP team 2017
Copyright Ericsson AB 2010-2018. All Rights Reserved
copyright 2003 Richard Carlsson%% @author Richard Carlsson <carlsson.richard@gmail.com>%% @see edoc%% @see edoc_parse_ref%% @end%%
=====
(C) of false -> true; _ -> collect_and_sort_tags(C,[]) end; _ -> collect_and_sort_tags(C,[]) end.
(C) when C - $0;
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eudev v3.2.10 alpine:eudev-dev/3.2.10-r1/aarch64
Copyright 2008-2012 Kay Sievers <kay@vrfy.org>
Copyright (C) 2003-2010 Kay Sievers <kay@vrfy.org>
Copyright (C) 2003 Greg Kroah-Hartman <greg@kroah.com>
EV 4.33 debian:libev-perl/4.33-1+b1/arm64
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copyright-format/1.0/Upstream-Name: EVUpstream-Contact: Marc Lehmann <schmorp@schmorp.de>Source: https://metacpan.org/release/EV
File Roller 0.8 debian:file-roller/0.8-1
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)#endif
(C) ((C) == DIRECTORY_SEPARATOR)#endif
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Copyright (C) 2000-2001 Free Software Foundation, Inc.
Copyright (C) 2000, 2001 Free Software Foundation, Inc.
Copyright (C) 2001 The Free Software Foundation, Inc.
Copyright (C) 2001 The Free Software Foundation, Inc.", authors, _("An archive manager for GNOME."),
NULL);
Copyright (C) year name of author
copyright note follows : *
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
findutils 4.6.0+git+20190209 ubuntu:findutils/4.6.0+git+20190209-2
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 copyright date in# Do the same for the \$(sample-test) and the main doc/.texi file.sc\_copyright\_check:  
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 Copyright (C) 2005, 2007-2019 Free Software Foundation, Inc.  
 (C) (C)# ifdef \_LIBC# define STRLEN(S) \_\_wcslen (S)# define STRCAT(D, S) \_\_wcscat (D, S)# define MEMPCPY(D, S, N) \_\_wmempcpy (D, S,  
 N)# else# define STRLEN(S) wcslen (S)# defin  
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(C) tests whether C is a directory separator character. IS\_ABSOLUTE\_PATH(P) tests whether P is an absolute path. If it is not, it may be concatenated to a direct  
(C) ((C) ==#else# define FILE\_SYSTEM\_PREFIX\_LEN(Filename) 0  
(C) to the copyright symbol  
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copyright line is 72.# Set UPDATE\_COPYRIGHT\_MAX\_LINE\_LENGTH to use a different length.# 5. Set UPDATE\_COPYRIGHT HOLDER if the copyright holder is other# than "Free Software Foundation,  
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findutils v4.7.0 debian:findutils/4.7.0-1
(C) add (1, #endif)
(C) add (1, fputc (C, p))#else
(C)# B. (c)
(C) __btowc (C)# else
(C) __btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef _LIBC# define MEMPCPY(D, S, N) __mempcpy (D, S, N)# else# if HAVE_MEMPCPY# define MEMPCPY(D, S, N) memcpy (D, S, N)# endif# define HAS_DEVICE(P) \
(((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \
&& (P)[1] == ':'# define IS_ABSOLUTE_PATH(P) (P[0] == '/' || HAS_DEVICE (P))# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define FILE_SYSTEM_PREFIX_LEN(P) 0# endif
(C) ((C) == #else# define FILE_SYSTEM_PREFIX_LEN(FileName) 0
(C) ((C) == #endif
(C) (C)# ifdef _LIBC# define STRLEN(S) __wcslen (S)# define STRCAT(D, S) __wcscat (D, S)# define MEMPCPY(D, S, N) __wmempcpy (D, S, N)# else# define STRLEN(S) wcslen (S)# define STRCAT(D, S) wcscat (D, S)# endif
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copyright goes to the FSF.
copyright information, for version-etc.
copyright in $_sc_search_regexp)
copyright in $_sc_search_regexp)
copyright line above was added in 2015 based on James# Youngman's best estimate of the original publication date of this# file (it appeared in findutils
release 4.1.7 but may have appeared# in earl
copyright line is 72.# Set UPDATE_COPYRIGHT_MAX_LINE_LENGTH to use a different length.# 5. Set UPDATE_COPYRIGHT HOLDER if the
copyright holder is other# than "Free Software Foundation,
Copyright-paperwork-exempt: Yes
copyright: prereq# By default, exclude all variants of COPYING; you can also# add exemptions (such as ChangeLog.* for rotated change logs)# in the

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file .x-update-copyright.
copyright program gets found.PATH=$abs_aux_dir:$PATHexport PATH
Copyright/request-assign.changes:
Copyright/request-assign.changes;hb=HEAD
Copyright/request-assign.future:
Copyright/request-assign.future;hb=HEAD
copyright=`sed -n
Copyright /{/s/ grep .` || exit 1; \ sed \ -e "s,@""bindir""@,$(bindir)," \ -e "s,@""libexecdir""@,$(libexecdir)," \ -e
"s,@""LOCATE_DB""@,$(LOCATE_DB)," \ -e "s,@""VERSION""@,$(VERSION)," \ -
copyright symbol suitable for this locale, and
copyright target rule# in maint.mk from gnu/lib's maintainer-makefile module.
copyright-tests
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1compare - $TMP <<EOF || exit 1
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1compare - $TMP <<EOF || exit 1 Undisturbed
text.dnl Undisturbed text.
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1tr @ '\015' > $TMP-exp <<\EOF
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare - $TMP-stderr <<EOF || exit 1$TMP: warning: copyright statement not
foundEOFcompare - $TMP <<EOF || exit 1
copyright $TMP.compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1compare - $TMP.star <<EOF || exit 1
copyright $TMP.compare /dev/null $TMP-stdout || exit 1compare - $TMP-stderr <<EOF || exit 1$TMP.4: warning: copyright statement not found$TMP.5:
warning: copyright statement not foundEOFcompare
COPYRIGHT/' | tr -d compare $TMP - || ERR=1
copyrightupdate-copyright:
copyright \# verify \# version-etc \# version-etc-fsf \# warnings \# wchar \# wwidth \# xalloc \# xalloc-die \# xgetcwd \# xstrtod \# xstrtol \#
xstrtroumax \# year2038 \#
copyright verify version-etc version-etc-fsf warnings wchar wwidth xalloc xalloc-die xgetcwd xstrtod xstrtol xstrtroumax year2038
yesno]gl_AVOID([)]gl_SOURCE_BASE([gl/
copyright year.
copyright year intervals in a reformatted FSF copyright statement# are expanded instead.# If UPDATE_COPYRIGHT_USE_INTERVALS=2, convert
a sequence with gaps# to the minimal containing
copyright year intervals. (See "Environment variables"# below.)
copyright year is 2 or 4 digits, and years are separated by# commas or dashes. Whitespace may appear after commas.
copyright year lists in your project.# If you have an additional project-specific rule,
copyright year list to include the current year.
copyright years (such as 90, 1991, 1992-2007, 2008) in a# reformatted FSF copyright statement is collapsed to a single# interval (such as 1990-
2008). If unset or set to 0, all existing
(C)" takes one of the following forms or is omitted# entirely:
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C)" to the copyright symbol
(C) ((unsigned int) (C) <= 'z' - 'a')# define FILE_SYSTEM_PREFIX_LEN(FileName) \ (_IS_DRIVE_LETTER ((FileName)[0]) &&
(FileName)[1] == ':' ? 2 : 0)# ifndef _
©);my$holder ||= 'Free Software Foundation, Inc. ';my $prefix_max = 5;my $margin = $ENV{UPDATE_COPYRIGHT_MAX_LINE_LENGTH};!$margin ||
$margin !~ m/^\d+$/ and $margin = 72;
findutils v4.7.0 openseuse:findutils/4.7.0-6.9/s390x
(C) add (1,#endif
(C) add (1, fputc (C, p))#else
(C)# B. (c)
(C) __btowc (C)# else
(C) btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef _LIBC# define MEMPCPY(D, S, N) __mempcpy (D, S,
N)# else# if HAVE_MEMPCPY# define MEMPCPY(D
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define
FILE_SYSTEM_PREFIX_LEN(P) 0# endif
(C) ((C) ==#else# define FILE_SYSTEM_PREFIX_LEN(FileName) 0
(C) ((C) ==#endif
(C) (C)# ifdef _LIBC# define STRLEN(S) __wcslen (S)# define STRCAT(D, S) __wscat (D, S)# define MEMPCPY(D, S, N) __wmempcpy (D, S,
N)# else# define STRLEN(S) wcslen (S)# defin
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  Copyright \{C\} in_vc_files=$(sample-test)
  copyright\{\}\|\|\|\|co
copyright: # Code from module update-copyright-tests: # Code from module useless-if-before-free:
Copyright &copy; 1990, 2005, 2007, 2008, 2009, 2010 Free Software * Foundation, Inc. */EOFcompare - $TMP.4 <<EOF || exit 1
Copyright &copy; 1990, 2005, 2007-2010 Free Software Foundation, Inc. */EOFcompare - $TMP.4 <<EOF || exit 1

```

```

Copyright &copy; 90,2005,2007-2009# * Free Software Foundation, Inc.# */
Copyright &copy; 90,2005,2007-2009 * Free Software Foundation, Inc. */EOFcat > $TMP.4 <<EOF
Copyright &copy; 90,2005,2007-2009 * Free Software Foundation, Inc. */EOFcompare - $TMP.4 <<EOF || exit 1
Copyright @copyright1998, 1999, 2000, 2001, 2002, 2003, 2004, 2005, 2007, 2008, 2009, 2010Free Software Foundation, Inc.EOFcompare - $TMP.2
<<EOF || exit 1
copyright{copyright \defglyph\dots{...}% \defglyph\enddots{...}% \defglyph\equiv{==}% \defglyph\error{error}% \defglyph\euro{euro}%
\defglyph\expansion{==>}% \defglyph\geq{>=}% \defgly
Copyright @copyright# Foundation, Inc.
Copyright @copyrightFoundation, Inc.EOFcat > $TMP.2 <<EOF
Copyright @copyrightFoundation, Inc.EOFcompare - $TMP.2 <<EOF || exit 1
Copyright @copyrightInc.EOFcompare - $TMP.2 <<EOF || exit 1
Copyright @copyright\in_vc_files=$(texi) \
Copyright (C)", then a list of copyright years,# and then the name of the copyright holder.
copyright{# D.
copyright date in# Do the same for the $(sample-test) and the main doc/.texi file.sc_copyright_check:
copyright{}}% \DeclareUnicodeCharacter \DeclareUnicodeCharacter{00AB}{\guillemetleft{}}% \DeclareUnicodeCharacter{00AC}{\ensurermath\lnot}%
\DeclareUnicodeCharacter{00AD}{\-}% \DeclareUnicode
copyright \definedummyword\registersymbol \definedummyword\dots \definedummyword\enddots \definedummyword\entrybreak
\definedummyword\equiv \definedummyword\error \definedummyword\euro
Copyright/ directory.If you want to assign a single change, you should use the file,
copyright-env) xargs
Copyright \ $(export VC_LIST_EXCEPT_DEFAULT=COPYING
COPYRIGHT Free Software Foundation, Inc.
copyright{}} \gdefchar \gdefchar^^ab{\guillemetleft{}} \gdefchar^^ac{\ensurermath\lnot} \gdefchar^^ad{\-} \gdefchar^^ae{\registersymbol{}}
\gdefchar^^af{\=} % \gdefchar^^b0{\textdeg
copyright goes to the FSF.
copyright information, for version-etc.
copyright in $_sc_search_regexp)
copyright in $_sc_search_regexp)
copyright line above was added in 2015 based on James# Youngman's best estimate of the original publication date of this# file (it appeared in findutils
release 4.1.7 but may have appeared# in earl
copyright line is 72.# Set UPDATE_COPYRIGHT_MAX_LINE_LENGTH to use a different length.# 5. Set UPDATE_COPYRIGHT HOLDER if the
copyright holder is other# than "Free Software Foundation,
Copyright-paperwork-exempt: Yes
copyright: prereq# By default, exclude all variants of COPYING; you can also# add exemptions (such as ChangeLog.* for rotated change logs)# in the
file .x-update-copyright.
copyright program gets found.PATH=$abs_aux_dir:$PATHexport PATH
Copyright/request-assign.changes:
Copyright/request-assign.changes;hb=HEAD
Copyright/request-assign.future:
Copyright/request-assign.future;hb=HEAD
copyright=` sed -n
Copyright /{/s/ grep .` || exit 1; \ sed \-e "s,@""bindir""@,$(bindir)," \ -e "s,@""libexecdir""@,$(libexecdir)," \ -e
"s,@""LOCATE_DB""@,$(LOCATE_DB)," \ -e "s,@""VERSION""@,$(VERSION)," \ -
copyright symbol suitable for this locale, and
copyright target rule# in maint.mk from gnulib's maintainer-makefile module.
copyright-tests
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1compare - $TMP <<EOF || exit 1
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1compare - $TMP <<EOF || exit 1 Undisturbed
text.dnl Undisturbed text.
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1tr @ '\015' > $TMP-exp <<\EOF
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare - $TMP-stderr <<EOF || exit 1$TMP: warning: copyright statement not
foundEOFcompare - $TMP <<EOF || exit 1
copyright $TMP.compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1compare - $TMP.star <<EOF || exit 1
copyright $TMP.compare /dev/null $TMP-stdout || exit 1compare - $TMP-stderr <<EOF || exit 1$TMP.4: warning: copyright statement not found$TMP.5:
warning: copyright statement not foundEOFcompare
COPYRIGHT/' | tr -d compare $TMP - || ERR=1
copyrightupdate-copyright:
copyright \# verify \# version-etc \# version-etc-fsf \# warnings \# wchar \# wwidth \# xalloc \# xalloc-die \# xgetcwd \# xstrtod \# xstrtol \#
xstrtoumax \# year2038 \#
copyright verify version-etc version-etc-fsf warnings wchar wwidth xalloc xalloc-die xgetcwd xstrtod xstrtol xstrtoumax year2038

```

```

yesno)]gl_AVOID([])gl_SOURCE_BASE([gl/
copyright year.
copyright year intervals in a reformatted FSF copyright statement# are expanded instead.# If UPDATE_COPYRIGHT_USE_INTERVALS=2, convert
a sequence with gaps# to the minimal containing
copyright year intervals. (See "Environment variables"# below.)
copyright year is 2 or 4 digits, and years are separated by# commas or dashes. Whitespace may appear after commas.
copyright year lists in your project.# If you have an additional project-specific rule,
copyright year list to include the current year.
copyright years (such as 90, 1991, 1992-2007, 2008) in a# reformatted FSF copyright statement is collapsed to a single# interval (such as 1990-
2008). If unset or set to 0, all existing
(C)" takes one of the following forms or is omitted# entirely:
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C)" to the copyright symbol
(C) ((unsigned int) (C) <= 'z' - 'a')# define FILE_SYSTEM_PREFIX_LEN(Filename) \ (_IS_DRIVE_LETTER ((Filename)[0]) &&
(Filename)[1] == ':' ? 2 : 0)# ifndef _
©);my$holder ||= 'Free Software Foundation, Inc. ';my $prefix_max = 5;my $margin = $ENV{UPDATE_COPYRIGHT_MAX_LINE_LENGTH};!$margin ||
$margin !~ m/^\^d+$/ and $margin = 72;
FLAC - Free Lossless Audio Codec 1.3.3 github:xiph/flac:1.3.3
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$!t_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;;*-*so
(C) AC_MSG_CHECKING( xiph_stack_check_old_cflags="$CFLAGS" SSP_FLAGS="-fstack-protector --param ssp-buffer-size=4"
CFLAGS=$SSP_FLAGS AC_TRY_LINK([ #include <stdio.h> ], [ puts("Hello, Worl
(C) AC_TRY_LINK( #include <stdio.h> ],
(C)!b go :more \./!{ N s|\n# | | b more
(C) if test "x AC_TRY_LINK([ #include <stdio.h> ],
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(C) AC_LINK_IFELSE( AC_LANG_POP) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -belf
CFLAGS=$SAVE_CFLAGS fi ;*-*so
(C) AC_MSG_CHECKING( xiph_stack_check_old_cflags="$CFLAGS" SSP_FLAGS="-fstack-protector --param ssp-buffer-size=4"
CFLAGS=$SSP_FLAGS AC_TRY_LINK([ #include <stdio.h> ], [ puts("Hello, World") ], [ ] )
(C) AC_TRY_LINK([ #include <stdio.h> ], [ ] )
(C) \b go :more \./!{ N s|\n# || b more
(C) if test "x" AC_TRY_LINK([ #include <stdio.h> ], [ ] )
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(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -

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(C)!/b go :more /\.!{ N s|\n# || b more
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(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$!t_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;:*-*so
(C)!/b go :more /\.!{ N s|\n# || b more
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Copyright 1994 Free Software Foundation, Inc...# (gdb) shell echo 000800 >/tmp/seq# (gdb) r '{ printf "%06d", $1 + 1 >"/tmp/seq"; printf "%06d", $1
+ 1 }(gdb) r '{ printf "%06d", $1 + 1 >"/tmp/seq";
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Copyright (C) 1989, 1991-2007 Free Software Foundation.#
Copyright (C) 1989, 1991-2011 Free Software Foundation.# # # bug due to trying to make field splitting more efficient by not parse all fields # $ echo
a,b,,c |gawk '{for(i=1;i<=4;++i)print i, $i}'
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Copyright (C) 2013 by CosumoGEN" \_PRODUCT\_FILENAME  
 ="\_main.awk"}# \_\_\_\_\_ func \_DS(c,t,P, a,A) {  
 #####

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Copyright (C) Michael D. Brennan# # % nawk -V# awk version 20001115  
 copyright{copyright \defglyph\dots{...}% \defglyph\enddots{...}% \defglyph\equiv{=}% \defglyph\error{error}% \defglyph\euro{euro}%  
 \defglyph\expansion{=>}% \defglyph\geq{>=}% \defgly  
 Copyright `date -e "s/^/level0 save def/\|level0 save def 30 -48 translate/"SEDME2 = sed '/%%Page: 10 10/,/0 Cg EP/d'  
 copyright{}}% \DeclareUnicodeCharacter \DeclareUnicodeCharacter{00AB}{\guillemetleft{}}% \DeclareUnicodeCharacter{00AC}{\ensuremath{\not{}}%  
 \DeclareUnicodeCharacter{00AD}{\~}% \DeclareUnicode

```

copyright \definedummyword\registered symbol \definedummyword\dots \definedummyword\enddots \definedummyword\entrybreak
\definedummyword\equiv \definedummyword\error \definedummyword\euro
copyright for feedback      supersedes absence of required program or data file */  if (Present("USAGE")) {      complaint = 0;          /* clean exit
*/  } else if (Present("VERSION") || Present("COPYRI
copyright{}} \gdefchar \gdefchar^^ab{\guillemetleft{}} \gdefchar^^ac{\ensuremath\lnot} \gdefchar^^ad{\-} \gdefchar^^ae{\registered symbol{}}
\gdefchar^^af{\= {}} % \gdefchar^^b0{\textdeg
copyright information
copyright info,      * then exit successfully, do nothing else.      */  copyleft(); exit(EXIT_SUCCESS);
COPYRIGHT)          _ln()          (_cmdln_version=="v"
return #
COPYRIGHT)          _ln()          _ln(" Usage:")          _ln()          _ln(" " _PRODUCT_FILENAME "
#
copyright\n"), fp); fputs(_("\t-d[file]\t\t-dump-variables[=file]\n"), fp); fputs(_("\t-D[file]\t\t--debug[=file]\n"), fp); fputs(_("\t-e 'program-
text'\t-t-source='program-text'\n"), fp); fputs(_
copyright */  return short_circ      : native_dcl ? vms_usage(USAGE_FILE_RQRD) : 0; /* insufficient parameters */  else if (CondVal(sts) ==
CondVal(CLI$ _CONFLICT)) return vms_usage(USAGE_BAD_COMBO);
COPYRIGHT /USAGE \n\ and /OUTPUT=out_file \n\, /* omitted: /LINT=INVALID /CHARACTERS_AS_BYTES /USE_LC_NUMERIC /GEN_POT */
*no_prog = "missing required element: /COMMANDS or /INPUT", *no_fil
Gawk 5.0.1 opensuse:gawk-debugsource/5.0.1-2.1/aarch64
(C) 1997-2005      * Sean Eron Anderson. The code and descriptions are distributed in the      * hope that they will be useful, but WITHOUT ANY
WARRANTY and without      * even the implied warranty of merchant
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 copyright information  
 copyright info, \* then exit successfully, do nothing else. \*/ copleft(); exit(EXIT\_SUCCESS);  
 copyright(\n"), fp); fputs(\_("\t-d[file]\t\t--dump-variables[=file]\n"), fp); fputs(\_("\t-D[file]\t\t--debug[=file]\n"), fp); fputs(\_("\t-e 'program-text'\t\t--source='program-text'\n"), fp); fputs(\_  
 Gawk 5.0.1 ubuntu:gawk-doc/5.0.1-1  
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 \defglyph\expansion{==>}% \defglyph\geq{>=}% \defgly  
 Copyright `date -e "s/^\\level0 save def/\\level0 save def 30 -48 translate/"SEDME2 = sed '/%%Page: 10 10/,/0 Cg EP/d'  
 copyright{}}% \DeclareUnicodeCharacter \DeclareUnicodeCharacter{00AB}{\guillemetleft{}}% \DeclareUnicodeCharacter{00AC}{\ensuremath{\not{}}%  
 \DeclareUnicodeCharacter{00AD}{\}-}% \DeclareUnicode  
 copyright \definedummyword\registeredsymbol \definedummyword\dots \definedummyword\enddots \definedummyword\entrybreak



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\definedummyword\equiv \definedummyword\error \definedummyword\euro
copyright-format/1.0/Upstream-Contact: bug-gawk@gnu.orgSource: https://ftp.gnu.org/gnu/gawk/
copyright{} \gdefchar \gdefchar^^ab{\guillemetleft} \gdefchar^^ac{\ensuremath\inot} \gdefchar^^ad{-} \gdefchar^^ae{\registeredsymbol{}}
\gdefchar^^af{=} \gdefchar^^b0{\textdeg}
gawk-doc 5.0.1 debian:gawk-doc/5.0.1-1/all
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Copyright &copy; 2007 Free Software Foundation, Inc. <a href="https://fsf.org/">https://fsf.org/</a>
Copyright (C) <var>year</var> <var>name of author</var>
Copyright (C) <var>year</var> <var>your name</var>.
copyright-format/1.0/Upstream-Contact: bug-gawk@gnu.orgSource: https://ftp.gnu.org/gnu/gawk/
Copyright&rdquo; also means copyright-like laws that apply to other kinds
copyright&rdquo; line and a pointer to where the full notice is found.</p><div class="example"><pre class="example"><var>one line to give the
program's name and a brief idea of what it does.</var>
copyrightresulting from the compilation is not used to limit the legal rights
gcc-9 9.3.0 debian:gcc-9-source/9.3.0-16/all
(C) 1995-2004 Jean-loup Gailly and Mark Adler
(C) 1995-2017 Jean-loup Gailly and Mark Adler
(C) 1995-2017 Jean-loup Gailly END END BLOCK "VarFileInfo" BEGIN VALUE "Translation", 0x0409, 1252 END
(C) 1995-2017 Jean-loup Gailly END END BLOCK "VarFileInfo" BEGIN VALUE "Translation", 0x0409, 1252 END
(C) 1995-2017 Jean-loup Gailly VALUE "OriginalFilename", "zlib1.dll\0" VALUE "ProductName", "zlib\0" VALUE "ProductVersion",
ZLIB_VERSION "\0" VALUE "Comments", "For more info
(C) 1998, 2000, 2002, 2003, 2007 Free Software Foundation
(C) 2003 Free Software Foundation// Origin: PR/12832, Jonathan Wakely <redi@gcc.gnu.org>
(C) 2009, 2010
(C) 2013 Free Software Foundation
(C)<<2) (__v16sf)(__m512)_mm512_setzero_ps(), \ (__mmask16)(U),\
(R))#define __mm_getmant_round_sd (__v16sf)(__m512)_mm512_undefined_ps(), \ (__mmask16)-1,\
(R)) (C)<<2) (__v16sf)(__m512)(W), \ (__mmask16)(U),\
__MM_FROUND_CUR_DIRECTION)) (__v16sf)(__m512)(W), \ (__mmask16)(U),\
(R)) (C)<<2) (__v16sf)_mm512_setzero_ps(), \ (__mmask16)(U),\
__MM_FROUND_CUR_DIRECTION))#define (C)<<2) (__v16sf)_mm512_undefined_ps(), \ (__mmask16)-1,\
__MM_FROUND_CUR_DIRECTION)) (C)<<2) (__v2df)(__m128d)_mm_setzero_pd (), \ (__mmask8)-1))
(C)<<2) (__v2df)(__m128d)_mm_setzero_pd (), \ (__mmask8)(U)))
(C)<<2) (__v2df)(__m128d)(W), \ (__mmask8)(U)))
(C)<<2) (__v4df)(__m256d)_mm256_setzero_pd (),\ (__mmask8)-1))
(C)<<2) (__v4df)(__m256d)_mm256_setzero_pd (),\ (__mmask8)(U)))
(C)<<2) (__v4df)(__m256d)(W), \ (__mmask8)(U)))
(C)<<2) (__v4sf)(__m128)_mm_setzero_ps (), \ (__mmask8)-1))
(C)<<2) (__v4sf)(__m128)_mm_setzero_ps (), \ (__mmask8)(U)))
(C)<<2) (__v4sf)(__m128)(W), \ (__mmask8)(U)))
(C)<<2) (__v8df)(__m512d)_mm512_setzero_pd(), \ (__mmask8)(U),\
(R))#define __mm512_getmant_roun (__v8df)(__m512d)_mm512_undefined_pd(), \ (__mmask8)-1,\
(R)) (C)<<2) (__v8df)(__m512d)(W), \ (__mmask8)(U),\
__MM_FROUND_CUR_DIRECTION)) (__v8df)(__m512d)(W), \ (__mmask8)(U),\
(R)) (C)<<2) (__v8df)_mm512_setzero_pd(), \ (__mmask8)(U),\
__MM_FROUND_CUR_DIRECTION))#defin (C)<<2) (__v8df)_mm512_undefined_pd(), \ (__mmask8)-1,\

```

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_MM_FROUND_CUR_DIRECTION))
(C)<<2)      (_v8sf)(_m256)_mm256_setzero_ps (), \      (__mmask8)-1))
(C)<<2)      (_v8sf)(_m256)_mm256_setzero_ps (), \      (__mmask8)(U)))
(C)<<2)      (_v8sf)(_m256)(W), \      (__mmask8)(U)))
(C)(6); assert (ci.x == 6); static assert(!__traits(compiles, pmesswith(&cs,ci))); static assert(!__traits(compiles, rmesswith(cs,ci))); //cs[ $\$$ -1].x =
14; //assert (ci.x == 14); //
(C) A, B, A-- SUBTYPES.
(C) A > B IS THE SAME AS NOT (A <= B).-- (D) ADJACENT MODEL NUMBERS GIVE CORRECT RESULTS.-- (E) NON-MODEL NUMBERS WITH
DISTINCT MODEL INTERVALS GIVE-- CORRECT RESULTS.--
(C)'access; end;begin null;
(C) ACCESS PARAMETERS TO PROCEDURES.
(C) ACCESS PARAMETERS TO PROCEDURES.-- (D) ACCESS PARAMETERS TO FUNCTIONS.
(C) ACCESS PARAMETERS TO PROCEDURES.-- (D) ACCESS PARAMETERS TO FUNCTIONS.
(C) AC_LINK_IFELSE( AC_LANG_POP] if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need
for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
(C)! Add a check that the fortran missing optional is accepted by the
(C) alias declaration"/
(C) alias int T1;
(C) alias personality_routine = _Unwind_Reason_Code function(_Unwind_State, _Unwind_Control_Block*,
_Unwind_Context*);
(C) alias RunDepTests = int function();
(C) alias RunDepTests = int function(const char
(C) alias RunTests = int function();
(C) alias SetFinalizeCounter = void function(shared(size_t
(C) alias T2 = int;
(C) alias _Unwind_Exception_Cleanup_Fn = void function(_Unwind_Reason_Code, _Unwind_Exception*);
(C) alias _Unwind_Personality_Fn = _Unwind_Reason_Code function(int, _Unwind_Action, _Unwind_Exception_Class,
_Unwind_Exceptio
(C) alias _Unwind_Stop_Fn = _Unwind_Reason_Code function(int, _Unwind_Action, _Unwind_Exception_Class,
_Unwind_Control_Block*,
(C) alias _Unwind_Stop_Fn = _Unwind_Reason_Code function(int, _Unwind_Action, _Unwind_Exception_Class,
_Unwind_Exception*,
(C) alias _Unwind_Trace_Fn = _Unwind_Reason_Code function(_Unwind_Context*, void*);
(C) alias void function() Callback;
(C) alias void function() fpw_t;
(C) AN ARRAY OF RECORD OF TASK OBJECT, IN A PACKAGE BODY,-- IN A TASK BODY.
(C) and extern (D) calling convention matches the C calling convention used by the supported C compiler on the host system. */ break;
(C) and for derived types that are parameters to functions that are BIND(C). All fields of the derived type are required to be interoperable, and are
tested for such. If an error occurs, th
(C) and SEQUENCE types!type :: t integer :: iend type t
(C) and their params are all interoperable. */ if (sym->attr.flavor == FL_PROCEDURE) { if (sym->attr.is_bind_c == 0)
(C) and then C /= Standard_Standard loop Set_Is_Immediately_Visible (C); Set_Is_Visible_Lib_Unit (C); C := Scope (C);
end loop; end;
(C) and then (Entity (C) = Standard_False or else Entity (C) = Standard_True) then Test_Result := Entity (C) =
Standard_True; return True;
(C) and then Expr_Value (C) = Lo then Error_Msg_N ("duplication of choice value: &#!", C);
(C) and then List_Containing (C) = Statements (P) then Onode := Original_Node (Parent (P));
(C) and then not Before (At_Least, Get_Priority (Sets.Element (C).Item));
(C) and then not Is_Digit (C) and then C /= ' ';
(C) and then Present (Variant_Part (C)) then Freeze_Choices_In_Variant_Part (Variant_Part (C)); end if;
(C) and then Sloc (C) = Standard_Location then Error_Msg_N ("redefinition of entity& in Standard?K?", N); end if; end; end if;
end Warn_If_Standard_Redefin
(C) applied to return false; }
(C) Apr 19 1983 in BASIC version by:Professor W. M. Kahan,567 Evans HallElectrical Engineering & Computer Science Dept.University of
CaliforniaBerkeley, California 94720USA
(C) A RECORD OF ARRAY OF TASK ALLOCATOR, IN A PACKAGE BODY,-- IN A TASK BODY.
(C) A RECORD OF ARRAY OF TASK ALLOCATOR, IN A TASK BODY.
(C) A RECORD OF ARRAY OF TASK ALLOCATOR, IN A TASK BODY, NOT-- DURING RENDEZVOUS.-- (D) A LIMITED PRIVATE TASK
ALLOCATOR, IN A TASK BODY, DURING-- RENDEZVOUS.
(C) A RECORD OF TASK ALLOCATOR, IN A PACKAGE SPECIFICATION.-- (D) A RECORD OF ARRAY OF TASK ALLOCATOR, IN A PACKAGE BODY.-- (E)
AN ARRAY OF RECORD OF TASK ALLOCATOR, IN A TASK BODY.

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(C) A RECORD OF TASK OBJECT, IN A PACKAGE SPECIFICATION.-- (D) A RECORD OF ARRAY OF TASK OBJECT, IN A PACKAGE BODY.-- (E) AN
ARRAY OF RECORD OF TASK OBJECT, IN A TASK BODY.
(C) A RECORD PARAMETER WHOSE COMPONENTS HAVE NON-STATIC-- CONSTRAINTS INITIALIZED WITH A STATIC AGGREGATE.
(C) A RECORD PARAMETER WHOSE COMPONENTS HAVE NON-STATIC-- CONSTRAINTS INITIALIZED WITH A STATIC AGGREGATE.
(C) A RECORD PARAMETER WHOSE COMPONENTS HAVE NON-STATIC-- CONSTRAINTS INITIALIZED WITH A STATIC AGGREGATE.-- (D)
AN ARRAY PARAMETER CONSTRAINED WITH STATIC BOUNDS ON SUB--
(C)! as defined in TS 29913!interface subroutine test (xx) bind(C, name="myBindC") type(*), dimension(,;) :: xx end subroutine testend interface
(C) ASSERT_CONST_TYPE((C), C90_SUFU_TYPE((C)))
(C) ASSERT_CONST_TYPE((C), C90_SUFU_TYPE((C)))
(C) ASSERT_CONST_TYPE((C), C90_UNSUUF_DEC_TYPE((C)))#define CHECK_UNSUUF_OCTHEX_TYPE(C) \
ASSERT_CONST_TYPE((C), C90_UNSUUF_OCTHEX_TYPE((C)))
(C) ASSERT_CONST_TYPE((C), C99_SUFU_DEC_TYPE((C)))#define CHECK_SUFU_OCTHEX_TYPE(C) \ ASSERT_CONST_TYPE((C),
C99_SUFU_OCTHEX_TYPE((C)))
(C) ASSERT_CONST_TYPE((C), C99_SUFU_TYPE((C)))
(C) ASSERT_CONST_TYPE((C), C99_SUFU_TYPE((C)))#define CHECK_SUFU_TYPE(C) \ ASSERT_CONST_TYPE((C),
C99_SUFU_TYPE((C)))
(C) ASSERT_CONST_TYPE((C), C99_SUFU_TYPE((C)))
(C) ASSERT_CONST_TYPE((C), C99_SUFU_TYPE((C)))#define CHECK_SUFU_TYPE(C) \ ASSERT_CONST_TYPE((C),
C99_SUFU_TYPE((C)))
(C) ASSERT_CONST_TYPE((C), C99_UNSUUF_DEC_TYPE((C)))#define CHECK_UNSUUF_OCTHEX_TYPE(C) \
ASSERT_CONST_TYPE((C), C99_UNSUUF_OCTHEX_TYPE((C)))
(C) ASSERT_CONST_TYPE((C), long long int)#define CHECK_SUFU_OCTHEX_TYPE(C) \ ASSERT_CONST_TYPE((C),
C99_SUFU_OCTHEX_TYPE((C)))
(C) ASSERT_CONST_TYPE((C), unsigned long int)
(C) ASSERT_CONST_TYPE((C), unsigned long long int)
(C) at return false;
(C) at return MATCH_ERROR; }
(C) at return MATCH_ERROR; }
(C) at return MATCH_ERROR; }
(C) attribute at "and may be inaccessible by the C companion processor", derived_sym->name, &(derived_sym->declared_at));
derived_sym->ts.is_c_interop = 1; derived_sym->attr.is_b
(C) attribute at "an interface with BIND(C)"; return MATCH_ERROR; } /* Check for C1217. */ if (has_name_equals &&
sym->attr.pointer)
(C) attribute "at %L may not be specified for an internal " "procedure", &gfc_current_locus) return MATCH_ERROR;
(C) attribute at return MATCH_ERROR; }
(C) attribute at "variables or common blocks", &loc); }
(C) attribute at "variables or common blocks", &loc); }
(C) attribute at "variables or common blocks", where); else if (attr->is_bind_c) gfc_error_now ("Duplicate BIND attribute specified at %L",
where); else attr->is_bind_c = 1;
(C) __attribute__((noinline));C f1 (C x){ return x;
(C) ATTRIBUTES ( -- (D) FUNCTION CALLS -- (E) QUALIFIED EXPRESSIONS -- (F) TYPE CONVERSIONS -- (G)
PARENTHEZIZED EXPRESSIONS OF THE A
(C) attribute specification statement.
(C) attributes", tmp_sym->name, &(tmp_sym->declared_at)); retval = false; }
*/ (C) attribute to be equal. STRUCTUREs are special because they can be anonymous; therefore two structures with different names may be equal.
*/
(C) attribute with a NAME= specifier.static int num_idents_on_line;/* Need to know if a NAME= specifier was found during gfc_match_bind_c so we
can supply a name if the curr_binding_label is nil
(C) attribute with NAME"; return MATCH_ERROR; } /* Set binding label for BIND(C). */ if (!set_binding_label (&sym->binding_label, sym-
>name, num)) return MATCH_ERROR;
(C) auto d2 = d;
(C) auto Stub13476(alias func)(ParameterTypeTuple13476{ ++flag13476;
(C) auto Stub13476Alt(alias func)(int args) nothrow{ ++flag13476;
(C) ~b; c[0] ^= c[0] <= 0x1234; a = (a >> 1) | (a << 127); b += (D) {45, 123};
(C) because it "is neither a COMMON block nor declared at the " "module level scope", sym->name, &(sym->declared_at));
t = false; } else if (sym->ts.type == BT_CHARACTER
(C) BEFORE CALL, IN OUT MODE, STATIC ONE DIMENSIONAL BOUNDS.
(C) before the declaration itself. Return
(C) BEGIN -- (C)
(C) BEGIN FAILED ("EXCEPTION NOT RAISED BEFORE CALL - P2 " & WHO); EXCEPTION WHEN OTHERS => FAILED
("EXCEPTION RAISED IN P2 FOR " & WHO); END P2;

```

```

(C)/!b go :more /\./!{ N s/\n# // b more
(C) b integer i end type b ! { dg-error "Expecting END MODULE statement" }
(C) BlkInfo function(size_t, uint, const TypeInfo) gc_qlloc;
(C) BlkInfo function(void
(C) BlkInfo_ gc_qlloc( size_t sz, uint ba = 0, const TypeInfo = null ) pure nothrow;
(C) BlkInfo gc_qlloc( size_t sz, uint ba = 0, const TypeInfo ti = null ){ if ( proxy is null )
(C) BlkInfo_ gc_query( void
(C) BlkInfo gc_query( void{ if ( proxy is null ) return BlkInfo.init; return proxy.gc_query( p );
(C) bool_aaDelX(AA aa, in TypeInfo keyti, in void{ if (aa.empty) return false;
(C) BOOLEAN TYPE, STATIC NULL RANGE, NO PARAMETERS.-- (D) USER-DEFINED ENUMERATED TYPE, DYNAMIC LOWER BOUND, ONE--
PARAMETER.-- (E) DERIVED INTEGER TYPE, DYNAMIC NULL RANGE, ONE PA
(C) BOOL := F1 (REC.A (2..4)); IF REC.A /= (7, 7, 7, 9, 9) THEN FAILED ("IN PARAM CHANGED BY FUNCTION");
END IF; EXCEPTION
(C) bool function() gc_inFinalizer; }
(C) bool gc_inFinalizer();}
(C) bool gc_inFinalizer(){ if ( proxy !is null ) return proxy.gc_inFinalizer(); return false;
(C) bool runModuleUnitTests();
(C) bool runModuleUnitTests(){ // backtrace version (GNU) import gcc.backtrace; version (CRuntime_Glibc) import
core.sys.linux.execinfo; else version (Darwin)
(C) bool thread_inCriticalRegion() @nogcin{ assert(Thread.getThis());
(C) bool thread_isMainThread() nothrow @nogc{ return Thread.getThis() is Thread.sm_main;
(C) buff.ptr; initializeClassInstance(c, 0); assert(si);
(C) byteout (result){ auto tinext = unqualify(ti.next); auto sizeelem = tinext.tsize; // array element size debug(PRINTF)
printf(" d_arraycatT(%d,%p ~ %d,%p sizeelem = %d =>
(C) CALL G(C1) CALL H(C1(1:4))
(C) call sub3 (D)
(C) call sub4 (D)
(C) CArgs rt_cArgs() @nogc;}
(C) CArgs rt_cArgs() @nogc{ return_cArgs;
(C) CASE 2, IN OUT MODE, STATIC PRIVATE DISCRIMINANT.-- (D) CASE 2, OUT MODE, DYNAMIC TWO DIMENSIONAL BOUNDS.-- (E) SAME AS
(C), WITH TYPE CONVERSION.-- (F) SAME AS (D), WITH TYPE
(C) CASE ("AAA":"EEE") STOP 1 CASE ("R":"T") CONTINUE CASE DEFAULT STOP 2END SELECT
(C) CASE (A), BUT ACTUAL PARAMETER IS A TYPE CONVERSION.-- (D) CASE (B), BUT ACTUAL PARAMETER IS A TYPE CONVERSION.
(C)(C } auto a = DummyOutputRange(); auto spec = FormatSpec!Char(fmt); //dummy write spec.writeUpToNextSpec(a);
(C)(C if (is(C : dchar)){ import std.traits : isAggregateType, OriginalType, Unqual;
(C) cc; immutable(B) ib;
(C)(C c) @safe if (isSomeChar { import std.traits : Parameters; static auto trustedFPUTC(int ch, _jobuf* h) @trusted
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_PATH_WITH_DIR(P) \ (strchr (P, '/') != NULL ||
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)#endif
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])#endif
(C) ((C) ==# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define FILESYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) == DIRECTORY_SEPARATOR)#endif
(C) c; else
(C) ((C) ==#endif
(C) CE substitutes for helpful life information.
(C) c.f() // makes a copy c.g()
(C) character( if (str(2) .ne. "ghi") stop 2 str = ['uvw','xyz'] end subroutine
(C) char const(char**) getEnvironPtr() @trusted { return *_NSGetEnviron;
(C) char{ if (a.length > 1)
(C) CHECK_OCTHEX_CONST(C)#define CHECK_HEX_CONST(C) \ CHECK_OCTHEX_CONST(0x##C); \
CHECK_OCTHEX_CONST(0x##C);
(C) CHECK RECORDS HAVING A DISCRIMINANT, WITH MORE THAN ONE ARRAY-- COMPONENT, WHERE THE BOUNDS OF THE ARRAY DEPEND ON
THE-- DISCRIMINANT.
(C) CHECKS FOR ATTRIBUTES.
(C) Chinese (traditional). "\u4ED6\u5011\u7232\u4EC0\u9EBD\u4E0D\u8AAA\u4E2D\u6587", "ihqwctvzc91f659drss3x8bo0yb" },
(C)(Cif (is(C : wchar) || is(C : char)){ static struct Decoder
(C)(C if (is(immutable C == immutable char)){ return 1;
(C)(Cif (isSomeChar!C){ import std.algorithm.searching : find; static struct Result

```

```

(C)(Cif (isSomeChar!C){ return _baseName(path);
(C)(Cif (isSomeChar!C){ return _dirName(path);
(C)(C input){ return C(input.im, input.re);
(C) class( class(*), pointer :: P allocate(P, source=C) select type (P) type is (character(*)) if (P /= 'test string') STOP 53 class default
STOP 54 end
(C) clause but an error was encountered, or MATCH_YES if the specifier was correct and the binding label and
(C) c == lowercase
(C) (CLS_GETINFO((struct objc_class
(C) (Cmd.Params (C)
(C)(C obj) assert(obj.foo() == 1 || obj.foo() == 2); return obj; }
(C) CollectHandler rt_getCollectHandler();
(C) CollectHandler rt_getCollectHandler(){ return collectHandler;
(C) CONCAT3 (C, f, WIDTH)
(C) CONCAT3 (C, f, WIDTH)# define FMAC(M) CONCAT4 (FLT, WIDTH, _, M)#endif
(C) CONCAT3 (C, f, WIDTH)# define FN(F) CONCAT3 (F, f, WIDTH)#endif
(C) CONCAT3 (C, f, WIDTH)# define MAX CONCAT3 (FLT, WIDTH, _MAX)# define MIN CONCAT3 (FLT, WIDTH, _MIN)# define TRUE_MIN CONCAT3
(FLT, WIDTH, _TRUE_MIN)#endif
(C) CONCAT3 (C, f, WIDTH)# define MAX CONCAT3 (FLT, WIDTH, _MAX)#endif
(C) CONCAT3 (C, f, WIDTH)# define TRUE_MIN CONCAT3 (FLT, WIDTH, _TRUE_MIN)#endif
(C) CONCAT3 (C, F, WIDTH)#endif
(C) CONCAT3 (C, if, WIDTH)
(C) CONCAT4 (C, F, WIDTH, i)#endif
(C) CONCAT4 (C, f, WIDTH, x)
(C) CONCAT4 (C, f, WIDTH, x)# define FMAC(M) CONCAT4 (FLT, WIDTH, X_, M)#else# define TYPE CONCAT (_Float, WIDTH)
(C) CONCAT4 (C, f, WIDTH, x)# define FN(F) CONCAT4 (F, f, WIDTH, x)#else# define TYPE CONCAT (_Float, WIDTH)
(C) CONCAT4 (C, f, WIDTH, x)# define MAX CONCAT3 (FLT, WIDTH, X_MAX)# define MIN CONCAT3 (FLT, WIDTH, X_MIN)# define TRUE_MIN
CONCAT3 (FLT, WIDTH, X_TRUE_MIN)#else# define TYPE CONCAT (_Float, WI
(C) CONCAT4 (C, f, WIDTH, x)# define MAX CONCAT3 (FLT, WIDTH, X_MAX)#else# define TYPE CONCAT (_Float, WIDTH)
(C) CONCAT4 (C, f, WIDTH, x)# define TRUE_MIN CONCAT3 (FLT, WIDTH, X_TRUE_MIN)#else# define TYPE CONCAT (_Float, WIDTH)
(C) CONCAT4 (C, F, WIDTH, x)#else# define TYPE CONCAT (_Float, WIDTH)
(C) CONCAT4 (C, F, WIDTH, xi)#else# define TYPE CONCAT (_Float, WIDTH)
(C) CONCAT4 (C, if, WIDTH, x)
(C) const(char)
(C) const(char)
(C)(const(C)if (isSomeChar!C){ return toUTFz!(const(wchar*)(str);
(C)(const(C)if (isSomeChar!C){ return walkLength(str);
(C)(const(C)if (isSomeChar!C){ static if (is(Unqual!C == dchar))
(C)(const(C)if (isSomeChar!C){ static if (is(Unqual!C == dchar)) return index; else
(C)(const(C) { import std.format : format; string diff = "src: %( %x %)\nres: %( %x %)\ntru: %( %x %)"; auto low = s.toLowerCase() , up
= s.toUpperCase(); auto lowInp = s.d
(C)(const(C) { return 3;
(C)(const(C) @safe pure nothrowif (isSomeChar!C){ return buildPath!(typeof(paths))(paths);
(C)(const(C) { size_t len = 0; while (*ptr != '\0') { ++ptr; ++len; } return len;
(C)(const(C) @trusted pure nothrowif (isSomeChar!C){ import std.array : array;
(C) CONSTRAINED ACTUAL PARAMETERS OF LIMITED PRIVATE TYPE.
(C) CONSTRAINT_ERROR IS RAISED BEFORE THE CALL FOR CONVERSION TO A-- CONSTRAINED ARRAY TYPE WHEN THE NUMBER OF
COMPONENTS PER-- DIMENSION OF THE ACTUAL DIFFERS FROM THAT OF THE FORMAL.-
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all copies. This software is provided // "
(C) csets_fold_lower
(C) csets_fold_upperextern char Fold_Lower[], Fold_Upper[];
(C) c; S[] s = [ S(new C) ];
(C) c } static struct PutS(C)
(C) c == uppercase
(C) CURLcode curl_easy_getinfo(CURL
(C) CURLcode curl_easy_recv(CURL
(C) CURLcode curl_easy_send(CURL
(C) CURLMcode curl_multi_add_handle(CURLM

```

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(C) CURLMcode curl_multi_assign(CURLM
(C) CURLMcode curl_multi_cleanup(CURLM
(C) CURLMcode curl_multi_fdset( CURLM *multi_handle, fd_set *read_fd_set, fd_set *write_fd_set, fd_set *exc_fd_set, int *max_fd);
(C) CURLMcode curl_multi_perform(CURLM
(C) CURLMcode curl_multi_remove_handle(CURLM
(C) CURLMcode curl_multi_setopt(CURLM
(C) CURLMcode curl_multi_timeout(CURLM
(C)(C version (Posix) { assert(buildPath("foo") == "foo"); assert(buildPath("/foo/") == "/foo/"); assert(buildPath("foo", "bar") ==
"foo/bar"); assert(buildPath
(C)(C { while (!str.empty) str.popFront();
(C) DAY_NUMBER.-- (D) DAY_DURATION.
(C)(dchar c)if (is(C == char)){ return true;
(C)(dchar c)if (isSomeChar!C){ static if (C.sizeof == 1) return c <= 0x7F; else static if (C.sizeof == 2) return c <= 0xD7FF || (0xE000 <=
c && c <= 0xFFFF); else static
(C)(dchar c) @safe pure nothrow @nogcif (isSomeChar!C){ static if (C.sizeof == 1)
(C)(dchar c){ static if (C.sizeof == 1)
(C) debug::function_requires< debug::C >()
(C)#define DF F16_C (D)#define EF F16_C (E)#define GF F16_C (G)#define HF F16_C (H)
(C)#define DP P16_C (D)#define EP P16_C (E)#define FP P16_C (F)#define GP P16_C (G)#define HP P16_C (H)
(C)#define DS S16_C (D)#define ES S16_C (E)#define FS S16_C (F)#define GS S16_C (G)#define HS S16_C (H)
(C)#define DU U16_C (D)#define EU U16_C (E)#define FU U16_C (F)#define GU U16_C (G)#define HU U16_C (H)
(C)#define F(X) __has_attribute(X)#if !__has_attribute(used)#error unused attribute not supported - 1#endif
(C) definition for use in external C programs.
(C) derived type &curr_comp->loc, derived_sym->name, &derived_sym->declared_at; retval = false; }
(C) derived type curr_comp->name, &(curr_comp->loc), derived_sym->name, &(derived_sym->declared_at)); retval
= false; }
(C) derived types in the namespace so we can report errors for them once, rather than for each variable declared of that type. */
(C) derived types must have interoperable components. if (curr_comp->ts.type == BT_DERIVED && curr_comp->ts.u.derived->ts.is_iso_c != 1
&& curr_comp->ts.u.derived != derived_sym)
(C) dg) { C c = new C; void* cptr = cast(void*) c; void* iptr = cast(void*) cast(I) c; void* xptr = cast(void*) dg(c);
assert(cptr != iptr); assert(
(C) d = new C; //d = new C;}
(C) end subroutine b end interface procedure(b), bind(c,name="hjj") :: a ! { dg-error "may not have BIND.C. attribute with NAME" }
procedure(b),intent(in):: c end subroutine foo
(C) end subroutine foosub interface
(C) end subroutine procend module m
(C) end subroutine subend interfaceinteger(c_int), pointer :: intprocedure(sub), pointer :: fsub
(C) end subroutine two subroutine three() bind(C,name="three") ! { dg-error "NAME not allowed on BIND.C. for ABSTRACT INTERFACE" } end
subroutine three ! { dg-error "Expecting END INTERFACE stat
(C) end subroutine x end interface type(C_FUNPTR) aux
(C) enumerator :: OFFLOAD_SUCCESS = 0 enumerator :: OFFLOAD_DISABLED ! offload is disabled enumerator :: OFFLOAD_UNAVAILABLE
! card is not available enumerator :: OFFLO
(C)#error unused attribute not supported - 2#endif#if !D#error unused attribute not supported - 3#endif#if !E#error unused attribute not supported -
4#endif#if !F(unused)#error unused attrib
(C)#error unused attribute not supported - 6#endifint a = __has_attribute (unused) + __has_attribute (C) + D + E + F (unused) + F (C);int b =
__has_attribute (unused);int c = __has_attribute (C);
(C) EXCEPTION_DISPOSITION __GCC_specific_handler(void _Unwind_Personality_Fn);
(C) EXCEPTION_DISPOSITION __gdc_personality_seh0(void void* ms_orig_context, void* ms_disp) {
return __GCC_specific_handler(ms_exc
(C) extern(C)extern(C++, foo) extern(C++, bar) void f8() {} // OK
(C) extern const GUID CLSID_InternetShortcut,
(C) extern const GUID IID_IRichEditOle, IID_IRichEditOleCallback;
(C) extern const IID IID_IContinueCallback, IID_IEnumOleDocumentViews, IID_IPrint, IID_IOleDocumentView, IID_IOleDocument,
IID_IOleCommandTarget, IID_IOleDocumentSite;
(C) extern(C) void f6()
(C) extern __gshared const char const(char**) getEnvironPtr() @trusted { return environ;
(C) extern __gshared int __tls_index;
(C) ! Failed at trans--array.c:8078 class(Bug) A type(Bug) C select type (A) type is (bug) C = A C%scalar = onenineeight
class default
(C) feature of standard Fortran instead. gfc_source_file, pkgversion_string, version_string);
(C) f()func (*C) g() {} // pointer receiver

```

```

(C) f integer m end type f ! { dg-error "Expecting END MODULE statement" }
(C) First, Cmd.Config.Prefixes (P).all then -- If we are still in the same section, group the switches
(C) float floatPrintf(const char int func(float);
(C) Foo1 ctest1();
(C) Foo2 ctest2();
(C) Foo3 ctest3();
(C) Foo4 ctest4();
(C) Foo5 ctest5();
(C) Foo6 ctest6();
(C) fopen64, http://www.unix.org/version2/whatsnew/lfs20mar.html */ import core.sys.posix.stdio : fopen; return fopen(namez,
modez);
(C) for ABSTRACT INTERFACE at return MATCH_ERROR; }
(C) for ENTRY PR fortran/34079 To be linked with bind_c_usage_10.f03*/
(C) for internal procedures. if (gfc_current_state () == COMP_CONTAINS && sym->ns->proc_name->attr.flavor != FL_MODULE
(C) FORMAL CONSTRAINED, FORMAL AND ACTUAL HAVE DIFFERENT NUMBER-- COMPONENTS PER DIMENSION, BOTH FORMAL AND ACTUAL ARE
NULL-- ARRAYS.-- (D) FORMAL CONSTRAINED, ACTUAL NULL, WITH INDEX
(C) form. if intro.endswith ('right');
(C) FOR NON-MODEL NUMBERS A > 0.0, THAT ABS A VALUES ARE-- WITHIN THE APPROPRIATE MODEL INTERVAL.-- (D) FOR NON-
MODEL NUMBERS A < 0.0, THAT ABS A VALUES ARE-- WIT
(C) fp) { C c = new C; void* cptr = cast(void*) c; void* iptr = cast(void*) cast(I) c; void* xptr = cast(void*) fp(c); assert(cptr
!= iptr); assert(
(C) function "be an array", tmp_sym->name, &(tmp_sym->declared_at));
(C) FUNCTION INIT_3 RETURN INTEGER;
(C) function "%L may not be C interoperable", tmp_sym->name, &tmp_sym->declared_at); tmp_sym->ts.f90_type
= tmp_sym->ts.type; /* Mark it as C intero
(C) function, make sure the return value is a scalar value. The previous tests in this function made sure the type is interoperable. */ if
(bind_c_function && tmp_sym->as != NULL)
(C) FUNCTION "OR" (I1, I2 : INTEGER) RETURN CHARACTER IS BEGIN IF I1 > I2 THEN RETURN 'G'; ELSE
RETURN 'L'; END IF;
(C) FUNCTION "OR" (I1, I2 : INTEGER) RETURN CHARACTER RENAMES TWO_PARAMS;
(C) FUNCTION "OR" IS NEW TWO_PARAMS (ELEMENT => INTEGER);
(C) functions cannot return a character string. if (bind_c_function && tmp_sym->ts.type == BT_CHARACTER) if (tmp_sym->ts.u.cl == NULL ||
tmp_sym->ts.u.cl->length == NULL || tmp_sym->ts.u.c
(C) functions in the standard library that have `__attribute__(nothrow)` are considered `@trusted`. - The purity of a built-in can vary depending
on compiler flags set upon initialization,
(C) functions.m4_define([AC_LANG_FUNC_LINK_TRY(D)],
(C) functions PR fortran/34079 To be linked with bind_c_usage_16.f03*/
(C) functions that match any known GCC library builtin function in D and override its internal back-end symbol. */
(C) function "type at %L must have length 1", tmp_sym->name, &(tmp_sym->declared_at)); }
(C) FUNCTION "XOR" (I1, I2 : INTEGER) RETURN CHARACTER IS BEGIN IF I1 > I2 THEN RETURN 'G';
ELSE RETURN 'L'; END IF
(C) GC.Stats gc_stats ( ) nothrow @nogc;
(C) g()func (*C) h()
(C) __gshared bool rt_cmdline_enabled;
(C) __gshared bool rt_cmdline_enabled = false;
(C) __gshared bool rt_envvars_enabled;
(C) __gshared bool rt_envvars_enabled = true;
(C) __gshared bool rt_trapExceptions = true;
(C) __gshared char
(C) __gshared immutable(ModuleInfo
(C) __gshared nothrow:
(C) __gshared static int initCount;
(C) __gshared string
(C) __gshared void}
(C) hash_t_aaGetHash(in AA{ if (aa.empty) return 0;
(C) I := F;
(C) IF ABS NON_MODEL_VAR NOT IN 0.0 .. 64.0 OR ABS DECIMAL_M4'(NON_MODEL_CONST) NOT IN 0.0 .. 64.0 THEN FAILED
("ABS (2.0 / 3) NOT IN CORRECT RANGE - B");
(C) IF ABS NON_MODEL_VAR NOT IN 0.65625 .. 0.671875 OR ABS LIKE_DURATION_M23'(NON_MODEL_CONST) NOT IN
0.65625 .. 0.671875 THEN FAILED ("ABS (2.0
(C) IF A > B XOR NOT (A <= B) THEN FAILED ("A > B IS NOT THE SAME AS NOT (A <= B)"); END IF;

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(C) IF ARRAY_3 = ARRAY_4 THEN          FAILED ("INCORRECT RESULTS FROM COMPARING MULTI-" &          "DIMENSIONAL
ARRAYS - 3");          END IF;
(C) IF (IDENT_INT (10) OR 1) /=          (5 OR 10) /= 'L' THEN          FAILED ("OVERLOADING OF ""OR"" OPERATOR DEFECTIVE");
END IF;          END; -- (C)
(C) IF (IDENT_INT (10) Xor 1) /=          (5 XOR 10) /= 'L' THEN          FAILED ("OVERLOADING OF ""XOR"" OPERATOR DEFECTIVE");
END IF;          END; -- (C)
(C) If we are dealing with a positional aggregate with an others -- choice make sure the number of positional elements specified -- does
not overflow the aggregate bounds. We als
(C) ignored struct { B int } // missing field (A) ignored; data will be dropped
(C) I := IDENT_INT (10); P2 (I, "10");          FAILED ("CONSTRAINT_ERROR NOT RAISED FOR P2 (10)");          EXCEPTION          WHEN
CONSTRAINT_ERROR =>          COUNT := COUNT
(C) I := IDENT_INT (10); T2.E2 (I, "10");          FAILED ("CONSTRAINT_ERROR NOT RAISED FOR E2 (10)");          EXCEPTION          WHEN
CONSTRAINT_ERROR =>          COUNT := CO
(C) I : INTEGER;
(C)/ill-formed (C D = UINT_MAX+1 /* OK */
(C)Image); exception when Assertion_Error => Got_Assertion := True; end;
(C) immutable int_aaVersion = 1;
(C) IMPLICIT INTEGER(4) (I) DATA IPROG, IFILE, ITOTR, IRLGN, IREC�, IEOF, ICON21, ICON22, ICON31, ICON32, ICON33, ICON34, ICON55,
ICON56 /14*0/ OPEN (7, ACCESS = 'DIRECT', RECL = 80,
(C) Impl void[] vals){ assert(keys.length == vals.length);
(C) import Bug class(Bug) A type(Bug) C end function end interface
(C) import :: c_char, c_int character(kind=c_char,len=*), intent(in) :: this(..) integer(c_int), intent(inout) :: ierr end subroutine
process_string end interface!
(C) import ckind complex(ckind) stub end function end interface
(C) import c_ptr!GCC$ attributes NO_ARG_CHECK :: x type(*) :: x type(c_ptr) :: my_c_loc1 end function end interface my_c_loccontains
subroutine sub_scalar (arg1, presnt)
(C) import c_ptr type(*) :: x type(c_ptr) :: my_c_loc1 end function
(C) import c_ptr type(*) :: x(*) type(c_ptr) :: my_c_loc2 end function end interface my_c_loccontains subroutine sub_scalar (arg1, presnt)
type(*), target, optional ::
(C) import implicit none type(C_FUNPTR) kill_C_FUNPTR end function kill_C_FUNPTR end interfaceend module m3
(C) import implicit none type(C_PTR) kill_C_PTR end function kill_C_PTR end interface
(C) import integer(C_INT), value, intent(in) :: n END SUBROUTINE set_vm_limitEND INTERFACE
(C) import integer(int16), intent(in) :: x end function end interface integer(int16) :: x16 = 12345 x16 = bigendc16(x16) print *,x16
(C) import logical(c_bool), value :: is_present integer(c_int), optional :: var end subroutine end interfacecontains
(C) in 16 end if; end Is_Special_Character;
(C) in 16 or else
(C) IN A MAIN PROGRAM TASK BODY.
(C)(in Cif (is(C : dchar)){ auto src = input[index..$]; auto n = src.length; genericDecodeGrapheme!(false)(src); return n - src.length;
(C)(in C if (is(C : dchar)) { import std.conv : to; CodepointSet set; if (loadUnicodeSet!table(name, set)) return set;
throw new Exception("No u
(C)(in C if (is(C : dchar)) { return loadAny(name);
(C)(in C if (is(C : dchar)) { this ~= chars;
(C)(in C{ import std.algorithm.searching : find; auto names = [ "L", "Letter", "LC", "Cased Letter", "M", "Mark", "N", "Number",
"P", "Punctuation",
(C)(in C { import std.utf : decode, codeLength; size_t codeLen = 0; size_t lastNonTrivial = 0; size_t curIdx = 0; while (curIdx
!= str.length)
(C)(in C{ size_t j = 0; auto p = cast(dchar*)malloc(dchar.sizeof * s.length); auto r = p[0..s.length]; // r[] will never be longer than s[] foreach
(dchar c; s) r[j++] = c;
(C) I := NEW INTEGER P (I, I, I);          FAILED ("EXCEPTION NOT RAISED - C");          EXCEPTION          WHEN E =>          IF (I.ALL /=
101) THEN          FAILED
(C) IN MODE, TWO DIMENSIONS, DYNAMIC VARIABLE.-- (D) IN OUT MODE, THREE DIMENSIONS, STATIC VARIABLE.-- (E) OUT MODE, ONE
DIMENSION, DYNAMIC VARIABLE.-- (F) IN OUT MODE, NULL STRING A
(C) Inner function(double, string) cFuncPtr;
(C)(inout Cif (isSomeChar!C)in { assert(cstr); }body{ size_t length = 0; while (cstr[length]) ++length; return cstr[0 .. length];
(C) IN OUT, OUT OF RANGE ON CALL.-- (D) OUT, OUT OF RANGE ON RETURN.-- (E) IN OUT, OUT OF RANGE ON RETURN.
(C) IN OUT, OUT OF RANGE ON CALL.-- (D) OUT, OUT OF RANGE ON RETURN.-- (E) IN OUT, OUT OF RANGE ON RETURN.
(C) inout(void const TypeInfo tiValueArray) pure nothrow{ if (aa.empty) return null;
(C) inout(void){ if (aa.empty) return null;
(C) inout(void){ if (aa.empty) return null;
(C) inout(void) in void* pkey){ return _aaInX(aa, keyti, pkey);
(C) IN PARAMETER, VARIABLE.-- (D) IN OUT PARAMETER, EXCEPTION RAISED ON CALL.-- (E) OUT PARAMETER, EXCEPTION RAISED ON

```



```

CALL.
(C) int _aaApply2(AA aa, in size_t keysz, dg2_t dg){ if (aa.empty) return 0;
(C) int _aaApply2(void * _aaApply2(aggr, keysize, flde) */ static const char *name[2] = {
"_aaApply", "_aaApply2" };
(C) int _aaApply(AA aa, in size_t keysz, dg_t dg){ if (aa.empty) return 0;
(C) int _aaApply(void * _aaApply(aggr, keysize, flde) *
(C) int _aaEqual(in TypeInfo tiRaw, in AA aa1, in AA aa2){ if (aa1.impl is aa2.impl) return true;
(C) int _aApplycd1(in char{ int result; size_t len = aa.length;
(C) int _aApplycd2(in char{ int result; size_t len = aa.length;
(C) int _aApplycw1(in char{ int result; size_t len = aa.length;
(C) int _aApplycw2(in char{ int result; size_t len = aa.length;
(C) int _aApplydc1(in dchar{ int result;
(C) int _aApplydc2(in dchar{ int result; size_t len = aa.length;
(C) int _aApplydw1(in dchar{ int result;
(C) int _aApplydw2(in dchar{ int result;
(C) int _aApplyRcd1(in char{ int result;
(C) int _aApplyRcd2(in char{ int result; size_t i; size_t len = aa.length;
(C) int _aApplyRcw1(in char{ int result;
(C) int _aApplyRcw2(in char{ int result;
(C) int _aApplyRdc1(in dchar{ int result;
(C) int _aApplyRdc2(in dchar{ int result;
(C) int _aApplyRdw1(in dchar{ int result;
(C) int _aApplyRdw2(in dchar{ int result;
(C) int _aApplyRwc1(in wchar{ int result;
(C) int _aApplyRwc2(in wchar{ int result;
(C) int _aApplyRwd1(in wchar{ int result;
(C) int _aApplyRwd2(in wchar{ int result;
(C) int _aApplywc1(in wchar{ int result; size_t len = aa.length;
(C) int _aApplywc2(in wchar{ int result; size_t len = aa.length;
(C) int _aApplywd1(in wchar{ int result; size_t len = aa.length;
(C) int _aApplywd2(in wchar{ int result; size_t len = aa.length;
(C) int _adCmp2(void{ debug(adi) printf("_adCmp2(a1.length = %d, a2.length = %d)\n", a1.length, a2.length); return ti.compare(&a1, &a2);
(C) int _adCmpChar(void{ version (D_InlineAsm_X86)
(C) int _adCmp(void{ debug(adi) printf("adCmp()\n"); auto len = a1.length; if (a2.length < len) len = a2.length; auto sz = ti.tsize; void
*p1 = a1.ptr; void *p2 = a2.p
(C) int _adEq2(void{ debug(adi) printf("_adEq2(a1.length = %d, a2.length = %d)\n", a1.length, a2.length); if (a1.length != a2.length) return
0; // not equal if (!ti
(C) int _adEq(void{ debug(adi) printf("_adEq(a1.length = %d, a2.length = %d)\n", a1.length, a2.length); if (a1.length != a2.length) return 0;
// not equal auto sz = ti.tsize;
(C) int atoi(const char
(C) int bar(int a, int b) { //printf("a = %d, b = %d\n", a, b); assert(b - a == 1); return ++g;
(C) int brk(shared void
(C) int callback(dl_phdr_info { auto p = cast(DG*)arg; if (findSegmentForAddr(*info, p.addr))
(C) int cfc(int x, int y){ return x * 10 + y;
(C) int cfunc6596()
(C) int cmp(scope const void { return (cast(TypeInfo)ti).compare(p1, p2);
(C) int cmp(scope const void { return tiglobal.compare(p1, p2);
(C) int cmp(scope void { return (cast(TypeInfo)ti).compare(p1, p2);
(C) int cstyle(int, ...); extern(D) int dstyle(...); extern(D) int typesafe(int[...]);}@safe unittest
(C) int _d_run_main(int argc, char{ // Remember the original C argc/argv _cArgs.argc = argc; _cArgs.argv = argv;
(C) integer(C_INT) :: argend subroutine Callback
(C) integer :: x
(C) interface for the ARM EABI and C6X unwinders.// This corresponds to unwind-arm-common.h
(C) interface for the ARM EABI unwinder library.// This corresponds to unwind-arm.h
(C) interface for the C6X EABI unwinder library.// This corresponds to unwind-c6x.h
(C) interface for the generic unwinder library.// This corresponds to unwind-generic.h
(C) interface for the GNU/GCC pointer encoding library.// This corresponds to unwind-pe.h
(C) int F13(int); alias extern(D) int F14(int); alias extern(Windows) int F15(int); static assert(is(DerivedFunctionType!(F13, F14) == void)); static
assert(is(DerivedFunctionType!(F13
(C) int Foo1(int a, int b, int c) { assert(a == 1); assert(b == 2); assert(c == 3); return 1;
(C) int foo7();

```



```

(C) int sysctl (const int      size_t* oldlenp, const void* newp, size_t newlen);else version (NetBSD)
(C) int test88a();
(C) int test88a(){ return 0;
(C) int testc(int i); extern (Windows) int testw(int i); extern (D) int testd(int i);}
(C) int testc(int i) { printf("foo.testc(%p)\n", this); assert(x == 47); return i + x;
(C) is allowed since TS 29113!
(C) is not yet" " supported", sym->name, &(sym->declared_at), sym->ns->proc_name->name); retval = false; }
(C) i; static assert(!_traits(compiles, m = i));void bug5284_2a()
(C) is when '1' => Debug_Flag_1 := Val; when '2' => Debug_Flag_2 := Val; when '3' =>
Debug_Flag_3 := Val; w
(C) is when '1' => Debug_Flag_Dot_1 := Val; when '2' => Debug_Flag_Dot_2 := Val; when '3' =>
Debug_Flag_Dot_3 := Val;
(C) is when '1' => Debug_Flag_Underscore_1 := Val; when '2' => Debug_Flag_Underscore_2 := Val; when
'3' => Debug_Flag_U
(C) is when 'A' => Debug_Flag_AA := Val; when 'B' => Debug_Flag_BB := Val; when 'C' =>
Debug_Flag_CC := Val;
(C) is when 'a' => Debug_Flag_A := Val; when 'b' => Debug_Flag_B := Val; when 'c' =>
Debug_Flag_C := Val; w
(C) is when 'A' => Debug_Flag_Dot_AA := Val; when 'B' => Debug_Flag_Dot_BB := Val; when 'C' =>
Debug_Flag_Dot_CC := Val
(C) is when 'a' => Debug_Flag_Dot_A := Val; when 'b' => Debug_Flag_Dot_B := Val; when 'c' =>
Debug_Flag_Dot_C := Val;
(C) is when 'A' => Debug_Flag_Underscore_AA := Val; when 'B' => Debug_Flag_Underscore_BB := Val;
when 'C' => Debug_Flag
(C) is when 'a' => Debug_Flag_Underscore_A := Val; when 'b' => Debug_Flag_Underscore_B := Val; when
'c' => Debug_Flag_U
(C) is when '^' => IP := Emit_Node (if (Flags and Multiple_Lines) /= 0 then MBOL elseif (Flags and
Single_Line) /= 0 t
(C)'Last)); First := C; end if; end if; end loop;
(C)'Last); Free (Params (C)); end if;
(C)'Last) = Param)) then Remove (Cmd.Expanded, C); Remove (Cmd.Params, C); Remove (Cmd.Sections,
C);
(C) LIMITED PRIVATE TYPE.
(C) LIMITED PRIVATE TYPE, UNCONSTRAINED ACTUALS, NO DEFAULTS.-- (D) ARRAY TYPE, CONSTRAINED ACTUALS, DEFAULTS.
(C) logical(c_bool), intent(in), value :: is_present integer(c_int), intent(inout), optional :: var if (is_present) then if (.not. present (var))
STOP 1 if (var /= 43) S
(C) loop Check_Static_Type (Etype (C), N, DT); Next_Component_Or_Discriminant (C); end loop;
(C) loop Ensure_Type_Is_SA (Etype (C)); Next_Formal (C); end loop;
(C) loop exit when C = Character'Last; C := Character'Succ (C); end loop;
(C) loop Expr := Expression (C);
(C) loop Field_Name := Chars (Defining_Identifier (C));
(C) loop if C = CR or else C = LF then Skip_Line; C := Nextc;
(C) loop if Chars (C) = Chars (Comp) then return C; end if;
(C) loop if Chars (C) = Chars (Selectr) then
(C) loop if Ekind (C) = E_Discriminant or else Ekind (C) = E_Component then Ensure_Type_Is_SA (Etype (C));
(C) loop if Etype (C) = B_Typ then Set_Entity_With_Checks (N, C); Generate_Reference (C, N); return; end
if;
(C) loop if Is_Access_Type (Etype (C)) then return True; else C := Next_Discriminant (C); end if;
end loop;
(C) loop if Is_Limited_Type (Etype (C)) then return True; end if;
(C) loop if Nkind (Parent (C)) = N_Component_Declaration then Copy_Dimensions (Expression (Parent
(Corresponding_Record_Component (C))), Expression (P
(C) loop Indx := First_Index (Etype (Prefix (N)));
(C) LOOP OLD_TIME := CLOCK; DELAY X; LAPSE := CLOCK - OLD_TIME; EXIT; END LOOP;
COMMENT ("(C) - ZERO DELAY LAP
(C) loop Store_String_Char (C); Src_Ptr := Src_Ptr + 1; C := Sbuffer (Src_Ptr); end loop;
(C) loop Write_Indent; Write_Id (C); Write_Str (" : "); Write_Id (Etype (C));
(C) make_complex_modes (MODE_static voidmake_complex_modes (enum mode_class cl, const char *file, unsigned int line){ struct
mode_data *m; struct mode_data *c; enum mode_class cclass =
(C) MarshalJSON() ( return []byte("<>"), nil}
(C) mc := make(map mc[nil] = 42 mc[c] = 42mc[make(C)] = 42

```

```

(C) Method()func (Z) Method() {}
(C) must not have a CONTAINS" " section at %C", gfc_current_block ()->name);
(C) must not have a CONTAINS section. */ if (gfc_current_block ()->attr.sequence) gfc_error ("Derived-type %qs with SEQUENCE must not have a
CONTAINS" " section at %C", gfc_current
(C) names ( sym->refs++; /* Needed to avoid an ICE in gfc_release_symbol */ return MATCH_ERROR; }
(C) names ( sym->refs++; /* Needed to avoid an ICE in gfc_release_symbol */ return MATCH_ERROR; }
(C) NEW_PAGE (FILE); NEW_LINE (FILE); NEW_PAGE (FILE);
(C) @nogc nothrow:
(C) @nogc nothrow{ version (CRuntime_Glibc) int pthread_getattr_np(pthread_t thread, pthread_attr_t* attr); version (FreeBSD) int
pthread_attr_get_np(pthread_t thread, pthread_attr_t* attr);
(C) nothrow{ alias ssize_t function(void *cookie, char *buf, size_t size) cookie_read_function_t; alias ssize_t function(void *cookie, const(char) *buf,
size_t size) cookie_write_function_t;
(C) nothrow @nogc:
(C) nothrow @nogc{
(C) nothrow @nogc FILE
(C) nothrow @nogc FILE
(C) nothrow @nogc { int __libc_current_sigrtmin(); int __libc_current_sigrtmax();
(C) nothrow @nogc{ ptrdiff_t getdelim(char**, size_t*, int, FILE*); // getline() always comes together with getdelim() ptrdiff_t getline(char**,
size_t*, FILE*);
(C) nothrow @nogc @system:
(C) nothrow @system:
(C) NOW : TIME;
(C) NOW := TIME_OF (1984, 8, 12, 500.0); LATER := NOW + INCREMENT;
(C) NOW := TIME_OF (1984, 8, 13); IF SECONDS (NOW) /= 0.0 THEN FAILED ("TIME_OF() DID NOT ZERO SECONDS - (C)");
END IF; END; -- (C)
(C)\n";} //pragma( msg, d_generate_functions() );mixin( d_generate_functions() );
(C) NULL;
(C) NULL; END; -- (C)
(C) obj0; static assert(is(typeof(obj0) == C));
(C) objc_get_class(C)#endif
(C) Object_d_newclass(const ClassInfo ci){ void* p;
(C) Object_d_newclass(const TypeInfo_Class ci);
(C) Object_d_toObject(void
(C) object_getClassName(C)
(C) OBJ_INT := F1; IF GLOBAL /= 3 THEN FAILED ("DEPENDENT TASK NOT TERMINATED BEFORE " &
"FUNCTION EXIT - 3"); END IF; END; --
(C) ONE SUBPROGRAM HAS ONE LESS PARAMETER THAN THE OTHER.
Copyright -- -----
Copyright = 0; char[ACMDRIVERDETAILS_LICENSEING_CHARS] szLicensing = 0; char[ACMDRIVERDETAILS_FEATURES_CHARS] szFeatures =
0;}alias ACMDRIVERDETAILS* LPACMDRIVERDETAILS;
Copyright = 0; WCHAR[ACMDRIVERDETAILS_LICENSEING_CHARS] szLicensing = 0; WCHAR[ACMDRIVERDETAILS_FEATURES_CHARS] szFeatures =
0;}alias ACMDRIVERDETAILS* LPACMDRIVERDETAILS;
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Copyright 1994 by Stephen L. Moshier * * Arguments |x| <= 13 are reduced by recurrence and the function * approximated by a rational function of
degree 7/8 in the * interval (2,3). Large argument

```

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COPYRIGHT 1999 SPACKMAN REAL(R4\_KV), dimension (10) :: QLA1, QLA2, QLA3, & QCA = ((i, i= 1, 10)) EQUIVALENCE (QLA1, QLA2) QLA1 = QCA QLA3 = QCA

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Copyright (C) 2019 Free Software Foundation, Inc.\n"); printf ("This is free software; see the source for copying conditions.\n" "There is NO  
warranty; not even for MERCHANTABILITY or \n" "F  
Copyright (C) "2019 if (contributed) { FIO\_WriteString (f, (char \*) "Contributed by ", 15); contributedContents = SFIO\_WriteS (f,  
contributedContents); FIO\_WriteString (f, (ch  
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copyright{\}/(c)/g; s/\@dots{\}/.../g; s/\@enddots{\}/.../g; s/\@([! ? ])/\$1/g; s/\@[: -]/g; s/\@bullet(?:\{\})?/\*g; s/\@TeX{\}/TeX/g;  
s/\@pounds{\}/\#/g; s/\@mi  
Copyright (c) Henrik Ravn 2004  
copyright comment if present. comments := file.Comments if len(comments) > 0 && strings.HasPrefix(comments[0].Text(), "Copyright") {



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comments = comments[1:] }
COPYRIGHT COMMENT PROBLEM.
copyright conditions above. * * 2005 - Adaptation to Microsoft C Compiler for AMD64 by Gilles Vollant
copyright conditions above. * * Dec-29-2003 -- I added AMD64 inflate asm support. This version is also * slightly quicker on x86 systems because,
instead of using rep movsb to copy * data, it use
copyright conditions above.; *; * Mar-13-2003 -- Most of this is derived from inffast.S which is derived from; * the gcc -S output of zlib-1.2.0/inffast.c.
Zlib-1.2.0 is in beta release at; *
copyright conditions above. * * This version (Jan-23-2003) of inflate_fast was coded and tested under * GNU/Linux on a pentium 3, using the gcc-3.2
compiler distribution. On that * machine, I fou
copyright; const char const char *main_d; // dummy filename for dummy main() Array<const char *> *path; // Array of char*'s which
form the import lookup path Array<const
Copyright (content, state): global html
Copyright: Copyright */
Copyright: Copyright 2003-2004 by Matthew Wilson and Synesis Software
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copyright{copyright \def\dots{...}% \def\enddots{...}% \def\equiv{==}% \def\error{error}% \def\euro{euro}% \def\expansion{==>}%
\def\geq{>=}% \def\guillemetleft{<<}% \def\guillemetri
Copyright (Copyright): def __init__(self, errors):
Copyright: Copyright Digital Mars 1999 - 2013.
Copyright: Copyright Digital Mars 2000 - 2009.
Copyright: Copyright Digital Mars 2000 - 2010.
Copyright: Copyright Digital Mars 2000 - 2011.
Copyright: Copyright Digital Mars 2000 - 2011
Copyright: Copyright Digital Mars 2000 - 2011. * D implementations of tan, atan, atan2, exp, expm1, exp2, log, log10, log1p, * log2,
floor, ceil and lrint functions are based o

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Copyright: Copyright Sean Kelly 2010 - 2014.
copyright = Copyright):      self.errors = Errors()
copyright = copyright (self.errors)      self.dirs = []      self.default_dirs = []      self.chosen_dirs = []      self.option_handlers = dict()
self.option_help = []
Copyright: Copyright Sociomantic Labs GmbH.
Copyright: Copyright the respective authors, 2008-
copyright{'|copyright = u\      '@set\s+copyright[\w-]+'
copyright cp -p html/gccgo.html echo $(p_god) >> debian/indep_binaries
copyright cp -p html/gfortran.htmlendif      echo $(p_g95d) >> debian/indep_binaries
Copyright (C) <replaceable>year</replaceable> <replaceable>name of author</replaceable>
Copyright (C) %s Free Software Foundation, Inc.\n
Copyright (C)");      Write_Str (Initial_Year);      Write_Char ('-');      Write_Str (Gnatvsn.Current_Year);      Write_Str (" ");      Write_Str
(Gnatvsn.Copyright_Holder);      Write_Eol;
Copyright (C) year name of author
Copyright: Danny Milosavljevic 2014
Copyright dates
Copyright dates.
Copyright dates and added GPL notices to all files.
Copyright David Nadlinger 2011.
copyright) {      dc->copyright->nooutput = 1;
copyright debian/dh_rmemptydirs -p echo $(p_cppd) >> debian/indep_binaries
copyright debian/dh_rmemptydirs -p echo $(p_doc) >> debian/indep_binaries
copyright} \DeclareUnicodeCharacter \DeclareUnicodeCharacter{00AB}{\guillemetleft} \DeclareUnicodeCharacter{00AD}{\-}
\DeclareUnicodeCharacter{00AE}{\registeredsymbol} \DeclareUnicodeCharact
copyright \definedummyword\registeredsymbol \definedummyword\dots \definedummyword\enddots \definedummyword\entrybreak
\definedummyword\equiv \definedummyword\error \definedummyword\euro
Copyright: def __init__(self, errors):      self.errors = errors
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copyright (dir, filename, filter,      file, line, match)      (this_changed, line, next_line) = res
copyright disaster and is nowhelping with the new documentation.
Copyright() displayMenu() displayLibraryClass()else:
Copyright; -- Display the copyright notice
Copyright; -- Display the Copyright notice. If called several times, display the -- Copyright notice only the first time.
copyright>)dnl
copyright does not belong to the
Copyright: Dominic Sayers, Jacob Carlborg 2008-.
Copyright Don Clugston 2008 - 2010.
copyright = ') else      + canon_form + self.separator      + line[match.end (2):]
Copyright;      end if;
copyright */enum LIBCURL_COPYRIGHT = "1996 - 2010 Daniel Stenberg, <daniel@haxx.se>.";
copyright = fd.read()
copyright files common to all libraries contained in the GNU Compiler Collection (GCC).ifndef(`BASE_ONLY', `dnl . This version of GCC is not yet available
for this architecture. Please use the com

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copyright { font-size: smaller; background: #f2f2f9; border: 2px solid #3366cc; border-style: solid; border-width: thin; padding: 4px;}
Copyright FSF 1993, 2007 and fall
CopyrightFSF 1995, 2007 and fall under the GPL version 3 or later.
Copyright functions endFunctions[display] = doDisplayEnd functions[email] = doEmail functions[end] = doEnd functions[enumerate] =
doEnumerate endFunctions[enumerate]
copyright} \gdef \gdef^^ab{\guillemetleft} \gdef^^ac{\not$} \gdef^^ad{-} \gdef^^ae{\registered} \gdef^^af{=} %
\gdef^^b0{\textdegree} \gdef^^b1{\pms} \gdef^^b2{^2$}
copyright = get_copyright()
Copyright Guillaume Chatelet 2016.
© Copyright Henrik Ravn 2004
Copyright Henrik Ravn 2004
copyright in changelog entries. return re.compile ('\t')
copyright information already in place.
copyright information # and isn't updated. if filename == 'README' and os.path.basename (dir) == 'g++.niklas':
copyright is associated with the package # author. canon_form = self.holders[holder] if not canon_form:
copyright is incomplete, add the next line. while not self.is_complete (match):
Copyright ISO/IEC (International Organization for Standardization and International Electrotechnical Commission) 1996, 1997, 1998,1999, 2000, 2001,
2002, 2003, 2004, 2005, 2006, 2007, 2008, 2009, 201
Copyright ISO/IEC (International Organization for Standardization and International Electrotechnical Commission) 1996-2020.
copyright issues, IMO, since it* doesn't do anything substantive. SUBROUTINE OUTDNS(A,B,LCONV) IMPLICIT REAL(kind=8) (A-H,O-
Z),INTEGER(I-N) COMMON/ARRAYS/Z(64,8),AB(30,30),PAIRS(9,9
copyright' is too noisy, and # HTML header markers, so check for 'copyright' and two digits. self.other_copyright_re = re.compile ('copyright.*
[0-9][0-9]',
Copyright Jacob Carlborg 2015.
Copyright Janice Caron 2008 - 2009.
Copyright Jeremie Pelletier 2008 - 2009.
Copyright Jeremy Siek return GenericFilter.get_line_filter (self, dir, filename)
Copyright Johannes Pfau 2011 - 2012.
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Copyright Jonas Drewsen 2011 - 2012.
Copyright Kai Nacke 2012.
copyright" line and a pointer to where the full notice is found. </para> <screen><replaceable>one line to give the program's name and a brief idea
of what it does.</replaceable>
copyright" line and a pointer to where the full notice is found. </p><pre class="screen"><em class="replaceable"><code>one line to give the
program's name and a brief idea of what it does.</cod
copyright lines that might have slipped by. elif self.other_copyright_re.search (line):
copyright) (m2-fsf-notice) (insert "DEFINITION MODULE ") (insert (substring (buffer-name) 0 -4) " ; ") (m2-dialect-tag) (insert "\n\n\n\nEND "
(substring (buffer-name) 0 -4) ".\n") (previ
copyright) (m2-fsf-notice) (let ((type (read-string "(i)mplementation or (m)odule:"))) (if (string-equal type "i") (insert "IMPLEMENTATION "))
(insert "MODULE " (substring (buffer-name)
Copyright Martin Nowak 2012.
Copyright Martin Nowak 2012. Etienne Cimon 2015.
Copyright: Masahiro Nakagawa 2010-.
copyright messages
COPYRIGHT))\n\ </body></html>\n\n\B = <b>$0</b>\nI = <i>$0</i>\nU = <u>$0</u>\nP = <p>$0</p>\nDL =
<dl>$0</dl>\nDT = <dt>$0</dt>\nDD = <dd>$0</dd>\nTA
copyright(NULL), macros(NULL), pmacrotable(NULL), pescapetable(NULL) { }
copyright of its own, but quotes the GPL. 'intro.xml', ])
copyright of that document, you can freely ---- copy and modify this specification, provided that if you redistribute a ---- modified version, any changes
that you have made are clearly indicated.
Copyright(outfile, l, n, leader1, leader2) outfile.writelines(l[n:]) outfile.close() print(("> mv tmp tidy", file))
command = "mv tmp tidy
copyright - output the (C) symbol. #
Copyright Patrick Powell 1995 * This code is based on code written by Patrick Powell <papowell@astart.com> * It may be used for any purpose as long
as this notice remains intact * on all source cod
copyright p:nth-child(3)
Copyright (): print("@c Copyright (C) 2000, 2001, 2002, 2003, 2004, 2005, 2006, 2007, 2008")
Copyright: Public Domain
Copyright; Put_Line ("Usage: gnatclean New_Line;
Copyright: Red-black tree code copyright (C) 2008- by Steven Schveighoffer. Other code
copyright# Redistribution and use in source and binary forms, with or without# modification, are permitted provided that the following conditions# are

```

```

met:
copyright() return content, state
copyright(): return u
Copyright %s 2019 Free Software Foundation, Inc.\n", _("(C)"); fnotice (stdout, _("This is free software; see the source for copying
conditions.\n" "There is NO w
Copyright %s 2019 Free Software Foundation, Inc.\n", _("(C)"); fnotice (stdout, _("This is free software; see the source for copying
conditions.\n" "There is NO warranty; not even for
Copyright %s 2019 Free Software Foundation, Inc.\n", _("(C)"); fputs (_("This is free software; see the source for copying conditions. There is
NO\n\warranty; not even for MERCHANTABILITY
Copyright %s 2019 Free Software Foundation, Inc.\n", _("(C)"); fputs (_("This is free software; see the source for copying conditions. There is
NO\n\warranty; not even for MERCHANTABILITY or
Copyright Sean Kelly 2005 - 2009.
Copyright Sean Kelly 2005 - 2016.
Copyright Sean Kelly 2008 - 2009.
Copyright Sean Kelly 2009 - 2014.
copyright = sec; } if (icmp("macros", sec->name, sec->namelen) == 0)
copyright; Section Macro **pmacrotable; Escape **pescapetable;
copyright (self, dir, filename, filter, file, line, match): orig_line = line next_line = None pathname = os.path.join (dir, filename)
copyright (self): self.raw('&copy;') # # emitTagName - write out tag names
COPYRIGHT SIGN
COPYRIGHT SIGN {"copysr", 0x02117}, // SOUND RECORDING COPYRIGHT {"CounterClockwiseContourIntegral", 0x02233}, //
ANTICLOCKWISE CONTOUR INTEGRAL {"crrar",
copyright: %s % line.strip()) lines.append (line) line = next_line
Copyright Sociomantic Labs GmbH.
Copyright Sociomantic Labs GmbH 2016.
copyright's starting year.
copyright string in the executable of your product. */
Copyright \textcopyright 2014-2016, Intel Corporation. All rights reserved
Copyright The Go Authors.\n\n" "package build\n\n" + "// shouldBuild checks tags given by lines of the form\n" + "// +build tag\n"
+ "func shouldBuild(content []byte)\n" want3 := map[string]b
copyright# THIS SOFTWARE IS PROVIDED BY THE COPYRIGHT HOLDERS AND CONTRIBUTORS# "AS IS" AND ANY EXPRESS OR IMPLIED
WARRANTIES, INCLUDING, BUT NOT# LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY
copyright to help * - Send help to stdout instead of stderr * - Add some preemptive typecasts * - Add L to
constants in lseek() calls
copyright to this source code.
copyright to this source code.// https://sites.google.com/site/murmurhash/static inline uint32_t calcHash(const uint8_t *data, size_t len){ // 'm' and 'r'
are mixing constants generated offline
copyright to this source code. In place of** a legal notice, here is a blessing:
copyright = True
copyright (void){ static const char copyright[] = "\v* Unicode characters and various properties.\n\
Copyright; Write_Line ("Usage: gnatprep "infile outfile [deffile]"); Write_Eol; Write_Line (" infile Name of the input file");
Write_Line (" outfile
copyright (); write_table (); write_context_switch (); return 0;
Copyright XXXX-YYYY Free Software Foundation, Inc.
Copyright years on GCC source files may be listed using rangenotation, e.g., 1987-2012, indicating that every year in the range,inclusive, is a
copyrightable year that could otherwise be listed
copyright year years = match.group (2).strip() try:
(C)* or //[C] -> C* or const(C)* static if (( is(const(Unqual!InChar) == InChar) && is(const(Unqual!OutChar) == OutChar)) ||
(!is(const(Unqual!InChar) == InChar) && !is(immutable
(C)* or //[C] -> immutable(C)* else { import std.array : uninitializedArray; auto copy = uninitializedArray!(Unqual!OutChar[])(str.length
+ 1); copy[0 .. $ - 1] = s
(C) or else C = Character C := Character'Succ (C); end loop;
(C) or else C = end Is_Alnum;
(C) or not, depending on the given parameter, is_bind_c. */
(C) P1 (R); FAILED ("EXCEPTION NOT RAISED IN SUBTEST (C)"); EXCEPTION WHEN CONSTRAINT_ERROR => NULL;
WHEN OTHERS => FAILED ("
(C) PACKAGE BODY.-- (D) GENERIC FUNCTION BODY.-- (E) GENERIC PACKAGE BODY.-- (F) TASK BODY.
(C) (Params (C)
(C) PC (IDENT_INT(1), IDENT_INT(3)); EXCEPTION WHEN OTHERS => FAILED ("EXCEPTION RAISED IN CALL TO PC");
END; -- (C)
(C) pragma(mangle, "clock else clock_t clock());else version (Darwin) // other Darwins (iOS, TVOS, WatchOS)

```

```

(C) _Pragma("moose") ALPHA(C) BETA(C) GAMMA(baz);
(C) PRIVATE ACCESS PARAMETERS TO PROCEDURES.-- (D) PRIVATE ACCESS PARAMETERS TO FUNCTIONS.
(C) private pure nothrow @nogc static // TODO remove pure when https://issues.dlang.org/show_bug.cgi { pragma(mangle, "free") void
pureFree( void *ptr ); pragma(mangle, "gc_addRang
(C) private pure @system @nogc nothrow{ pragma(mangle, "getErrno") int fakePureGetErrno(); pragma(mangle, "setErrno") int
fakePureSetErrno(int);
(C) private{ pure @trusted @nogc nothrow pragma(mangle, "fakePureReprintReal") void pureReprintReal(char[] nptr);
(C) private pure @trusted @nogc nothrow { static if (GNU_Thread_Model == ThreadModel.Posix)
(C) private static int _progressCallback(void *ptr, double dltotal, double dlnow, double ultotal, double ulnow) {
auto b = cast(Curl
(C) private static int _seekCallback(void *ptr, curl_off_t offset, int origin) { auto b = cast(Curl*) ptr; if (b._onSeek == null) return
CurlSeek.cantseek;
(C) private static int _socketOptionCallback(void *ptr, curl_socket_t curlfd, curlsocktype purpose) { auto b = cast(Curl*)
ptr; if (b._onSocketOpt
(C) private static size_t _receiveCallback(const char* str, size_t size, size_t nmemb, void* ptr) { auto b = cast(Curl*) ptr;
if (b._onReceive != nu
(C) private static size_t _receiveHeaderCallback(const char* str, size_t size, size_t nmemb, void* ptr) { import std.string
: chomp;
(C) private static size_t _sendCallback(char *str, size_t size, size_t nmemb, void *ptr) { Curl* b = cast(Curl*) ptr; auto a = cast(void[])
str[0 .. size*nmemb]; if (b.
(C) procedure,
(C) procedure and the symbol is not BIND(C), or vice versa, we haven't found the right procedure. */
(C) procedure "because derived type %qs is not C interoperable", sym->name, &(sym->declared_at),
sym->ns->proc_name->name, sym->ts.u.derived->name); else if (sym->ts.typ
(C) procedure "because it is polymorphic", sym->name, &(sym->declared_at), sym->ns->proc_name-
>name); else if (warn_c_binding_type) gfc_warning (OPT_Wc_binding_type,
(C) procedure. call foo_opt (flag = 2) if (status == 0) then write(*,*) 'OK' else stop 1 end if end subroutine
(C) procedure "interoperable", sym->name, &(sym->declared_at), sym->ns->proc_name->name);
}
(C) procedure " %qs", sym->name, &sym->declared_at, sym->ns->proc_name->name)) return; else if
(!gfc_logical_kinds[j].c_bool && !gfc_notify_std (GFC_STD_GNU, "LOGICAL
(C) procedures as ISO Fortran Interop descriptors. These have to be converted to gfortran descriptors and back again. This has to be done here so
that the conversion occurs at the start of the
(C) procedures so set it from the descriptor element length. */ if (sym->ts.type == BT_CHARACTER && sym->ts.u.cl->backend_decl && VAR_P
(sym->ts.u.cl->backend_decl)) { tmp = build_f
(C) procedure &sym->declared_at, sym->attr.function ? sym->name : sym->ns-
>proc_name->name))return; }
(C) procedure sym->name, &(sym->declared_at), sym->ns->proc_name->name, &(sym->ns-
>proc_name->declared_at)); sym->ts.f90_type = sym->ts.type; }
(C) procedure! 'test' but may not be C interoperable!)
(C) procedure with NAME may not have " "POINTER attribute at %C"); return MATCH_ERROR; } if (has_name_equals &&
sym->attr.dummy)
(C) prohibited in an " "elemental procedure", &entry->declared_at); return MATCH_ERROR; }
(C) @property void f3() deprecated("") @safe @safe void f4() {} @1) @1) void f5() {} // OK
(C) @property void popFront(){} } struct FrontS(C)
(C) prototypes. if (flag_c_prototypes) { for (gfc_current_ns = gfc_global_ns_list; gfc_current_ns; gfc_current_ns = gfc_current_ns->sibling)
gfc_dump_c_prototypes (gfc_current_ns, std
(C) Proxy{ return &pthis;
(C) pure nothrow @nogc:
(C) pure nothrow @nogc @safe{ Range_aaRange(AA aa)
(C) pure nothrow @nogc @system:
(C) pure nothrow Object typecons_d_toObject(void)
(C) pure nothrow @safe @nogc void function(), DG5 = extern(D) pure nothrow @safe @nogc void delegate();
(C) pure static if (false) void F10(){} else void F11(){} }
(C) pure static if (false) void G10(){}else
(C) putchar_unlocked (C)# endif# ifdef HAVE_GETC_UNLOCKED# undef getc# define getc(Stream) getc_unlocked (Stream)# endif# ifdef
HAVE_GETCHAR_UNLOCKED# undef getchar# define getchar() getch
(C) REAL C(LDC, LOGICAL LSAME LOGICAL UPPER IF (LSAME) THEN DO 170 J = 1,N DO 140 K = 1,J IF
(UPPER) THEN END
(C) real, contiguous :: x(:) if (any(abs(x - [2.,4.,6.] > 1.e-6)) stop 1 x = [2.,4.,6.]*10.0 end subroutine
(C) real rndtonl(real x);

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(C) RECORD ACCESS TYPE.
(C)(ref Cif (is(C == char) || is(C == wchar) || is(C == dchar)){ toCaseInPlace!(LowerTriple)(s);
(C)(ref Cif (is(C == char) || is(C == wchar) || is(C == dchar)){ toCaseInPlace!(UpperTriple)(s);
(C)(ref Cif (isNarrowString!(C[])){ import std.algorithm.comparison : min;
(C)(ref C if (isSomeChar!C && is(Unqual!C == C) && !is(C == enum)) { import std.exception : enforce;
(C)(ref Cif (isSomeChar!C && is(Unqual!C == C) && !is(C == enum)){ return stdin.readLine(buf, terminator);
(C)(ref C if (isSomeChar!C) { return fwdStr!"match"(str);
(C)(ref C if (isSomeChar!C) { return fwdStr!"skip"(str);
(C)(ref C if (isSomeChar!C) { return fwdStr!"test"(str);
(C)(ref C size_t destIdx)@trusted pure if (is(C == char) || is(C == wchar) || is(C == dchar)) { import std.utf : decode; alias
caseLength = toCaseLength!(indexFn,
(C)(ref C{ str = str; str = str[1 .. str.length];
(C) RELATIONS BASED ON SECONDS.-- (D) RELATIONS AT EXTREMES OF THE PERMITTED RANGE OF TIME.
(C) (Result C)
(C) (Result C)
(C) result(res) logical(kind=2) :: res logical(kind=4) :: local ! OKend function sub4
(C) return value.implicit none INTERFACE FUNCTION my() BIND(C,name="my") RESULT(r) ! { dg-error "cannot be an array" } USE iso_c_binding
CHARACTER(kind=C_CHAR) :: r(10) END F
(C) routine may not be interoperable if they are implicitly typed. */ gfc_warning_now (OPT_Wc_binding_type, "Implicitly declared variable
" "%qs at %L may not be C intero
(C) R : REC := (IDENT_INT(2), "AA");
(C) R : REC := (IDENT_INT(2), "AA");
(C) RT function(P) nothrow @nogc;}
(C) RT function(P) nothrow @nogc @system;}
(C) // rt.minfo{ void rt_moduleTIsCtor(); void rt_moduleTIsDtor();
(C) S11 ctest11(ubyte x, S11, ubyte y);
(C) S12 ctest12(ubyte x, S12, ubyte y);
(C) S13 ctest13(ubyte x, S13, ubyte y);
(C) S14 ctest14(ubyte x, S14, ubyte y);
(C) S15 ctest15(ubyte x, S15, ubyte y);
(C) S16 ctest16(ubyte x, S16, ubyte y);
(C) S7 ctest10();
(C) SAME AS (A), WITH TYPE CONVERSION.-- (D) SAME AS (B), WITH TYPE CONVERSION.
(C)! See PR fortran/39239! { dg-do compile }module m use iso_c_binding implicit none integer(c_int) :: i1, i2 bind(C) :: i2 equivalence(i1,i2) ! { dg-
error "EQUIVALENCE attribute conflic
(C) seven = 46 END FUNCTION seven integer FUNCTION eight() bind(C, name='nine') eight = 48 END FUNCTION eight integer FUNCTION ten()
bind(C, name='') ten = 48 END FUNCTION ten
(C) %s function(double, string", inner_name));
(C) short HIWORD_(uint i){ return cast(short)(( i >> 16) & 0xFFFF);
(C) since that only implies that they're interoperable, and they are checked fully for interoperability when a variable is declared of that type. */ if
(sym->attr.is_bind_c && sym->attr.u
(C) / sizeof(int); const int Q = sizeof(D) / sizeof(int); const int R = sizeof(E) / sizeof(int);
(C) size_t _aaLen(in AA aa) pure nothrow @nogc{ return aa ? aa.length : 0;
(C) size_t _d_arraysetcapacity(const TypeInfo ti, size_t newcapacity, void)
(C) size_t _d_arraysetcapacity(const TypeInfo ti, size_t newcapacity, voidin{ assert(ti); assert(!(*p).length || (*p).ptr);
(C) size_t function(size_t) gc_reserve;
(C) size_t function(void
(C) size_t function(void
(C) size_t gc_extend( void
(C) size_t gc_extend( void{ if ( proxy is null ) return 0; return proxy.gc_extend( p, mx, sz, ti );
(C) size_t gc_reserve( size_t sz ){ if ( proxy is null ) return 0; return proxy.gc_reserve( sz );
(C) size_t gc_reserve( size_t sz ) nothrow;
(C) size_t gc_sizeOf( void
(C) size_t gc_sizeOf( void{ if ( proxy is null ) return 0; return proxy.gc_sizeOf( p );
(C) size_t strlen(const char
(C) specifier,
(C) specifier, with the optional given, and set the binding label in either the given symbol (if not NULL), or in the current_ts. The symbol may be NULL
because we may
(C)'s separately in different range of signs.
(C) ssize_t specialRead(void { memset(buf, 'a', size); return size;
(C) ssize_t specialWrite(void { int* c = cast(int*) cookie; flag = *c; written += size; return size;

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(C) statement at return MATCH_ERROR;
(C) statement in which ORIG_INDEX is the expression to be tested. If ORIG_TYPE is not NULL, it is the original ORIG_INDEX type as given in the
source before any compiler conversions. Gener
(C) static int cmp(const void * arr; int[4] arr; qsort(arr.ptr, arr[0].sizeof, arr.length, &S.cmp); int key; bsearch(&key, arr.ptr, arr[0].sizeof,
arr.length, &S.cmp);
(C) static void xtrnC() extern(D) static void xtrnD() {} } auto dg_xtrnC = toDelegate(&S.xtrnC); auto dg_xtrnD =
toDelegate(&S.xtrnD); static assert(! is(ty
(C) string{ return _d_args;
(C) strings with descriptor. int opr_flag_int; if (gfc_is_proc_ptr_comp (opr_expr)) { gfc_symbol *sym = gfc_get_proc_ptr_comp (opr_expr)-
>ts.interface; opr_flag_int = sym->attr.dime
(C) struct curl_fileinfo{ char *filename; // curlfiletype filetype; // time_t time; // uint perm; // int uid;
(C) struct curl_forms{ CURLformoption option; // const(char) *value; //
(C) struct curl_httppost{ curl_httppost *next; /** next entry in the list */ char *name; /** pointer to allocated name */ c_long
namelength; /** length of
(C) struct curl_khkey{ const(char) *key; /** points to a zero-terminated string encoded with base64 if len is zero, otherwise to the
"raw" data */ size_t len; //
(C) struct CURLMsg{ CURLMSG msg; /** what this message means */ CURL *easy_handle; /** the handle it concerns */ _N31 data; //
(C) struct curl_sockaddr{ int family; // int socktype; // int protocol; // uint addrlen; /** addrlen was a socklen_t type before 7.18.0 but it
turned really
(C) struct F5celse struct F5d {}
(C) struct _N28{ CURLversion age; /** age of the returned struct */ const(char) *version_; /** LIBCURL_VERSION */ uint version_num; /**
LIBCURL_VERSION_NUM */ const(char) *host;
(C) struct _N2{ char *time; // char *perm; // char *user; // char *group; // char *target; /** pointer to the target filename of a
symlink */
(C) (((struct objc_class# define method_getImplementation(M) (((Method)M)->method_imp)# define method_getTypeEncoding(M) (((Method)M)-
>method_types)# define object_getClass(O) (*(struct objc_cl
(C) // Subject: exception handling problem// Message-ID: <01H6I5GEAF5UPBJ0UV@VENUS.CPQD.ANSP.BR>
(C) symbol is C interoperable. If it is not, an appropriate error message is reported. */
(C) & " TERMINATED A " "STRING = COMPARISON"); END IF; END LOOP;
(C) test_dg(c => c); test_dg(delegate(C c) => c);
(C) test_fp(c => c); test_fp(function(C c) => c);}
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C) tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if defined
__WIN32 || defined __WIN32__ || defined __E
(C) T{ foreach (i; 0 .. a.length) a[i] = b[i] + c[i]; return a;
(C) THE BASE TYPE OF A PARAMETER IS DIFFERENT FROM THAT-- OF THE CORRESPONDING ONE.
(C) (the is_bind_c flag and the binding label), and verify that the type is C interoperable. Errors are reported by the functions used to set/test these
fields. */
(C) then Buf.Chars (Buf.Length) := 'W'; Set_Hex_Chars (C / 256); Set_Hex_Chars (C mod 256);
(C) then declare CC : constant Character := Get_Character (C); begin if CC in 'a' .. 'z' or else CC in '0' .. '9' then
Buf.Chars (Buf.Length) :
(C) then declare True_Branch : Boolean := Test_Result; Cond : Node_Id := C;
(C) then Ensure_Type_Is_SA (C); end if;
(C) then Error_Msg_Node_2 := Typ; Error_Msg_N ("& is not a component of}", Selectr); end if;
end;
(C) THEN FAILED("C DOES NOT EQUAL E"); END IF;
(C) then if Base_Char = '#' then Error_Msg_S -- CODEFIX ("missing '#"); else
Error_Msg_S -- CODEFIX
(C) then List.Container.Append (New_Item); end if;
(C) then L := N + 1;
(C) then Max_Ranges (Range_Num). High := C; exit; else Max_Ranges (Range_Num). High := Character'Pred (C);
end if; end loop;
(C) then Msglen := Msglen - 1; end if; end Buffer_Remove;
(C) then null;
(C) then OK := False; Ptr := J; exit;
(C) then P := P + 1;
(C) then return C; end if; end loop;
(C) then return Chars (Ent);
(C) then return; end if;
(C) then return; end if;

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(C) then      return False;      end if;
(C) then      return True;       end if;
(C)          then      return True;      end if;
(C) then      Scan_Reserved_Identifier (Force_Msg => False);  Ident_Node := Token_Node;  Scan; -- past the node  return Ident_Node;
(C) then      Scan_Reserved_Identifier (Force_Msg => True);
(C) then      Store_Compilation_Switch ("-gnatw_" & C);          else          Bad_Switch ("-gnatw_" & Switch_Chars (Ptr ..
Max));
(C) then      en          Store_Compilation_Switch ("-gnatw." & C);          else          Bad_Switch ("-gnatw." & Switch_Chars (Ptr ..
Max));
(C) then      en          Store_Compilation_Switch ("-gnatw" & C);          else          Bad_Switch ("-gnatw" & Switch_Chars (Ptr ..
Max));
(C) then      end
(C) then twiddle, if it  -- then frozzle and if it's another magic char then push C and  -- terminate the string. If none of the above, push C on the
-- string and go around again.
(C) then      Ungetc (ch, File);      return;      end if;
(c) there is clear notice in each\n\ modified Data File or in the Software as well as in the\n\ documentation associated with the Data File(s) or
Software that the\n\ data or software has bee
(C) THERE IS NO CLOSING
(C) Thread thread_attachByAddrB( ThreadID addr, void { GC.disable(); scope(exit) GC.enable();
(C) Thread thread_attachByAddr( ThreadID addr ) { return thread_attachByAddrB( addr, getThreadStackBottom( addr ) );
(C) Thread thread_attachThis(){ GC.disable(); scope(exit) GC.enable();
(C) Throwable.TraceInfo_d_traceContext(void{ if (traceHandler is null) return null; return traceHandler(ptr);
(C) TIME_OF() GIVES THE PARAMETER SECONDS A DEFAULT VALUE OF 0.0.-- (D) THE FUNCTIONS YEAR(), MONTH(), DAY(), AND SECONDS()
RETURN-- CORRECT VALUES USING NAMED NOTATION.-- (E) A VALUE RET
(C) to      max (sizeof(C), offset(D)+max (nvszize(D), dszize(D))). /* tree nvszize = CLASSTYPE_SIZE_UNIT (type); /* end_of_class doesn't
always give dszize, but it does in the case of a cla
(C) to max (sizeof(C), offset(D) offset = size_binop (PLUS_EXPR, DECL_FIELD_OFFSET (field), TYPE_SIZE_UNIT (TREE_TYPE
(field))); if (tree_int_cst_lt (result, offset)) result = offset;
(C) to try and prevent multiple errors being reported. /* has_non_interop_arg = 1;
(C) TraceHandler rt_getTraceHandler();
(C) TraceHandler rt_getTraceHandler(){ return traceHandler;
(C) @trusted nothrow:
(C) TSK1.E (R); FAILED ("EXCEPTION NOT RAISED IN SUBTEST (C)"); EXCEPTION WHEN CONSTRAINT_ERROR =>
NULL;
WHEN
(C) type at c->name, &c->loc; return false; }
(C) TYPE CONVERSIONS
(C) uint function(void
(C) uint function(void
(C) uint gc_clrAttr( void
(C) uint gc_clrAttr( void{ if ( proxy is null ) return 0; return proxy.gc_clrAttr( p, a );
(C) uint gc_getAttr( void
(C) uint gc_getAttr( void{ if ( proxy is null ) return 0; return proxy.gc_getAttr( p );
(C) uint gc_setAttr( void
(C) uint gc_setAttr( void{ if ( proxy is null ) return 0; return proxy.gc_setAttr( p, a );
(C) uintptr_t_beginthreadex(void
(C) UNCONSTRAINED ACTUAL PARAMETERS OF LIMITED PRIVATE TYPE.
(C) union _N31{ void *whatever; /** message-specific data */ CURLcode result; /** return code for transfer */
(C) unittest align unittest {}
(C) _Unwind_Reason_Code gdc_personality(int iversion, _Unwind_Action actions,
_Unwind_Exception_Clas
(C) _Unwind_Reason_Code gdc_personality(_Unwind_State state, _Unwind_Exception* unwindHeader,
_Unwind
(C) _Unwind_Reason_Code unwindCB(_Unwind_Context { UnwindBacktraceData* bt = cast(UnwindBacktraceData*)d; if (bt.numframes >=
MAXFRAMES) return _URC_NO_REASON;
(C) USE, INTRINSIC :: ISO_C_BINDING L = .FALSE. END FUNCTION
(C) USE ISO_C_BINDING IMPLICIT NONE
(C) USE ISO_C_BINDING IMPLICIT NONE
(C) USE ISO_C_BINDING IMPLICIT NONE INTEGER(c_int) liter_cb TYPE, bind(C) :: info_t INTEGER(c_int) :: type END TYPE info_t
TYPE(info_t) :: link_info END FUNCTI
(C) use iso_c_binding implicit none integer :: i type(c_funptr) :: fptr fptr = c_funloc(foo) ! { dg-error "Function result 'foo' at .1. is invalid as X
argument to C_FUNLOC" } block fp
(C) use ISO_C_BINDING implicit none real(C_FLOAT) fun end function fun end interface

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(C) use ISO_C_BINDING implicit none real(C_FLOAT) fun end function fun end interface kill_C_FUNPTR = C_FUNLOC(fun)
(C) use ISO_C_BINDING implicit none real(C_FLOAT) fun fun = 1.0end function fun
(C) use ISO_C_BINDING implicit none type(C_FUNPTR) kill_C_FUNPTR interface
(C) use ISO_C_BINDING implicit none type(C_PTR) kill_C_PTR integer(C_INT), pointer :: p allocate(p) kill_C_PTR = C_LOC(p)end function
kill_C_PTR
(C) use iso_c_binding integer(c_int), optional :: zend subroutine sub2
(C) use iso_c_binding integer(c_int), value :: zend subroutine sub
(C) use iso_c_binding integer(c_int) :: z optional :: zend subroutine sub2a
(C) USE ISO_C_BINDING INTEGER(KIND=C_INT), VALUE :: a END FUNCTION SUBROUTINE init() BIND(C,name="init") END
SUBROUTINE END INTERFACE
(C) use iso_c_binding, only : c_bool logical(kind=c_bool) :: y ! OK
(C) use iso_c_binding, only: c_int integer(c_int) :: j ! output j = 42
(C) use iso_c_binding optional :: z integer(c_int) :: zend subroutine sub2b
(C) use iso_c_binding real(c_float) :: x,y end subroutine all_subs end interface
(C) * U) U := Tmp_UI; end if;
(C) val, f, base, Unsigned}
(C) vararg functions
(C) vararg functionsfail_compilation/fail11532.d(19): Error: cannot pass static arrays to extern(C++) vararg functionsfail_compilation/fail11532.d(20):
Error: cannot pass dynamic arrays to extern(C+
(C) variables should not be implicitly declared. gfc_warning_now (OPT_Wc_binding_type, "Implicitly declared BIND(C) " "variable %qs
at %L may not be C interoperable", sym->name
(C) Variable sym->name, &sym->declared_at); t = false; } else if (sym->common_head != NULL && sym->attr.implicit_type == 0)
(C) void {
(C) void }
(C) void __aaClear(AA aa) pure nothrow{ if (!aa.empty)
(C) void addCallback(void function(void * * // Allocate an object on the GC heap (this would usually be * // some application-specific context
data). * auto context = new Object;
(C) void _aligned_free(void
(C) void{ auto length = x.length; auto tnext = unqualify(ti.next); auto sizeelem = tnext.tsize; // array element size
_d_arrayappendcTX(ti, x, y.length); memcpy(x.p
(C) void{ auto old = ExceptionHeader.stack; ExceptionHeader.stack = cast(ExceptionHeader*)newContext; return old;
(C) void{ auto p = _d_newitemU(_ti); auto init = _ti.initializer(); assert(init.length <= _ti.tsize); memcpy(p, init.ptr, init.length); return p;
(C) void{ auto p = _d_newitemU(_ti); memset(p, 0, _ti.tsize); return p;
(C) void{ auto tnext = unqualify(ti.next); auto size = tnext.tsize;
(C) void{ auto ti = unqualify(_ti); auto flags = !(ti.flags & 1) ? BkAttr.NO_SCAN : 0; immutable tiSize = structTypeInfoSize(ti); immutable size =
ti.tsize + tiSize; if (tiSize)
(C) void{ // c could encode into from 1 to 2 w characters wchar[2] buf = void; byte[] appendthis; // passed to appendT if (c <= 0xFFFF)
(C) void{ // c could encode into from 1 to 4 characters char[4] buf = void; byte[] appendthis; // passed to appendT if (c <= 0x7F)
(C) void cFoo() enforce(false, &cFoo);}
(C) void Cfunc() static assert(functionLinkage!Dfunc == "D"); static assert(functionLinkage!Cfunc == "C");
(C) void// C interface for Runtime.unloadLibrary, returns 1/0 instead of bool
(C) void cleanup() { if (_handle is null) return; _api.global_cleanup(); version (Posix)
(C) void cstyle(int, ...)extern(C++) void cppstyle(int, ...) {}void dstyle(...) {}void typesafe(int[...]...) {}
(C) void cstyle(int, ...) extern(D) void dstyle(...) {} extern(D) void typesafe(int[...]...) {}
(C) void cstyle(int, ...), use core.stdc.stdarg * "typesafe" void typesafe(T[] ...) */ // get symbol linkage as a string if (dim != 1)
return dimError(e
(C) void curl_easy_reset(CURL
(C) void cvararg(int, ...);extern(C++) void cppvararg(int, ...);
(C) void cVarArg(int, ...); void dVarArg(...); void dVarArg2(int, ...); void typesafeVarArg(int[] ...);
(C) void _d_arrayshrinkfit(const TypeInfo ti, void
(C) void _d_arrayshrinkfit(const TypeInfo ti, void{ // note, we do not care about shared. We are setting the length no matter // what, so no lock is
required. debug(PRINTF) printf("_d_ar
(C) void _d_callfinalizer(void{ rt_finalize( p );
(C) void _d_callinterfacefinalizer(void{ if (p)
(C) void _d_criticalenter(D_CRITICAL_SECTION{ ensureMutex(cast(shared(D_CRITICAL_SECTION*)) cs); lockMutex(&cs.mtx);
(C) void _d_criticlexit(D_CRITICAL_SECTION{ unlockMutex(&cs.mtx);
(C) void _d_critical_init();
(C) void _d_critical_init(){ initMutex(cast(Mutex*)&gcs.mtx); head = &gcs;
(C) void _d_critical_term();
(C) void _d_critical_term(){ for (auto p = head; p; p = p.next) destroyMutex(cast(Mutex*)&p.mtx);
(C) void _d_delarray_t(void{ if (p)

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(C) void _d_delarray(void{ _d_delarray_t(p, null);
(C) void _d_delclass(Object{ if (*p)
(C) void _d_delinterface(void{ if (*p)
(C) void _d_delmemory(void{ if (*p)
(C) void _d_delstruct(void{ if (*p)
(C) void _d_dso_registry(CompilerDSOData{ // only one supported currently safeAssert(data._version >= 1, "Incompatible compiler-generated DSO
data version.");
(C) void{ debug(PRINTF) printf("_d_arrayassign(from = %p,%d, to = %p,%d) size = %d\n", from.ptr, from.length, to.ptr, to.length, ti.tsize);
(C) void{ debug(PRINTF) printf("_d_arrayassign_l(src = %p,%d, dst = %p,%d) size = %d\n", src.ptr, src.length, dst.ptr, dst.length, ti.tsize);
(C) void{ debug(PRINTF) printf("_d_arrayassign_r(src = %p,%d, dst = %p,%d) size = %d\n", src.ptr, src.length, dst.ptr, dst.length, ti.tsize);
(C) void{ debug(PRINTF) printf("_d_arrayctor(from = %p,%d, to = %p,%d) size = %d\n", from.ptr, from.length, to.ptr, to.length, ti.tsize);
(C) void{ debug(PRINTF) printf("_d_newarraymiT(dims.length = %d)\n", dims.length);
(C) void{ debug(PRINTF) printf("_d_newarraymT(dims.length = %d)\n", dims.length);
(C) void{ debug(PRINTF) printf("__tls_get_addr input - %p\n", p); immutable offset = cast(size_t)(p - cast(void*)&_tlsstart); auto tls =
getTLSBlockAlloc(); assert(offset < tls.leng
(C) void delegate() cfunc; extern(D) void delegate() dfunc;
(C) void _d_emutls_init() nothrow @nogc{ memcpy(_emutlsMutex.ptr, typeid(Mutex).initializer.ptr, _emutlsMutex.length); (cast(Mutex)
_emutlsMutex.ptr).__ctor());
(C) void _d_initMonoTime();
(C) void _d_initMonoTime(){ // We need a mutable pointer to the ticksPerSecond array. Although this // would appear to break immutability, it is
logically the same as a static // ctor. So
(C) void// ditto
(C) void dmd_coverDestPath(string path);
(C) void dmd_coverSetMerge(bool flag);
(C) void dmd_coverSourcePath(string path);
(C) void _d_monitordelete(Object h, bool det);
(C) void _d_monitordelete(Object h, bool det){ auto m = getMonitor(h); if (m is null) return;
(C) void _d_monitorenter(Object h)in{ assert(h != null, "Synchronized object must not be null.");
(C) void _d_monitorexit(Object h){ auto m = cast(Monitor*) getMonitor(h); auto i = m.impl; if (i is null) unlockMutex(&m.mtx); else
i.unlock();
(C) void _d_monitor_staticctor();
(C) void _d_monitor_staticctor(){ version (Posix)
(C) void _d_monitor_staticctor();
(C) void _d_monitor_staticctor(){ destroyMutex(&gmtx); version (Posix) pthread_mutexattr_destroy(&gattr);
(C) void _d_print_throwable(Throwable t);
(C) void _d_print_throwable(Throwable t){ // On Windows, a console may not be present to print the output to. // Show a message box instead. If
the console is present, convert to // the c
(C) void _d_setSameMutex(shared Object ownee, shared Object owner) nothrow;
(C) void _d_setSameMutex(shared Object ownee, shared Object owner) nothrowin{ assert(ownee.__monitor is null);
(C) void dso_ { if (dso_initialized != condition)
(C) void dtestrir(int x1, int x2, int x3, int x4, int x5, int x6, c_long_double a, int b, c_long_double c){ assert(a == 300.0); assert(b == 68); assert(c
== 401.0);
(C) void _d_throw(Throwable object){ // If possible, avoid always allocating new memory for exception headers. ExceptionHeader *eh =
ExceptionHeader.create(object);
(C) void emutlsDestroyThread(void{ auto arr = cast(TlsArray*) ptr; emutlsMutex.lock_nothrow(); emutlsArrays.remove(arr);
emutlsMutex.unlock_nothrow();
(C) void __emutls_register_common(__emutls_object{ if (obj.size < size)
(C) void exception_cleanup(_Unwind_Reason_Code code, _Unwind_Exception { // If we haven't been caught by a foreign handler, then this is
// some sort of unwind error. In that case
(C) void{ ExceptionHeader* header = ExceptionHeader.toExceptionHeader(unwindHeader);
(C) void fiber_entryPoint() nothrow { Fiber obj = Fiber.getThis(); assert( obj );
(C) void fiber_switchContext( void { // NOTE: The data pushed and popped in this routine must match the // default stack created by
Fiber.initStack or the initial //
(C) void fiber_switchContext( void version (AArch64)
(C) void fiber_trampoline() nothrow; } else
(C) void foo6056()
(C) void fooC(); static extern(Pascal) void fooP(); static extern(Windows) void fooW(); static extern(C++) void fooCPP();
(C) void function();
(C) void function("");
(C) void function() cfunc; extern(D) void function() dfunc;
(C) void function() externCVoidFunc; static void impersonate_thread( uint id, externCVoidFunc fn ) { impersonate_thread(id, () => fn());

```

```

(C) void function() Func;
(C) void function() gc_collect;
(C) void function() gc_disable;
(C) void function() gc_enable;
(C) void function() gc_minimize;
(C) void function(int) nothrow
(C) void function(int) nothrow impl = (i) return (*func = impl)(args);}
(C) void function(int) nothrow impl = (i) return (func = impl)(args);}
(C) void function(int) nothrow someFunc13476 =
(C) void function(int) nothrow someFunc13476Alt =
(C) void function(in void
(C) void function() pure nothrow " /static assert(DG5.stringof == "void delegate() pure nothrow " /* ~ "@safe " */ ~ "@nogc");
(C) void function()"; static assert(typeof(fp1).mangleof == "PUZv"); static assert(typeof(fp2).mangleof == "PUZv");}
(C) void function(void
(C) void function(void
(C) void function(void
(C) void gc_addRange( in void
(C) void gc_addRange( void{ //printf("gcstub::gc_addRange() proxy = %p\n", proxy); if ( proxy is null )
(C) void gc_addRoot( in void
(C) void gc_addRoot( void{ if ( proxy is null )
(C) void gc_clrProxy(){ foreach ( r; ranges[0 .. nranges] ) proxy.gc_removeRange( r.pos ); foreach ( r; roots[0 .. nroots] )
(C) void gc_removeRange( r ); proxy = null;
(C) void gc_removeRoot( r ); proxy = null;
(C) void gc_collect(){ if ( proxy is null ) return; return proxy.gc_collect();
(C) void gc_collect() nothrow;
(C) void gc_disable(){ if ( proxy is null ) return; return proxy.gc_disable();
(C) void gc_disable() nothrow;
(C) void gc_enable(){ if ( proxy is null ) return; return proxy.gc_enable();
(C) void gc_enable() nothrow;
(C) void gc_free( void
(C) void gc_free( void{ if ( proxy is null ) return free( p ); return proxy.gc_free( p );
(C) void gc_init();
(C) void gc_init(){ // NOTE: The GC must initialize the thread library before its first // collection, and always before returning from gc_init().
thread_init(); initProxy();
(C) void gc_minimize(){ if ( proxy is null ) return; return proxy.gc_minimize();
(C) void gc_minimize() nothrow;
(C) void gc_removeRange( in void
(C) void gc_removeRange( void{ if ( proxy is null )
(C) void gc_removeRoot( in void
(C) void gc_removeRoot( void{ if ( proxy is null )
(C) void gc_runFinalizers( in void
(C) void gc_runFinalizers( in void{ if ( proxy !is null ) proxy.gc_runFinalizers( segment );
(C) void gc_setProxy( Proxy{ if ( proxy !is null )
(C) void gc_term();
(C) void gc_term();
(C) void gc_term(){ free( roots ); free( ranges );
(C) void{ if (!paa.empty) paa.resize(nextpow2(INIT_DEN * paa.length / INIT_NUM)); return *paa;
(C) void{ if ( proxy is null )
(C) void{ if ( proxy is null ) return null; return proxy.gc_addrOf( p );
(C) void { immutable save = _rtLoading; _rtLoading = true; scope (exit) _rtLoading = save;
(C) void{ import core.internal.traits : TypeTuple;
(C) voidin{ assert(!(*p).length || (*p).ptr);
(C) voidin{ assert(Thread.getThis());
(C) voidin{ assert(ti); assert(!(*p).length || (*p).ptr);
(C) void initLibFoo(int argc, char * * void main()
(C) void init_msvc();}
(C) voidin{ // Not strictly required, but it gives us more flexibility. assert(Thread.getThis());
(C) void in size_t valsz, in void* pkey){ bool found; return aaGetX(aa, ti, valsz, pkey, found);
(C) void in size_t valsz, in void* pkey, out bool found){ // lazily alloc implementation if (aa.impl is null) aa.impl = new Impl(ti);
(C) void(int, ..."); static assert(fqn!(typeof(dVarArg)) == "void(...)"); static assert(fqn!(typeof(dVarArg2)) == "void(int, ...)"); static
assert(fqn!(typeof(typesafeVarArg))
(C) void lifetime_init();

```

```

(C) void lifetime_init(){ // this is run before static ctors, so it is safe to modify immutables
rt_configOption("callStructDtorsDuringGC"); if (s != null
(C) void main (char * ___ * * Same as the above `argc`.
(C) void main (char * ___ * * Same as the above `argv`.
(C) void main (char * ___ * * Same as the above `envp`.
(C) void main(){ scope string s; s = "[" ~ s ~ "]"";
(C) void maythrow();
(C) void _m_init()
(C) void _m_init() nothrow @nogc;
(C) void myHandler(void * { * // Assuming that the callback is invoked only once, the * // added root can be removed again now to allow
the GC * // to collect it later.
(C) void my_memcmp(const(void)
(C) void onAssertErrorMsg(string file, size_t line, string msg);
(C) void onAssertErrorMsg( string file, size_t line, string msg ) nothrow{ if ( _assertHandler is null ) throw new AssertionError( msg, file, line );
_assertHandler( file, line, msg );
(C) void onAssertError( string file = __FILE__, size_t line = __LINE__ ) nothrow{ if ( _assertHandler is null ) throw new AssertionError( file, line );
_assertHandler( file, line, null);
(C) void onAssertError(string file, size_t line);
(C) void onFinalizeError( TypeInfo info, Throwable e, string file = __FILE__, size_t line = __LINE__ ) @trusted nothrow{ // This error is thrown during
a garbage collection, so no allocation must
(C) void onHiddenFuncError(Object o);
(C) void onHiddenFuncError( Object o ) @safe pure nothrow{ throw new HiddenFuncError( typeid(o) );
(C) void onInvalidMemoryOperationError(void{ // The same restriction applies as for onOutOfMemoryError. The GC is in an // undefined state, thus
no allocation must occur while generating this
(C) void onOutOfMemoryErrorNoGC() @trusted nothrow @nogc{ // suppress stacktrace until they are @nogc throw
staticError!OutOfMemoryError(false);
(C) void onOutOfMemoryError(void
(C) void onOutOfMemoryError(void{ // NOTE: Since an out of memory condition exists, no allocation must occur // while generating this object.
throw staticError!OutOfMemoryError();
(C) void onRangeError( string file = __FILE__, size_t line = __LINE__ ) @safe pure nothrow{ throw new RangeError( file, line, null );
(C) void onRangeError(string file, size_t line);
(C) void onSwitchError( string file = __FILE__, size_t line = __LINE__ ) @safe pure nothrow{ throw new SwitchError( file, line, null );
(C) void onSwitchError(string file, size_t line);+
(C) void onUnicodeError( string msg, size_t idx, string file = __FILE__, size_t line = __LINE__ ) @safe pure;
(C) void onUnicodeError( string msg, size_t idx, string file = __FILE__, size_t line = __LINE__ ) @safe pure{ throw new UnicodeException( msg, idx,
file, line );
(C) void onUnittestErrorMsg(string file, size_t line, string msg);
(C) void onUnittestErrorMsg( string file, size_t line, string msg ) nothrow{ onAssertErrorMsg( file, line, msg );
(C) void pcinforCallback(void { auto context = cast(SymbolCallbackInfo*)data;
(C) void{ pointer offset; if (__gthread_active_p())
(C) void printArrayCache() { auto ptr = __blkcache; printf("CACHE: \n"); foreach (i; 0 .. N_CACHE_BLOCKS)
(C) void profilegc_setlogfilename(string name);
(C) void qsort_r(scope void
(C) void{ return GC.malloc(sz);
(C) void { return initLibrary(.LoadLibraryA(name));
(C) void { return initLibrary(.LoadLibraryW(name));
(C) void rt_attachDisposeEvent(Object h, DEvent e){ synchronized (h)
(C) void rt_attachDisposeEvent( Object obj, DisposeEvt evt );
(C) void rt_detachDisposeEvent(Object h, DEvent e){ synchronized (h)
(C) void rt_detachDisposeEvent( Object obj, DisposeEvt evt );//debug=signal;
(C) void rt_finalize2(void{ debug(PRINTF) printf("rt_finalize2(p = %p)\n", p);
(C) void rt_finalizeFromGC(void{ // to verify: reset memory necessary? if (!(attr & BlkAttr.STRUCTFINAL)) rt_finalize2(p, false, false); // class
else if (attr & BlkAttr.APPENDABL
(C) void rt_finalize(void)
(C) void rt_finalize(void{ rt_finalize2(p, det, true);
(C) void rt_moduleCtor();
(C) void rt_moduleDtor();
(C) void rt_moduleTlsCtor();
(C) void rt_moduleTlsCtor();
(C) void rt_moduleTlsCtor(); /*
(C) void rt_moduleTlsDtor();

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```

(C) void rt_moduleTlsDtor(); */
(C) void rt_moduleTlsDtor();
(C) void rt_setCollectHandler( CollectHandler h );
(C) void rt_setCollectHandler(CollectHandler h){ collectHandler = h;
(C) void rt_setTraceHandler( TraceHandler h );
(C) void rt_setTraceHandler(TraceHandler h){ traceHandler = h;
(C) void sections_osx_onAddImage(in mach_header{ foreach (e; dataSegs)
(C) void setFinalizeCounter(shared(size_t){ _finalizeCounter = p;
(C) void simpleErrorCallback(void { if (data) // context is not available in backtrace_create_state
(C) void{ size_t length; auto tnext = unqualify(ti.next); auto size = tnext.tsize; // array element size
(C) void syminfoCallback2(void const(char)* symname, uintptr_t symval) { auto context =
cast(SymbolCallbackInfo2*)data;
(C) void syminfoCallback(void const(char)* symname, uintptr_t symval) { auto context = cast(SymbolCallbackInfo*)data;
(C) void test4();
(C) void testC8504()
(C) void thread_cleanupHandler( void { Thread obj = cast(Thread) arg; assert( obj );
(C) void thread_detachByAddr( ThreadID addr ){ if ( auto t = thread_findByAddr( addr ) Thread.remove( t );
(C) void thread_detachInstance( Thread t ) noexcept @nogc{ Thread.remove( t );
(C) void thread_detachThis() noexcept @nogc{ if (auto t = Thread.getThis()) Thread.remove(t);
(C) void thread_enterCriticalRegion() @nogcin{ assert(Thread.getThis());
(C) void thread_exitCriticalRegion() @nogcin{ assert(Thread.getThis());
(C) void thread_init();
(C) void thread_init(){ // NOTE: If thread_init itself performs any allocations then the thread // routines reserved for garbage collector use may
be called while // thread_in
(C) void thread_joinAll();
(C) void thread_joinAll(){ Lagain:
(C) void thread_processGCMarks( scope IsMarkedDg isMarked ) noexcept{ for ( Thread t = Thread.sm_tbeg; t; t = t.next )
(C) void thread_resumeAll() noexcept{ assert( suspendDepth > 0 );
(C) void thread_resumeHandler( int sig ) noexcept in { assert( sig == resumeSignalNumber );
(C) void thread_scanAll( scope ScanAllThreadsFn scan ) noexcept{ thread_scanAllType((type, p1, p2) => scan(p1, p2));
(C) void thread_scanAllType( scope ScanAllThreadsTypeFn scan ) noexcept{ assert( suspendDepth > 0 );
(C) void thread_setGCSignals(int suspendSignalNo, int resumeSignalNo) noexcept @nogc {
(C) void thread_setGCSignals(int suspendSignalNo, int resumeSignalNo) noexcept @nogc in { assert(suspendSignalNumber == 0);
assert(resumeSignalNumber == 0); assert(susp
(C) void thread_setThis(Thread t) noexcept @nogc{ Thread.setThis(t);
(C) void thread_suspendAll() noexcept{ // NOTE: We've got an odd chicken & egg problem here, because while the GC // is required to call
thread_init before calling any other thread /
(C) void thread_suspendHandler( int sig ) noexcept in { assert( sig == suspendSignalNumber );
(C) void thread_term();
(C) void thread_term(){ assert(Thread.sm_tbeg && Thread.sm_tlen == 1); assert(!Thread.nAboutToStart); if (Thread.pAboutToStart) // in case
realloc(p, 0) doesn't return null
(C) void trace_setdefilename(string name);
(C) void trace_setlogfilename(string name);
(C) void unittestSegvHandler( int signum, signum_t { import core.stdc.stdio; fprintf(stderr, "Segmentation fault while running
unittests:\n"); fprintf(stde
(C) void unittestSegvHandler( int signum, signum_t { static enum MAXFRAMES = 128; void*[MAXFRAMES] callstack;
(C) void { version (Shared)
(C) void{ void* pstart = p;
(C) void{ void* pstart = p; auto element_size = ti.tsize;
(C) void{ void[] result = _d_newarrayU(ti, length); auto tnext = unqualify(ti.next); auto size = tnext.tsize;
(C) void W7585a()
(C) V; \ } \ while (0)
(C) wchar{ if (a.length > 1)
(C) with a BIND(C) attribute and that is not allowed for procedures. */ if (entry->attr.is_bind_c == 1) { locus loc;
(C) with a BIND(C) attribute and that is not allowed for procedures. */ if (sym->attr.is_bind_c == 1) { sym->attr.is_bind_c = 0;
(C) with OPTIONAL!module m use iso_c_binding interface
(C) work.!! Contributed by Richard Maine
(C) < WT.Table (Right)(C); end Lt;
(C) X : DURATION := 0.0; OLD_TIME : TIME; LAPSE : DURATION;
(C) * x) in the non-vectorized version, but for vectorization we don't have vectorized exp2. */ if (logc && TREE_CODE (logc) ==
REAL_CST && exp_decl && lookup_attribute

```

```

(C) * x). Normally match.pd does that, but if C is a power of 2, we want to use
(C) ZERO DELAY ARGUMENT.-- (D) DURATION'SMALL DELAY ARGUMENT.-- (E) EXPRESSION OF TYPE DURATION AS DELAY ARGUMENT.
gcc-riscv64-unknown-elf 9.3.0 ubuntu:gcc-riscv64-unknown-elf/9.3.0-0ubuntu1
(C) 1995-2004 Jean-loup Gailly and Mark Adler
(C) 1995-2017 Jean-loup Gailly and Mark Adler
(C) 1995-2017 Jean-loup Gailly END END BLOCK "VarFileInfo" BEGIN VALUE "Translation", 0x0409, 1252 END
(C) 1995-2017 Jean-loup Gailly END END BLOCK "VarFileInfo" BEGIN VALUE "Translation", 0x0409, 1252 END
(C) 1995-2017 Jean-loup Gailly VALUE "OriginalFilename", "zlib1.dll\0" VALUE "ProductName", "zlib\0" VALUE "ProductVersion",
ZLIB_VERSION "\0" VALUE "Comments", "For more info
(C) 1998, 2000, 2002, 2003, 2007 Free Software Foundation
(C) 2003 Free Software Foundation// Origin: PR/12832, Jonathan Wakely <redi@gcc.gnu.org>
(C) 2009, 2010
(C) 2013 Free Software Foundation
(C)<<2) (__v16sf)(__m512)_mm512_setzero_ps(), \ (__mmask16)(U),\
(R))#define __mm_getmant_round_sd (__v16sf)(__m512)_mm512_undefined_ps(), \ (__mmask16)-1,\
(C)<<2) (__v16sf)(__m512)(W), \ (__mmask16)(U),\
_MM_FROUND_CUR_DIRECTION)) (__v16sf)(__m512)(W), \ (__mmask16)(U),\
(C)<<2) (__v16sf)_mm512_setzero_ps(), \ (__mmask16)(U),\
_MM_FROUND_CUR_DIRECTION))#define (__v16sf)_mm512_undefined_ps(), \ (__mmask16)-1,\
_MM_FROUND_CUR_DIRECTION)) (__v2df)(__m128d)_mm_setzero_pd (), \ (__mmask8)-1))
(C)<<2) (__v2df)(__m128d)_mm_setzero_pd (), \ (__mmask8)(U)))
(C)<<2) (__v2df)(__m128d)(W), \ (__mmask8)(U)))
(C)<<2) (__v4df)(__m256d)_mm256_setzero_pd (),\ (__mmask8)-1))
(C)<<2) (__v4df)(__m256d)_mm256_setzero_pd (),\ (__mmask8)(U)))
(C)<<2) (__v4df)(__m256d)(W), \ (__mmask8)(U)))
(C)<<2) (__v4sf)(__m128)_mm_setzero_ps (), \ (__mmask8)-1))
(C)<<2) (__v4sf)(__m128)_mm_setzero_ps (), \ (__mmask8)(U)))
(C)<<2) (__v4sf)(__m128)(W), \ (__mmask8)(U)))
(C)<<2) (__v8df)(__m512d)_mm512_setzero_pd(), \ (__mmask8)(U),\
(R))#define __mm512_getmant_round (__v8df)(__m512d)_mm512_undefined_pd(), \ (__mmask8)-1,\
(C)<<2) (__v8df)(__m512d)(W), \ (__mmask8)(U),\
_MM_FROUND_CUR_DIRECTION)) (__v8df)(__m512d)(W), \ (__mmask8)(U),\
(C)<<2) (__v8df)_mm512_setzero_pd(), \ (__mmask8)(U),\
_MM_FROUND_CUR_DIRECTION))#defin (__v8df)_mm512_undefined_pd(), \ (__mmask8)-1,\
_MM_FROUND_CUR_DIRECTION)) (__v8sf)(__m256)_mm256_setzero_ps (), \ (__mmask8)-1))
(C)<<2) (__v8sf)(__m256)_mm256_setzero_ps (), \ (__mmask8)(U)))
(C)<<2) (__v8sf)(__m256)(W), \ (__mmask8)(U)))
(C)(6); assert (ci.x == 6); static assert(!__traits(compiles, pmesswith(&cs,ci))); static assert(!__traits(compiles, rmesswith(cs,ci))); //cs[$-1].x ==
14; //assert (ci.x == 14); //
(C) A, B, A-- SUBTYPES.
(C) A > B IS THE SAME AS NOT (A <= B).-- (D) ADJACENT MODEL NUMBERS GIVE CORRECT RESULTS.-- (E) NON-MODEL NUMBERS WITH
DISTINCT MODEL INTERVALS GIVE-- CORRECT RESULTS.--
(C)'access; end;begin null;
(C) ACCESS PARAMETERS TO PROCEDURES.
(C) ACCESS PARAMETERS TO PROCEDURES.-- (D) ACCESS PARAMETERS TO FUNCTIONS.
(C) ACCESS PARAMETERS TO PROCEDURES.-- (D) ACCESS PARAMETERS TO FUNCTIONS.
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test x'$lt_cv_cc_needs_belf' != x'yes'; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need
for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
(C)! Add a check that the fortran missing optional is accepted by the

```

```

(C) alias declaration*****/
(C) alias int T1;
(C) alias personality_routine = _Unwind_Reason_Code function(_Unwind_State, _Unwind_Control_Block*,
_Unwind_Context*);
(C) alias RunDepTests = int function();
(C) alias RunDepTests = int function(const char
(C) alias RunTests = int function();
(C) alias SetFinalizeCounter = void function(shared(size_t
(C) alias T2 = int;
(C) alias _Unwind_Exception_Cleanup_Fn = void function(_Unwind_Reason_Code, _Unwind_Exception*);
(C) alias _Unwind_Personality_Fn = _Unwind_Reason_Code function(int, _Unwind_Action, _Unwind_Exception_Class,
_Unwind_Exceptio
(C) alias _Unwind_Stop_Fn = _Unwind_Reason_Code function(int, _Unwind_Action, _Unwind_Exception_Class,
_Unwind_Control_Block*,
(C) alias _Unwind_Stop_Fn = _Unwind_Reason_Code function(int, _Unwind_Action, _Unwind_Exception_Class,
_Unwind_Exception*,
(C) alias _Unwind_Trace_Fn = _Unwind_Reason_Code function(_Unwind_Context*, void*);
(C) alias void function() Callback;
(C) alias void function() fpw_t;
(C) AN ARRAY OF RECORD OF TASK OBJECT, IN A PACKAGE BODY,-- IN A TASK BODY.
(C) and extern (D) calling convention matches the C calling convention used by the supported C compiler on the host system. */ break;
(C) and for derived types that are parameters to functions that are BIND(C). All fields of the derived type are required to be interoperable, and are
tested for such. If an error occurs, th
(C) and SEQUENCE types!type :: t integer :: iend type t
(C) and their params are all interoperable. */ if (sym->attr.flavor == FL_PROCEDURE) { if (sym->attr.is_bind_c == 0)
(C) and then C /= Standard_Standard loop Set_Is_Immediately_Visible (C); Set_Is_Visible_Lib_Unit (C); C := Scope (C);
end loop; end;
(C) and then (Entity (C) = Standard_False or else Entity (C) = Standard_True) then Test_Result := Entity (C) =
Standard_True; return True;
(C) and then Expr_Value (C) = Lo then Error_Msg_N ("duplication of choice value: &#!", C);
(C) and then List_Containing (C) = Statements (P) then Onode := Original_Node (Parent (P));
(C) and then not Before (At_Least, Get_Priority (Sets.Element (C).Item));
(C) and then not Is_Digit (C) and then C /= ' ';
(C) and then Present (Variant_Part (C)) then Freeze_Choices_In_Variant_Part (Variant_Part (C)); end if;
(C) and then Sloc (C) = Standard_Location then Error_Msg_N ("redefinition of entity& in Standard?K?", N); end if; end; end if;
end Warn_If_Standard_Redefin
(C) applied to return false; }
(C) Apr 19 1983 in BASIC version by:Professor W. M. Kahan,567 Evans HallElectrical Engineering & Computer Science Dept.University of
CaliforniaBerkeley, California 94720USA
(C) A RECORD OF ARRAY OF TASK ALLOCATOR, IN A PACKAGE BODY,-- IN A TASK BODY.
(C) A RECORD OF ARRAY OF TASK ALLOCATOR, IN A TASK BODY.
(C) A RECORD OF ARRAY OF TASK ALLOCATOR, IN A TASK BODY, NOT-- DURING RENDEZVOUS.-- (D) A LIMITED PRIVATE TASK
ALLOCATOR, IN A TASK BODY, DURING-- RENDEZVOUS.
(C) A RECORD OF TASK ALLOCATOR, IN A PACKAGE SPECIFICATION.-- (D) A RECORD OF ARRAY OF TASK ALLOCATOR, IN A PACKAGE BODY.-- (E)
AN ARRAY OF RECORD OF TASK ALLOCATOR, IN A TASK BODY.
(C) A RECORD OF TASK OBJECT, IN A PACKAGE SPECIFICATION.-- (D) A RECORD OF ARRAY OF TASK OBJECT, IN A PACKAGE BODY.-- (E) AN
ARRAY OF RECORD OF TASK OBJECT, IN A TASK BODY.
(C) A RECORD PARAMETER WHOSE COMPONENTS HAVE NON-STATIC-- CONSTRAINTS INITIALIZED WITH A STATIC AGGREGATE.
(C) A RECORD PARAMETER WHOSE COMPONENTS HAVE NON-STATIC-- CONSTRAINTS INITIALIZED WITH A STATIC AGGREGATE.
(C) A RECORD PARAMETER WHOSE COMPONENTS HAVE NON-STATIC-- CONSTRAINTS INITIALIZED WITH A STATIC AGGREGATE.-- (D)
AN ARRAY PARAMETER CONSTRAINED WITH STATIC BOUNDS ON SUB---
(C)! as defined in TS 29913!interface subroutine test (xx) bind(C, name="myBindC") type(*), dimension(,:,:) :: xx end subroutine testend interface
(C) ASSERT_CONST_TYPE((C), C90_SUFI_TYPE((C)))
(C) ASSERT_CONST_TYPE((C), C90_SUFu_TYPE((C)))
(C) ASSERT_CONST_TYPE((C), C90_UNSUf_DEC_TYPE((C)))#define CHECK_UNSUf_OCTHEX_TYPE(C) \
ASSERT_CONST_TYPE((C), C90_UNSUf_OCTHEX_TYPE((C)))
(C) ASSERT_CONST_TYPE((C), C99_SUFI_DEC_TYPE((C)))#define CHECK_SUFI_OCTHEX_TYPE(C) \ ASSERT_CONST_TYPE((C),
C99_SUFI_OCTHEX_TYPE((C)))
(C) ASSERT_CONST_TYPE((C), C99_SUFuI_TYPE((C)))
(C) ASSERT_CONST_TYPE((C), C99_SUFuI_TYPE((C)))#define CHECK_SUFII_TYPE(C) \ ASSERT_CONST_TYPE((C),
C99_SUFuI_TYPE((C)))

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(C) ASSERT_CONST_TYPE((C), C99_SUfU_TYPE((C)))
(C) ASSERT_CONST_TYPE((C), C99_SUfU_TYPE((C)))#define CHECK_SUfI_TYPE(C) \ ASSERT_CONST_TYPE((C),
C99_SUfI_TYPE((C)))
(C) ASSERT_CONST_TYPE((C), C99_UNSUf_DEC_TYPE((C)))#define CHECK_UNSUf_OCTHEX_TYPE(C) \
ASSERT_CONST_TYPE((C), C99_UNSUf_OCTHEX_TYPE((C)))
(C) ASSERT_CONST_TYPE((C), long long int)#define CHECK_SUfII_OCTHEX_TYPE(C) \ ASSERT_CONST_TYPE((C),
C99_SUfII_OCTHEX_TYPE((C)))
(C) ASSERT_CONST_TYPE((C), unsigned long int)
(C) ASSERT_CONST_TYPE((C), unsigned long long int)
(C) at return false;
(C) at return MATCH_ERROR; }
(C) at return MATCH_ERROR; }
(C) at return MATCH_ERROR; }
(C) attribute at "and may be inaccessible by the C companion processor", derived_sym->name, &(derived_sym->declared_at));
derived_sym->ts.is_c_interop = 1; derived_sym->attr.is_b
(C) attribute at "an interface with BIND(C)"; return MATCH_ERROR; } /* Check for C1217. */ if (has_name_equals &&
sym->attr.pointer)
(C) attribute at "at %L may not be specified for an internal " "procedure", &gfc_current_locus) return MATCH_ERROR;
(C) attribute at return MATCH_ERROR; }
(C) attribute at "variables or common blocks", &loc; }
(C) attribute at "variables or common blocks", &loc; }
(C) attribute at "variables or common blocks", where); else if (attr->is_bind_c) gfc_error_now ("Duplicate BIND attribute specified at %L",
where); else attr->is_bind_c = 1;
(C) __attribute__((noinline));C f1 (C x){ return x;
(C) ATTRIBUTES ( -- (D) FUNCTION CALLS -- (E) QUALIFIED EXPRESSIONS -- (F) TYPE CONVERSIONS -- (G)
PARENTHEZIZED EXPRESSIONS OF THE A
(C) attribute specification statement.
(C) attributes", tmp_sym->name, &(tmp_sym->declared_at)); retval = false; }
(C) attribute to be equal. STRUCTUREs are special because they can be anonymous; therefore two structures with different names may be equal.
*/
(C) attribute with a NAME= specifier.static int num_idents_on_line;/* Need to know if a NAME= specifier was found during gfc_match_bind_c so we
can supply a name if the curr_binding_label is nil
(C) attribute with NAME"; return MATCH_ERROR; } /* Set binding label for BIND(C). */ if (!set_binding_label (&sym->binding_label, sym-
>name, num)) return MATCH_ERROR;
(C) auto d2 = d;
(C) auto Stub13476(alias func)(ParameterTypeTuple13476{ ++flag13476;
(C) auto Stub13476Alt(alias func)(int args) nothrow{ ++flag13476;
(C) ~b; c[0] ^= c[0] <= 0x1234; a = (a >> 1) | (a << 127); b += (D) {45, 123};
(C) because it " "is neither a COMMON block nor declared at the " "module level scope", sym->name, &(sym->declared_at));
t = false; } else if (sym->ts.type == BT_CHARACTER
(C) BEFORE CALL, IN OUT MODE, STATIC ONE DIMENSIONAL BOUNDS.
(C) before the declaration itself. Return
(C) BEGIN -- (C)
(C) BEGIN FAILED ("EXCEPTION NOT RAISED BEFORE CALL - P2 " & WHO); EXCEPTION WHEN OTHERS => FAILED
("EXCEPTION RAISED IN P2 FOR " & WHO); END P2;
(C)/!b go :more \./!{ N s/\n# // b more
(C) b integer i end type b ! { dg-error "Expecting END MODULE statement" }
(C) BlkInfo function(size_t, uint, const TypeInfo) gc_qlloc;
(C) BlkInfo function(void
(C) BlkInfo_gc_qlloc( size_t sz, uint ba = 0, const TypeInfo = null ) pure nothrow;
(C) BlkInfo_gc_qlloc( size_t sz, uint ba = 0, const TypeInfo ti = null ){ if ( proxy is null )
(C) BlkInfo_gc_query( void
(C) BlkInfo_gc_query( void{ if ( proxy is null ) return BlkInfo.init; return proxy.gc_query( p );
(C) bool_aaDelX(AA aa, in TypeInfo keyti, in void{ if (aa.empty) return false;
(C) BOOLEAN TYPE, STATIC NULL RANGE, NO PARAMETERS.-- (D) USER-DEFINED ENUMERATED TYPE, DYNAMIC LOWER BOUND, ONE--
PARAMETER.-- (E) DERIVED INTEGER TYPE, DYNAMIC NULL RANGE, ONE PA
(C) BOOL := F1 (REC.A (2..4)); IF REC.A /= (7, 7, 7, 9, 9) THEN FAILED ("IN PARAM CHANGED BY FUNCTION");
END IF; EXCEPTION
(C) bool function() gc_inFinalizer; }
(C) bool gc_inFinalizer();}
(C) bool gc_inFinalizer(){ if ( proxy !is null ) return proxy.gc_inFinalizer(); return false;

```

```

(C) bool runModuleUnitTests();
(C) bool runModuleUnitTests(){ // backtrace version (GNU) import gcc.backtrace; version (CRuntime_Glibc) import
core.sys.linux.execinfo; else version (Darwin)
(C) bool thread_inCriticalRegion() @nogcin{ assert(Thread.getThis());
(C) bool thread_isMainThread() nothrow @nogc{ return Thread.getThis() is Thread.sm_main;
(C) buff_ptr; initializeClassInstance(c, 0); assert(si);
(C) byteout (result){ auto tinext = unqualify(ti.next); auto sizeelem = tinext.tsize; // array element size debug(PRINTF)
printf(" _d_arraycatT(%d,%p ~ %d,%p sizeelem = %d =>
(C) CALL G(C1) CALL H(C1(1:4))
(C) call sub3 (D)
(C) call sub4 (D)
(C) CArgs rt_cArgs() @nogc;}
(C) CArgs rt_cArgs() @nogc{ return _cArgs;
(C) CASE 2, IN OUT MODE, STATIC PRIVATE DISCRIMINANT.-- (D) CASE 2, OUT MODE, DYNAMIC TWO DIMENSIONAL BOUNDS.-- (E) SAME AS
(C), WITH TYPE CONVERSION.-- (F) SAME AS (D), WITH TYPE
(C) CASE ("AAA":"EEE") STOP 1 CASE ("R":"T") CONTINUE CASE DEFAULT STOP 2END SELECT
(C) CASE (A), BUT ACTUAL PARAMETER IS A TYPE CONVERSION.-- (D) CASE (B), BUT ACTUAL PARAMETER IS A TYPE CONVERSION.
(C)C } auto a = DummyOutputRange(); auto spec = FormatSpec!Char(fmt); //dummy write spec.writeUpToNextSpec(a);
(C)C c)if (is(C : dchar)){ import std.traits : isAggregateType, OriginalType, Unqual;
(C) cc; immutable(B) ib;
(C)C c) @safe if (isSomeChar { import std.traits : Parameters; static auto trustedFPUTC(int ch, _jobuf* h) @trusted
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_PATH_WITH_DIR(P) \ (strchr (P, '/') != NULL ||
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)#endif
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])#endif
(C) ((C) ==# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define FILESYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) == DIRECTORY_SEPARATOR)#endif
(C) c; } else
(C) ((C) ==#endif
(C) CE substitutes for helpful life information.
(C) c.f() // makes a copy c.g()
(C) character( if (str(2) .ne. "ghi") stop 2 str = ['uvw','xyz'] end subroutine
(C) char const(char**) getEnvironPtr() @trusted { return *_NSGetEnviron;
(C) char{ if (a.length > 1)
(C) CHECK_OCTHEX_CONST(C)#define CHECK_HEX_CONST(C) \ CHECK_OCTHEX_CONST(0x##C); \
CHECK_OCTHEX_CONST(0X##C);
(C) CHECK RECORDS HAVING A DISCRIMINANT, WITH MORE THAN ONE ARRAY-- COMPONENT, WHERE THE BOUNDS OF THE ARRAY DEPEND ON
THE-- DISCRIMINANT.
(C) CHECKS FOR ATTRIBUTES.
(C) Chinese (traditional). "\u4ED6\u5011\u7232\u4EC0\u9EBD\u4E0D\u8AAA\u4E2D\u6587", "ihqwctvzc91f659drss3x8bo0yb", },
(C)Cif (is(C : wchar) || is(C : char)){ static struct Decoder
(C)C if (is(immutable C == immutable char)){ return 1;
(C)Cif (isSomeChar!C){ import std.algorithm.searching : find; static struct Result
(C)Cif (isSomeChar!C){ return _baseName(path);
(C)Cif (isSomeChar!C){ return _dirName(path);
(C)C input){ return C(input.im, input.re);
(C) class( class(*), pointer :: P allocate(P, source=C) select type (P) type is (character(*)) if (P /= 'test string') STOP 53 class default
STOP 54 end
(C) clause but an error was encountered, or MATCH_YES if the specifier was correct and the binding label and
(C) c) == lowercase
(C) CLS_GETINFO((struct objc_class
(C) Cmd.Params (C)
(C)C obj) assert(obj.foo() == 1 || obj.foo() == 2); return obj; }
(C) CollectHandler rt_getCollectHandler();
(C) CollectHandler rt_getCollectHandler(){ return collectHandler;
(C) CONCAT3 (C, f, WIDTH)
(C) CONCAT3 (C, f, WIDTH)# define FMAC(M) CONCAT4 (FLT, WIDTH, _, M)#endif
(C) CONCAT3 (C, f, WIDTH)# define FN(F) CONCAT3 (F, f, WIDTH)#endif
(C) CONCAT3 (C, f, WIDTH)# define MAX CONCAT3 (FLT, WIDTH, _MAX)# define MIN CONCAT3 (FLT, WIDTH, _MIN)# define TRUE_MIN CONCAT3

```

```

(FLT, WIDTH, _TRUE_MIN)#endif
(C) CONCAT3 (C, f, WIDTH)# define MAX CONCAT3 (FLT, WIDTH, _MAX)#endif
(C) CONCAT3 (C, f, WIDTH)# define TRUE_MIN CONCAT3 (FLT, WIDTH, _TRUE_MIN)#endif
(C) CONCAT3 (C, F, WIDTH)#endif
(C) CONCAT3 (C, if, WIDTH)
(C) CONCAT4 (C, F, WIDTH, i)#endif
(C) CONCAT4 (C, f, WIDTH, x)
(C) CONCAT4 (C, f, WIDTH, x)# define FMAC(M) CONCAT4 (FLT, WIDTH, X_, M)#else# define TYPE CONCAT (_Float, WIDTH)
(C) CONCAT4 (C, f, WIDTH, x)# define FN(F) CONCAT4 (F, f, WIDTH, x)#else# define TYPE CONCAT (_Float, WIDTH)
(C) CONCAT4 (C, f, WIDTH, x)# define MAX CONCAT3 (FLT, WIDTH, X_MAX)# define MIN CONCAT3 (FLT, WIDTH, X_MIN)# define TRUE_MIN
CONCAT3 (FLT, WIDTH, X_TRUE_MIN)#else# define TYPE CONCAT (_Float, WI
(C) CONCAT4 (C, f, WIDTH, x)# define MAX CONCAT3 (FLT, WIDTH, X_MAX)#else# define TYPE CONCAT (_Float, WIDTH)
(C) CONCAT4 (C, f, WIDTH, x)# define TRUE_MIN CONCAT3 (FLT, WIDTH, X_TRUE_MIN)#else# define TYPE CONCAT (_Float, WIDTH)
(C) CONCAT4 (C, F, WIDTH, x)#else# define TYPE CONCAT (_Float, WIDTH)
(C) CONCAT4 (C, F, WIDTH, xi)#else# define TYPE CONCAT (_Float, WIDTH)
(C) CONCAT4 (C, if, WIDTH, x)
(C) const(char)
(C) const(char)
(C)(const(C)if (isSomeChar!C){ return toUTFz!(const(wchar*)(str);
(C)(const(C)if (isSomeChar!C){ return walkLength(str);
(C)(const(C)if (isSomeChar!C){ static if (is(Unqual!C == dchar))
(C)(const(C)if (isSomeChar!C){ static if (is(Unqual!C == dchar)) return index; else
(C)(const(C) { import std.format : format; string diff = "src: %( %x %) \nres: %( %x %) \ntru: %( %x %)"; auto low = s.toLower() , up
= s.toUpper(); auto lowInp = s.d
(C)(const(C) { return 3;
(C)(const(C) @safe pure nothrowif (isSomeChar!C){ return buildPath!(typeof(paths))(paths);
(C)(const(C) { size_t len = 0; while (*ptr != '\0') { ++ptr; ++len; } return len;
(C)(const(C) @trusted pure nothrowif (isSomeChar!C){ import std.array : array;
(C) CONSTRAINED ACTUAL PARAMETERS OF LIMITED PRIVATE TYPE.
(C) CONSTRAINT_ERROR IS RAISED BEFORE THE CALL FOR CONVERSION TO A-- CONSTRAINED ARRAY TYPE WHEN THE NUMBER OF
COMPONENTS PER-- DIMENSION OF THE ACTUAL DIFFERS FROM THAT OF THE FORMAL.-
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all copies. This software is provided // "
(C) csets__fold_lower
(C) csets__fold_upperextern char Fold_Lower[], Fold_Upper[];
(C) c; S[] s = [ S(new C) ];
(C) c } static struct PutS(C)
(C) c == uppercase
(C) CURLcode curl_easy_getinfo(CURL
(C) CURLcode curl_easy_recv(CURL
(C) CURLcode curl_easy_send(CURL
(C) CURLMcode curl_multi_add_handle(CURLM
(C) CURLMcode curl_multi_assign(CURLM
(C) CURLMcode curl_multi_cleanup(CURLM
(C) CURLMcode curl_multi_fdset( CURLM *multi_handle, fd_set *read_fd_set, fd_set *write_fd_set, fd_set *exc_fd_set, int *max_fd);
(C) CURLMcode curl_multi_perform(CURLM
(C) CURLMcode curl_multi_remove_handle(CURLM
(C) CURLMcode curl_multi_setopt(CURLM
(C) CURLMcode curl_multi_timeout(CURLM
(C)(C version (Posix) { assert(buildPath("foo") == "foo"); assert(buildPath("/foo/") == "/foo/"); assert(buildPath("foo", "bar") ==
"foo/bar"); assert(buildPath
(C)(C { while (!str.empty) str.popFront();
(C) DAY_NUMBER.-- (D) DAY_DURATION.
(C)(dchar c)if (is(C == char)){ return true;
(C)(dchar c)if (isSomeChar!C){ static if (C.sizeof == 1) return c <= 0x7F; else static if (C.sizeof == 2) return c <= 0xD7FF || (0xE000 <=
c && c <= 0xFFFF); else static
(C)(dchar c) @safe pure nothrow @nogcif (isSomeChar!C){ static if (C.sizeof == 1)
(C)(dchar c){ static if (C.sizeof == 1)

```

```

(C) debug::function_requires< debug::C >()
(C)#define DF F16_C (D)#define EF F16_C (E)#define FF F16_C (F)#define GF F16_C (G)#define HF F16_C (H)
(C)#define DP P16_C (D)#define EP P16_C (E)#define FP P16_C (F)#define GP P16_C (G)#define HP P16_C (H)
(C)#define DS S16_C (D)#define ES S16_C (E)#define FS S16_C (F)#define GS S16_C (G)#define HS S16_C (H)
(C)#define DU U16_C (D)#define EU U16_C (E)#define FU U16_C (F)#define GU U16_C (G)#define HU U16_C (H)
(C)#define F(X) __has_attribute(X)#if !__has_attribute(UNUSED)#error unused attribute not supported - 1#endif
(C) definition for use in external C programs.
(C) derived type          &curr_comp->loc, derived_sym->name,          &derived_sym->declared_at;          retval = false;          }
(C) derived type          curr_comp->name, &(curr_comp->loc),          derived_sym->name, &(derived_sym->declared_at));          retval
= false;          }
(C) derived types in the namespace so we can report errors for them once, rather than for each variable declared of that type. */
(C) derived types must have interoperable components. if (curr_comp->ts.type == BT_DERIVED && curr_comp->ts.u.derived->ts.is_iso_c != 1
&& curr_comp->ts.u.derived != derived_sym)
(C) dg { C c = new C; void* cptr = cast(void*) c; void* iptr = cast(void*) cast(I) c; void* xptr = cast(void*) dg(c);
assert(cptr != iptr); assert(
(C) d = new C; //d = new C;}
(C) end subroutine b end interface procedure(b), bind(c,name="hjj") :: a ! { dg-error "may not have BIND.C. attribute with NAME" }
procedure(b),intent(in):: c end subroutine foo
(C) end subroutine fooend interface
(C) end subroutine procend module m
(C) end subroutine subend interfaceinteger(c_int), pointer :: intprocedure(sub), pointer :: fsub
(C) end subroutine two subroutine three() bind(C,name="three") ! { dg-error "NAME not allowed on BIND.C. for ABSTRACT INTERFACE" } end
subroutine three ! { dg-error "Expecting END INTERFACE stat
(C) end subroutine x end interface type(C_FUNPTR) aux
(C) enumerator :: OFFLOAD_SUCCESS = 0 enumerator :: OFFLOAD_DISABLED ! offload is disabled enumerator :: OFFLOAD_UNAVAILABLE
! card is not available enumerator :: OFFLO
(C)#error unused attribute not supported - 2#endif#if !D#error unused attribute not supported - 3#endif#if !E#error unused attribute not supported -
4#endif#if !F(unused)#error unused attrib
(C)#error unused attribute not supported - 6#endifint a = __has_attribute (unused) + __has_attribute (C) + D + E + F (unused) + F (C);int b =
__has_attribute (unused);int c = __has_attribute (C);
(C) EXCEPTION_DISPOSITION _GCC_specific_handler(void _Unwind_Personality_Fn);
(C) EXCEPTION_DISPOSITION __gdc_personality_seh0(void void* ms_orig_context, void* ms_disp) {
return _GCC_specific_handler(ms_exc
(C) extern(Cextern(C++, foo) extern(C++, bar) void f8() {} // OK
(C) extern const GUID CLSID_InternetShortcut,
(C) extern const GUID IID_IRichEditOle, IID_IRichEditOleCallback;
(C) extern const IID IID_IContinueCallback, IID_IEnumOleDocumentViews, IID_IPrint, IID_IOleDocumentView, IID_IOleDocument,
IID_IOleCommandTarget, IID_IOleDocumentSite;
(C) extern(C) void f6()
(C) extern __gshared const char const(char**) getEnvironPtr() @trusted { return environ;
(C) extern __gshared int _tls_index;
(C) ! Failed at trans--array.c:8078 class(Bug) A type(Bug) C select type (A) type is (bug) C = A C%scalar = onenineeight
class default
(C) feature of standard Fortran instead. gfc_source_file, pkgversion_string, version_string);
(C) f(func (*C) g() {} // pointer receiver
(C) f integer m end type f ! { dg-error "Expecting END MODULE statement" }
(C)'First, Cmd.Config.Prefixes (P).all then -- If we are still in the same section, group the switches
(C) float floatPrintf(const char int func(float);
(C) Foo1 ctest1();
(C) Foo2 ctest2();
(C) Foo3 ctest3();
(C) Foo4 ctest4();
(C) Foo5 ctest5();
(C) Foo6 ctest6();
(C) fopen64, http://www.unix.org/version2/whatsnew/lfs20mar.html */ import core.sys.posix.stdio : fopen; return fopen(namez,
modez);
(C) for ABSTRACT INTERFACE at return MATCH_ERROR; }
(C) for ENTRY PR fortran/34079 To be linked with bind_c_usage_10.f03*/
(C) for internal procedures. if (gfc_current_state () == COMP_CONTAINS && sym->ns->proc_name->attr.flavor != FL_MODULE
(C) FORMAL CONSTRAINED, FORMAL AND ACTUAL HAVE DIFFERENT NUMBER-- COMPONENTS PER DIMENSION, BOTH FORMAL AND ACTUAL ARE
NULL-- ARRAYS.-- (D) FORMAL CONSTRAINED, ACTUAL NULL, WITH INDEX

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```

(C) form. if intro.endswith ('right'):
(C) FOR NON-MODEL NUMBERS A > 0.0, THAT ABS A VALUES ARE-- WITHIN THE APPROPRIATE MODEL INTERVAL.-- (D) FOR NON-
MODEL NUMBERS A < 0.0, THAT ABS A VALUES ARE-- WIT
(C) fp) { C c = new C; void* cptr = cast(void*) c; void* iptr = cast(void*) cast(I) c; void* xptr = cast(void*) fp(c); assert(cptr
!= iptr); assert(
(C) function "be an array", tmp_sym->name, &(tmp_sym->declared_at));
(C) FUNCTION INIT_3 RETURN INTEGER;
(C) function "%L may not be C interoperable", tmp_sym->name, &tmp_sym->declared_at); tmp_sym->ts.f90_type =
tmp_sym->ts.type; /* Mark it as C intero
(C) function, make sure the return value is a scalar value. The previous tests in this function made sure the type is interoperable. */ if
(bind_c_function && tmp_sym->as != NULL)
(C) FUNCTION "OR" (I1, I2 : INTEGER) RETURN CHARACTER IS BEGIN IF I1 > I2 THEN RETURN 'G'; ELSE
RETURN 'L'; END IF;
(C) FUNCTION "OR" (I1, I2 : INTEGER) RETURN CHARACTER RENAMES TWO_PARAMS;
(C) FUNCTION "OR" IS NEW TWO_PARAMS (ELEMENT => INTEGER);
(C) functions cannot return a character string. if (bind_c_function && tmp_sym->ts.type == BT_CHARACTER) if (tmp_sym->ts.u.cl == NULL ||
tmp_sym->ts.u.cl->length == NULL || tmp_sym->ts.u.c
(C) functions in the standard library that have `__attribute__((nothrow))` are considered `@trusted`. - The purity of a built-in can vary depending
on compiler flags set upon initialization,
(C) functions.m4_define([AC_LANG_FUNC_LINK_TRY(D)],
(C) functions PR fortran/34079 To be linked with bind_c_usage_16.f03*/
(C) functions that match any known GCC library builtin function in D and override its internal back-end symbol. */
(C) function "type at %L must have length 1", tmp_sym->name, &(tmp_sym->declared_at)); }
(C) FUNCTION "xOR" (I1, I2 : INTEGER) RETURN CHARACTER IS BEGIN IF I1 > I2 THEN RETURN 'G';
ELSE RETURN 'L'; END IF
(C) GC.Stats gc_stats ( ) nothrow @nogc;
(C) g()func (*C) h()
(C) __gshared bool rt_cmdline_enabled;
(C) __gshared bool rt_cmdline_enabled = false;
(C) __gshared bool rt_envvars_enabled;
(C) __gshared bool rt_envvars_enabled = true;
(C) __gshared bool rt_trapExceptions = true;
(C) __gshared char
(C) __gshared immutable(ModuleInfo
(C) __gshared nothrow:
(C) __gshared static int initCount;
(C) __gshared string
(C) __gshared void}
(C) hash_t_aaGetHash(in AA{ if (aa.empty) return 0;
(C) I := F;
(C) IF ABS NON_MODEL_VAR NOT IN 0.0 .. 64.0 OR ABS DECIMAL_M4'(NON_MODEL_CONST) NOT IN 0.0 .. 64.0 THEN FAILED
("ABS (2.0 / 3) NOT IN CORRECT RANGE - B");
(C) IF ABS NON_MODEL_VAR NOT IN 0.65625 .. 0.671875 OR ABS LIKE_DURATION_M23'(NON_MODEL_CONST) NOT IN
0.65625 .. 0.671875 THEN FAILED ("ABS (2.0
(C) IF A > B XOR NOT (A <= B) THEN FAILED ("A > B IS NOT THE SAME AS NOT (A <= B)"); END IF;
(C) IF ARRAY_3 = ARRAY_4 THEN FAILED ("INCORRECT RESULTS FROM COMPARING MULTI- & "DIMENSIONAL
ARRAYS - 3"); END IF;
(C) IF (IDENT_INT (10) OR 1) /= (5 OR 10) /= 'L' THEN FAILED ("OVERLOADING OF ""OR"" OPERATOR DEFECTIVE");
END IF; END; -- (C)
(C) IF (IDENT_INT (10) XoR 1) /= (5 xOR 10) /= 'L' THEN FAILED ("OVERLOADING OF ""xOR"" OPERATOR DEFECTIVE");
END IF; END; -- (C)
(C) If we are dealing with a positional aggregate with an others -- choice make sure the number of positional elements specified -- does
not overflow the aggregate bounds. We also
(C) ignored struct { B int } // missing field (A) ignored; data will be dropped
(C) I := IDENT_INT (10); P2 (I, "10"); FAILED ("CONSTRAINT_ERROR NOT RAISED FOR P2 (10)"); EXCEPTION WHEN
CONSTRAINT_ERROR => COUNT := COUNT
(C) I := IDENT_INT (10); T2.E2 (I, "10"); FAILED ("CONSTRAINT_ERROR NOT RAISED FOR E2 (10)"); EXCEPTION WHEN
CONSTRAINT_ERROR => COUNT := CO
(C) I : INTEGER;
(C)/ill-formed (C D = UINT_MAX+1 /* OK */
(C)Image); exception when Assertion_Error => Got_Assertion := True; end;

```

```

(C) immutable int _aaVersion = 1;
(C) IMPLICIT INTEGER(4) (I) DATA IPROG, IFILE, ITOTR, IRLGN, IRECN, IEOF, ICON21, ICON22, ICON31, ICON32, ICON33, ICON34, ICON55,
ICON56 /14*0/ OPEN (7, ACCESS = 'DIRECT', RECL = 80,
(C) Impl void[] vals){ assert(keys.length == vals.length);
(C) import Bug class(Bug) A type(Bug) C end function end interface
(C) import :: c_char, c_int character(kind=c_char,len=*), intent(in) :: this(..) integer(c_int), intent(inout) :: ierr end subroutine
process_string end interface!
(C) import ckind complex(ckind) stub end function end interface
(C) import c_ptr!GCC$ attributes NO_ARG_CHECK :: x type(*) :: x type(c_ptr) :: my_c_loc1 end function end interface my_c_loccontains
subroutine sub_scalar (arg1, presnt)
(C) import c_ptr type(*) :: x type(c_ptr) :: my_c_loc1 end function
(C) import c_ptr type(*) :: x(*) type(c_ptr) :: my_c_loc2 end function end interface my_c_loccontains subroutine sub_scalar (arg1, presnt)
type(*), target, optional ::
(C) import implicit none type(C_FUNPTR) kill_C_FUNPTR end function kill_C_FUNPTR end interfaceend module m3
(C) import implicit none type(C_PTR) kill_C_PTR end function kill_C_PTR end interface
(C) import integer(C_INT), value, intent(in) :: n END SUBROUTINE set_vm_limitEND INTERFACE
(C) import integer(int16), intent(in) :: x end function end interface integer(int16) :: x16 = 12345 x16 = bigendc16(x16) print *,x16
(C) import logical(c_bool), value :: is_present integer(c_int), optional :: var end subroutine end interfacecontains
(C) in 16 end if; end Is_Special_Character;
(C) in 16 or else
(C) IN A MAIN PROGRAM TASK BODY.
(C)(in Cif (is(C : dchar)){ auto src = input[index.$]; auto n = src.length; genericDecodeGrapheme!(false)(src); return n - src.length;
(C)(in C if (is(C : dchar)) { import std.conv : to; CodepointSet set; if (loadUnicodeSet!table(name, set)) return set;
throw new Exception("No u
(C)(in C if (is(C : dchar)) { return loadAny(name);
(C)(in C if (is(C : dchar)) { this ~= chars;
(C)(in C{ import std.algorithm.searching : find; auto names = [ "L", "Letter", "LC", "Cased Letter", "M", "Mark", "N", "Number",
"P", "Punctuation",
(C)(in C { import std.utf : decode, codeLength; size_t codeLen = 0; size_t lastNonTrivial = 0; size_t curIdx = 0; while (curIdx
!= str.length)
(C)(in C{ size_t j = 0; auto p = cast(dchar*)malloc(dchar.sizeof * s.length); auto r = p[0..s.length]; // r[] will never be longer than s[] foreach
(dchar c; s) r[j++] = c;
(C) I := NEW INTEGER P (I, I, I); FAILED ("EXCEPTION NOT RAISED - C"); EXCEPTION WHEN E => IF (I.ALL /=
101) THEN FAILED
(C) IN MODE, TWO DIMENSIONS, DYNAMIC VARIABLE.-- (D) IN OUT MODE, THREE DIMENSIONS, STATIC VARIABLE.-- (E) OUT MODE, ONE
DIMENSION, DYNAMIC VARIABLE.-- (F) IN OUT MODE, NULL STRING A
(C) Inner function(double, string) cFuncPtr;
(C)(inout Cif (isSomeChar!C)in { assert(cstr); }body{ size_t length = 0; while (cstr[length]) ++length; return cstr[0 .. length];
(C) IN OUT, OUT OF RANGE ON CALL.-- (D) OUT, OUT OF RANGE ON RETURN.-- (E) IN OUT, OUT OF RANGE ON RETURN.
(C) IN OUT, OUT OF RANGE ON CALL.-- (D) OUT, OUT OF RANGE ON RETURN.-- (E) IN OUT, OUT OF RANGE ON RETURN.
(C) inout(void const TypeInfo tiValueArray) pure nothrow{ if (aa.empty) return null;
(C) inout(void){ if (aa.empty) return null;
(C) inout(void){ if (aa.empty) return null;
(C) inout(void) in void* pkey){ return _aaInX(aa, keyti, pkey);
(C) IN PARAMETER, VARIABLE.-- (D) IN OUT PARAMETER, EXCEPTION RAISED ON CALL.-- (E) OUT PARAMETER, EXCEPTION RAISED ON
CALL.
(C) int _aaApply2(AA aa, in size_t keysz, dg2_t dg){ if (aa.empty) return 0;
(C) int _aaApply2(void * _aaApply2(aggr, keysize, flde) */ static const char *name[2] = {
"_aaApply", "_aaApply2" };
(C) int _aaApply(AA aa, in size_t keysz, dg_t dg){ if (aa.empty) return 0;
(C) int _aaApply(void * _aaApply(aggr, keysize, flde) *
(C) int _aaEqual(in TypeInfo tiRaw, in AA aa1, in AA aa2){ if (aa1.impl is aa2.impl) return true;
(C) int _aApplycd1(in char{ int result; size_t len = aa.length;
(C) int _aApplycd2(in char{ int result; size_t len = aa.length;
(C) int _aApplycw1(in char{ int result; size_t len = aa.length;
(C) int _aApplycw2(in char{ int result; size_t len = aa.length;
(C) int _aApplydc1(in dchar{ int result;
(C) int _aApplydc2(in dchar{ int result; size_t len = aa.length;
(C) int _aApplydw1(in dchar{ int result;
(C) int _aApplydw2(in dchar{ int result;
(C) int _aApplyRcd1(in char{ int result;

```

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(C) int _aApplyRcd2(in char{ int result; size_t i; size_t len = aa.length;
(C) int _aApplyRcw1(in char{ int result;
(C) int _aApplyRcw2(in char{ int result;
(C) int _aApplyRdc1(in dchar{ int result;
(C) int _aApplyRdc2(in dchar{ int result;
(C) int _aApplyRdw1(in dchar{ int result;
(C) int _aApplyRdw2(in dchar{ int result;
(C) int _aApplyRwc1(in wchar{ int result;
(C) int _aApplyRwc2(in wchar{ int result;
(C) int _aApplyRwd1(in wchar{ int result;
(C) int _aApplyRwd2(in wchar{ int result;
(C) int _aApplywc1(in wchar{ int result; size_t len = aa.length;
(C) int _aApplywc2(in wchar{ int result; size_t len = aa.length;
(C) int _aApplywd1(in wchar{ int result; size_t len = aa.length;
(C) int _aApplywd2(in wchar{ int result; size_t len = aa.length;
(C) int _adCmp2(void{ debug(adi) printf("_adCmp2(a1.length = %d, a2.length = %d)\n", a1.length, a2.length); return ti.compare(&a1, &a2);
(C) int _adCmpChar(void{ version (D InlineAsm_X86)
(C) int _adCmp(void{ debug(adi) printf("adCmp()\n"); auto len = a1.length; if (a2.length < len) len = a2.length; auto sz = ti.tsize; void
*p1 = a1.ptr; void *p2 = a2.p
(C) int _adEq2(void{ debug(adi) printf("_adEq2(a1.length = %d, a2.length = %d)\n", a1.length, a2.length); if (a1.length != a2.length) return
0; // not equal if (!ti.
(C) int _adEq(void{ debug(adi) printf("_adEq(a1.length = %d, a2.length = %d)\n", a1.length, a2.length); if (a1.length != a2.length) return 0;
// not equal auto sz = ti.tsize;
(C) int atoi(const char
(C) int bar(int a, int b) { //printf("a = %d, b = %d\n", a, b); assert(b - a == 1); return ++g;
(C) int brk(shared void
(C) int callback(dl_phdr_info { auto p = cast(DG*)arg; if (findSegmentForAddr(*info, p.addr))
(C) int cfc(int x, int y){ return x * 10 + y;
(C) int cfunc6596()
(C) int cmp(scope const void { return (cast(TypeInfo)ti).compare(p1, p2);
(C) int cmp(scope const void { return tiglobal.compare(p1, p2);
(C) int cmp(scope void { return (cast(TypeInfo)ti).compare(p1, p2);
(C) int cstyle(int, ...); extern(D) int dstyle(...); extern(D) int typesafe(int[...]);}@safe unittest
(C) int _d_run_main(int argc, char{ // Remember the original C argc/argv _cArgs.argc = argc; _cArgs.argv = argv;
(C) integer(C_INT) :: argend subroutine Callback
(C) integer :: x
(C) interface for the ARM EABI and C6X unwinders.// This corresponds to unwind-arm-common.h
(C) interface for the ARM EABI unwinder library.// This corresponds to unwind-arm.h
(C) interface for the C6X EABI unwinder library.// This corresponds to unwind-c6x.h
(C) interface for the generic unwinder library.// This corresponds to unwind-generic.h
(C) interface for the GNU/GCC pointer encoding library.// This corresponds to unwind-pe.h
(C) int F13(int); alias extern(D) int F14(int); alias extern(Windows) int F15(int); static assert(is(DerivedFunctionType!(F13, F14) == void)); static
assert(is(DerivedFunctionType!(F13
(C) int Foo1(int a, int b, int c) { assert(a == 1); assert(b == 2); assert(c == 3); return 1;
(C) int foo7();
(C) int fooc();alias aliasc = fooc;
(C) int foosize6();
(C) int function();
(C) int function();
(C) int function("");
(C) int function(char
(C) int function(dl_phdr_info
(C) int function(dl_phdr_infoextern int dl_iterate_phdr(dl_iterate_phdr_cb __callback, void* __data);
(C) int function(dl_phdr_infoextern int dl_iterate_phdr(dl_iterate_phdr_cb __callback, void* __data);extern int dl_iterate_phdr(dl_iterate_phdr_cb_ngc
__callback, void* __data) @nogc;
(C) int function(dl_phdr_infoextern int dl_iterate_phdr(dl_iterate_phdr_cb __callback, void* __data);extern int dl_iterate_phdr(dl_iterate_phdr_cb_ngc
__callback, void* __data) @nogc;extern int rtdl
(C) int function() FP2; return true;}());
(C) int function (int, int) fpc;extern (Pascal) int function (int, int) fpp;int function (int, int) fpd;
(C) int function() pfunc6596;
(C) int function(scope const void

```

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(C) int function(scope void
(C) int global;static assert(__traits(getLinkage, global) == "C");
(C) int lib_init(){ return rt_init();
(C) int lib_term(){ return rt_term();
(C) int main()
(C) int main() $2}]
(C) int main(char printf("hello world\n"); foo(3); return 0;}
(C) int main(char printf("hello world\n"); int[3] a; foo(a[], 3); return 0;}
(C) int main(){ test(1); return 0;
(C) int memcmp(const void
(C) int myprintf(const(char
(C) int _NSGetExecutablePath(charelse version (FreeBSD)
(C) int pcinfolCallback(void int lineno, const(char)* func) { auto context = cast(SymbolCallbackInfo*)data;
(C) int plugin_init(){ return rt_init();
(C) int plugin_term(){ return rt_term();
(C) int printf (const char
(C) int printf(const char
(C) int printf(const char
(C) int printf(const(char
(C) int printf(const(charimport core.vararg;
(C) int printf(const chartemplate TypeTuple(TL...) { alias TypeTuple = TL; }
(C) int printf(in char
(C) int printf(in charint ldap_perror(LDAP* handle, char* message) { return printf("%s: %s\n", message, handle.ld_error);}
(C) int printf(in char static assert(variadicFunctionStyle!printf == Variadic.c);}
(C) int rt_hasFinalizerInSegment(void{ if (attr & BlkAttr.STRUCTFINAL)
(C) int rt_init(){ /* @@@BUG 11380 @@@ Need to synchronize rt_init/rt_term calls for version (Shared) druntime, because multiple C threads might
initialize different D libraries witho
(C) int rt_init();// C interface for Runtime.terminate, returns 1/0 instead of bool
(C) int rt_term();
(C) int rt_term(){ if (!_initCount) return 0; // was never initialized if (atomicOp!=="(_initCount, 1)) return 1;
(C) int rt_unloadLibrary(void
(C) int rt_unloadLibrary(void { gcClrFn gcClr = cast(gcClrFn) GetProcAddress(ptr, "gc_clrProxy"); if (gcClr !is null) gcClr(); return
FreeLibrary(ptr) != 0;
(C) int rt_unloadLibrary(void { if (handle is null) return false;
(C) int runDepTests(const char{ auto h = rt_loadLibrary(name); if (h is null) return false; auto runTests = cast(RunTests).dlsym(h, "runTests");
assert(runTests !is null); if (!ru
(C) int runDepTests(){ return runTests();
(C) int runTests(){ try
(C) int runTests(){ try runTestsImpl(); catch (Throwable) return 0; return 1;
(C) int simpleCallback(void { auto context = cast(LibBacktrace)data;
(C) int spawnvp(int, in charalias P_WAIT = _P_WAIT;alias P_NOWAIT = _P_NOWAIT;
(C) int specialClose(void { ++closed; return 0;
(C) int sprintf(char
(C) int sysctlbyname( const char *, void *, size_t *, void *, size_t );
(C) int sysctl (const int size_t* oldlenp, const void* newwp, size_t newlen);
(C) int sysctl (const int size_t* oldlenp, const void* newwp, size_t newlen);else version (NetBSD)
(C) int test88a();
(C) int test88a(){ return 0;
(C) int testc(int i); extern (Windows) int testw(int i); extern (D) int testd(int i);}
(C) int testc(int i) { printf("foo.testc(%p)\n", this); assert(x == 47); return i + x;
(C) is allowed since TS 29113!
(C) is not yet" " supported", sym->name, &(sym->declared_at), sym->ns->proc_name->name); retval = false; }
(C) i; static assert(!__traits(compiles, m = i));}void bug5284_2a()
(C) is when '1' => Debug_Flag_1 := Val; when '2' => Debug_Flag_2 := Val; when '3' =>
Debug_Flag_3 := Val; w
(C) is when '1' => Debug_Flag_Dot_1 := Val; when '2' => Debug_Flag_Dot_2 := Val; when '3' =>
Debug_Flag_Dot_3 := Val;
(C) is when '1' => Debug_Flag_Underscore_1 := Val; when '2' => Debug_Flag_Underscore_2 := Val; when
'3' => Debug_Flag_U
(C) is when 'A' => Debug_Flag_AA := Val; when 'B' => Debug_Flag_BB := Val; when 'C' =>
Debug_Flag_CC := Val;

```



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(C) is when 'a' => Debug_Flag_A := Val; when 'b' => Debug_Flag_B := Val; when 'c' =>
Debug_Flag_C := Val; w
(C) is when 'A' => Debug_Flag_Dot_AA := Val; when 'B' => Debug_Flag_Dot_BB := Val; when 'C' =>
Debug_Flag_Dot_CC := Val
(C) is when 'a' => Debug_Flag_Dot_A := Val; when 'b' => Debug_Flag_Dot_B := Val; when 'c' =>
Debug_Flag_Dot_C := Val;
(C) is when 'A' => Debug_Flag_Underscore_AA := Val; when 'B' => Debug_Flag_Underscore_BB := Val;
when 'C' => Debug_Flag
(C) is when 'a' => Debug_Flag_Underscore_A := Val; when 'b' => Debug_Flag_Underscore_B := Val; when
'c' => Debug_Flag_U
(C) is when '^' => IP := Emit_Node (if (Flags and Multiple_Lines) /= 0 then MBOL elsif (Flags and
Single_Line) /= 0 t
(C)'Last)); First := C; end if; end if; end loop;
(C)'Last); Free (Params (C)); end if;
(C)'Last) = Param)) then Remove (Cmd.Expanded, C); Remove (Cmd.Params, C); Remove (Cmd.Sections,
C);
(C) LIMITED PRIVATE TYPE.
(C) LIMITED PRIVATE TYPE, UNCONSTRAINED ACTUALS, NO DEFAULTS.-- (D) ARRAY TYPE, CONSTRAINED ACTUALS, DEFAULTS.
(C) logical(c_bool), intent(in), value :: is_present integer(c_int), intent(inout), optional :: var if (is_present) then if (.not. present (var))
STOP 1 if (var /= 43) S
(C) loop Check_Static_Type (Etype (C), N, DT); Next_Component_Or_Discriminant (C); end loop;
(C) loop Ensure_Type_Is_SA (Etype (C)); Next_Formal (C); end loop;
(C) loop exit when C = Character'Last; C := Character'Succ (C); end loop;
(C) loop Expr := Expression (C);
(C) loop Field_Name := Chars (Defining_Identifier (C));
(C) loop if C = CR or else C = LF then Skip_Line; C := Nextc;
(C) loop if Chars (C) = Chars (Comp) then return C; end if;
(C) loop if Chars (C) = Chars (Selectr) then
(C) loop if Ekind (C) = E_Discriminant or else Ekind (C) = E_Component then Ensure_Type_Is_SA (Etype (C));
(C) loop if Etype (C) = B_Typ then Set_Entity_With_Checks (N, C); Generate_Reference (C, N); return; end
if;
(C) loop if Is_Access_Type (Etype (C)) then return True; else C := Next_Discriminant (C); end if;
end loop;
(C) loop if Is_Limited_Type (Etype (C)) then return True; end if;
(C) loop if Nkind (Parent (C)) = N_Component_Declaration then Copy_Dimensions (Expression (Parent
(Corresponding_Record_Component (C))), Expression (P
(C) loop Indx := First_Index (Etype (Prefix (N)));
(C) LOOP OLD_TIME := CLOCK; DELAY X; LAPSE := CLOCK - OLD_TIME; EXIT; END LOOP;
COMMENT ("(C) - ZERO DELAY LAP
(C) loop Store_String_Char (C); Src_Ptr := Src_Ptr + 1; C := Sbuffer (Src_Ptr); end loop;
(C) loop Write_Indent; Write_Id (C); Write_Str (" : "); Write_Id (Etype (C));
(C) make_complex_modes (MODE_static voidmake_complex_modes (enum mode_class cl, const char *file, unsigned int line){ struct mode_data
*m; struct mode_data *c; enum mode_class cclass =
(C) MarshalJSON() ( return []byte("<>"), nil}
(C) mc := make(map mc[nil] = 42 mc[c] = 42 mc[make(C)] = 42
(C) Method()func (Z) Method() {}
(C) must not have a CONTAINS" " section at %C", gfc_current_block ()->name);
(C) must not have a CONTAINS section. */ if (gfc_current_block ()->attr.sequence) gfc_error ("Derived-type %qs with SEQUENCE must not have a
CONTAINS" " section at %C", gfc_current
(C) names ( sym->refs++; /* Needed to avoid an ICE in gfc_release_symbol */ return MATCH_ERROR; }
(C) names ( sym->refs++; /* Needed to avoid an ICE in gfc_release_symbol */ return MATCH_ERROR; }
(C) NEW_PAGE (FILE); NEW_LINE (FILE); NEW_PAGE (FILE);
(C) @nogc nothrow:
(C) @nogc nothrow{ version (CRuntime_Glibc) int pthread_getattr_np(pthread_t thread, pthread_attr_t* attr); version (FreeBSD) int
pthread_attr_get_np(pthread_t thread, pthread_attr_t* attr);
(C) nothrow{ alias ssize_t function(void *cookie, char *buf, size_t size) cookie_read_function_t; alias ssize_t function(void *cookie, const(char) *buf,
size_t size) cookie_write_function_t;
(C) nothrow @nogc:
(C) nothrow @nogc{
(C) nothrow @nogc FILE
(C) nothrow @nogc FILE

```

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(C) nothrow @nogc { int __libc_current_sigrmin(); int __libc_current_sigrmax();
(C) nothrow @nogc{ ptrdiff_t getdelim(char**, size_t*, int, FILE*); // getline() always comes together with getdelim() ptrdiff_t getline(char**,
size_t*, FILE*);
(C) nothrow @nogc @system:
(C) nothrow @system:
(C) NOW : TIME;
(C) NOW := TIME_OF (1984, 8, 12, 500.0); LATER := NOW + INCREMENT;
(C) NOW := TIME_OF (1984, 8, 13); IF SECONDS (NOW) /= 0.0 THEN FAILED ("TIME_OF() DID NOT ZERO SECONDS - (C)");
END IF; END; -- (C)
(C)\n";};//pragma( msg, d_generate_functions() );mixin( d_generate_functions() );
(C) NULL;
(C) NULL; END; -- (C)
(C) obj0; static assert(is(typeof(obj0) == C));
(C) objc_get_class(C)#endif
(C) Object_d_newclass(const ClassInfo ci){ void* p;
(C) Object_d_newclass(const TypeInfo_Class ci);
(C) Object_d_toObject(void
(C) object_getClassName(C)
(C) OBJ_INT := F1; IF GLOBAL /= 3 THEN FAILED ("DEPENDENT TASK NOT TERMINATED BEFORE " &
"FUNCTION EXIT - 3"); END IF; END; --
(C) ONE SUBPROGRAM HAS ONE LESS PARAMETER THAN THE OTHER.
Copyright -- -----
Copyright = 0; char[ACMDRIVERDETAILS_LICENSING_CHARS] szLicensing = 0; char[ACMDRIVERDETAILS_FEATURES_CHARS] szFeatures =
0;}alias ACMDRIVERDETAILSA* LPACMDRIVERDETAILSA;
Copyright = 0; WCHAR[ACMDRIVERDETAILS_LICENSING_CHARS] szLicensing = 0; WCHAR[ACMDRIVERDETAILS_FEATURES_CHARS] szFeatures =
0;}alias ACMDRIVERDETAILSW* LPACMDRIVERDETAILSW;
COPYRIGHT=0x2000;
COPYRIGHT=0x400C;
Copyright 1984, 1987, 1989, 1992, 2000 by Stephen L. Moshier
Copyright 1984, 1991 by Stephen L. Moshier
Copyright 1985, 1986, 1988, 1990, 1991, 1992, 1993, 1994, 1995,
copyright 1987,1988,1989 john o. hallquist, lstcc| all rights reserved
Copyright 1989, 1990 Advanced Micro Devices, Inc.
Copyright © 1991-2013 Unicode, Inc.CLDL data files are interpreted according to the LDML specification (http://unicode.org/reports/tr35/)For terms of
use, see http://www.unicode.org/copyright.html-
Copyright 1992, 1993, 1994 Henry Spencer. All rights reserved
copyright 1992-1999, 2001 The Free Software Foundation
copyright 1992-1999, 2004 The Free Software Foundation
copyright 1992-1999 The Free Software Foundation
Copyright 1992-" Current_Year & ", Free Software Foundation, Inc."); Write_Eol; end if; end if;
Copyright 1992-" & ", Free Software Foundation, Inc."); Write_Eol; end if;
Copyright © 1994-1999 Lucent Technologies Inc. All rights reserved
Copyright 1994 by Stephen L. Moshier
Copyright 1994 by Stephen L. Moshier * * Arguments |x| <= 13 are reduced by recurrence and the function * approximated by a rational function of
degree 7/8 in the * interval (2,3). Large argument
Copyright 1994 by Stephen L. Moshier * * For arguments greater than 33, the logarithm of the gamma * function is approximated by the
logarithmic version of * Stirling's formula usi
Copyright © 1995-1997 C H Forsyth (forsyth@terzarima.net)
Copyright 1995-2017 Jean-loup Gailly and Mark Adler "/* If you use the zlib library in a product, an acknowledgment is welcome in the documentation
of your product. If for some reason you cannot
Copyright 1995-2017 Mark Adler "/* If you use the zlib library in a product, an acknowledgment is welcome in the documentation of your product. If
for some reason you cannot include such an ac
Copyright 1995 Free Software Foundation, Inc...* Core was generated by `f771'.* Program terminated with signal 11, Segmentation fault.* Couldn't read
input and local registers from core file* find
Copyright 1995 Free Software Foundation, Inc...* Core was generated by* `/usr/local/lib/gcc-lib/sparc-sun-solaris2.5/2.7.2/f771 quick.f -quiet -
dumpbase'.* Program terminated with signal 11, Segmen
Copyright 1996, 1999, 2007 Free Software Foundation, Inc.
Copyright 1996-" & ", Free Software Foundation, Inc."); end Output_Version;
copyright 1996 Loren P. Meissner -- May be distributed if this line is included.
Copyright © 1997-1999 Vita Nuova Limited
Copyright 1998-2004 Gilles Vollant - http://www.winimage.com/zLibDll";

```

```
Copyright: 1998 */myint f;enum E { e } /// comment1int g; /// comment2private int h; /// comment for Hstatic int i;int j;wchar LS = 0x2028;    /// UTF
line separatorwchar PS = 0x2029;    /// UTF pa
COPYRIGHT 1999 SPACKMAN REAL(R4_KV), dimension (10) :: QLA1, QLA2, QLA3, & QCA = /(i, i= 1, 10)/) EQUIVALENCE
(QLA1, QLA2) QLA1 = QCA QLA3 = QCA
Copyright © 2000-2007 Lucent Technologies Inc. and others
Copyright © 2000-2007 Vita Nuova Holdings Limited (www.vitanuova.com)
Copyright 2000 by Stephen L. Moshier */ const real M = 32_768.0; const real MINV = 3.0517578125e-5L;
Copyright 2000 Free Software Foundation
Copyright 2000 Free Software Foundation/* by Alexandre Oliva <aoliva@redhat.com> */
Copyright 2001 by Stephen L. Moshier
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Copyright 2002 Free Software Foundation// Contributed by Jason Merrill <jason@redhat.com>
Copyright 2002 Free Software Foundation, Inc.
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Copyright 2003 Free Software Foundation.
Copyright 2003 Free Software Foundation, Inc.
Copyright 2004, 2005, 2007, 2009, 2011 Free Software Foundation, Inc.
Copyright © 2004,2006 Bruce Ellis
Copyright 2004 Free Software Foundation, Inc. * Contributed and written by Nathanael Nerode. * * GCC 3.4 would attempt to open stdin as the included
file * (PR 17610), causing a sort of hang.
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Copyright 2005 Free Software Foundation
Copyright 2005 Free Software Foundation// by Alexandre Oliva <aoliva@redhat.com>// based on https://bugzilla.redhat.com/beta/show_bug.cgi?
id=149098
Copyright 2005 Free Software Foundation// contributed by Alexandre Oliva <aoliva@redhat.com>// inspired in the failure reported in Red Hat bugzilla
#168260.
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 Copyright (C) <em class="replaceable"><code>year</code></em> <em class="replaceable"><code>name of author</code></em>  
 copyright{\}/(c)/g; s/\@dots{\}/.../g; s/\@enddots{\}/.../g; s/\@[!?!]/\$1/g; s/\@[:-]/g; s/\@bullet{?:\{}}?/\*g; s/\@TeX{\}/TeX/g; s/\@pounds{\}/\#/g; s/\@mi  
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 copyright comment if present. comments := file.Comments if len(comments) > 0 && strings.HasPrefix(comments[0].Text(), "Copyright") {  
 comments = comments[1:] }  
 COPYRIGHT COMMENT PROBLEM.  
 copyright conditions above. \* \* 2005 - Adaptation to Microsoft C Compiler for AMD64 by Gilles Vollant  
 copyright conditions above. \* \* Dec-29-2003 -- I added AMD64 inflate asm support. This version is also \* slightly quicker on x86 systems because, instead of using rep movsb to copy \* data, it use  
 copyright conditions above.; \* \* Mar-13-2003 -- Most of this is derived from inffast.S which is derived from; \* the gcc -S output of zlib-1.2.0/inffast.c. Zlib-1.2.0 is in beta release at; \*  
 copyright conditions above. \* \* This version (Jan-23-2003) of inflate\_fast was coded and tested under \* GNU/Linux on a pentium 3, using the gcc-3.2 compiler distribution. On that \* machine, I fou  
 copyright; const char const char \*main\_d; // dummy filename for dummy main() Array<const char \*> \*path; // Array of char\*'s which form the import lookup path Array<const  
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copyright{copyright \def\dots{...}% \def\enddots{...}% \def\equiv{==}% \def\error{error}% \def\euro{euro}% \def\expansion{==>}%  
\def\geq{>=}% \def\guillemetleft{<<}% \def\guillemetri

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Copyright: Copyright Digital Mars 2000 - 2011. \* D implementations of tan, atan, atan2, exp, expm1, exp2, log, log10, log1p, \* log2,  
floor, ceil and lrint functions are based o

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copyright = Copyright):      self.errors = Errors()
copyright = copyright (self.errors)      self.dirs = []      self.default_dirs = []      self.chosen_dirs = []      self.option_handlers = dict()
self.option_help = []
Copyright: Copyright Sociomantic Labs GmbH.
Copyright: Copyright the respective authors, 2008-
copyright{'}'      '|copyright = u\      '|@set\s+copyright[\w-]+'
Copyright (C) <replaceable>year</replaceable> <replaceable>name of author</replaceable>
Copyright (C) ");      Write_Str (Initial_Year);      Write_Char ('-');      Write_Str (Gnatvsn.Current_Year);      Write_Str (" ");      Write_Str
(Gnatvsn.Copyright_Holder);      Write_Eol;
Copyright (C) year      name of author
Copyright: Danny Milosavljevic 2014
Copyright David Nadlinger 2011.
copyright) {      dc->copyright->nooutput = 1;
copyright} \DeclareUnicodeCharacter \DeclareUnicodeCharacter{00AB}{\guillemetleft} \DeclareUnicodeCharacter{00AD}{\-}
\DeclareUnicodeCharacter{00AE}{\registeredymbol} \DeclareUnicodeCharact
copyright \definedummyword\registeredymbol \definedummyword\dots \definedummyword\enddots \definedummyword\entrybreak
\definedummyword\equiv \definedummyword\error \definedummyword\euro
Copyright: def __init__(self, errors):      self.errors = errors
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copyright (dir, filename, filter, file, line, match) (this_changed, line, next_line) = res
copyright disaster and is nowhelping with the new documentation.
Copyright; -- Display the copyright notice
Copyright; -- Display the Copyright notice. If called several times, display the -- Copyright notice only the first time.
copyright does not belong to the
Copyright: Dominic Sayers, Jacob Carlborg 2008-.
Copyright Don Clugston 2008 - 2010.
copyright = ' else + canon_form + self.separator + line[match.end (2):]
Copyright; end if;
copyright */enum LIBCURL_COPYRIGHT = "1996 - 2010 Daniel Stenberg, <daniel@haxx.se>.";
copyright = fd.read()
Copyright FSF 1993, 2007 and fall
CopyrightFSF 1995, 2007 and fall under the GPL version 3 or later.
copyright} \gdef \gdef^^ab{\guillemetleft} \gdef^^ac{\$!not\$} \gdef^^ad{-} \gdef^^ae{\registered symbol} \gdef^^af{\=} %
\gdef^^b0{\textdegree} \gdef^^b1{\$!pm\$} \gdef^^b2{\$!^2\$}
copyright = get_copyright()
Copyright Guillaume Chatelet 2016.
© Copyright Henrik Ravn 2004
Copyright Henrik Ravn 2004
copyright in changelog entries. return re.compile ('\t')
copyright information already in place.
copyright information # and isn't updated. if filename == 'README' and os.path.basename (dir) == 'g++.niklas':
copyright is associated with the package # author. canon_form = self.holders[holder] if not canon_form:
copyright is incomplete, add the next line. while not self.is_complete (match):
copyright issues, IMO, since it* doesn't do anything substantive. SUBROUTINE OUTDNS(A,B,LCONV) IMPLICIT REAL(kind=8) (A-H,O-
Z),INTEGER(I-N) COMMON/ARRAYS/Z(64,8),AB(30,30),PAIRS(9,9
copyright' is too noisy, and # HTML header markers, so check for 'copyright' and two digits. self.other_copyright_re = re.compile ('copyright.*
[0-9][0-9]',
Copyright Jacob Carlborg 2015.
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Copyright Jeremy Siek return GenericFilter.get_line_filter (self, dir, filename)
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copyright" line and a pointer to where the full notice is found. </para> <screen><replaceable>one line to give the program's name and a brief idea
of what it does.</replaceable>
copyright" line and a pointer to where the full notice is found. </p><pre class="screen"><em class="replaceable"><code>one line to give the
program's name and a brief idea of what it does.</cod
copyright lines that might have slipped by. elif self.other_copyright_re.search (line):
Copyright Martin Nowak 2012.
Copyright Martin Nowak 2012. Etienne Cimon 2015.
Copyright: Masahiro Nakagawa 2010-.
copyright messages
COPYRIGHT))\n </body></html>\n\nB = <b>$0</b>\nI = <i>$0</i>\nU = <u>$0</u>\nP = <p>$0</p>\nDL =
<dl>$0</dl>\nDT = <dt>$0</dt>\nDD = <dd>$0</dd>\nTA
copyright(NULL), macros(NULL), pmacrotable(NULL), pescapetable(NULL) { }
copyright of its own, but quotes the GPL. 'intro.xml', ])
copyright of that document, you can freely ---- copy and modify this specification, provided that if you redistribute a ---- modified version, any changes
that you have made are clearly indicated.
Copyright: Public Domain
Copyright; Put_Line ("Usage: gnatclean New_Line;
Copyright: Red-black tree code copyright (C) 2008- by Steven Schveighoffer. Other code
copyright# Redistribution and use in source and binary forms, with or without# modification, are permitted provided that the following conditions# are
met:
copyright(): return u
Copyright %s 2019 Free Software Foundation, Inc.\n", _("(C)"); fnotice (stdout, _("This is free software; see the source for copying
conditions.\n" "There is NO w
Copyright %s 2019 Free Software Foundation, Inc.\n", _("(C)"); fnotice (stdout, _("This is free software; see the source for copying

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is NO\n\warranty; not even for MERCHANTABIL
  Copyright %s 2019 Free Software Foundation, Inc.\n",      _("(C)");   fputs (_("This is free software; see the source for copying conditions.
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  Copyright Sean Kelly 2009 - 2014.
copyright = sec;   }   if (icmp("macros", sec->name, sec->namelen) == 0)
copyright;   Section Macro **pmacrotable;   Escape **pescapetable;
copyright (self, dir, filename, filter, file, line, match):   orig_line = line   next_line = None   pathname = os.path.join (dir, filename)
COPYRIGHT SIGN
COPYRIGHT SIGN   {"copysr",   0x02117}, // SOUND RECORDING COPYRIGHT   {"CounterClockwiseContourIntegral", 0x02233}, //
ANTICLOCKWISE CONTOUR INTEGRAL   {"crarr",
copyright: %s   % line.strip())   lines.append (line)   line = next_line
Copyright Sociomantic Labs GmbH.
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copyright's starting year.
copyright string in the executable of your product. */
Copyright \textcopyright 2014-2016, Intel Corporation. All rights reserved
Copyright The Go Authors.\n\n"   "package build\n\n" +   "// shouldBuild checks tags given by lines of the form\n" +   "// +build tag\n"
+   "func shouldBuild(content []byte)\n"   want3 := map[string]b
copyright# THIS SOFTWARE IS PROVIDED BY THE COPYRIGHT HOLDERS AND CONTRIBUTORS# "AS IS" AND ANY EXPRESS OR IMPLIED
WARRANTIES, INCLUDING, BUT NOT# LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTA
copyright to help *   - Send help to stdout instead of stderr *   - Add some preemptive typecasts *   - Add L to
constants in lseek() calls
copyright to this source code.
copyright to this source code.// https://sites.google.com/site/murmurhash/static inline uint32_t calcHash(const uint8_t *data, size_t len){ // 'm' and 'r'
are mixing constants generated offline
copyright to this source code. In place of** a legal notice, here is a blessing:
copyright = True
copyright (void){ static const char copyright[] = "\v* Unicode characters and various properties.\n\
Copyright;   Write_Line ("Usage: gnatprep   "infile outfile [deffile]");   Write_Eol;   Write_Line (" infile   Name of the input file");
Write_Line (" outfile
copyright (); write_table (); write_context_switch (); return 0;
Copyright XXXX-YYYY Free Software Foundation, Inc.
Copyright years on GCC source files may be listed using rangenotation, e.g., 1987-2012, indicating that every year in the range,inclusive, is a
copyrightable year that could otherwise be listed
copyright year   years = match.group (2).strip()   try:
(C)* or //C[] -> C* or const(C)*   static if (( is(const(Unqual!InChar) == InChar) && is(const(Unqual!OutChar) == OutChar)) ||
(!is(const(Unqual!InChar) == InChar) && !is(immutable
(C)* or //C[] -> immutable(C)*   else {   import std.array : uninitializedArray;   auto copy = uninitializedArray!(Unqual!OutChar[])(str.length
+ 1);   copy[0 .. $ - 1] = s
(C) or else C = Character   C := Character'Succ (C);   end loop;
(C) or else C =   end Is_Alnum;
(C) or not, depending on the given parameter, is_bind_c. */
(C)   P1 (R);   FAILED ("EXCEPTION NOT RAISED IN SUBTEST (C)");   EXCEPTION   WHEN CONSTRAINT_ERROR =>   NULL;
WHEN OTHERS =>   FAILED ("
(C) PACKAGE BODY.--   (D) GENERIC FUNCTION BODY.--   (E) GENERIC PACKAGE BODY.--   (F) TASK BODY.
(C) (Params (C)
(C)   PC (IDENT_INT(1), IDENT_INT(3));   EXCEPTION   WHEN OTHERS =>   FAILED ("EXCEPTION RAISED IN CALL TO PC");
END;   -- (C)
(C) pragma(mangle, "clock else   clock_t clock());}else version (Darwin) // other Darwins (iOS, TVOS, WatchOS)
(C) _Pragma("moose") ALPHA(C) BETA(C) GAMMA(baz);
(C) PRIVATE ACCESS PARAMETERS TO PROCEDURES.--   (D) PRIVATE ACCESS PARAMETERS TO FUNCTIONS.
(C) private pure nothrow @nogc static // TODO remove pure when https://issues.dlang.org/show_bug.cgi {   pragma(mangle, "free") void
pureFree( void *ptr );   pragma(mangle, "gc_addRang
(C) private pure @system @nogc nothrow{   pragma(mangle, "getErrno") int fakePureGetErrno();   pragma(mangle, "setErrno") int
fakePureSetErrno(int);
(C) private{   pure @trusted @nogc nothrow pragma(mangle, "fakePureReprintReal") void pureReprintReal(char[] nptr);

```



```

(C) private pure @trusted @nogc nothrow { static if (GNU_Thread_Model == ThreadModel.Posix)
(C) private static int _progressCallback(void *ptr, double dttotal, double dlnow, double ulttotal, double ulnow) {
auto b = cast(Curl
(C) private static int _seekCallback(void *ptr, curl_off_t offset, int origin) { auto b = cast(Curl*) ptr; if (b._onSeek == null) return
CurlSeek.cantseek;
(C) private static int _socketOptionCallback(void *ptr, curl_socket_t curlfd, curlsocktype purpose) { auto b = cast(Curl*)
ptr; if (b._onSocketOpt
(C) private static size_t _receiveCallback(const char* str, size_t size, size_t nmemb, void* ptr) { auto b = cast(Curl*) ptr;
if (b._onReceive != nu
(C) private static size_t _receiveHeaderCallback(const char* str, size_t size, size_t nmemb, void* ptr) { import std.string
: chomp;
(C) private static size_t _sendCallback(char *str, size_t size, size_t nmemb, void *ptr) { Curl* b = cast(Curl*) ptr; auto a = cast(void[])
str[0 .. size*nmemb]; if (b.
(C) procedure,
(C) procedure and the symbol is not BIND(C), or vice versa, we haven't found the right procedure. */
(C) procedure "because derived type %qs is not C interoperable", sym->name, &(sym->declared_at),
sym->ns->proc_name->name, sym->ts.u.derived->name); else if (sym->ts.type
(C) procedure "because it is polymorphic", sym->name, &(sym->declared_at), sym->ns->proc_name-
>name); else if (warn_c_binding_type) gfc_warning (OPT_Wc_binding_type,
(C) procedure. call foo_opt (flag = 2) if (status == 0) then write(*,*) 'OK' else stop 1 end if end subroutine
(C) procedure "interoperable", sym->name, &(sym->declared_at), sym->ns->proc_name->name);
}
(C) procedure "
"%qs", sym->name, &sym->declared_at, sym->ns->proc_name->name)) return; else if
(!gfc_logical_kinds[]).c_bool && !gfc_notify_std (GFC_STD_GNU, "LOGICAL
(C) procedures as ISO Fortran Interop descriptors. These have to be converted to gfortran descriptors and back again. This has to be done here so
that the conversion occurs at the start of the
(C) procedures so set it from the descriptor element length. */ if (sym->ts.type == BT_CHARACTER && sym->ts.u.cl->backend_decl && VAR_P
(sym->ts.u.cl->backend_decl)) { tmp = build_f
(C) procedure &sym->declared_at, sym->attr.function ? sym->name : sym->ns-
>proc_name->name))return; }
(C) procedure sym->name, &(sym->declared_at), sym->ns->proc_name->name, &(sym->ns-
>proc_name->declared_at); sym->ts.f90_type = sym->ts.type; }
(C) procedure! 'test' but may not be C interoperable!
(C) procedure with NAME may not have " "POINTER attribute at %C"; return MATCH_ERROR; } if (has_name_equals &&
sym->attr.dummy)
(C) prohibited in an " "elemental procedure", &entry->declared_at); return MATCH_ERROR; }
(C) @property void f3() deprecated(!!!) @safe @safe void f4() {} @ (1) @ (1) void f5() {} // OK
(C) @property void popFront(){} } struct FrontS(C)
(C) prototypes. if (flag_c_prototypes) { for (gfc_current_ns = gfc_global_ns_list; gfc_current_ns; gfc_current_ns = gfc_current_ns->sibling)
gfc_dump_c_prototypes (gfc_current_ns, std
(C) Proxy{ return &pthis;
(C) pure nothrow @nogc:
(C) pure nothrow @nogc @safe{ Range _aaRange(AA aa)
(C) pure nothrow @nogc @system:
(C) pure nothrow Object typecons_d_toObject(void)
(C) pure nothrow @safe @nogc void function(), DG5 = extern(D) pure nothrow @safe @nogc void delegate();
(C) pure static if (false) void F10(){} else void F11(){} }
(C) pure static if (false) void G10(){}else
(C) putchar_unlocked (C)# endif# ifdef HAVE_GETC_UNLOCKED# undef getc# define getc(Stream) getc_unlocked (Stream)# endif# ifdef
HAVE_GETCHAR_UNLOCKED# undef getchar# define getchar() getch
(C) REAL C(LDC, LOGICAL LSAME LOGICAL UPPER IF (LSAME) THEN DO 170 J = 1,N DO 140 K = 1,J IF
(UPPER) THEN END
(C) real, contiguous :: x(:) if (any(abs(x - [2.,4.,6.]) > 1.e-6)) stop 1 x = [2.,4.,6.]*10.0 end subroutine
(C) real rndtonl(real x);
(C) RECORD ACCESS TYPE.
(C)(ref Cif (is(C == char) || is(C == wchar) || is(C == dchar)){ toCaseInPlace!(LowerTriple)(s);
(C)(ref Cif (is(C == char) || is(C == wchar) || is(C == dchar)){ toCaseInPlace!(UpperTriple)(s);
(C)(ref Cif (isNarrowString!(C[])){ import std.algorithm.comparison : min;
(C)(ref C if (isSomeChar!C && is(Unqual!C == C) && !is(C == enum)) { import std.exception : enforce;
(C)(ref Cif (isSomeChar!C && is(Unqual!C == C) && !is(C == enum)){ return stdin.readLine(buf, terminator);
(C)(ref C if (isSomeChar!C) { return fwdStr!"match"(str);

```

```

(C)(ref C      if (isSomeChar!C)  {      return fwdStr!"skip"(str);
(C)(ref C      if (isSomeChar!C)  {      return fwdStr!"test"(str);
(C)(ref C      size_t destIdx) @trusted pure  if (is(C == char) || is(C == wchar) || is(C == dchar)) {      import std.utf : decode;      alias
caseLength = toCaseLength!(indexFn,
(C)(ref C{ str = str; str = str[1 .. str.length];
(C) RELATIONS BASED ON SECONDS.-- (D) RELATIONS AT EXTREMES OF THE PERMITTED RANGE OF TIME.
(C)      (Result (C)
(C)      (Result (C)
(C) result(res) logical(kind=2) :: res logical(kind=4) :: local ! OKend function sub4
(C) return value.implicit none INTERFACE FUNCTION my() BIND(C,name="my") RESULT(r) ! { dg-error "cannot be an array" } USE iso_c_binding
CHARACTER(kind=C_CHAR) :: r(10) END F
(C) routine may not be interoperable if they are implicitly typed. */ gfc_warning_now (OPT_Wc_binding_type, "Implicitly declared variable
"
"%qs at %L may not be C intero
(C) R : REC := (IDENT_INT(2), "AA");
(C) R : REC := (IDENT_INT(2), "AA");
(C) RT function(P) nothrow @nogc;}
(C) RT function(P) nothrow @nogc @system;}
(C) // rt.minfo{ void rt_moduleTlsCtor(); void rt_moduleTlsDtor();
(C) S11 ctest11(ubyte x, S11, ubyte y);
(C) S12 ctest12(ubyte x, S12, ubyte y);
(C) S13 ctest13(ubyte x, S13, ubyte y);
(C) S14 ctest14(ubyte x, S14, ubyte y);
(C) S15 ctest15(ubyte x, S15, ubyte y);
(C) S16 ctest16(ubyte x, S16, ubyte y);
(C) S7 ctest10();
(C) SAME AS (A), WITH TYPE CONVERSION.-- (D) SAME AS (B), WITH TYPE CONVERSION.
(C)! See PR fortran/39239! { dg-do compile }module m use iso_c_binding implicit none integer(c_int) :: i1, i2 bind(C) :: i2 equivalence(i1,i2) ! { dg-
error "EQUIVALENCE attribute conflic
(C) seven = 46 END FUNCTION seven integer FUNCTION eight() bind(C, name='nine') eight = 48 END FUNCTION eight integer FUNCTION ten()
bind(C, name='') ten = 48 END FUNCTION ten
(C) %s function(double, string)", inner_name));
(C) short HIWORD_(uint i){ return cast(short)(( i >> 16) & 0xFFFF);
(C) since that only implies that they're interoperable, and they are checked fully for interoperability when a variable is declared of that type. */ if
(sym->attr.is_bind_c && sym->attr.u
(C) / sizeof(int); const int Q = sizeof(D) / sizeof(int); const int R = sizeof(E) / sizeof(int);
(C) size_t __aaLen(in AA aa) pure nothrow @nogc{ return aa ? aa.length : 0;
(C) size_t __d_arraysetcapacity(const TypeInfo ti, size_t newcapacity, void)
(C) size_t __d_arraysetcapacity(const TypeInfo ti, size_t newcapacity, voidin{ assert(ti); assert(!(*p).length || (*p).ptr);
(C) size_t function(size_t) gc_reserve;
(C) size_t function(void
(C) size_t function(void
(C) size_t gc_extend( void
(C) size_t gc_extend( void{ if ( proxy is null ) return 0; return proxy.gc_extend( p, mx, sz, ti );
(C) size_t gc_reserve( size_t sz){ if ( proxy is null ) return 0; return proxy.gc_reserve( sz );
(C) size_t gc_reserve( size_t sz) nothrow;
(C) size_t gc_sizeOf( void
(C) size_t gc_sizeOf( void{ if ( proxy is null ) return 0; return proxy.gc_sizeOf( p );
(C) size_t strlen(const char
(C) specifier,
(C) specifier, with the optional given, and set the binding label in either the given symbol (if not NULL), or in the current_ts. The symbol may be NULL
because we may
(C)'s separately in different range of signs.
(C) ssize_t specialRead(void { memset(buf, 'a', size); return size;
(C) ssize_t specialWrite(void { int* c = cast(int*) cookie; flag = *c; written += size; return size;
(C) statement at return MATCH_ERROR;
(C) statement in which ORIG_INDEX is the expression to be tested. If ORIG_TYPE is not NULL, it is the original ORIG_INDEX type as given in the
source before any compiler conversions. Gener
(C) static int cmp(const void } int[4] arr; qsort(arr.ptr, arr[0].sizeof, arr.length, &S.cmp); int key; bsearch(&key, arr.ptr, arr[0].sizeof,
arr.length, &S.cmp);
(C) static void xtrnC() extern(D) static void xtrnD() {} } auto dg_xtrnC = toDelegate(&S.xtrnC); auto dg_xtrnD =
toDelegate(&S.xtrnD); static assert(! is(ty

```

```

(C) string{ return _d_args;
(C) strings with descriptor. int opr_flag_int; if (gfc_is_proc_ptr_comp (opr_expr)) { gfc_symbol *sym = gfc_get_proc_ptr_comp (opr_expr)-
>ts.interface; opr_flag_int = sym->attr.dime
(C) struct curl_fileinfo{ char *filename; // curlfiletype filetype; // time_t time; // uint perm; // int uid;
(C) struct curl_forms{ CURLformoption option; // const(char) *value; //
(C) struct curl_httppost{ curl_httppost *next; // next entry in the list */ char *name; // pointer to allocated name */ c_long
namelength; // length of
(C) struct curl_khkey{ const(char) *key; // points to a zero-terminated string encoded with base64 if len is zero, otherwise to the
"raw" data */ size_t len; //
(C) struct CURLMsg{ CURLMSG msg; // what this message means */ CURL *easy_handle; // the handle it concerns */ _N31 data; //
(C) struct curl_sockaddr{ int family; // int socktype; // int protocol; // uint addrlen; // addrlen was a socklen_t type before 7.18.0 but it
turned really
(C) struct F5celse struct F5d {}
(C) struct _N28{ CURLversion age; // age of the returned struct */ const(char) *version_; // LIBCURL_VERSION */ uint version_num; //
LIBCURL_VERSION_NUM */ const(char) *host;
(C) struct _N2{ char *time; // char *perm; // char *user; // char *group; // char *target; // pointer to the target filename of a
symlink */
(C) (((struct objc_class# define method_getImplementation(M) (((Method)M)->method_imp)# define method_getTypeEncoding(M) (((Method)M)-
>method_types)# define object_getClass(O) (*(struct objc_cl
(C) // Subject: exception handling problem// Message-ID: <01H6I5GEAF5UPBJ0UV@VENUS.CPQD.ANSP.BR>
(C) symbol is C interoperable. If it is not, an appropriate error message is reported. */
(C) & " TERMINATED A " "STRING = COMPARISON"); END IF; END LOOP;
(C) test_dg(c => c); test_dg(delegate(C c) => c);
(C) test_fp(c => c); test_fp(function(C c) => c);}
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C) tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if defined
_WIN32 || defined __WIN32__ || defined __E
(C) T{ foreach (i; 0 .. a.length) a[i] = b[i] + c[i]; return a;
(C) THE BASE TYPE OF A PARAMETER IS DIFFERENT FROM THAT-- OF THE CORRESPONDING ONE.
(C) (the is_bind_c flag and the binding label), and verify that the type is C interoperable. Errors are reported by the functions used to set/test these
fields. */
(C) then Buf.Chars (Buf.Length) := 'W'; Set_Hex_Chars (C / 256); Set_Hex_Chars (C mod 256);
(C) then declare CC : constant Character := Get_Character (C); begin if CC in 'a' .. 'z' or else CC in '0' .. '9' then
Buf.Chars (Buf.Length) :
(C) then declare True_Branch : Boolean := Test_Result; Cond : Node_Id := C;
(C) then Ensure_Type_Is_SA (C); end if;
(C) then Error_Msg_Node_2 := Typ; Error_Msg_N ("& is not a component of)", Selectr); end if;
end;
(C) THEN FAILED("C DOES NOT EQUAL E"); END IF;
(C) then if Base_Char = '#' then Error_Msg_S -- CODEFIX ("missing '#"); else
Error_Msg_S -- CODEFIX
(C) then List.Container.Append (New_Item); end if;
(C) then L := N + 1;
(C) then Max_Ranges (Range_Num). High := C; exit; else Max_Ranges (Range_Num). High := Character'Pred (C);
end if; end loop;
(C) then Msglen := Msglen - 1; end if; end Buffer_Remove;
(C) then null;
(C) then OK := False; Ptr := J; exit;
(C) then P := P + 1;
(C) then return C; end if; end loop;
(C) then return Chars (Ent);
(C) then return; end if;
(C) then return; end if;
(C) then return False; end if;
(C) then return True; end if;
(C) then then return True; end if;
(C) then Scan_Reserved_Identifier (Force_Msg => False); Ident_Node := Token_Node; Scan; -- past the node return Ident_Node;
(C) then Scan_Reserved_Identifier (Force_Msg => True);
(C) then Store_Compilation_Switch ("-gnatw_" & C); else Bad_Switch ("-gnatw_" & Switch_Chars (Ptr ..
Max));
en

```

```

(C) then Store_Compilation_Switch ("-gnatw." & C); else Bad_Switch ("-gnatw." & Switch_Chars (Ptr ..
Max)); en
(C) then Store_Compilation_Switch ("-gnatw" & C); else Bad_Switch ("-gnatw" & Switch_Chars (Ptr ..
Max)); end
(C) then twiddle, if it -- then frozzle and if it's another magic char then push C and -- terminate the string. If none of the above, push C on the
-- string and go around again.
(C) then Ungetc (ch, File); return; end if;
(c) there is clear notice in each\n\ modified Data File or in the Software as well as in the\n\ documentation associated with the Data File(s) or
Software that the\n\ data or software has bee
(C) THERE IS NO CLOSING
(C) Thread thread_attachByAddrB( ThreadID addr, void { GC.disable(); scope(exit) GC.enable();
(C) Thread thread_attachByAddr( ThreadID addr ) { return thread_attachByAddrB( addr, getThreadStackBottom( addr ) );
(C) Thread thread_attachThis(){ GC.disable(); scope(exit) GC.enable();
(C) Throwable.TraceInfo_d_traceContext(void{ if (traceHandler is null) return null; return traceHandler(ptr);
(C) TIME_OF() GIVES THE PARAMETER SECONDS A DEFAULT VALUE OF 0.0.-- (D) THE FUNCTIONS YEAR(), MONTH(), DAY(), AND SECONDS()
RETURN-- CORRECT VALUES USING NAMED NOTATION.-- (E) A VALUE RET
(C) to max (sizeof(C), offset(D)+max (nvsz(D), dsz(D))). /* tree nvsz = CLASSTYPE_SIZE_UNIT (type); /* end_of_class doesn't
always give dsz, but it does in the case of a cla
(C) to max (sizeof(C), offset(D) offset = size_binop (PLUS_EXPR, DECL_FIELD_OFFSET (field), TYPE_SIZE_UNIT (TREE_TYPE
(field))); if (tree_int_cst_lt (result, offset)) result = offset;
(C) to try and prevent multiple errors being reported. /* has_non_interop_arg = 1;
(C) TraceHandler rt_getTraceHandler();
(C) TraceHandler rt_getTraceHandler(){ return traceHandler;
(C) @trusted nothrow:
(C) TSK1.E (R); FAILED ("EXCEPTION NOT RAISED IN SUBTEST (C)"); EXCEPTION WHEN CONSTRAINT_ERROR =>
NULL; WHEN
(C) type at c->name, &c->loc); return false; }
(C) TYPE CONVERSIONS
(C) uint function(void
(C) uint function(void
(C) uint gc_clrAttr( void
(C) uint gc_clrAttr( void{ if ( proxy is null ) return 0; return proxy.gc_clrAttr( p, a );
(C) uint gc_getAttr( void
(C) uint gc_getAttr( void{ if ( proxy is null ) return 0; return proxy.gc_getAttr( p );
(C) uint gc_setAttr( void
(C) uint gc_setAttr( void{ if ( proxy is null ) return 0; return proxy.gc_setAttr( p, a );
(C) uintptr_t_beginthreadex(void
(C) UNCONSTRAINED ACTUAL PARAMETERS OF LIMITED PRIVATE TYPE.
(C) union _N31{ void *whatever; /** message-specific data */ CURLcode result; /** return code for transfer */
(C) unittest align unittest {}
(C) _Unwind_Reason_Code gdc_personality(int iversion, _Unwind_Action actions,
_Unwind_Exception_Clas
(C) _Unwind_Reason_Code gdc_personality(_Unwind_State state, _Unwind_Exception* unwindHeader,
_Unwin
(C) _Unwind_Reason_Code unwindCB(_Unwind_Context { UnwindBacktraceData* bt = cast(UnwindBacktraceData*)d; if (bt.numframes >=
MAXFRAMES) return _URC_NO_REASON;
(C) USE, INTRINSIC :: ISO_C_BINDING L = .FALSE. END FUNCTION
(C) USE ISO_C_BINDING IMPLICIT NONE
(C) USE ISO_C_BINDING IMPLICIT NONE
(C) USE ISO_C_BINDING IMPLICIT NONE INTEGER(c_int) liter_cb TYPE, bind(C) :: info_t INTEGER(c_int) :: type END TYPE info_t
TYPE(info_t) :: link_info END FUNCTI
(C) use iso_c_binding implicit none integer :: i type(c_funptr) :: fptr fptr = c_funloc(foo) ! { dg-error "Function result 'foo' at .1. is invalid as X
argument to C_FUNLOC" } block fp
(C) use ISO_C_BINDING implicit none real(C_FLOAT) fun end function fun end interface
(C) use ISO_C_BINDING implicit none real(C_FLOAT) fun end function fun end interface kill_C_FUNPTR = C_FUNLOC(fun)
(C) use ISO_C_BINDING implicit none real(C_FLOAT) fun fun = 1.0end function fun
(C) use ISO_C_BINDING implicit none type(C_FUNPTR) kill_C_FUNPTR interface
(C) use ISO_C_BINDING implicit none type(C_PTR) kill_C_PTR integer(C_INT), pointer :: p allocate(p) kill_C_PTR = C_LOC(p)end function
kill_C_PTR
(C) use iso_c_binding integer(c_int), optional :: zend subroutine sub2
(C) use iso_c_binding integer(c_int), value :: zend subroutine sub

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(C) use iso_c_binding integer(c_int) :: z optional :: zend subroutine sub2a
(C) USE ISO_C_BINDING INTEGER(KIND=C_INT), VALUE :: a END FUNCTION SUBROUTINE init() BIND(C,name="init") END
SUBROUTINE END INTERFACE
(C) use iso_c_binding, only : c_bool logical(kind=c_bool) :: y ! OK
(C) use iso_c_binding, only: c_int integer(c_int) :: j ! output j = 42
(C) use iso_c_binding optional :: z integer(c_int) :: zend subroutine sub2b
(C) use iso_c_binding real(c_float) :: x,y end subroutine all_subs end interface
(C) * U) U := Tmp_UI; end if;
(C) val, f, base, Unsigned}
(C) vararg functions
(C) vararg functionsfail_compilation/fail11532.d(19): Error: cannot pass static arrays to extern(C++) vararg functionsfail_compilation/fail11532.d(20):
Error: cannot pass dynamic arrays to extern(C+
(C) variables should not be implicitly declared. gfc_warning_now (OPT_Wc_binding_type, "Implicitly declared BIND(C) " "variable %qs at
%L may not be C interoperable", sym->name
(C) Variable sym->name, &sym->declared_at); t = false; } else if (sym->common_head != NULL && sym->attr.implicit_type == 0)
(C) void {
(C) void {
(C) void _aaClear(AA aa) pure nothrow{ if (!aa.empty)
(C) void addCallback(void function(void * * // Allocate an object on the GC heap (this would usually be * // some application-specific context
data). * auto context = new Object;
(C) void _aligned_free(void
(C) void{ auto length = x.length; auto tinext = unqualify(ti.next); auto sizeelem = tinext.tsize; // array element size
_d_arrayappendcTX(ti, x, y.length); memcpy(x.p
(C) void{ auto old = ExceptionHeader.stack; ExceptionHeader.stack = cast(ExceptionHeader*)newContext; return old;
(C) void{ auto p = _d_newitemU(_ti); auto init = _ti.initializer(); assert(init.length <= _ti.tsize); memcpy(p, init.ptr, init.length); return p;
(C) void{ auto p = _d_newitemU(_ti); memset(p, 0, _ti.tsize); return p;
(C) void{ auto tinext = unqualify(ti.next); auto size = tinext.tsize;
(C) void{ auto ti = unqualify(_ti); auto flags = !(ti.flags & 1) ? BkAttr.NO_SCAN : 0; immutable tiSize = structTypeInfoSize(ti); immutable size =
ti.tsize + tiSize; if (tiSize)
(C) void{ // c could encode into from 1 to 2 w characters wchar[2] buf = void; byte[] appendthis; // passed to appendT if (c <= 0xFFFF)
(C) void{ // c could encode into from 1 to 4 characters char[4] buf = void; byte[] appendthis; // passed to appendT if (c <= 0x7F)
(C) void cFoo() enforce(false, &cFoo);}
(C) void Cfunc() static assert(functionLinkage!Dfunc == "D"); static assert(functionLinkage!Cfunc == "C");
(C) void/// C interface for Runtime.unloadLibrary, returns 1/0 instead of bool
(C) void cleanup() { if (_handle is null) return; _api.global_cleanup(); version (Posix)
(C) void cstyle(int, ...)extern(C++) void cppstyle(int, ...) {}void dstyle(...) {}void typesafe(int[...]...) {}
(C) void cstyle(int, ...) extern(D) void dstyle(...) {} extern(D) void typesafe(int[...]...) {}
(C) void cstyle(int, ...), use core.stdc.stdarg * "typesafe" void typesafe(T[...]...) */ // get symbol linkage as a string if (dim != 1)
return dimError(e
(C) void curl_easy_reset(CURL
(C) void cvararg(int, ...);extern(C++) void cppvararg(int, ...);
(C) void cVarArg(int, ...); void dVarArg(...); void dVarArg2(int, ...); void typesafeVarArg(int[...] ...);
(C) void _d_arrayshrinkfit(const TypeInfo ti, void
(C) void _d_arrayshrinkfit(const TypeInfo ti, void{ // note, we do not care about shared. We are setting the length no matter // what, so no lock is
required. debug(PRINTF) printf("_d_ar
(C) void _d_callfinalizer(void{ rt_finalize(p);
(C) void _d_callinterfacefinalizer(void{ if (p)
(C) void _d_criticalenter(D_CRITICAL_SECTION{ ensureMutex(cast(shared(D_CRITICAL_SECTION*)) cs); lockMutex(&cs.mtx);
(C) void _d_criticalexit(D_CRITICAL_SECTION{ unlockMutex(&cs.mtx);
(C) void _d_critical_init();
(C) void _d_critical_init(){ initMutex(cast(Mutex*)&gcs.mtx); head = &gcs;
(C) void _d_critical_term();
(C) void _d_critical_term(){ for (auto p = head; p; p = p.next) destroyMutex(cast(Mutex*)&p.mtx);
(C) void _d_delarray_t(void{ if (p)
(C) void _d_delarray(void{ _d_delarray_t(p, null);
(C) void _d_delclass(Object{ if (*p)
(C) void _d_delinterface(void{ if (*p)
(C) void _d_delmemory(void{ if (*p)
(C) void _d_delstruct(void{ if (*p)
(C) void _d_dso_registry(CompilerDSOData{ // only one supported currently safeAssert(data._version >= 1, "Incompatible compiler-generated DSO
data version.");

```

```

(C) void{ debug(PRINTF) printf("_d_arrayassign(from = %p,%d, to = %p,%d) size = %d\n", from.ptr, from.length, to.ptr, to.length, ti.tsize);
(C) void{ debug(PRINTF) printf("_d_arrayassign_l(src = %p,%d, dst = %p,%d) size = %d\n", src.ptr, src.length, dst.ptr, dst.length, ti.tsize);
(C) void{ debug(PRINTF) printf("_d_arrayassign_r(src = %p,%d, dst = %p,%d) size = %d\n", src.ptr, src.length, dst.ptr, dst.length, ti.tsize);
(C) void{ debug(PRINTF) printf("_d_arrayctor(from = %p,%d, to = %p,%d) size = %d\n", from.ptr, from.length, to.ptr, to.length, ti.tsize);
(C) void{ debug(PRINTF) printf("_d_newarraymiT(dims.length = %d)\n", dims.length);
(C) void{ debug(PRINTF) printf("_d_newarraymT(dims.length = %d)\n", dims.length);
(C) void{ debug(PRINTF) printf("__tls_get_addr input - %p\n", p); immutable offset = cast(size_t)(p - cast(void*)&_tlsstart); auto tls =
getTLSBlockAlloc(); assert(offset < tls.leng
(C) void delegate() cfunc; extern(D) void delegate() dfunc;
(C) void _d_emutls_init() nothrow @nogc{ memcpy(_emutlsMutex.ptr, typeid(Mutex).initializer.ptr, _emutlsMutex.length); (cast(Mutex)
_emutlsMutex.ptr).__ctor());
(C) void _d_initMonoTime();
(C) void _d_initMonoTime(){ // We need a mutable pointer to the ticksPerSecond array. Although this // would appear to break immutability, it is
logically the same as a static // ctor. So
(C) void// ditto
(C) void dmd_coverDestPath(string path);
(C) void dmd_coverSetMerge(bool flag);
(C) void dmd_coverSourcePath(string path);
(C) void _d_monitoredelete(Object h, bool det);
(C) void _d_monitoredelete(Object h, bool det){ auto m = getMonitor(h); if (m is null) return;
(C) void _d_monitorenter(Object h)in{ assert(h != null, "Synchronized object must not be null.");
(C) void _d_monitorexit(Object h){ auto m = cast(Monitor*) getMonitor(h); auto i = m.impl; if (i is null) unlockMutex(&m.mtx); else
i.unlock();
(C) void _d_monitor_staticctor();
(C) void _d_monitor_staticctor(){ version (Posix)
(C) void _d_monitor_staticctor();
(C) void _d_monitor_staticctor(){ destroyMutex(&gmtx); version (Posix) pthread_mutexattr_destroy(&gattr);
(C) void _d_print_throwable(Throwable t);
(C) void _d_print_throwable(Throwable t){ // On Windows, a console may not be present to print the output to. // Show a message box instead. If
the console is present, convert to // the c
(C) void _d_setSameMutex(shared Object ownee, shared Object owner) nothrow;
(C) void _d_setSameMutex(shared Object ownee, shared Object owner) nothrowin{ assert(ownee.__monitor is null);
(C) void dso_ { if (dso_initialized != condition)
(C) void dtestrir(int x1, int x2, int x3, int x4, int x5, int x6, c_long_double a, int b, c_long_double c){ assert(a == 300.0); assert(b == 68); assert(c
== 401.0);
(C) void _d_throw(Throwable object){ // If possible, avoid always allocating new memory for exception headers. ExceptionHeader *eh =
ExceptionHeader.create(object);
(C) void emutlsDestroyThread(void{ auto arr = cast(TlsArray*) ptr; emutlsMutex.lock_nothrow(); emutlsArrays.remove(arr);
emutlsMutex.unlock_nothrow();
(C) void __emutls_register_common(__emutls_object{ if (obj.size < size)
(C) void exception_cleanup(_Unwind_Reason_Code code, _Unwind_Exception { // If we haven't been caught by a foreign handler, then this is
// some sort of unwind error. In that case
(C) void{ ExceptionHeader* header = ExceptionHeader.toExceptionHeader(unwindHeader);
(C) void fiber_entryPoint() nothrow { Fiber obj = Fiber.getThis(); assert( obj );
(C) void fiber_switchContext( void { // NOTE: The data pushed and popped in this routine must match the // default stack created by
Fiber.initStack or the initial //
(C) void fiber_switchContext( void version (AArch64)
(C) void fiber_trampoline() nothrow; } else
(C) void foo6056()
(C) void fooC(); static extern(Pascal) void fooP(); static extern(Windows) void fooW(); static extern(C++) void fooCPP();
(C) void function();
(C) void function()");
(C) void function() cfunc; extern(D) void function() dfunc;
(C) void function() externCVoidFunc; static void impersonate_thread( uint id, externCVoidFunc fn ) { impersonate_thread(id, () => fn());
(C) void function() Func;
(C) void function() gc_collect;
(C) void function() gc_disable;
(C) void function() gc_enable;
(C) void function() gc_minimize;
(C) void function(int) nothrow
(C) void function(int) nothrow impl = (i) return (*func = impl)(args);}

```

```

(C) void function(int) nothrow impl = (i) return (func = impl)(args);}
(C) void function(int) nothrow someFunc13476 =
(C) void function(int) nothrow someFunc13476Alt =
(C) void function(in void
(C) void function() pure nothrow " /static assert(DG5.stringof == "void delegate() pure nothrow " /* ~ "@safe " */ ~ "@nogc");
(C) void function()); static assert(sizeof(fp1).mangleof == "PUZv"); static assert(sizeof(fp2).mangleof == "PUZv");}
(C) void function(void
(C) void function(void
(C) void function(void
(C) void gc_addRange( in void
(C) void gc_addRange( void{ //printf("gcstub::gc_addRange() proxy = %p\n", proxy); if ( proxy is null )
(C) void gc_addRoot( in void
(C) void gc_addRoot( void{ if ( proxy is null )
(C) void gc_clrProxy(){ foreach ( r; ranges[0 .. nranges] ) proxy.gc_removeRange( r.pos ); foreach ( r; roots[0 .. nroots] )
proxy.gc_removeRoot( r ); proxy = null;
(C) void gc_collect(){ if ( proxy is null ) return; return proxy.gc_collect();
(C) void gc_collect() nothrow;
(C) void gc_disable(){ if ( proxy is null ) return; return proxy.gc_disable();
(C) void gc_disable() nothrow;
(C) void gc_enable(){ if ( proxy is null ) return; return proxy.gc_enable();
(C) void gc_enable() nothrow;
(C) void gc_free( void
(C) void gc_free( void{ if ( proxy is null ) return free( p ); return proxy.gc_free( p );
(C) void gc_init();
(C) void gc_init(){ // NOTE: The GC must initialize the thread library before its first // collection, and always before returning from gc_init().
thread_init(); initProxy();
(C) void gc_minimize(){ if ( proxy is null ) return; return proxy.gc_minimize();
(C) void gc_minimize() nothrow;
(C) void gc_removeRange( in void
(C) void gc_removeRange( void{ if ( proxy is null )
(C) void gc_removeRoot( in void
(C) void gc_removeRoot( void{ if ( proxy is null )
(C) void gc_runFinalizers( in void
(C) void gc_runFinalizers( in void{ if ( proxy != null ) proxy.gc_runFinalizers( segment );
(C) void gc_setProxy( Proxy{ if ( proxy != null )
(C) void gc_term();
(C) void gc_term();
(C) void gc_term(){ free( roots ); free( ranges );
(C) void{ if (!paa.empty) paa.resize(nextpow2(INIT_DEN * paa.length / INIT_NUM)); return *paa;
(C) void{ if ( proxy is null )
(C) void{ if ( proxy is null ) return null; return proxy.gc_addrOf( p );
(C) void { immutable save = _rtLoading; _rtLoading = true; scope (exit) _rtLoading = save;
(C) void{ import core.internal.traits : TypeTuple;
(C) voidin{ assert(!(*p).length || (*p).ptr);
(C) voidin{ assert(Thread.getThis());
(C) voidin{ assert(ti); assert(!(*p).length || (*p).ptr);
(C) void initLibFoo(int argc, char * * void main()
(C) void init_msvc();}
(C) voidin{ // Not strictly required, but it gives us more flexibility. assert(Thread.getThis());
(C) void in size_t valsz, in void* pkey){ bool found; return aaGetX(aa, ti, valsz, pkey, found);
(C) void in size_t valsz, in void* pkey, out bool found){ // lazily alloc implementation if (aa.impl is null) aa.impl = new Impl(ti);
(C) void(int, ...); static assert(fqn!(typeof(dVarArg)) == "void(...)"); static assert(fqn!(typeof(dVarArg2)) == "void(int, ...)"); static
assert(fqn!(typeof(typesafeVarArg))
(C) void lifetime_init();
(C) void lifetime_init(){ // this is run before static ctors, so it is safe to modify immutables import rt.config; string s =
rt_configOption("callStructDtorsDuringGC"); if (s != null
(C) void main (char * ___ * * Same as the above `argc`.
(C) void main (char * ___ * * Same as the above `argv`.
(C) void main (char * ___ * * Same as the above `envp`.
(C) void main(){ scope string s; s = "[" ~ s ~ "]";
(C) void maythrow();

```

```

(C) void _minit()
(C) void _minit() noexcept @nogc;
(C) void myHandler(void * { * // Assuming that the callback is invoked only once, the * // added root can be removed again now to allow
the GC * // to collect it later.
(C) void my_memcmp(const(void)
(C) void onAssertErrorMsg(string file, size_t line, string msg);
(C) void onAssertErrorMsg( string file, size_t line, string msg ) noexcept{ if ( _assertHandler is null ) throw new AssertionError( msg, file, line );
_assertHandler( file, line, msg );
(C) void onAssertError( string file = __FILE__, size_t line = __LINE__ ) noexcept{ if ( _assertHandler is null ) throw new AssertionError( file, line );
_assertHandler( file, line, null);
(C) void onAssertError(string file, size_t line);
(C) void onFinalizeError( TypeInfo info, Throwable e, string file = __FILE__, size_t line = __LINE__ ) @trusted noexcept{ // This error is thrown during
a garbage collection, so no allocation must
(C) void onHiddenFuncError(Object o);
(C) void onHiddenFuncError( Object o ) @safe pure noexcept{ throw new HiddenFuncError( typeid(o) );
(C) void onInvalidMemoryOperationError(void{ // The same restriction applies as for onOutOfMemoryError. The GC is in an // undefined state, thus
no allocation must occur while generating this
(C) void onOutOfMemoryErrorNoGC() @trusted noexcept @nogc{ // suppress stacktrace until they are @nogc throw
staticError!OutOfMemoryError!OutOfMemoryError(false);
(C) void onOutOfMemoryError(void
(C) void onOutOfMemoryError(void{ // NOTE: Since an out of memory condition exists, no allocation must occur // while generating this object.
throw staticError!OutOfMemoryError();
(C) void onRangeError( string file = __FILE__, size_t line = __LINE__ ) @safe pure noexcept{ throw new RangeError( file, line, null );
(C) void onRangeError(string file, size_t line);
(C) void onSwitchError( string file = __FILE__, size_t line = __LINE__ ) @safe pure noexcept{ throw new SwitchError( file, line, null );
(C) void onSwitchError(string file, size_t line);+/
(C) void onUnicodeError( string msg, size_t idx, string file = __FILE__, size_t line = __LINE__ ) @safe pure;
(C) void onUnicodeError( string msg, size_t idx, string file = __FILE__, size_t line = __LINE__ ) @safe pure{ throw new UnicodeException( msg, idx,
file, line );
(C) void onUnittestErrorMsg(string file, size_t line, string msg);
(C) void onUnittestErrorMsg( string file, size_t line, string msg ) noexcept{ onAssertErrorMsg( file, line, msg );
(C) void pcinforCallback(void { auto context = cast(SymbolCallbackInfo*)data;
(C) void{ pointer offset; if (__gthread_active_p())
(C) void printArrayCache() { auto ptr = __blkcache; printf("CACHE: \n"); foreach (i; 0 .. N_CACHE_BLOCKS)
(C) void profilegc_setlogfilename(string name);
(C) void qsort_r(scope void
(C) void{ return GC.malloc(sz);
(C) void { return initLibrary(.LoadLibraryA(name));
(C) void { return initLibrary(.LoadLibraryW(name));
(C) void rt_attachDisposeEvent(Object h, DEvent e){ synchronized (h)
(C) void rt_attachDisposeEvent( Object obj, DisposeEvt evt );
(C) void rt_detachDisposeEvent(Object h, DEvent e){ synchronized (h)
(C) void rt_detachDisposeEvent( Object obj, DisposeEvt evt );//debug=signal;
(C) void rt_finalize2(void{ debug(PRINTF) printf("rt_finalize2(p = %p)\n", p);
(C) void rt_finalizeFromGC(void{ // to verify: reset memory necessary? if (!(attr & BlkAttr.STRUCTFINAL)) rt_finalize2(p, false, false); // class
else if (attr & BlkAttr.APPENDABL
(C) void rt_finalize(void)
(C) void rt_finalize(void{ rt_finalize2(p, det, true);
(C) void rt_moduleCtor();
(C) void rt_moduleDtor();
(C) void rt_moduleTlsCtor();
(C) void rt_moduleTlsCtor();
(C) void rt_moduleTlsCtor();*/
(C) void rt_moduleTlsDtor();
(C) void rt_moduleTlsDtor();)*/
(C) void rt_moduleTlsDtor();
(C) void rt_setCollectHandler( CollectHandler h );
(C) void rt_setCollectHandler(CollectHandler h){ collectHandler = h;
(C) void rt_setTraceHandler( TraceHandler h );
(C) void rt_setTraceHandler(TraceHandler h){ traceHandler = h;
(C) void sections_osx_onAddImage(in mach_header{ foreach (e; dataSegs)

```



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(C) void setFinalizeCounter(shared(size_t){ _finalizeCounter = p;
(C) void simpleErrorCallback(void { if (data) // context is not available in backtrace_create_state
(C) void{ size_t length; auto tnext = unqualify(ti.next); auto size = tnext.tsize; // array element size
(C) void syminfoCallback2(void const(char)* symname, uintptr_t symval) { auto context =
cast(SymbolCallbackInfo2*)data;
(C) void syminfoCallback(void const(char)* symname, uintptr_t symval) { auto context = cast(SymbolCallbackInfo*)data;
(C) void test4();
(C) void testC8504()
(C) void thread_cleanupHandler( void { Thread obj = cast(Thread) arg; assert( obj );
(C) void thread_detachByAddr( ThreadID addr ) { if ( auto t = thread_findByAddr( addr ) ) Thread.remove( t );
(C) void thread_detachInstance( Thread t ) noexcept @nogc{ Thread.remove( t );
(C) void thread_detachThis() noexcept @nogc{ if (auto t = Thread.getThis()) Thread.remove(t);
(C) void thread_enterCriticalRegion() @nogcin{ assert(Thread.getThis());
(C) void thread_exitCriticalRegion() @nogcin{ assert(Thread.getThis());
(C) void thread_init();
(C) void thread_init(){ // NOTE: If thread_init itself performs any allocations then the thread // routines reserved for garbage collector use may
be called while // thread_in
(C) void thread_joinAll();
(C) void thread_joinAll(){ Lagain:
(C) void thread_processGCMarks( scope IsMarkedDg isMarked ) noexcept{ for ( Thread t = Thread.sm_tbeg; t; t = t.next )
(C) void thread_resumeAll() noexcept{ assert( suspendDepth > 0 );
(C) void thread_resumeHandler( int sig ) noexcept in { assert( sig == resumeSignalNumber );
(C) void thread_scanAll( scope ScanAllThreadsFn scan ) noexcept{ thread_scanAllType((type, p1, p2) => scan(p1, p2));
(C) void thread_scanAllType( scope ScanAllThreadsTypeFn scan ) noexcept{ assert( suspendDepth > 0 );
(C) void thread_setGCSignals(int suspendSignalNo, int resumeSignalNo) noexcept @nogc {
(C) void thread_setGCSignals(int suspendSignalNo, int resumeSignalNo) noexcept @nogc in { assert(suspendSignalNumber == 0);
assert(resumeSignalNumber == 0); assert(susp
(C) void thread_setThis(Thread t) noexcept @nogc{ Thread.setThis(t);
(C) void thread_suspendAll() noexcept{ // NOTE: We've got an odd chicken & egg problem here, because while the GC // is required to call
thread_init before calling any other thread /
(C) void thread_suspendHandler( int sig ) noexcept in { assert( sig == suspendSignalNumber );
(C) void thread_term();
(C) void thread_term(){ assert(Thread.sm_tbeg && Thread.sm_tlen == 1); assert(!Thread.nAboutToStart); if (Thread.pAboutToStart) // in case
realloc(p, 0) doesn't return null
(C) void trace_setdefilename(string name);
(C) void trace_setlogfilename(string name);
(C) void unittestSegvHandler( int signum, siginfo_t { import core.stdc.stdio; fprintf(stderr, "Segmentation fault while running
unittests:\n"); fprintf(stde
(C) void unittestSegvHandler( int signum, siginfo_t { static enum MAXFRAMES = 128; void*[MAXFRAMES] callstack;
(C) void { version (Shared)
(C) void{ void* pstart = p;
(C) void{ void* pstart = p; auto element_size = ti.tsize;
(C) void{ void[] result = _d_newarrayU(ti, length); auto tnext = unqualify(ti.next); auto size = tnext.tsize;
(C) void W7585a()
(C) V; \ } \ while (0)
(C) wchar{ if (a.length > 1)
(C) with a BIND(C) attribute and that is not allowed for procedures. */ if (entry->attr.is_bind_c == 1) { locus loc;
(C) with a BIND(C) attribute and that is not allowed for procedures. */ if (sym->attr.is_bind_c == 1) { sym->attr.is_bind_c = 0;
(C) with OPTIONAL!module m use iso_c_binding interface
(C) work.!! Contributed by Richard Maine
(C) < WT.Table (Right)(C); end Lt;
(C) X : DURATION := 0.0; OLD_TIME : TIME; LAPSE : DURATION;
(C) * x) in the non-vectorized version, but for vectorization we don't have vectorized exp2. */ if (logc && TREE_CODE (logc) ==
REAL_CST && exp_decl && lookup_attribute
(C) * x). Normally match.pd does that, but if C is a power of 2, we want to use
(C) ZERO DELAY ARGUMENT.-- (D) DURATION'SMALL DELAY ARGUMENT.-- (E) EXPRESSION OF TYPE DURATION AS DELAY ARGUMENT.
GD 2.2.5 opensuse:gd-debugsource/2.2.5-2.2/ppc64le
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Copyright 2000 Doug Becker, mailto:thebeckers@home.com * * Modification 4/18/00 TBB: JPEG_DEBUG rather than just DEBUG, * so VC++ builds don't

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spew to standard output, causing * major CGI brain da
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accompanying README file. * * This file con
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optimal colour quantization" * in "Network: Computation
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copyright info was found in the original bdf. */
GDB 7.6 gnu:gdb:7.6
(C) AC_LINK_IFELSE( AC_LANG_POP] if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need
for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
(C)# B. (c)
(C)/!b go :more \./!{ N s/\n# // b more
(C) __btowc (C)# else
(C) btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef _LIBC# define MEMPCPY(D, S, N) __mempcpy (D, S,
N)# else# if HAVE_MEMPCPY# define MEMPCPY(D
(C) (((C)
(C) ((C >= C-'0' : ((C >= 'A' && C <= 'F') ? \ C-'A'+10 : ((C >= 'a' && C <= 'f') ? \ C-'a'+10 :
-1)))
(C) (((C)#define CC3(OP) (((OP)>>8)&0x3)#define SBIT(OP) ((OP)&0xF)#define MMR(OP) ((OP)&0x7F)#define MMRX(OP)
(((OP)>>4)&0xF)+16)#define MMRY(OP) (((
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_PATH_WITH_DIR(P) \ (strchr (P, '/') != NULL ||
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)#endif
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])#endif
(C) ((C) ==# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define FILESYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) == DIRECTORY_SEPARATOR)#endif
(C) ((C) ==#endif
(C) (C)# ifdef _LIBC# define STRLEN(S) __wcslen (S)# define STRCAT(D, S) __wscat (D, S)# define MEMPCPY(D, S, N) __wmempcpy (D, S,
N)# else# define STRLEN(S) wcslen (S)# defin
(C) Copyright 1984 by Third Eye Software, Inc. * * Third Eye Software, Inc. grants reproduction and use rights to * all parties, PROVIDED that this
comment is maintained in the copy.
(C) ea OBITOP (O_BIST, 1, 1, ea &= ~m; if (!C) ea |= m); /* bist */ OBITOP (O_BSTZ, 1, 1, ea &= ~m; if (Z)
ea |= m); /* bstz */ OBITOP (O_BISTZ, 1, 1, ea &= ~m; if (!Z) ea |=
(C) is the return code for exiting with status C. - M32C_MAKE_STOPPED (S) is the return code for stopping on signal S. */#define
M32C_MAKE_STOPPED() (0)#define M32C_MAKE_HIT_BREAK() (1)
(C) is the return code for exiting with status C. - RL78_MAKE_STOPPED (S) is the return code for stopping on signal S. */#define
RL78_MAKE_STOPPED() (1)#define RL78_MAKE_HIT_BREAK() (2)
(C) is the return code for exiting with status C. - RX_MAKE_STOPPED (S) is the return code for stopping on signal S. */#define RX_MAKE_STOPPED()
(1)#define RX_MAKE_HIT_BREAK() (2)
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'/^\.ifndef,$$p' \ < $(top_srcdir)/import/extra/snippet/warn-on
Copyright Header\n"); fprintf (file, _(" copyright: %.*s\n"), (int)(rec_len - sizeof (struct vms_emh_common)), (char *)rec +
sizeof (struct vms_emh_common)
copyright header of the files in the given list.
copyright headers. This script is only# fixing the first one it finds, so we need to finish the update# by hand.MULTIPLE_COPYRIGHT_HEADERS = (
"gdb/doc/gdb.texinfo", "gdb/doc/refcard.tex",
Copyright' in line: return True lineno += 1 if lineno > 50:
copyright is a brief statement that the program is
copyright Jiri Gaisler 1995\n", sis_version); printf(" Bug-reports to jgais@wd.estec.esa.nl\n\n"); while (stat < argc) { if (argv[stat][0] == '-') {
if (strcmp(argv[stat], "-v") == 0) {
copyright lib/alloca.c lib/alloca.in.h lib/config.charset lib/fnmatch.c lib/fnmatch.in.h lib/fnmatch_loop.c lib/inttypes.in.h lib/localcharset.c

```

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lib/localcharset.h lib/mbrtowc.c
copyright line is 72.# Set UPDATE_COPYRIGHT_MAX_LINE_LENGTH to use a different length.# 5. Set UPDATE_COPYRIGHT HOLDER if the
copyright holder is other# than "Free Software Foundation,
copyright message after the interpreter is installed when it is any sane interpreter. */ if (!quiet && !current_interp_named_p (INTERP_MI1)) {
/* Print all the junk at the top, with
copyright message before the interpreter is installed. That way it isn't encapsulated in MI output. */ if (!quiet && strcmp (interpreter_p,
INTERP_MI1) == 0) { /* Print all the junk
copyright\n");
COPYRIGHT\n"); printf_filtered ("HOLDERS AND/OR OTHER PARTIES PROVIDE THE PROGRAM \"AS IS\" WITHOUT WARRANTY\n"); printf_filtered ("OF
ANY KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING, BUT NOT LIM
copyright script for that. "" # We want to use year intervals in the copyright notices, and # all years should be collapsed to one single year
interval, # even if there are "holes" in
copyright script prints # a well defined warning when it did not find the copyright notice. # For each of those, do a sanity check and see if they may
in fact # have one. For the files th
Copyright/s/ -e '/Starting program: /s|[A-z]:/.*|'| \ -e '/main (/s/=0x[0-9a-f][0-9a-f]*/=XYZ/g' \ > ${base}.tst if diff --binary -u
${base}.out ${base}.tst ; then
Copyright sign { ISFUNC, rl_insert }, /* Feminine ordinal indicator */ { ISFUNC, rl_insert }, /* Left pointing double angle quotation mark */ {
ISFUNC, rl_insert }, /* Not sign */ { ISFUNC, rl
copyright string. len = obj_som_copyright_hdr (abfd)->header_id.length - 4; obj_som_file_hdr (abfd)->aux_header_size += len;
current_offset += len; if (bfd_bwrite ((void *) obj
copyright target rule# in maint.mk from gnuilib's maintainer-makefile module.
copyright year intervals in a reformatted FSF copyright statement# are expanded instead.# If UPDATE_COPYRIGHT_USE_INTERVALS=2, convert
a sequence with gaps# to the minimal containing
copyright year intervals. (See "Environment variables"# below.)
copyright year is 2 or 4 digits, and years are separated by# commas or dashes. Whitespace may appear after commas.
copyright year list to include the current year.
copyright years...
copyright years (such as 90, 1991, 1992-2007, 2008) in a# reformatted FSF copyright statement is collapsed to a single# interval (such as 1990-
2008). If unset or set to 0, all existing
(C) rd c = res; }
(C) rd c = res; } else
(C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 1/0 - SUBF3 Alias: i, i3*/#define
S_CLASS_INSN(name, opcode, level) \
(C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 2/0 - ADDF3, MPYF3 Alias: i,
i3*/#define SC_CLASS_INSN(name, opcode,
(C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) Instr: 1/0 - CMPF3 Alias: i, i3*/#define S2_CLASS_INSN(name, opcode,
level) \ { name, opcode|0x20000000, 0
(C)<<srca);", 8 }
(C)" takes one of the following forms or is omitted# entirely:
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C) tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if defined
_WIN32 || defined __WIN32__ || defined _E
GDB 7.6 redhat:devtoolset-2-gdb/7.6-34.el6/x86_64
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need
for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
(C)# B. (c)
(C)/!b go :more \./!{ N s\n# // b more
(C) _btowc (C)# else
(C) btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef _LIBC# define MEMPCPY(D, S, N) __mempcpy (D, S,
N)# else# if HAVE_MEMPCPY# define MEMPCPY(D
(C) (((C)
(C) ((C >=
C-'0' : ((C >= 'A' && C <= 'F') ? \ C-'A'+10 : ((C >= 'a' && C <= 'f') ? \ C-'a'+10 :
-1)))
(C) (((C)#define CC3(OP) (((OP)>>8)&0x3)#define SBIT(OP) ((OP)&0xF)#define MMR(OP) ((OP)&0x7F)#define MMRX(OP)
(((OP)>>4)&0xF)+16)#define MMRYP(OP) (((
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_PATH_WITH_DIR(P) \ (strchr (P, '/') != NULL ||
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)#endif

```

```

(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISSLASH ((P)[0])#endif
(C) ((C) ==# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define FILESYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) == DIRECTORY_SEPARATOR)#endif
(C) ((C) ==#endif
(C) (C)# ifdef_LIBC# define STRLEN(S) __wcslen (S)# define STRCAT(D, S) __wcscat (D, S)# define MEMPCPY(D, S, N) __wmemcpy (D, S,
N)# else# define STRLEN(S) wcslen (S)# defin
(C) Copyright 1984 by Third Eye Software, Inc. * * Third Eye Software, Inc. grants reproduction and use rights to * all parties, PROVIDED that this
comment is maintained in the copy.
(C) ea OBITOP (O_BIST, 1, 1, ea &= ~m; if (!C) ea |= m); /* bist */ OBITOP (O_BSTZ, 1, 1, ea &= ~m; if (Z)
ea |= m); /* bstz */ OBITOP (O_BISTZ, 1, 1, ea &= ~m; if (!Z) ea |=
(C) is the return code for exiting with status C. - M32C_MAKE_STOPPED (S) is the return code for stopping on signal S. */#define
M32C_MAKE_STOPPED() (0)#define M32C_MAKE_HIT_BREAK() (1)
(C) is the return code for exiting with status C. - RL78_MAKE_STOPPED (S) is the return code for stopping on signal S. */#define
RL78_MAKE_STOPPED() (1)#define RL78_MAKE_HIT_BREAK() (2)
(C) is the return code for exiting with status C. - RX_MAKE_STOPPED (S) is the return code for stopping on signal S. */#define RX_MAKE_STOPPED()
(1)#define RX_MAKE_HIT_BREAK() (2)
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'/^\.ifndef/,$$p' \ < $(top_srcdir)/import/extra/snippet/warn-on
Copyright Header\n"); fprintf (file, _(" copyright: %.*s\n"), (int)(rec_len - sizeof (struct vms_emh_common)), (char *)rec +
sizeof (struct vms_emh_common)
copyright header of the files in the given list.
copyright headers. This script is only# fixing the first one it finds, so we need to finish the update# by hand.MULTIPLE_COPYRIGHT_HEADERS = (
"gdb/doc/gdb.texinfo", "gdb/doc/refcard.tex",
Copyright' in line: return True lineno += 1 if lineno > 50:
copyright is a brief statement that the program is
copyright Jiri Gaisler 1995\n", sis_version); printf(" Bug-reports to jgais@wd.estec.esa.nl\n\n"); while (stat < argc) { if (argv[stat][0] == '-') {
if (strcmp(argv[stat], "-v") == 0) {
copyright lib/alloca.c lib/alloca.in.h lib/config.charset lib/fnmatch.c lib/fnmatch.in.h lib/fnmatch_loop.c lib/inttypes.in.h lib/localcharset.c
lib/localcharset.h lib/mbrtowc.c
copyright line is 72.# Set UPDATE_COPYRIGHT_MAX_LINE_LENGTH to use a different length.# 5. Set UPDATE_COPYRIGHT HOLDER if the
copyright holder is other# than "Free Software Foundation,
copyright message after the interpreter is installed when it is any sane interpreter. */ if (!quiet && !current_interp_named_p (INTERP_MI1)) {
/* Print all the junk at the top, with
copyright message before the interpreter is installed. That way it isn't encapsulated in MI output. */ if (!quiet && strcmp (interpreter_p,
INTERP_MI1) == 0) { /* Print all the junk
copyright\n");
COPYRIGHT\n"); printf_filtered ("HOLDERS AND/OR OTHER PARTIES PROVIDE THE PROGRAM \"AS IS\" WITHOUT WARRANTY\n"); printf_filtered ("OF
ANY KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING, BUT NOT LIM
copyright script for that. "" # We want to use year intervals in the copyright notices, and # all years should be collapsed to one single year
interval, # even if there are "holes" in
copyright script prints # a well defined warning when it did not find the copyright notice. # For each of those, do a sanity check and see if they may
in fact # have one. For the files th
Copyright/s/ -e '/Starting program: /s/[A-z]:.*|'| \ -e '/main (/s/=0x[0-9a-f][0-9a-f]*/=XYZ/g' \ > ${base}.tst if diff --binary -u
${base}.out ${base}.tst ; then
Copyright sign { ISFUNC, rl_insert }, /* Feminine ordinal indicator */ { ISFUNC, rl_insert }, /* Left pointing double angle quotation mark */ {
ISFUNC, rl_insert }, /* Not sign */ { ISFUNC, rl
copyright string. len = obj_som_copyright_hdr (abfd)->header_id.length - 4; obj_som_file_hdr (abfd)->aux_header_size += len;
current_offset += len; if (bfd_bwrite ((void *) obj

```

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copyright target rule# in maint.mk from gnu's maintainer-makefile module.
copyright year intervals in a reformatted FSF copyright statement# are expanded instead.# If UPDATE_COPYRIGHT_USE_INTERVALS=2, convert
a sequence with gaps# to the minimal containing
copyright year intervals. (See "Environment variables"# below.)
copyright year is 2 or 4 digits, and years are separated by# commas or dashes. Whitespace may appear after commas.
copyright year list to include the current year.
copyright years...
copyright years (such as 90, 1991, 1992-2007, 2008) in a# reformatted FSF copyright statement is collapsed to a single# interval (such as 1990-
2008). If unset or set to 0, all existing
(C) rd c = res; }
(C) rd c = res; } else
(C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 1/0 - SUBF3 Alias: i, i3*/#define
S_CLASS_INSN(name, opcode, level) \
(C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 2/0 - ADDF3, MPYF3 Alias: i,
i3*/#define SC_CLASS_INSN(name, opcode,
(C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) Instr: 1/0 - CMPF3 Alias: i, i3*/#define S2_CLASS_INSN(name, opcode,
level) \ { name, opcode|0x20000000, 0
(C)<<srca);", 8 }
(C)" takes one of the following forms or is omitted# entirely:
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C) tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if defined
__WIN32 || defined __WIN32__ || defined __E
GDB 7.6 redhat:devtoolset-2-gdb-debuginfo/7.6-34.el5/i386
(C) __btowc (C)# else
(C) btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef _LIBC# define MEMPCPY(D, S, N) __mempcpy (D, S,
N)# else# if HAVE_MEMPCPY# define MEMPCPY(D
(C) ((C >=
C-'0' : ((C >= 'A' && C <= 'F') ? \
C-'A'+10 : ((C >= 'a' && C <= 'f') ? \
C-'a'+10 :
-1)))
(C) (C)# ifdef _LIBC# define STRLEN(S) __wcslen (S)# define STRCAT(D, S) __wscat (D, S)# define MEMPCPY(D, S, N) __wmempcpy (D, S,
N)# else# define STRLEN(S) wcslen (S)# defin
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copyright does not belong to the
copyright is a brief statement that the program is
copyright message after the interpreter is installed when it is  any sane interpreter.  */  if (!quiet && !current_interp_named_p (INTERP_MI1))  {
/* Print all the junk at the top, with
copyright message before the interpreter is installed.  That way  it isn't encapsulated in MI output.  */  if (!quiet && strcmp (interpreter_p,
INTERP_MI1) == 0)  {  /* Print all the junk
copyright\n");
COPYRIGHT\n"); printf_filtered ("HOLDERS AND/OR OTHER PARTIES PROVIDE THE PROGRAM \"AS IS\" WITHOUT WARRANTY\n"); printf_filtered ("OF
ANY KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING, BUT NOT LIM
Copyright sign { ISFUNC, rl_insert },  /* Feminine ordinal indicator */ { ISFUNC, rl_insert },  /* Left pointing double angle quotation mark */ {
ISFUNC, rl_insert },  /* Not sign */ { ISFUNC, rl
GDB 7.6 redhat:devtoolset-2-gdb-debuginfo/7.6-34.el5/x86_64
(C)  __btowc (C)# else
(C)  btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef _LIBC# define MEMPCPY(D, S, N) __mempcpy (D, S,
N)# else# if HAVE_MEMPCPY# define MEMPCPY(D
(C) ((C >=
C-'0' : ((C >= 'A' && C <= 'F') ? \
C-'A'+10 : ((C >= 'a' && C <= 'f') ? \
C-'a'+10 :
-1)))
(C) (C)# ifdef _LIBC# define STRLEN(S) __wcslen (S)# define STRCAT(D, S) __wscat (D, S)# define MEMPCPY(D, S, N) __wmempcpy (D, S,
N)# else# define STRLEN(S) wcslen (S)# defin
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copyright is a brief statement that the program is
copyright message after the interpreter is installed when it is any sane interpreter. */ if (!quiet && !current_interp_named_p (INTERP_MI1)) {
/* Print all the junk at the top, with
copyright message before the interpreter is installed. That way it isn't encapsulated in MI output. */ if (!quiet && strcmp (interpreter_p,
INTERP_MI1) == 0) { /* Print all the junk
copyright\n");
COPYRIGHT\n"); printf_filtered ("HOLDERS AND/OR OTHER PARTIES PROVIDE THE PROGRAM \"AS IS\" WITHOUT WARRANTY\n"); printf_filtered ("OF
ANY KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING, BUT NOT LIM
Copyright sign { ISFUNC, rl_insert }, /* Feminine ordinal indicator */ { ISFUNC, rl_insert }, /* Left pointing double angle quotation mark */ {
ISFUNC, rl_insert }, /* Not sign */ { ISFUNC, rl
GDB 7.6.50.20130731 opensuse:gdb/7.6.50.20130731-3.1.2/x86_64
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need

```

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for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
(C)# B. (c)
(C)!b go :more \.!\{ N s\n# // b more
(C) btowc (C)# else
(C) btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef _LIBC# define MEMPCPY(D, S, N) __mempcpy (D, S, N)# else# if HAVE_MEMPCPY# define MEMPCPY(D
(C) (((C)
(C) ((C >=
-1)))
(C) (((C)#define CC3(OP) (((OP)>>8)&0x3)#define SBIT(OP) ((OP)&0xF)#define MMR(OP) ((OP)&0x7F)#define MMRX(OP)
(((OP)>>4)&0xF)+16)#define MMRY(OP) (((
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_PATH_WITH_DIR(P) \ (strchr (P, '/') != NULL ||
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)#endif
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])#endif
(C) ((C) ==# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define FILESYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) == DIRECTORY_SEPARATOR)#endif
(C) ((C) ==#endif
(C) (C)# ifdef _LIBC# define STRLEN(S) __wcslen (S)# define STRCAT(D, S) __wscat (D, S)# define MEMPCPY(D, S, N) __wmempcpy (D, S,
N)# else# define STRLEN(S) wcslen (S)# defin
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comment is maintained in the copy.
(C) ea OBITOP (O_BIST, 1, 1, ea &= ~m; if (!C) ea |= m); /* bist */ OBITOP (O_BSTZ, 1, 1, ea &= ~m; if (Z)
ea |= m); /* bstz */ OBITOP (O_BISTZ, 1, 1, ea &= ~m; if (!Z) ea |=
(C) is the return code for exiting with status C. - M32C_MAKE_STOPPED (S) is the return code for stopping on signal S. */#define
M32C_MAKE_STOPPED() (0)#define M32C_MAKE_HIT_BREAK() (1)
(C) is the return code for exiting with status C. - RL78_MAKE_STOPPED (S) is the return code for stopping on signal S. */#define
RL78_MAKE_STOPPED() (1)#define RL78_MAKE_HIT_BREAK() (2)
(C) is the return code for exiting with status C. - RX_MAKE_STOPPED (S) is the return code for stopping on signal S. */#define RX_MAKE_STOPPED()
(1)#define RX_MAKE_HIT_BREAK() (2)
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copyright (fp);
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copyright header cut# off.arg-nonnull.h: $(top_srcdir)/import/extra/snippet/arg-nonnull.h $(AM_V_GEN)rm -f $@-t $@ && \ sed -n -e
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copyright header cut off.c++defs.h: $(top_srcdir)/import/extra/snippet/c++defs.h $(AM_V_GEN)rm -f $@-t $@ && \ sed -n -e
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'/^\.ifndef,$$p' \ < $(top_srcdir)/import/extra/snippet/warn-on
Copyright Header\n"); fprintf (file, _(" copyright: %.*s\n"), (int)(rec_len - sizeof (struct vms_emh_common)), (char *)rec +
sizeof (struct vms_emh_common)
copyright header of the files in the given list.
copyright headers. This script is only# fixing the first one it finds, so we need to finish the update# by hand.MULTIPLE_COPYRIGHT_HEADERS = (
"gdb/doc/gdb.texinfo", "gdb/doc/refcard.tex",
Copyright' in line: return True lineno += 1 if lineno > 50:
copyright is a brief statement that the program is
copyright Jiri Gaisler 1995\n", sis_version); printf(" Bug-reports to jgais@wd.estec.esa.nl\n\n"); while (stat < argc) { if (argv[stat][0] == '-') {

```

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if (strcmp(argv[stat], "-v") == 0) {
  copyright lib/alloca.c lib/alloca.in.h lib/config.charset lib/float+.h lib/float.c lib/float.in.h lib/fnmatch.c lib/fnmatch.in.h lib/fnmatch_loop.c
lib/fpucw.h lib/frexp.c lib/
  copyright line is 72.# Set UPDATE_COPYRIGHT_MAX_LINE_LENGTH to use a different length.# 5. Set UPDATE_COPYRIGHT HOLDER if the
copyright holder is other# than "Free Software Foundation,
  copyright message after the interpreter is installed when it is any sane interpreter. */ if (!quiet && !current_interp_named_p (INTERP_MI1)) {
/* Print all the junk at the top, with
  copyright message before the interpreter is installed. That way it isn't encapsulated in MI output. */ if (!quiet && strcmp (interpreter_p,
INTERP_MI1) == 0) { /* Print all the junk
  copyright\n");
  COPYRIGHT\n"); printf_filtered ("HOLDERS AND/OR OTHER PARTIES PROVIDE THE PROGRAM \"AS IS\" WITHOUT WARRANTY\n"); printf_filtered ("OF
ANY KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING, BUT NOT LIM
  copyright script for that. "" # We want to use year intervals in the copyright notices, and # all years should be collapsed to one single year
interval, # even if there are "holes" in
  copyright script prints # a well defined warning when it did not find the copyright notice. # For each of those, do a sanity check and see if they may
in fact # have one. For the files th
  Copyright/s/ -e '/Starting program: /s/[A-z]:./.*|'| \ -e '/main (/s/=0x[0-9a-f][0-9a-f]*/=XYZ/g' \ > ${base}.tst if diff --binary -u
${base}.out ${base}.tst; then
  Copyright sign { ISFUNC, rl_insert }, /* Feminine ordinal indicator */ { ISFUNC, rl_insert }, /* Left pointing double angle quotation mark */ {
ISFUNC, rl_insert }, /* Not sign */ { ISFUNC, rl
  copyright string. len = obj_som_copyright_hdr (abfd)->header_id.length - 4; obj_som_file_hdr (abfd)->aux_header_size += len;
current_offset += len; if (bfd_bwrite ((void *) obj
  copyright (table);
  copyright target rule# in maint.mk from gnulib's maintainer-makefile module.
  copyright unistd pathmax"
  copyright year intervals in a reformatted FSF copyright statement# are expanded instead.# If UPDATE_COPYRIGHT_USE_INTERVALS=2, convert
a sequence with gaps# to the minimal containing
  copyright year intervals. (See "Environment variables"# below.)
  copyright year is 2 or 4 digits, and years are separated by# commas or dashes. Whitespace may appear after commas.
  copyright year list to include the current year.
  copyright years...
  copyright years (such as 90, 1991, 1992-2007, 2008) in a# reformatted FSF copyright statement is collapsed to a single# interval (such as 1990-
2008). If unset or set to 0, all existing
  (C) rd c = res; }
  (C) rd c = res; } else
  (C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 1/0 - SUBF3 Alias: i, i3*/#define
S_CLASS_INSN(name, opcode, level) \
  (C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 2/0 - ADDF3, MPYF3 Alias: i,
i3*/#define SC_CLASS_INSN(name, opcode,
  (C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) Instr: 1/0 - CMPF3 Alias: i, i3*/#define S2_CLASS_INSN(name, opcode,
level) \ { name, opcode|0x20000000, 0
  (C)<<srca);", 8 }
  (C)" takes one of the following forms or is omitted# entirely:
  (C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
  (C) tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if defined
__WIN32 || defined __WIN32__ || defined __E
GDB 9.1 ubuntu:gdb/9.1-0ubuntu1
  (C) 1995-2017 Jean-loup Gailly and Mark Adler
  (C) 1995-2017 Jean-loup Gailly END BLOCK "VarFileInfo" BEGIN VALUE "Translation", 0x0409, 1252 END
  (C) 1995-2017 Jean-loup Gailly END END BLOCK "VarFileInfo" BEGIN VALUE "Translation", 0x0409, 1252 END
  (C) 1995-2017 Jean-loup Gailly VALUE "OriginalFilename", "zlib1.dll\0" VALUE "ProductName", "zlib\0" VALUE "ProductVersion",
ZLIB_VERSION "\0" VALUE "Comments", "For more info
  (C) AC_LINK_IFELSE( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need
for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
  (C)# B. (c)
  (C)/!b go :more /\./!{ N s/\n# // b more
  (C) __btowc (C)# else
  (C) btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef _LIBC# define MEMPCPY(D, S, N) __mempcpy (D, S,
N)# else# if HAVE_MEMPCPY# define MEMPCPY(D
  (C) (((C)

```

```

(C) ((C >= C-'0' : ((C >= 'A' && C <= 'F') ? \ C-'A'+10 : ((C >= 'a' && C <= 'f') ? \ C-'a'+10 :
-1)))
(C) (((C)#define CC3(OP) ((OP)>>8)&0x3)#define SBIT(OP) ((OP)&0xF)#define MMR(OP) ((OP)&0x7F)#define MMRX(OP)
(((OP)>>4)&0xF)+16)#define MMRYP(OP) ((
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_PATH_WITH_DIR(P) \ (strchr (P, '/') != NULL ||
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define
FILE_SYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)#endif
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])#endif
(C) ((C) ==# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define FILESYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) == DIRECTORY_SEPARATOR)#endif
(C) ((C) ==#else# define FILE_SYSTEM_PREFIX_LEN(Filename) 0
(C) ((C) ==#endif
(C) (C)# ifdef LIBC# define STRLEN(S) __wcslen (S)# define STRCAT(D, S) __wcscat (D, S)# define MEMPCPY(D, S, N) __wmempcpy (D, S,
N)# else# define STRLEN(S) wcslen (S)# defin
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comment is maintained in the copy.
(C) ea OBITOP (O_BIST, 1, 1, ea &= ~m; if (!C) ea |= m); /* bist */ OBITOP (O_BSTZ, 1, 1, ea &= ~m; if (Z)
ea |= m); /* bstz */ OBITOP (O_BISTZ, 1, 1, ea &= ~m; if (!Z) ea |=
(C) is the return code for exiting with status C. - M32C_MAKE_STOPPED (S) is the return code for stopping on signal S. */#define
M32C_MAKE_STOPPED() (0)#define M32C_MAKE_HIT_BREAK() (1)
(C) is the return code for exiting with status C. - RL78_MAKE_STOPPED (S) is the return code for stopping on signal S. */#define
RL78_MAKE_STOPPED() (1)#define RL78_MAKE_HIT_BREAK() (2)
(C) is the return code for exiting with status C. - RX_MAKE_STOPPED (S) is the return code for stopping on signal S. */#define RX_MAKE_STOPPED()
(1)#define RX_MAKE_HIT_BREAK() (2)
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copyright \$lecho '#include "server.h"'echo '#include "regdef.h"'echo '#include  
"tdesc.h"'echooffset=0i=0name=xxmltarget=xxmlarch=xxmlsabi=xexpedite=xfeature=xexec < \$1while do\_readdd  
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= $ENV{UPDATE_COPYRIGHT_MAX_LINE_LENGTH};!$margin || $ma
copyright conditions above. * * 2005 - Adaptation to Microsoft C Compiler for AMD64 by Gilles Vollant
copyright conditions above. * * Dec-29-2003 -- I added AMD64 inflate asm support. This version is also * slightly quicker on x86 systems because,
instead of using rep movsb to copy * data, it use
copyright conditions above.; *; * Mar-13-2003 -- Most of this is derived from inffast.S which is derived from; * the gcc -S output of zlib-1.2.0/inffast.c.
Zlib-1.2.0 is in beta release at; *
copyright conditions above. * * This version (Jan-23-2003) of inflate_fast was coded and tested under * GNU/Linux on a pentium 3, using the gcc-3.2
compiler distribution. On that * machine, I fou
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\definedummyword\error \definedummyword\euro \definedummyword\guillemetl
copyright does not belong to the
copyright (FILE{ fprintf (fp, "/* This file is automatically generated by i386-gen. Do not edit! */\n\
copyright-format/1.0/Upstream-Name: GDB: The GNU Project DebuggerUpstream-Contact: gdb@sourceware.orgSource:
http://www.gnu.org/software/gdb/download/ w/ GFDL'd files strippedDisclaimer: Autogener
copyright (fp);
copyright FSF :-(. "sim/ppc/filter.h", "sim/ppc/gen-support.h", "sim/ppc/ld-insn.h", "sim/ppc/hw_sem.c", "sim/ppc/hw_disk.c",
"sim/ppc/idecode_branch.h", "sim/ppc/sim-endian.h", "sim/ppc/t
copyright} \gdef \gdef^^ab{\guillemetleft} \gdef^^ac{\$ \not$} \gdef^^ad{\ -} \gdef^^ae{\registersymbol} \gdef^^af{\ ={} } %
\gdef^^b0{\textdegree} \gdef^^b1{\$ \pm$} \gdef^^b2{\$ ^2$}
copyright header and single statement only). */
copyright header cut# off.arg-nonnull.h: $(top_srcdir)/import/extra/snippet/arg-nonnull.h $(AM_V_GEN)rm -f $@-t $@ && \ sed -n -e
'/GL_ARG_NONNULL,$$p' \ < $(top_srcdir)/import/extra/snippet/a
copyright header cut off.c++defs.h: $(top_srcdir)/import/extra/snippet/c++defs.h $(AM_V_GEN)rm -f $@-t $@ && \ sed -n -e

```

```

'_GL_CXXDEFS/,$$p' \ < $(top_srcdir)/import/extra/snippet/c++defs.h \
copyright header cut# off.warn-on-use.h: $(top_srcdir)/import/extra/snippet/warn-on-use.h $(AM_V_GEN)rm -f $@-t $@ && \ sed -n -e
'/.ifndef/,$$p' \ < $(top_srcdir)/import/extra/snippet/warn-on
copyright header followed by a single C statement. When we place a breakpoint on the line where the function name is declared, we expect GDB to
skip the function's prologue, and insert the b
Copyright Header\n"); fprintf (file, _(" copyright: %.*s\n"), extra, (char *) (emh + 1)); break;
copyright header of the files in the given list.
copyright header: /* See README file in this directory for implementation notes, coding conventions, et.al. */
copyright headers must be updated by hand:" "\033[0m") for filename in MULTIPLE_COPYRIGHT_HEADERS:
copyright headers. This script is only# fixing the first one it finds, so we need to finish the update# by hand.MULTIPLE_COPYRIGHT_HEADERS = (
"gdb/doc/gdb.texinfo", "gdb/doc/refcard.tex",
© Copyright Henrik Ravn 2004
Copyright Henrik Ravn 2004
Copyright' in line: return True lineno += 1 if lineno > 50:
copyright is a brief statement that the program is
copyright Jiri Gaisler 1995\n", sis_version); printf(" Bug-reports to jgais@wd.estec.esa.nl\n\n"); while (stat < argc) { if (argv[stat][0] == '-') {
if (strcmp(argv[stat], "-v") == 0) {
copyright lib/alloca.c lib/alloca.in.h lib/arpa_inet.in.h lib/assure.h lib/at-func.c lib/basename-1gpl.c lib/canonicalize-1gpl.c lib/chdir-long.c lib/chdir-
long.h lib/cloexec.c |
copyright line is 72.# Set UPDATE_COPYRIGHT_MAX_LINE_LENGTH to use a different length.# 5. Set UPDATE_COPYRIGHT HOLDER if the
copyright holder is other# than "Free Software Foundation,
copyright message after the interpreter is installed when it is any sane interpreter. */ if (!quiet && !current_interp_named_p (INTERP_MI1)) {
/* Print all the junk at the top, with
copyright message before the interpreter is installed. That way it isn't encapsulated in MI output. */ if (!quiet && strcmp (interpreter_p,
INTERP_MI1) == 0) { /* Print all the junk
copyright\n");
COPYRIGHT\n"); printf_filtered ("HOLDERS AND/OR OTHER PARTIES PROVIDE THE PROGRAM \"AS IS\" WITHOUT WARRANTY\n"); printf_filtered ("OF
ANY KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING, BUT NOT LIM
copyright script for that. "" # We want to use year intervals in the copyright notices, and # all years should be collapsed to one single year
interval, # even if there are "holes" in
copyright script prints # a well defined warning when it did not find the copyright notice. # For each of those, do a sanity check and see if they may
in fact # have one. For the files th
Copyright/s/ -e '/Starting program: /s/[A-z]:/.*|/' \ -e '/main (/s/=0x[0-9a-f][0-9a-f]*/=XYZ/g' \ > ${base}.tst if diff --binary -u
${base}.out ${base}.tst ; then
Copyright sign { ISFUNC, rl_insert }, /* Feminine ordinal indicator */ { ISFUNC, rl_insert }, /* Left pointing double angle quotation mark */ {
ISFUNC, rl_insert }, /* Not sign */ { ISFUNC, rl
copyright string in the executable of your product. */
copyright string. len = obj_som_copyright_hdr (abfd)->header_id.length - 4; obj_som_file_hdr (abfd)->aux_header_size += len;
current_offset += len; if (bfd_bwrite ((void *) obj
copyright (table);
copyright target rule# in maint.mk from gnulib's maintainer-makefile module.
copyright to help * - Send help to stdout instead of stderr * - Add some preemptive typecasts * - Add L to
constants in lseek() calls
copyright \ wchar \ wctype-h \"
copyright wchar wctype-h
copyright wchar wctype-
h])gl_AVOID([])gl_SOURCE_BASE([import])gl_M4_BASE([import/m4])gl_PO_BASE([])gl_DOC_BASE([doc])gl_TESTS_BASE([tests])gl_LIB([libgnu])gl_MAKEFIL
E_NAME([])gl_MACRO_PR
copyright year intervals in a reformatted FSF copyright statement# are expanded instead.# If UPDATE_COPYRIGHT_USE_INTERVALS=2, convert
a sequence with gaps# to the minimal containing
copyright year intervals. (See "Environment variables"# below.)
copyright year is 2 or 4 digits, and years are separated by# commas or dashes. Whitespace may appear after commas.
copyright year list to include the current year.
copyright years...
copyright years (such as 90, 1991, 1992-2007, 2008) in a# reformatted FSF copyright statement is collapsed to a single# interval (such as 1990-
2008). If unset or set to 0, all existing
(C) rd c = res; }
(C) rd c = res; } else
(C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 1/0 - SUBF3 Alias: i, i3*/#define
S_CLASS_INSN(name, opcode, level) \
(C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 2/0 - ADDF3, MPYF3 Alias: i,

```



```

i3*/#define SC_CLASS_INSN(name, opcode,
(C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) Instr: 1/0 - CMPF3 Alias: i, i3*/#define S2_CLASS_INSN(name, opcode,
level) \ { name, opcode|0x20000000, 0
(C)<<srca);", 8 }
(C)" takes one of the following forms or is omitted# entirely:
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C) tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if defined
_WIN32 || defined __WIN32__ || defined __E
(C) (((unsigned int) (C) <= 'z' - 'a')# define FILE_SYSTEM_PREFIX_LEN(Filename) \ (_IS_DRIVE_LETTER ((Filename)[0]) &&
(Filename)[1] == ':' ? 2 : 0)# ifndef _
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```

```

operation support and MIPS-3D support contribu
copyright message after the interpreter is installed when it is any sane interpreter. */ if (!quiet && !current_interp_named_p (INTERP_MI1)) {
/* Print all the junk at the top, with
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(C) ea OBITOP (O_BIST, 1, 1, ea &= ~m; if (!C) ea |= m); /* bist */ OBITOP (O_BSTZ, 1, 1, ea &= ~m; if (Z)
ea |= m); /* bstz */ OBITOP (O_BISTZ, 1, 1, ea &= ~m; if (!Z) ea |=
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 (C) is the return code for exiting with status C. - RL78\_MAKE\_STOPPED (S) is the return code for stopping on signal S. \*/#define  
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 "gdb/doc/gdb.texinfo", "gdb/doc/refcard.tex",  
 copyright year intervals in a reformatted FSF copyright statement# are expanded instead.# If UPDATE\_COPYRIGHT\_USE\_INTERVALS=2, convert  
 a sequence with gaps# to the minimal containing  
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copyright message before the interpreter is installed. That way it isn't encapsulated in MI output. */ if (!quiet && strcmp (interpreter_p,
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(C) (((C)#define CC3(OP) (((OP)>>8)&0x3)#define SBIT(OP) ((OP)&0xF)#define MMR(OP) ((OP)&0x7F)#define MMRX(OP)
((((OP)>>4)&0xF)+16)#define MMRY(OP) (((
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ZLIB_VERSION "\0"  VALUE "Comments",  "For more info
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(C) is the return code for exiting with status C. - RX\_MAKE\_STOPPED (S) is the return code for stopping on signal S. \*/#define RX\_MAKE\_STOPPED()

(1)#define RX\_MAKE\_HIT\_BREAK() (2)

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(C) tests whether C is a directory separator character. IS\_PATH\_WITH\_DIR(P) tests whether P contains a directory specification. \*/#if defined

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(C) btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef \_LIBC# define MEMPCPY(D, S, N) \_\_memcpy (D, S, N)# else# if HAVE\_MEMPCPY# define MEMPCPY(D  
N)# else# if HAVE\_MEMPCPY# define MEMPCPY(D  
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copyright \$1echo '#include "server.h"'echo '#include "regdef.h"'echo '#include "tdesc.h"'echooffset=0i=0name=xxmltarget=xxmlarch=xxmlsabi=xexpedite=xfeature=xexec < \$1while do\_reado

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 (C) is the return code for exiting with status C. - M32C\_MAKE\_STOPPED (S) is the return code for stopping on signal S. \*/#define  
 M32C\_MAKE\_STOPPED() (0)#define M32C\_MAKE\_HIT\_BREAK() (1)  
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(C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 2/0 - ADDF3, MPYF3 Alias: i,
i3*/#define SC_CLASS_INSN(name, opcode,
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(C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 1/0 - SUBF3 Alias: i, i3\*/#define  
S\_CLASS\_INSN(name, opcode, level) \

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copyright string. len = obj\_som\_copyright\_hdr (abfd)->header\_id.length - 4; obj\_som\_file\_hdr (abfd)->aux\_header\_size += len;  
current\_offset += len; if (bfd\_bwrite ((void \*) obj

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(C) AC_LINK_IFELSE( AC_LANG_POP) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;:*-*so
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(C) ((C) ==# define IS\_PATH\_WITH\_DIR(P) (strchr (P, '/') != NULL)# define FILE\_SYSTEM\_PREFIX\_LEN(P) 0#endif  
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(C) ((C) ==# define IS\_ABSOLUTE\_PATH(P) ISLASH ((P)[0])#endif

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(C) tests whether C is a directory separator character. IS\_PATH\_WITH\_DIR(P) tests whether P contains a directory specification. \*/#if ((defined \_\_WIN32 || defined \_\_WIN32\_) && !defined

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(C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define

IS\_PATH\_WITH\_DIR(P) \ (strchr(P, '/') != NULL ||

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(C) tests whether C is a directory separator character. IS\_ABSOLUTE\_PATH(P) tests whether P is an absolute path. If it is not, it may be concatenated to a direct

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(C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define

IS\_ABSOLUTE\_PATH(P) (ISLASH ((P)[0]) || HAS\_DEVICE (

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gettext 0.20.1 fedora:gettext/0.20.1-3.fc32/armv7hl

(C) 2003, 2019 Free Software Foundation", NULL, NULL, "bug-gettext@gnu.org"); KCmdLineArgs::init

(argc, argv, &aboutData); K

(C) AC\_LINK\_IFELSE( AC\_LANG\_POP] if test yes != "\$lt\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -

belf CFLAGS=\$SAVE\_CFLAGS fi ;\*-\*so

(C)!b go :more \.!\{ N s|\n# | | b more

(C) \_\_btowc (C)# else

(C) btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef \_LIBC# define MEMPCPY(D, S, N) \_\_mempcpy (D, S,

N)# else# if HAVE\_MEMPCPY# define MEMPCPY(D

(C) "can be omitted entirely; the word

(C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define

IS\_ABSOLUTE\_PATH(P) (ISLASH ((P)[0]) || HAS\_DEVICE (

(C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define

IS\_PATH\_WITH\_DIR(P) \ (strchr (P, '/') != NULL ||

(C) ((C) ==# define IS\_ABSOLUTE\_PATH(P) ISLASH ((P)[0])# define IS\_PATH\_WITH\_DIR(P) (strchr (P, '/') != NULL)# define

FILE\_SYSTEM\_PREFIX\_LEN(P) 0#endif

(C) ((C) ==# define IS\_ABSOLUTE\_PATH(P) ISLASH ((P)[0])# define IS\_PATH\_WITH\_DIR(P) (strchr (P, '/') != NULL)#endif

(C) ((C) ==# define IS\_ABSOLUTE\_PATH(P) ISLASH ((P)[0])#endif

(C) ((C) ==# define IS\_PATH\_WITH\_DIR(P) (strchr (P, '/') != NULL)# define FILE\_SYSTEM\_PREFIX\_LEN(P) 0#endif

(C) ((C) ==# define IS\_PATH\_WITH\_DIR(P) (strchr (P, '/') != NULL)# define FILESYSTEM\_PREFIX\_LEN(P) 0#endif

(C) ((C) == DIRECTORY\_SEPARATOR)#endif

(C) ((C) ==#else# define FILE\_SYSTEM\_PREFIX\_LEN(FileName) 0

(C) ((C) ==#else /\* Unix \*/

(C) ((C) ==#endif

(C) (C)# ifdef \_LIBC# define STRLEN(S) \_\_wcslen (S)# define STRCAT(D, S) \_\_wcscat (D, S)# define MEMPCPY(D, S, N) \_\_wmempcpy (D, S,

N)# else# define STRLEN(S) wcslen (S)# defin

(C) (IN\_CTYPE\_DOMAIN (C)

(C) (ISALNUM(C)

(C) (ISUPPER(C)

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Copyright for the status of this software. * * joelwreed@comcast.net
Copyright for the status of this software. * * UTF8 string routines from:
copyright{}} \gdefchar \gdefchar^^ab{\guillemetleft{}} \gdefchar^^ac{\ensuremath\lnot} \gdefchar^^ad{\-\} \gdefchar^^ae{\registeredsymbol{}}
\gdefchar^^af{\={}} % \gdefchar^^b0{\textdeg
copyright} \gdef \gdef^^ab{\guillemetleft} \gdef^^ac{\$ \lnot\$} \gdef^^ad{\-\} \gdef^^ae{\registeredsymbol} \gdef^^af{\={}} %
\gdef^^b0{\textdegree} \gdef^^b1{\$ \pms} \gdef^^b2{\$^2\$}
copyright header.func_check_file (){ case "/$1" in
copyright header in file fail=true }done
copyright header. Namely, since
copyright header. return 0 ;;
copyright header. return 0 fi
copyright headers.
copyright-headers
copyright-headers \ build-aux/ac-help.sed build-aux/git-version-gen build-aux/txi2html \ m4/fixautomake.m4 m4/woe32-dll.m4 \ m4/libtool.m4
copyright headers resp. license notices.
copyright-headers UNPACKED-GETTEXT-TARBALL
copyright-headers UNPACKED-GETTEXT-TARBALL" 1> exit 1fi
copyright header# to a file which different programmers would write in the same way.# This includes tiny files, as well as files that contain only
machine-# generated data.
copyright header to each. return 0 ;;
copyright header, with the Copyright line spread # across three lines. return 0 ;;
copyright header, with the Copyright line spread # across two lines. return 0 ;;
copyright information. */
copyright information. *
copyright information */
copyright informations. */
copyright in output for foreign user\n"); printf (_("\ --package-name=PACKAGE set package name in output\n")); printf (_("\ --package-
version=VERSION set package version in
copyright of the package (or part of it). In particular,
copyright sign, U{ 170, "ordf", "feminine ordinal indicator, U+00AA ISOnum" },{ 171, "laquo","left-pointing double angle quotation mark = left
pointing guillemet, U+00AB ISOnum" },{ 172, "not", "no

```



```

copyright status of the file, that is, that the .diff
copyright status to the file.    return 0 ;;
Copyright' suffices.  if head -n 15 "$dir/$1" | LC_ALL=C grep 'Copyright .* Free Software Foundation' >/dev/null; then
Copyright without changing the namespace pointer to * something else can produce stale links. One way to do it is * to keep a reference counter but this
doesn't work as soon * as one move the eleme
(C)    tests whether C is a directory separator character. */#if defined _WIN32 || defined __CYGWIN__ || defined __EMX__ || defined __DJGPP__
/* Native Windows, Cygwin, OS/2, DOS */
(C)    tests whether C is a directory separator character.  IS_ABSOLUTE_PATH(P) tests whether P is an absolute path.  If it is not,
it may be concatenated to a direct
(C)    tests whether C is a directory separator character.  IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if (defined
_WIN32 && !defined __CYGWIN__) || defined
(C)    tests whether C is a directory separator character.  IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if ((defined
_WIN32 || defined __WIN32__) && !defined
(C)    tests whether C is a directory separator character.  IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if defined
_WIN32 || defined __WIN32__ || defined __C
(C)    tests whether C is a directory separator character.  IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if defined
_WIN32 || defined __WIN32__ || defined __E
(C) ((unsigned char) (C))
(C) (((unsigned int) (C)
=<= 'z' - 'a')# define FILE_SYSTEM_PREFIX_LEN(Filename) \      (_IS_DRIVE_LETTER ((Filename)[0]) &&
(Filename)[1] == ':' ? 2 : 0)# ifndef _
gettext 0.20.1 opensuse:gettext-runtime-mini/0.20.1-2.31/s390x
(C) 2003, 2019 Free Software Foundation",          NULL,          NULL,          "bug-gettext@gnu.org"); KCmdLineArgs::init
(argc, argv, &aboutData); K
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$!t_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;:*-*so
(C)/!b go :more \./!{ N s|\n# | | b more
(C) __btowc (C)# else
(C) btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef _LIBC# define MEMPCPY(D, S, N) __mempcpy (D, S,
N)# else# if HAVE_MEMPCPY# define MEMPCPY(D
(C) "can be omitted entirely; the word
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_PATH_WITH_DIR(P) \ (strchr (P, '/') != NULL ||
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define
FILE_SYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)#endif
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])#endif
(C) ((C) ==# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define FILE_SYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) ==# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define FILESYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) == DIRECTORY_SEPARATOR)#endif
(C) ((C) ==#else# define FILE_SYSTEM_PREFIX_LEN(Filename) 0
(C) ((C) ==#else /* Unix */
(C) ((C) ==#endif
(C) (C)# ifdef _LIBC# define STRLEN(S) __wcslen (S)# define STRCAT(D, S) __wcscat (D, S)# define MEMPCPY(D, S, N) __wmempcpy (D, S,
N)# else# define STRLEN(S) wcslen (S)# defin
(C) (IN_CTYPE_DOMAIN (C)
(C) (ISALNUM(C)
(C) (ISUPPER(C)
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copyright{}} \gdefchar \gdefchar^^ab{\guillemetleft{}} \gdefchar^^ac{\ensuremath\lnot} \gdefchar^^ad{\~} \gdefchar^^ae{\registeredymbol{}}
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copyright} \gdef \gdef^^ab{\guillemetleft} \gdef^^ac{\lnot$} \gdef^^ad{\~} \gdef^^ae{\registeredymbol} \gdef^^af{\={}} %
\gdef^^b0{\textdegree} \gdef^^b1{\p$} \gdef^^b2{\$2$}
copyright header.func_check_file () { case "$1" in
copyright header in file fail=true }done
copyright header. Namely, since
copyright header. return 0 ;;
copyright header. return 0 fi
copyright headers.
copyright-headers
copyright-headers \ build-aux/ac-help.sed build-aux/git-version-gen build-aux/txi2html \ m4/fixautomake.m4 m4/woe32-dll.m4 \ m4/libtool.m4
copyright headers resp. license notices.
copyright-headers UNPACKED-GETTEXT-TARBALL
copyright-headers UNPACKED-GETTEXT-TARBALL" 1> exit 1fi
copyright header# to a file which different programmers would write in the same way.# This includes tiny files, as well as files that contain only
machine-# generated data.
copyright header to each. return 0 ;;
copyright header, with the Copyright line spread # across three lines. return 0 ;;
copyright header, with the Copyright line spread # across two lines. return 0 ;;
copyright information. */
copyright information. *
copyright information */
copyright informations. */
copyright in output for foreign user\n"); printf (_("\ --package-name=PACKAGE set package name in output\n")); printf (_("\ --package-
version=VERSION set package version in
copyright of the package (or part of it). In particular,
copyright sign, U{ 170, "ordf", "feminine ordinal indicator, U+00AA ISOnum" },{ 171, "laquo","left-pointing double angle quotation mark = left
pointing guillemet, U+00AB ISOnum" },{ 172, "not", "no
copyright status of the file, that is, that the .diff
copyright status to the file. return 0 ;;
Copyright' suffices. if head -n 15 "$dir/$1" | LC_ALL=C grep 'Copyright .* Free Software Foundation' >/dev/null; then
Copyright without changing the namespace pointer to * something else can produce stale links. One way to do it is * to keep a reference counter but this

```

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doesn't work as soon * as one move the eleme
(C) tests whether C is a directory separator character. /*#if defined _WIN32 || defined __CYGWIN__ || defined __EMX__ || defined __DJGPP__
/* Native Windows, Cygwin, OS/2, DOS */
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C) tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. /*#if (defined
_WIN32 && !defined __CYGWIN__) || defined
(C) tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. /*#if ((defined
_WIN32 || defined __WIN32__) && !defined
(C) tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. /*#if defined
_WIN32 || defined __WIN32__ || defined __C
(C) tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. /*#if defined
_WIN32 || defined __WIN32__ || defined __E
(C) ((unsigned char) (C))
(C) (((unsigned int) (C) <= 'z' - 'a')# define FILE_SYSTEM_PREFIX_LEN(Filename) \ (_IS_DRIVE_LETTER ((Filename)[0]) &&
(Filename)[1] == ':' ? 2 : 0)# ifndef _
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COPYRIGHT: e->components = (strlen (\_(" e->format = EXIF\_FORMAT\_ASCII; e->size = exif\_format\_get\_size (e->format) \*  
e->components; e->data = exif\_entry\_alloc (e, e->size); if (!e->data) brea  
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"../json\_tokener.h" int main(void) { json\_object \*json;  
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 (C) AC\_LINK\_IFELSE( AC\_LANG\_POP) if test x"\$lt\_cv\_cc\_needs\_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need  
 for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;  
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(C) AC_TRY_LINK( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS="$SAVE_CFLAGS" fi ;;AC
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_PATH_WITH_DIR(P) \ (strchr (P, '/') != NULL ||
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)#endif
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])#endif
(C) ((C) ==# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define FILESYSTEM_PREFIX_LEN(P) 0#endif
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 \*root\_window; GdkWindow \*draw\_window;  
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 (C) tests whether C is a directory separator character. IS\_ABSOLUTE\_PATH(P) tests whether P is an absolute path. If it is not,  
 it may be concatenated to a direct  
 (C) tests whether C is a directory separator character. IS\_PATH\_WITH\_DIR(P) tests whether P contains a directory specification. \*/#if defined  
 \_WIN32 || defined \_\_WIN32\_\_ || defined \_\_E  
 GLib 2.62.6 fedora:glib2/2.62.6-1.fc31/armv7hl  
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 guint64 ui64; const gchar \*s, \*save;  
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GMP 6.2.0 opensuse:gmp/6.2.0-3.3/aarch64

```
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
belf CFLAGS=$SAVE_CFLAGS fi ;:*-*so
```

```
(C)!/b go :more \./!{ N s|\n# | | b more
```

(C) could use less temporary memory. The powtab allocation could be trimmed with some computation, and the tmp area could be reduced, or perhaps eliminated if up is reused for both quotient

(C) in normalized form, with the normalization count. Quotients will usually need to be left-shifted before each divide, and remainders will either need to be left-shifted or right-shifted.

```
(C) ((mp_limb_t) C#else /* not_LONG_LONG_LIMB */
```

```
(C) ((mp_limb_t) C#endif /*_LONG_LONG_LIMB */
```

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 GNU Autoconf 2.69 debian:autoconf/2.69-11  
 (C)(BODY)# -----# We can't use '#line \$LINENO "configure"' here, since# Sun c89 (Sun WorkShop 6 update 2 C 5.3 Patch 111679-08  
 2002/05/09)# rejects \$LINENO greater than 32767,  
 (C)' for# instance, the latter must be AC\_DEFUN'd, not just define'd.m4\_define([AC\_LANG\_COMPILER],  
 [AC\_BEFORE([AC\_LANG\_COMPILER][\_AC\_LANG]), [AC\_LANG\_PREPROC][\_AC\_LANG]])dn\AC\_LANG\_DISPATCH(  
 (C)' for instance, the latter must# be AC\_DEFUN'd, not just define'd. Since the preprocessor depends# upon the compiler, look for the  
 compiler.m4\_define([AC\_LANG\_PREPROC],[AC\_LANG\_COMPILER\_REQUIRE  
 (C)(FUNCTION)# -----# Don't include <ctype.h> because on OSF/1 3.0 it includes# <sys/types.h> which includes  
 <sys/select.h> which contains a# prototype for select. Si

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permission to copy, distribute and modify it."
copyright {copyright} \def\dots{...}% \def\equiv{==}% \def\error{error}% \def\euro{euro}% \def\expansion{==>}%
\def\geq{>=}% \def\guillemetleft{<<}% \def\guillemetri
Copyright @copyright\in_vc_files=$(texi)
COPYRIGHT# Copyright notice(s)# - M4SH-INIT# Initialization of bottom layers.
COPYRIGHT# Copyright notice(s)# - M4SH-SANITIZE# M4sh's shell setup# - M4SH-INIT-FN# M4sh initialization (shell functions)# - M4SH-INIT#
M4sh initialization (detection code)# - BODY#
Copyright (C) @RELEASE_YEAR@ Free Software Foundation, Inc.
copyright date in# Do the same for the $(sample-test) and the main doc/.texi file.sc_copyright_check:
copyright} \DeclareUnicodeCharacter \DeclareUnicodeCharacter{00AB}{\guillemetleft} \DeclareUnicodeCharacter{00AD}{\-}
\DeclareUnicodeCharacter{00AE}{\registeredsymbol} \DeclareUnicodeCharact
copyright \definedummyword\registeredsymbol \definedummyword\dots \definedummyword\enddots \definedummyword\entrybreak
\definedummyword\equiv \definedummyword\error \definedummyword\euro
copyright-env = \ UPDATE_COPYRIGHT_USE_INTERVALS=1 \ UPDATE_COPYRIGHT_MAX_LINE_LENGTH=72
copyright-env) xargs
Copyright \ $$ (export VC_LIST_EXCEPT_DEFAULT=COPYING
copyright-format/1.0/Source: http://www.gnu.org/software/autoconf/Upstream-Name: AutoconfUpstream-Contact: GNU Autoconf Maintainers
<autoconf@gnu.org>
copyright} \gdef \gdef ^^ab{\guillemetleft} \gdef ^^ac{ $\notin$ } \gdef ^^ad{\-} \gdef ^^ae{\registeredsymbol} \gdef ^^af{\=} %
\gdef ^^b0{\textdegree} \gdef ^^b1{ $\pms$ } \gdef ^^b2{ $^2$ }
copyright in $_sc_search_regexp)
copyright in $_sc_search_regexp)
copyright in this work against any person or entity, or prevent any person or entity from copying, publishing, distributing or creating derivative
COPYRIGHT(m4_defn(m4_copyright_condense)) [AT_COPYRIGHT([This test suite is free software; the Free Software Foundation gives unlimited
permission to copy, distribute and modify it.], [m4_echo])]
COPYRIGHT(m4_defn(m4_copyright_condense)) \AC_COPYRIGHT([This configure script is free software; the Free Software Foundation gives unlimited
permission to copy, distribute and modify it.],
COPYRIGHTm4_divert_text(m4_text_box([Autoconf initialization.]))m4_divert_popm4_ifval([ $2 ], , [m4_ifval([ $1 ],
[AC_CONFIG_SRCDIR([ $1 ])])]) \dnl\dnl Substitute for predefined variables.AC_SUBST(
copyright: prereq# By default, exclude all variants of COPYING; you can also# add exemptions (such as ChangeLog.* for rotated change logs)# in the
file .x-update-copyright.

```

```

copyright $(srcdir)/build-aux cp $(gnulib_dir)/build-aux/useless-if-before-free $(srcdir)/build-aux cp $(gnulib_dir)/build-aux/vc-list-files
$(srcdir)/build-aux cp $(gnulib_dir)/doc/fdl.texi $(srcdir)
COPYRIGHT(TEXT)# -----# Emit TEXT, a copyright notice, as a shell comment near the top of the# script. TEXT is evaluated once; to
accomplish that, we do not prepend# `# ' but `@%:@ '
COPYRIGHT(TEXT)# -----# Emit TEXT, a copyright notice, in the top of the test suite and in# --version output. Macros in
TEXT are evaluated once. Process# the
COPYRIGHT(TEXT,# [FILTER = m4_newline])# -----# Emit TEXT, a copyright notice, in the top of
`configure' and in# --version output. Ma
copyright that they may have in this work, whether granted under contract or by operation of law or international treaty, and hereby commit to the
public, at large, that
copyrightupdate-copyright:
COPYRIGHT# -----# We dump to VERSION_FSF to make sure we are inserted before the# user copyrights, and after the setup of the --
version handling.m4_define([_AC_INIT_COPYRIGHT],
copyright year lists in your project.# If you have an additional project-specific rule,
copyright year range specified as YYYY-ZZZZ in this packagenote that the range specifies every single year in that closed interval.
(C)(PROLOGUE, EXPRESSION)# -----# Return a program that is valid if EXPRESSION is nonzero.# EXPRESSION must
be an integer constant expression.# Be sure
(C)(PROLOGUE, EXPRESSION)# -----# We need `stdio.h' to open a `FILE' and `stdlib.h' for `exit'.# But we include them
only after the EXPRESSION has been evaluated
(C)(PROLOGUE, FUNCTION)# -----# Avoid conflicting decl of main.m4_define([AC_LANG_CALL(C)],
[AC_LANG_PROGRAM([$1m4_if([$2], [main], ,[/* Override any GCC internal pr
(C)' (the parens are# part of the name, it is not an argument) has not been run, then# call it.'# Had you used#
m4_require([AC_LANG_COMPILER(C)], [AC_LANG_COMPILER(C)])# then m4_requi
GNU Autoconf 2.69 opensuse:autoconf-el/2.69-17.4/noarch
Copyright (C) 1992-1994, 1996, 1999-2012 Free Software Foundation,
Copyright (C) 1992-1996, 1998-2001, 2003-2004, 2006, 2009-2012 Free
COPYRIGHT# 4. M4SH-SANITIZE# 5. M4SH-INIT# 1000. BODY
copyright: prereq# By default, exclude all variants of COPYING; you can also# add exemptions (such as Changelog.* for rotated change logs)# in the
file .x-update-copyright.
Copyright (C) 2001, 2004-2012 Free Software Foundation, Inc.
Copyright (C) 2002, 2009-2012 Free Software Foundation, Inc.
Copyright (C) in_vc_files=$(sample-test)
(C)' for# instance, the latter must be AC_DEFUN'd, not just define'd.m4_define([AC_LANG_COMPILER],
[AC_BEFORE([AC_LANG_COMPILER([_AC_LANG[]]), [AC_LANG_PREPROC([_AC_LANG[]])])])dnl_AC_LANG_DISPATCH(
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Copyright (C) 1994, 1999-2001, 2003, 2009-2012 Free Software
Copyright (C) 2000, 2006-2012 Free Software Foundation, Inc.
COPYRIGHT(m4_defn(m4_copyright_condense))][AT_COPYRIGHT([This test suite is free software; the Free Software Foundation givesunlimited
permission to copy, distribute and modify it.], [m4_echo])][
Copyright (C) 2008-2012 Free Software Foundation, Inc.
copyright date in# Do the same for the $(sample-test) and the main doc/.texi file.sc_copyright_check:
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Copyright @copyrightFoundation, Inc.
Copyright (C) 1992-1994, 1998, 2000-2012 Free Software Foundation, Inc.
Copyright (C) 1992-1996, 1998-2006, 2008-2012 Free Software
Copyright (C) 1992-1996, 1998-2012 Free Software Foundation, Inc.
Copyright (C) 2003-2004, 2006, 2008-2012 Free Software Foundation,
Copyright (C) 2001, 2003, 2005, 2008 Free Software Foundation, Inc.
Copyright (C) 1996, 1997, 1998, 1999, 2000, 2001, 2002, 2003, 2004,# 2005, 2006, 2007, 2008, 2009 Free Software Foundation, Inc.# This file is free
software; the Free Software Foundation# gives un
Copyright (C) 2007 Free Software Foundation, Inc. <http://fsf.org/>
copyright year range specified as YYYY-ZZZZ in this packagenote that the range specifies every single year in that closed interval.
(C)' (the parens are# part of the name, it is not an argument) has not been run, then# call it.'# Had you used#
m4_require([AC_LANG_COMPILER(C)], [AC_LANG_COMPILER(C)])# then m4_requi
COPYRIGHT# Copyright notice(s)# - M4SH-SANITIZE# M4sh's shell setup# - M4SH-INIT-FN# M4sh initialization (shell functions)# - M4SH-INIT#
M4sh initialization (detection code)# - BODY#
Copyright (C) 1997, 1999, 2000, 2001, 2003, 2004, 2005, 2008# Free Software Foundation, Inc.
Copyright (C) 1996, 1997, 2000, 2001, 2003, 2005, 2008# Free Software Foundation, Inc.
COPYRIGHT(TEXT)# -----# Emit TEXT, a copyright notice, in the top of the test suite and in# --version output. Macros in

```

```

TEXT are evaluated once. Process# the
Copyright (C) 2001, 2003-2012 Free Software Foundation, Inc.
(C)(PROLOGUE, FUNCTION)# -----# Avoid conflicting decl of main.m4_define([AC_LANG_CALL(C)],
[AC_LANG_PROGRAM([$1m4_if([$2], [main], ,[/* Override any GCC internal pr
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Copyright (C) 2001, 2002, 2003, 2005, 2008 Free Software Foundation, Inc.
Copyright (C) 1997, 2000, 2001, 2003, 2004, 2005, 2006, 2008# Free Software Foundation, Inc.
Copyright (C) 2002, 2004, 2006-2012 Free Software Foundation, Inc.
COPYRIGHTm4_divert_text(m4_text_box([Autoconf initialization.]))m4_divert_popm4_ifval([$2], , [m4_ifval([$1],
[AC_CONFIG_SRCDIR([$1]])])dnl)dnl Substitute for predefined variables.AC_SUBST(
Copyright (C) 2001-2003, 2009-2012 Free Software Foundation, Inc.
Copyright (C) 2000-2002, 2009-2012 Free Software Foundation, Inc.
COPYRIGHT(m4_defn(m4_copyright_condense))dnlAC_COPYRIGHT([This configure script is free software; the Free Software Foundationgives unlimited
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Copyright (C) 1999-2012 Free Software Foundation, Inc.
Copyright (C) 2011-2012 Free Software Foundation, Inc.
Copyright (C) 1989, 1991 Free Software Foundation, Inc.,
(C)(PROLOGUE, EXPRESSION)# -----# We need `stdio.h' to open a `FILE' and `stdlib.h' for `exit'.# But we include them
only after the EXPRESSION has been evaluated
Copyright (C) 1992-1996, 1998-2002, 2008-2012 Free Software
Copyright (C) 2000, 2001, 2003, 2004, 2005, 2006, 2007, 2008, 2009 # 2010 Free Software Foundation, Inc.
copyright{copyright \def\dots{...}% \def\enddots{...}% \def\equiv{==}% \def\error{error}% \def\euro{euro}% \def\expansion{==>}%
\def\geq{>=}% \def\guillemetleft{<<}% \def\guillemetri
Copyright (C) @RELEASE_YEAR@ Free Software Foundation, Inc.
copyright} \DeclareUnicodeCharacter \DeclareUnicodeCharacter{00AB}{\guillemetleft} \DeclareUnicodeCharacter{00AD}{\ -}
\DeclareUnicodeCharacter{00AE}{\registered symbol} \DeclareUnicodeCharact
Copyright (C) 2003, 2006, 2009-2012 Free Software Foundation, Inc.
Copyright (C) 1999-2007, 2009-2012 Free Software Foundation, Inc.
Copyright @copyright\in_vc_files=$(text) \
COPYRIGHT(TEXT,# [FILTER = m4_newline])# -----# Emit TEXT, a copyright notice, in the top of
`configure' and in# --version output. Ma
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Copyright 1985, 1986, 1988, 1990, 1991, 1992, 1993, 1994, 1995,
copyright in $( _sc_search_regexp)
Copyright (C) 2001, 2009-2012 Free Software Foundation, Inc.
Copyright 2003, 2004, 2005, 2006, 2007, 2008, 2009, 2010 Free Software
Copyright (C) 2006, 2008 Free Software Foundation, Inc.
Copyright (C) 1992-1996, 1998-2005, 2009-2012 Free Software
copyright $(srcdir)/build-aux cp $(gnulib_dir)/build-aux/useless-if-before-free $(srcdir)/build-aux cp $(gnulib_dir)/build-aux/vc-list-files
$(srcdir)/build-aux cp $(gnulib_dir)/doc/fdl.texi $(srcd
copyright-env = \ UPDATE_COPYRIGHT_USE_INTERVALS=1 \ UPDATE_COPYRIGHT_MAX_LINE_LENGTH=72
Copyright (C) 2001-2012 Free Software Foundation, Inc.
copyright} \gdef \gdef^^ab{\guillemetleft} \gdef^^ac{\$ \not$} \gdef^^ad{\ -} \gdef^^ae{\registered symbol} \gdef^^af{\ ={}} %
\gdef^^b0{\textdegree} \gdef^^b1{\$ \pm$} \gdef^^b2{\$ ^2$}
Copyright (C) 2002-2012 Free Software Foundation, Inc.
copyright in $( _sc_search_regexp)
Copyright (C) 1999, 2000, 2001, 2002, 2006, 2007, 2009 echo '# Free Software Foundation, Inc.' && \ echo &&\ echo
'm4_define([m4_PACKAGE_NAME], [$(PACKAGE_NAME)])' && \ echo 'm4_defi
Copyright (C) 2001-2002, 2006-2012 Free Software Foundation, Inc.
Copyright (C) 2009 Free Software Foundation, Inc. <http://fsf.org/>
copyright \definedummyword\registered symbol \definedummyword\dots \definedummyword\enddots \definedummyword\entrybreak
\definedummyword\equiv \definedummyword\error \definedummyword\euro

```

```

Copyright \ $(export VC_LIST_EXCEPT_DEFAULT=COPYING
Copyright (C) m4_PACKAGE_YEAR Free Software Foundation, Inc.
Copyright (C) 1999-2004, 2006-2012 Free Software Foundation, Inc.
Copyright (C) 1996, 1997, 1998, 1999, 2000, 2001, 2002, 2003, 2004,# 2005, 2006, 2008, 2009 Free Software Foundation, Inc.
Copyright (C) 2009-2012 Free Software Foundation, Inc.
(C)' for instance, the latter must# be AC_DEFUN'd, not just define'd. Since the preprocessor depends# upon the compiler, look for the
compiler.m4_define([AC_LANG_PREPROC],[AC_LANG_COMPILER_REQUIRE
Copyright (C) 2000-2012 Free Software Foundation, Inc.])
Copyright (C) 1994-1996, 1999-2002, 2004-2012 Free Software Foundation,Inc.
copyright-env) xargs
Copyright (C) 2003, 2004, 2005, 2006 Free Software Foundation, Inc.
copyright year lists in your project.# If you have an additional project-specific rule,
Copyright (C) 2001, 2003, 2009-2012 Free Software Foundation, Inc.
Copyright (C) 2001-2002, 2006, 2009-2012 Free Software Foundation,
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Copyright (C) 1992-1995, 1999-2012 Free Software Foundation, Inc.
Copyright (C) 2002-2003, 2006, 2008-2012 Free Software Foundation,
Copyright (C) 2000-2002, 2004-2012 Free Software Foundation, Inc.
COPYRIGHT# -----# We dump to VERSION_FSF to make sure we are inserted before the# user copyrights, and after the setup of the --
version handling.m4_define([_AC_INIT_COPYRIGHT],
(C)(BODY)# -----# We can't use '#line $LINENO "configure"' here, since# Sun c89 (Sun WorkShop 6 update 2 C 5.3 Patch 111679-08
2002/05/09)# rejects $LINENO greater than 32767,
(C)(PROLOGUE, EXPRESSION)# -----# Return a program that is valid if EXPRESSION is nonzero.# EXPRESSION must
be an integer constant expression.# Be sure
Copyright (C) 2003-2004, 2007, 2009-2012 Free Software Foundation,
copyrightupdate-copyright:
Copyright 2010 Free Software Foundation, Inc.
Copyright (C) 2003, 2005 Free Software Foundation, Inc.
COPYRIGHT# 4. M4SH-INIT# 1000. BODYm4_define([_m4_divert(DEFAULTS)], 10)m4_define([_m4_divert(PARSE_ARGS)], 20)
COPYRIGHT(TEXT)# -----# Emit TEXT, a copyright notice, as a shell comment near the top of the# script. TEXT is evaluated once; to
accomplish that, we do not prepend# `#' but `@%:@'
Copyright (C) 2002, 2003, 2005, 2006, 2007, 2008 Free Software Foundation, Inc.
Copyright (C) 2001-2005, 2009-2012 Free Software Foundation, Inc.
(C)(FUNCTION)# -----# Don't include <sys/types.h> because on OSF/1 3.0 it includes# <sys/types.h> which includes
<sys/select.h> which contains a# prototype for select. Si
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COPYRIGHT# Copyright notice(s)# - M4SH-INIT# Initialization of bottom layers.
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(C) AC\_LINK\_IFELSE( AC\_LANG\_PROGRAM([[@%:@include <oci.h>]],  
OCI\_DEFAULT, 0, 0, 0, 0, 0, 0);if (envh) OCIHandl  
[[OCIEnv\* envh = 0;OCIEnvCreate(&envh,  
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copyright); do echo ) | sed -e '$$!s,$$,|,' | tr -d '\012\015'; \ $(GIT) ls-files \ | grep -Ev '^(|)/README$$' \ | grep -Ev '^PLANS(|/$$)' \ |
grep -Ev '^($$excluded_re)$$' \ | $(update
copyright-format/1.0/Upstream-Name: automake@gnu.orgSource: https://www.gnu.org/software/automake/
copyright{}} \gdefchar \gdefchar^^ab{\guillemetleft{}} \gdefchar^^ac{\ensuremath{\lnot}} \gdefchar^^ad{\-} \gdefchar^^ae{\registeredsymbol{}}
\gdefchar^^af{\={}} % \gdefchar^^b0{\textdeg
copyright += lib/install-sh
copyright += lib/mkinstalldirs
copyright line is 72.# Set UPDATE_COPYRIGHT_MAX_LINE_LENGTH to use a different length.# 5. Set UPDATE_COPYRIGHT HOLDER if the
copyright holder is other# than "Free Software Foundation,
copyright' rule in our Makefile.RELEASE_YEAR=2018AC_SUBST([RELEASE_YEAR])
copyright' script.
copyright \ $(SV_GIT_GL)doc/gendocs_template \ $(SV_GIT_GL)doc/INSTALL
copyright target rule# in maint.mk from gnulib's maintainer-makefile module.
Copyright|This is stdout stderr && exit 1
copyrightupdate-copyright: $(AM_V_GEN)set -e; \ if test -n "$$UPDATE_COPYRIGHT_YEAR"; then \
current_year=$$UPDATE_COPYRIGHT_YEAR; \ else \ current_year=`date +%Y` && test -n "$$current_year
copyright year intervals in a reformatted FSF copyright statement# are expanded instead.# If UPDATE_COPYRIGHT_USE_INTERVALS=2, convert
a sequence with gaps# to the minimal containing
copyright year intervals. (See "Environment variables"# below.)
copyright year is 2 or 4 digits, and years are separated by# commas or dashes. Whitespace may appear after commas.
copyright year list to include the current year.
copyright year range specified as YYYY-ZZZZ in this package,that the range specifies every single year in that closed interval.
copyright years of all committed files.# ----- #
copyright years (such as 90, 1991, 1992-2007, 2008) in a# reformatted FSF copyright statement is collapsed to a single# interval (such as 1990-
2008). If unset or set to 0, all existing
(C)" takes one of the following forms or is omitted# entirely:
©);my$holder ||= 'Free Software Foundation, Inc.';my $prefix_max = 5;my $margin = $ENV{UPDATE_COPYRIGHT_MAX_LINE_LENGTH};!$margin ||
$margin !~ m/^\d+$/ and $margin = 72;
GNU Automake v1.16.1 opensuse:automake/1.16.1-5.11/noarch
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copyright); do echo ) | sed -e '$$!s,$$,|,' | tr -d '\012\015'; \ $(GIT) ls-files \ | grep -Ev '(^|/)README$$'\ | grep -Ev '^PLANS(/|$$)'\ |
grep -Ev '^($$excluded_re)$$'\ | $(update
copyright{}} \gdefchar \gdefchar^ab{\guillemetleft{}} \gdefchar^ac{\ensuremath\lnot} \gdefchar^ad{\-} \gdefchar^ae{\registered symbol{}}
\gdefchar^af{\=}{}} % \gdefchar^b0{\textdeg
copyright += lib/install-sh
copyright += lib/mkinstalldirs
copyright line is 72.# Set UPDATE_COPYRIGHT_MAX_LINE_LENGTH to use a different length.# 5. Set UPDATE_COPYRIGHT HOLDER if the
copyright holder is other# than "Free Software Foundation,
copyright' rule in our Makefile.RELEASE_YEAR=2018AC_SUBST([RELEASE_YEAR])
copyright' script.
copyright \ $(SV_GIT_GL)doc/gendocs_template \ $(SV_GIT_GL)doc/INSTALL
copyright target rule# in maint.mk from gnulib's maintainer-makefile module.
Copyright|This is stdout stderr && exit 1
copyrightupdate-copyright: $(AM_V_GEN)set -e; \ if test -n "$$UPDATE_COPYRIGHT_YEAR"; then \
current_year=$$UPDATE_COPYRIGHT_YEAR; \ else \ current_year=`date +%Y` && test -n "$$current_year
copyright year intervals in a reformatted FSF copyright statement# are expanded instead.# If UPDATE_COPYRIGHT_USE_INTERVALS=2, convert
a sequence with gaps# to the minimal containing
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copyright year is 2 or 4 digits, and years are separated by# commas or dashes. Whitespace may appear after commas.
copyright year list to include the current year.
copyright year range specified as YYYY-ZZZZ in this package,that the range specifies every single year in that closed interval.
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copyright years (such as 90, 1991, 1992-2007, 2008) in a# reformatted FSF copyright statement is collapsed to a single# interval (such as 1990-
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 (C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 2/0 - ADDF3, MPYF3 Alias: i,  
 i3\*/#define SC\_CLASS\_INSN(name, opcode,  
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 (C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 1/0 - SUBF3 Alias: i, i3\*/#define  
 S\_CLASS\_INSN(name, opcode, level) \  
 copyright string. len = obj\_som\_copyright\_hdr (abfd)->header\_id.length - 4; obj\_som\_file\_hdr (abfd)->aux\_header\_size += len;  
 current\_offset += len; if (bfd\_bwrite ((void \*) obj  
 (C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define  
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 (C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 2/0 - ADDF3, MPYF3 Alias: i,  
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(C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) Instr: 1/0 - CMPF3 Alias: i, i3\*/#define S2\_CLASS(name, level) \ .ifdef level  
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(C) ((C) ==# define IS\_PATH\_WITH\_DIR(P) (strchr (P, '/') != NULL)# define FILESYSTEM\_PREFIX\_LEN(P) 0#endif  
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 (C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 1/0 - SUBF3 Alias: i, i3\*/#define  
 S\_CLASS\_INSN(name, opcode, level) \  
 copyright string. len = obj\_som\_copyright\_hdr (abfd)->header\_id.length - 4; obj\_som\_file\_hdr (abfd)->aux\_header\_size += len;  
 current\_offset += len; if (bfd\_bwrite ((void \*) obj  
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 by Hans-Peter Nilsson.  
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 (C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define  
 IS\_ABSOLUTE\_PATH(P) (ISLASH ((P)[0]) || HAS\_DEVICE (

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(C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 2/0 - ADDF3, MPYF3 Alias: i,

i3\*/#define SC\_CLASS(name, level) \ .i

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m4_require([AC_LANG_COMPILER(C)], [AC_LANG_COMPILER(C)])# then m4_requi
GNU Bison 3.5.4 gnu:bison:3.5.4
(C)# B. (c)
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IS_PATH_WITH_DIR(P) \ (strchr (P, '/') != NULL ||
(C)((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define
FILE_SYSTEM_PREFIX_LEN(P) 0#endif
(C)((C) ==# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define FILE_SYSTEM_PREFIX_LEN(P) 0#endif
(C)((C) ==#else# define FILE_SYSTEM_PREFIX_LEN(FileName) 0
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copyright: # Code from module useless-if-before-free: # Code from module vasprintf:  
Copyright &copy; 90,2005,2007-2009# \* Free Software Foundation, Inc.# \*/  
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Brendan Kehoe and Gavin Nicol}\vskip .2\baselin  
Copyright (C)", then a list of copyright years, # and then the name of the copyright holder.  
copyright{ }# D.  
copyright date in# Do the same for the \$(sample-test) and the main doc/.texi file.sc\_copyright\_check:  
copyright{ }% \DeclareUnicodeCharacter \DeclareUnicodeCharacter{00AB}{\guillemetleft}{ }% \DeclareUnicodeCharacter{00AC}{\ensuremath\lnot}%  
\DeclareUnicodeCharacter{00AD}{\ -}% \DeclareUnicode

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copyright \definedummyword\registereddsymbol \definedummyword\dots \definedummyword\enddots \definedummyword\entrybreak
\definedummyword\equiv \definedummyword\error \definedummyword\euro
copyright-env = \ UPDATE_COPYRIGHT_FORCE=1 UPDATE_COPYRIGHT_USE_INTERVALS=1
copyright-env) xargs
Copyright
copyright{} \    $$ (export VC_LIST_EXCEPT_DEFAULT=COPYING
\gdefchar^^af{\=}{ } % \gdefchar^^b0{\textdeg
copyright()/g) { my $start = pos () - length ($1); my $end = pos (); my $re = $old_re; pos () = $start; $re =~ s/\#BEFORE/\\G/; if
(!/$re/x)
copyright in $_sc_search_regex
copyright in $_sc_search_regex
copyright invocations or b4_copyright_years definitions to# include the current year.
copyright line is 72.# Set UPDATE_COPYRIGHT_MAX_LINE_LENGTH to use a different length.# 5. Set UPDATE_COPYRIGHT_HOLDER if the
copyright holder is other# than "Free Software Foundation,
copyright\n"; } pos () = $end;
copyright: prereq# By default, exclude all variants of COPYING; you can also# add exemptions (such as ChangeLog.* for rotated change logs)# in the
file .x-update-copyright.
copyright target rule# in maint.mk from gnulib's maintainer-makefile module.
copyright(TITL# -----# If YEARS are not defined, use b4_copyright_years.m4_define([b4_copyright],
[b4_generated_byb4_comment([ $1
copyright: update-b4-copyright update-package-copyright-year
copyrightupdate-copyright:
copyright update-package-copyright-year
copyright \# verify \# vsnprintf-posix \# vsprintf-posix \# warnings \# winsz-ioctl \# winsz-termios \# xalloc \# xalloc-die \# xconcat-filename \#
xhash \# xlist \# xmemdup0 \
copyright-year: $(AM_V_GEN)$(srcdir)/build-aux/$@ $(srcdir)/configure.ac
copyright year for this package
copyright year for this package#undef PACKAGE_COPYRIGHT_YEAR
copyright year intervals in a reformatted FSF copyright statement# are expanded instead.# If UPDATE_COPYRIGHT_USE_INTERVALS=2, convert
a sequence with gaps# to the minimal containing
copyright year intervals. (See "Environment variables"# below.)
copyright year is 2 or 4 digits, and years are separated by# commas, "-", or "--". Whitespace may appear after commas.
copyright year lists in your project.# If you have an additional project-specific rule,
copyright year list to include the current year.
copyright year list to include the current year.# Usage: update-copyright [FILE...]
copyright year range specified as YYYY-ZZZZ in this package, notethat the range specifies every single year in that closed interval.
copyright years (such as 90, 1991, 1992-2007, 2008) in a# reformatted FSF copyright statement is collapsed to a single# interval (such as 1990-
2008). If unset or set to 0, all existing
(C)" takes one of the following forms or is omitted# entirely:
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C) tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if (defined
__WIN32 && !defined __CYGWIN__) || defined
(C)' (the parens are# part of the name, it is not an argument) has not been run, then# call it.'# Had you used#
m4_require([AC_LANG_COMPILER(C)], [AC_LANG_COMPILER(C)])# then m4_requi
(C) (((unsigned int) (C) <= 'z' - 'a')# define FILE_SYSTEM_PREFIX_LEN(FileName) \ (_IS_DRIVE_LETTER ((FileName)[0]) &&
(FileName)[1] == ':' ? 2 : 0)# ifndef _
©);my$holder ||= 'Free Software Foundation, Inc.';my $prefix_max = 5;my $margin = $ENV{UPDATE_COPYRIGHT_MAX_LINE_LENGTH};!$margin ||
$margin !~ m/\^d+$/ and $margin = 72;
GNU Bison v3.5.3 opensuse:bison/3.5.3-1.10/s390x
(C)# B. (c)
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_PATH_WITH_DIR(P) \ (strchr (P, '/') != NULL ||
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define
FILE_SYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) ==# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define FILE_SYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) ==#else# define FILE_SYSTEM_PREFIX_LEN(FileName) 0
(C) ((C) ==#endif
(C)" if AC_PROG_CC has dnl not yet been invoked. _AC_COMPILE_IFELSE( [AC_LANG_PROGRAM([ #ifdef __clang__ barfbar

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copyright: $(AM_V_GEN)find | xargs $(srcdir)/build-aux/$@ @echo 'warning: src/parse-gram.[hc] may need to be regenerated.'
copyright build-aux/useless-if-before-free build-aux/vc-list-files doc/fdl.texi doc/gendocs_template doc/gendocs_template_min doc/gpl-3.0.texi
doc/relocatable.texi lib/_Noreturn.h li
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copyright: # Code from module useless-if-before-free: # Code from module vasnprintf:
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Brendan Kehoe and Gavin Nicol}\vskip .2\baselin
Copyright (C)", then a list of copyright years, # and then the name of the copyright holder.
copyright{\}# D.
copyright date in # Do the same for the $(sample-test) and the main doc/.texi file.sc_copyright_check:
copyright{\} % \DeclareUnicodeCharacter \DeclareUnicodeCharacter{00AB}{\guillemetleft}{\} % \DeclareUnicodeCharacter{00AC}{\ensuremath{\not}} %
\DeclareUnicodeCharacter{00AD}{\} % \DeclareUnicode
copyright \definedummyword\registered symbol \definedummyword\enddots \definedummyword\entrybreak
\definedummyword\equiv \definedummyword\error \definedummyword\euro
copyright-env = \ UPDATE_COPYRIGHT_FORCE=1 UPDATE_COPYRIGHT_USE_INTERVALS=1
copyright-env xargs
Copyright \ \ $(export VC_LIST_EXCEPT_DEFAULT=COPYING
copyright{\} \gdefchar \gdefchar^^ab{\guillemetleft}{\} \gdefchar^^ac{\ensuremath{\not}} \gdefchar^^ad{\} \gdefchar^^ae{\registered symbol}{\}
\gdefchar^^af{\} % \gdefchar^^b0{\textdeg
copyright\(/g) { my $start = pos () - length ($1); my $end = pos (); my $re = $old_re; pos () = $start; $re =~ s/\#BEFORE/\#G/; if
(!/$re/x)
copyright in $_sc_search_regexp
copyright in $_sc_search_regexp
copyright invocations or b4_copyright_years definitions to # include the current year.
copyright line is 72.# Set UPDATE_COPYRIGHT_MAX_LINE_LENGTH to use a different length.# 5. Set UPDATE_COPYRIGHT HOLDER if the
copyright holder is other# than "Free Software Foundation,
copyright\n"; } pos () = $end;
copyright: prereq# By default, exclude all variants of COPYING; you can also# add exemptions (such as ChangeLog.* for rotated change logs)# in the

```

```

file .x-update-copyright.
copyright target rule# in maint.mk from gnu/lib's maintainer-makefile module.
copyright(TITL# -----# If YEARS are not defined, use b4_copyright_years.m4_define([b4_copyright],
[b4_generated_byb4_comment([$1
copyright: update-b4-copyright update-package-copyright-year
copyrightupdate-copyright:
copyright update-package-copyright-year
copyright \# verify \# vsnprintf-posix \# vsprintf-posix \# warnings \# winsz-ioctl \# winsz-termios \# xalloc \# xalloc-die \# xconcat-filename \#
xhash \# xlist \# xmemdup0 \
copyright-year: $(AM_V_GEN)$(srcdir)/build-aux/$@ $(srcdir)/configure.ac
copyright year for this package
copyright year for this package#undef PACKAGE_COPYRIGHT_YEAR
copyright year intervals in a reformatted FSF copyright statement# are expanded instead.# If UPDATE_COPYRIGHT_USE_INTERVALS=2, convert
a sequence with gaps# to the minimal containing
copyright year intervals. (See "Environment variables"# below.)
copyright year is 2 or 4 digits, and years are separated by# commas, "-", or "--". Whitespace may appear after commas.
copyright year lists in your project.# If you have an additional project-specific rule,
copyright year list to include the current year.
copyright year list to include the current year.# Usage: update-copyright [FILE...]
copyright year range specified as YYYY-ZZZZ in this package, notethat the range specifies every single year in that closed interval.
copyright years (such as 90, 1991, 1992-2007, 2008) in a# reformatted FSF copyright statement is collapsed to a single# interval (such as 1990-
2008). If unset or set to 0, all existing
(C)" takes one of the following forms or is omitted# entirely:
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C) tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if (defined
_WIN32 && !defined __CYGWIN__) || defined
(C)' (the parens are# part of the name, it is not an argument) has not been run, then# call it.'# Had you used#
m4_require([AC_LANG_COMPILER(C)], [AC_LANG_COMPILER(C)])# then m4_require
(C) (((unsigned int) (C) <= 'z' - 'a')# define FILE_SYSTEM_PREFIX_LEN(Filename) \ (_IS_DRIVE_LETTER ((Filename)[0]) &&
(Filename)[1] == ':' ? 2 : 0)# ifndef _
©);my$holder ||= 'Free Software Foundation, Inc.';my $prefix_max = 5;my $margin = $ENV{UPDATE_COPYRIGHT_MAX_LINE_LENGTH};!$margin ||
$margin !~ m/^\d+$/ and $margin = 72;
GNU C Library 2.2.3pre1 ubuntu:libnss-db/2.2.3pre1-3.2ubuntu3
(C) AC_LINK_IFELSE( AC_LANG_POP) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need
for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
(C)/!b go :more \./!{ N s\n# // b more
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)#endif
(C) ((C) == DIRECTORY_SEPARATOR)#endif
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22:12:56 dbg * Added definitions used by Mach Ker
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copyright * * Revision 2.3 91/02/14 14:17:47 mrt * Added new Mach copyright * [91/02/13 12:44:20 mrt] * * Revision 2.2 90/08/06
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```

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 14:37:33 rpd \* Added malloc\_fork\* code. \* [90/11/02  
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 right to distribute this material elsewhere unde  
 Copyright (C) 1987 Sun Microsystems, Inc. /\*#include <stdio.h>#include <string.h>#include "rpc/types.h"#include "rpc\_scan.h"#include  
 "rpc\_parse.h"#include "rpc\_util.h"#include "proto.h"  
 Copyright (c) 1987, 1993 \* The Regents of the University of California. All rights reserved  
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 (C) AEss>>"); TEST ("onemore", " 1/2 \* 1/2 = 1/4 "); TEST ("onemore", " 1/2 \* 1/2 = 1/4 ");  
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 14:57:47 rpd \* Added err\_mach\_ipc for new IPC. \*  
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 (C)# define \_\_TGMATH\_1(F, X) \_\_builtin\_tgmath (\_\_TGMATH\_FUNCS (F) (X))# define \_\_TGMATH\_2(F, X, Y) \_\_builtin\_tgmath (\_\_TGMATH\_FUNCS (F)  
 (X), (Y))# define \_\_TGMATH\_2STD(F, X, Y) \_\_builtin\_tgmath  
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(C) btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# define MEMPCPY(D, S, N) __mempcpy (D, S, N)# define
MEMCHR(S, C, N) memchr (S, C, N)# define STRCOLL(S1, S2)
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MEMCHR(S, C, N) __wmemchr (S, C, N)# define STRCOLL(S1, S2)
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(C) btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# define MEMPCPY(D, S, N) __mempcpy (D, S, N)# define
MEMCHR(S, C, N) memchr (S, C, N)# define STRCOLL(S1, S2)
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copyright script. Any other arguments are ignored.# FSF copyright notices in the glibc source directory containing this# script will be updated; glibc
must then be built to update generated
(C) (C)# define STRLEN(S) __wcslen (S)# define STRCAT(D, S) __wscat (D, S)# define MEMPCPY(D, S, N) __wmempcpy (D, S, N)# define

```

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MEMCHR(S, C, N) __wmemchr(S, C, N) # define STRCOLL(S1, S2)
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    Copyright 1996 by Craig Metz, All Rights Reserved
    (C) ((C) == # define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':') # define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
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GNU Core Utilities v8.30 opensuse:coreutils-debugsource/8.30-1.4/aarch64
(C) add (1, #endif
(C) add (1, fputc (C, p)) #else
(C) ((C) >= : (C) >= 'A' && (C) <= 'F' ? (C) - 'A' + 10 : (C) - '0')
(C) obstack_1grow (#define APPEND_TWO_CHAR_STRING(S) \ do \ {
\ APPEND_CHAR (S[0]); \ APPEND_CHAR (S[1]); \
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 copyright symbol suitable for this locale, and  
 copyright year.  
 (C)" to the copyright symbol

GNU Core Utilities v8.31 fedora:coreutils/8.31-9.fc31/armv7hl  
 (C) add (1,#endif  
 (C) add (1, fputc (C, p))#else  
 (C)# B. (c)  
 (C) \_\_btowc (C)# else  
 (C) btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef \_LIBC# define MEMPCPY(D, S, N) \_\_mempcpy (D, S,  
 N)# else# if HAVE\_MEMPCPY# define MEMPCPY(D  
 (C) ((C) >= : (C) >= 'A' && (C) <= 'F' ? (C)-'A'+10 : (C)-'0')  
 (C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define  
 IS\_ABSOLUTE\_PATH(P) (ISLASH ((P)[0]) || HAS\_DEVICE (P))  
 (C) ((C) ==# define IS\_ABSOLUTE\_PATH(P) ISLASH ((P)[0])# define IS\_PATH\_WITH\_DIR(P) (strchr (P, '/') != NULL)# define  
 FILE\_SYSTEM\_PREFIX\_LEN(P) 0# endif  
 (C) ((C) ==#else# define FILE\_SYSTEM\_PREFIX\_LEN(FileName) 0  
 (C) ((C) ==#else /\* Unix \*/  
 (C) ((C) ==#endif  
 (C) (C)# ifdef \_LIBC# define STRLEN(S) \_\_wcslen (S)# define STRCAT(D, S) \_\_wscat (D, S)# define MEMPCPY(D, S, N) \_\_wmempcpy (D, S,  
 N)# else# define STRLEN(S) wcslen (S)# defin  
 (C) obstack\_1grow (#define APPEND\_TWO\_CHAR\_STRING(S) \ do \ {  
 \ APPEND\_CHAR (S[0]); \ APPEND\_CHAR (S[1]); \

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first <<EOF || exit 1
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Copyright (C)", then a list of copyright years,# and then the name of the copyright holder.
copyright{ }# D.
copyright date in# Do the same for the $(sample-test) and the main doc/.texi file.sc_copyright_check:
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copyright \definedummyword\registeredsymbol \definedummyword\dots \definedummyword\enddots \definedummyword\entrybreak
\definedummyword\equiv \definedummyword\error \definedummyword\euro
copyright-env = \ UPDATE_COPYRIGHT_FORCE=1 \ UPDATE_COPYRIGHT_USE_INTERVALS=2 \ UPDATE_COPYRIGHT_MAX_LINE_LENGTH=79
copyright-env) xargs
Copyright \ \ $$ (export VC_LIST_EXCEPT_DEFAULT=COPYING
COPYRIGHT Free Software Foundation, Inc.
copyright{ } } \gdefchar \gdefchar^^ab{\guillemetleft{}} \gdefchar^^ac{\ensuremath{\lnot}} \gdefchar^^ad{\-} \gdefchar^^ae{\registeredsymbol{}}
\gdefchar^^af{\={}} % \gdefchar^^b0{\textdeg
copyright goes to the FSF.
copyright information, for version-etc.
copyright in \$_sc_search_regexp)
copyright in \$_sc_search_regexp)
copyright line is 72.# Set UPDATE_COPYRIGHT_MAX_LINE_LENGTH to use a different length.# 5. Set UPDATE_COPYRIGHT_HOLDER if the
copyright holder is other# than "Free Software Foundation,
copyright note exit $$status
copyright * paperwork. * Jim Meyering, for his work merging this into the GNU fileutils while * still letting me feel a sense of ownership and pride.

```

```

Getting me to * tolerate the GNU brace style
copyright: prereq# By default, exclude all variants of COPYING; you can also# add exemptions (such as ChangeLog.* for rotated change logs)# in the
file .x-update-copyright.
copyright program gets found.PATH=$abs_aux_dir:$PATHexport PATH
copyrightsc_man_check_x_copyright: @status=0;
| awk '$$1 >= 20 || $$2 >= 1000 {print $$3}' \ | xargs grep -L 'Copyright .* Free Softwa \ cd $(srcdir) && wc -cl man/*.x | head -n-1 \
copyright symbol suitable for this locale, and
copyright target rule# in maint.mk from gnulib's maintainer-makefile module.
copyright-tests
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1compare - $TMP <<EOF || exit 1
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1compare - $TMP <<EOF || exit 1 Undisturbed
text.dnl Undisturbed text.
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1tr @ '\015' > $TMP-exp <<EOF
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare - $TMP-stderr <<EOF || exit 1$TMP: warning: copyright statement not
foundEOFcompare - $TMP <<EOF || exit 1
copyright $TMP.compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1compare - $TMP.star <<EOF || exit 1
copyright $TMP.compare /dev/null $TMP-stdout || exit 1compare - $TMP-stderr <<EOF || exit 1$TMP.4: warning: copyright statement not found$TMP.5:
warning: copyright statement not foundEOFcompare
COPYRIGHT' | tr -d compare $TMP - || ERR=1
copyrightupdate-copyright:
copyright \# uptime \# useless-if-before-free \# userspec \# utimecmp \# utimens \# vasprintf-posix \# vc-list-files \# verify \# verror \#
version-etc-fsf \# wchar-single \# w
copyright year.
copyright year intervals in a reformatted FSF copyright statement# are expanded instead.# If UPDATE_COPYRIGHT_USE_INTERVALS=2, convert
a sequence with gaps# to the minimal containing
copyright year intervals. (See "Environment variables"# below.)
copyright year is 2 or 4 digits, and years are separated by# commas or dashes. Whitespace may appear after commas.
copyright year lists in your project.# If you have an additional project-specific rule,
copyright year list to include the current year.
copyright year range specified as YYYY-ZZZZ in this packagenote that the range specifies every single year in that closed interval.
copyright years (such as 90, 1991, 1992-2007, 2008) in a# reformatted FSF copyright statement is collapsed to a single# interval (such as 1990-
2008). If unset or set to 0, all existing
(C)" takes one of the following forms or is omitted# entirely:
(C) tests whether C is a directory separator character. */#if defined _WIN32 || defined __CYGWIN__ || defined __EMX__ || defined __DJGPP__
/* Native Windows, Cygwin, OS/2, DOS */
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C)" to the copyright symbol
(C) (((unsigned int) (C) <= 'z' - 'a')# define FILE_SYSTEM_PREFIX_LEN(Filename) \ (_IS_DRIVE_LETTER ((Filename)[0]) &&
(Filename)[1] == ':' ? 2 : 0)# ifndef _
©);my$holder ||= 'Free Software Foundation, Inc. ';my $prefix_max = 5;my $margin = $ENV{UPDATE_COPYRIGHT_MAX_LINE_LENGTH};!$margin ||
$margin !~ m/^\d+$/ and $margin = 72;
GNU Core Utilities v8.31 opensuse:coreutils-debugsource/8.31-2.1/armv6hl
(C) add (1,#endif
(C) add (1, fputc (C, p))#else
(C) ((C) >= 'A' && (C) <= 'F' ? (C)-'A'+10 : (C)-'0')
(C) obstack_1grow (#define APPEND_TWO_CHAR_STRING(S) \ do \ {
\ APPEND_CHAR (S[0]); \ APPEND_CHAR (S[1]);
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(C) tests whether C is a directory separator character. */#if defined __WIN32 || defined __CYGWIN__ || defined __EMX__ || defined __DJGPP__
/* Native Windows, Cygwin, OS/2, DOS */
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copyrightsc_man_check_x_copyright: @status=0; \ cd $(srcdir) && wc -cl man/*.x | head -n-1
\ | awk '$$1 >= 20 || $$2 >= 1000 {print $$3}' \ | xargs grep -L 'Copyright .* Free Softwa
Copyright (C) 1996-1997, 1999-2004, 2008-2019 Free Software Foundation, Inc.
GNU Diff Utilities 3.7 debian:diffutils/1:3.7-3
(C) add (1,#endif

```

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(C) add (1, fputc (C, p))#else
(C)# B. (c)
(C) __btowc (C)# else
(C) btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef _LIBC# define MEMPCPY(D, S, N) __mempcpy (D, S, N)# else# if HAVE_MEMPCPY# define MEMPCPY(D
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define
FILE_SYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) ==#else# define FILE_SYSTEM_PREFIX_LEN(FileName) 0
(C) ((C) ==#endif
(C) (C)# ifdef _LIBC# define STRLEN(S) __wcslen (S)# define STRCAT(D, S) __wcscat (D, S)# define MEMPCPY(D, S, N) __wmempcpy (D, S,
N)# else# define STRLEN(S) wcslen (S)# defin
(C) is nonzero if the unsigned char C can safely be given as an argument to <ctype.h> macros like "isspace". */#ifndef STDC_HEADERS
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lib/alloca.c lib/alloca.in.h lib/allocat

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Copyright \{C\} in_vc_files=$(sample-test) \
copyright\{\}\|\\\|co
copyright: # Code from module update-copyright-tests: # Code from module useless-if-before-free:
Copyright &copy; 1990, 2005, 2007, 2008, 2009, 2010 Free Software * Foundation, Inc. */EOFcompare - $TMP.4 <<EOF || exit 1
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Copyright &copy; 90,2005,2007-2009# * Free Software Foundation, Inc.# */
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Copyright @copyright1998, 1999, 2000, 2001, 2002, 2003, 2004, 2005, 2007, 2008, 2009, 2010Free Software Foundation, Inc.EOFcompare - $TMP.2
<<EOF || exit 1
copyright\{copyright \defglyph\dots{...}% \defglyph\enddots{...}% \defglyph\equiv{==}% \defglyph\error{error}% \defglyph\euro{euro}%
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Copyright (C)", then a list of copyright years,# and then the name of the copyright holder.
copyright\{\}# D.
copyright date in# Do the same for the $(sample-test) and the main doc/.texi file.sc_copyright_check:
copyright\{\}% \DeclareUnicodeCharacter \DeclareUnicodeCharacter{00AB}{\guillemetleft}\}% \DeclareUnicodeCharacter{00AC}{\ensuremath\not}%
\DeclareUnicodeCharacter{00AD}{\-\}% \DeclareUnicode
copyright \definedummyword\registeredymbol \definedummyword\dots \definedummyword\enddots \definedummyword\entrybreak
\definedummyword\equiv \definedummyword\error \definedummyword\euro
copyright-env = \ UPDATE_COPYRIGHT_USE_INTERVALS=1 \ UPDATE_COPYRIGHT_MAX_LINE_LENGTH=79
copyright-env) xargs
Copyright \ $$\{export VC_LIST_EXCEPT_DEFAULT=COPYING
copyright\{\}\} \gdefchar \gdefchar^^ab{\guillemetleft}\} \gdefchar^^ac{\ensuremath\not} \gdefchar^^ad{\-\} \gdefchar^^ae{\registeredymbol}\}
\gdefchar^^af{\=\}\} % \gdefchar^^b0{\textdeg
copyright goes to the FSF.
copyright information, for version-etc.
copyright in $_sc_search_regexp)
copyright in $_sc_search_regexp)
copyright line is 72.# Set UPDATE_COPYRIGHT_MAX_LINE_LENGTH to use a different length.# 5. Set UPDATE_COPYRIGHT HOLDER if the
copyright holder is other# than "Free Software Foundation,
copyright: prereq# By default, exclude all variants of COPYING; you can also# add exemptions (such as ChangeLog..* for rotated change logs)# in the
file .x-update-copyright.
copyright program gets found.PATH=$abs_aux_dir:$PATHexport PATH
copyright symbol suitable for this locale, and
copyright target rule# in maint.mk from gnulib's maintainer-makefile module.
copyright-tests
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1compare - $TMP <<EOF || exit 1
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1compare - $TMP <<EOF || exit 1 Undisturbed
text.dnl Undisturbed text.
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1tr @ '\015' > $TMP-exp <<\EOF
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr <<EOF || exit 1$TMP: warning: copyright statement not
foundEOFcompare - $TMP <<EOF || exit 1
copyright $TMP.compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1compare - $TMP.star <<EOF || exit 1
copyright $TMP.compare /dev/null $TMP-stdout || exit 1compare - $TMP-stderr <<EOF || exit 1$TMP.4: warning: copyright statement not found$TMP.5:
warning: copyright statement not foundEOFcompare
copyrightupdate-copyright:
copyright \# vararrays \# verify \# version-etc \# version-etc-fs \# wcwidth \# xalloc \# xreopen \# xreadlink \# xstrtoimax \# xvasprintf
copyright year.
copyright year intervals in a reformatted FSF copyright statement# are expanded instead.# If UPDATE_COPYRIGHT_USE_INTERVALS=2, convert
a sequence with gaps# to the minimal containing
copyright year intervals. (See "Environment variables"# below.)
copyright year is 2 or 4 digits, and years are separated by# commas or dashes. Whitespace may appear after commas.

```

```

copyright year lists in your project.# If you have an additional project-specific rule,
copyright year list to include the current year.
copyright year range specified as YYYY-ZZZZ in this packagenote that the range specifies every single year in that closed interval.
copyright years (such as 90, 1991, 1992-2007, 2008) in a# reformatted FSF copyright statement is collapsed to a single# interval (such as 1990-
2008). If unset or set to 0, all existing
(C)" takes one of the following forms or is omitted# entirely:
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C)" to the copyright symbol
(C) (((unsigned int) (C) <= 'z' - 'a')# define FILE_SYSTEM_PREFIX_LEN(FileName) \ (_IS_DRIVE_LETTER ((FileName)[0]) &&
(FileName)[1] == ':' ? 2 : 0)# ifndef _
©);my$holder ||= 'Free Software Foundation, Inc.!'my $prefix_max = 5;my $margin = $ENV{UPDATE_COPYRIGHT_MAX_LINE_LENGTH};!$margin ||
$margin !~ m/^\^d+$/ and $margin = 72;
GNU Diff Utilities 3.7 opensuse:diffutils/3.7-4.2/s390x
(C) add (1,#endif
(C) add (1, fputc (C, p))#else
(C)# B. (c)
(C) __btowc (C)# else
(C) btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef _LIBC# define MEMPCPY(D, S, N) __mempcpy (D, S,
N)# else# if HAVE_MEMPCPY# define MEMPCPY(D
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define
FILE_SYSTEM_PREFIX_LEN(P) 0# endif
(C) ((C) ==#else# define FILE_SYSTEM_PREFIX_LEN(FileName) 0
(C) ((C) ==#endif
(C) (C)# ifdef _LIBC# define STRLEN(S) __wcslen (S)# define STRCAT(D, S) __wscat (D, S)# define MEMPCPY(D, S, N) __wmempcpy (D, S,
N)# else# define STRLEN(S) wcslen (S)# defin
(C) is nonzero if the unsigned char C can safely be given as an argument to <ctype.h> macros like "isspace". */#ifndef STDC_HEADERS
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Copyright 1987, 1988, 1991, 1992, 2010 Free Software Foundation, * Inc. End * More comments. */EOFcompare - $TMP.extra-text-space <<EOF ||
exit 1
Copyright 1987, 1988, 1991, 1992, 2010 Free Software Foundation, Inc. *** * End of comments. */EOFcompare - $TMP.two-digit-final-is-substr-of-
first <<EOF || exit 1
Copyright 1987, 1988, 1991, 1992 Free Software Foundation, Inc. End * More comments. */EOFcat > $TMP.extra-text-space <<EOF
Copyright 1987, 1988, 1991, 1992 Free Software Foundation, Inc. * End of comments. */EOFcat > $TMP.two-digit-final-is-substr-of-first <<EOF
Copyright 1987, 1991, 1992, 2010 Free Software Foundation, Inc.EOFcompare - $TMP.single-line-wrapped <<EOF || exit 1
Copyright 1988, 1991, 1992, 1993, 2010 Free Software Foundation, * Inc. */EOFcompare - $TMP.extra-text-star <<EOF || exit 1
Copyright 1988, 1991, 1992, 1993 Free Software Foundation, Inc.EOFcat > $TMP.extra-text-star <<EOF
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copyright goes to the FSF.
copyright information, for version-etc.
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copyright: prereq# By default, exclude all variants of COPYING; you can also# add exemptions (such as ChangeLog..* for rotated change logs)# in the
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copyright target rule# in maint.mk from gnulib's maintainer-makefile module.
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copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1tr @ '\015' > $TMP-exp <<\EOF
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare - $TMP-stderr <<\EOF || exit 1$TMP: warning: copyright statement not
foundEOFcompare - $TMP <<\EOF || exit 1
copyright $TMP.compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1compare - $TMP.star <<\EOF || exit 1
copyright $TMP.compare /dev/null $TMP-stdout || exit 1compare - $TMP-stderr <<\EOF || exit 1$TMP.4: warning: copyright statement not found$TMP.5:
warning: copyright statement not foundEOFcompare
copyrightupdate-copyright:
copyright \# vararrays \# verify \# version-etc \# version-etc-fsf \# wcwidth \# xalloc \# xfreopen \# xreadlink \# xstrtoimax \# xvasprintf
copyright year.
copyright year intervals in a reformatted FSF copyright statement# are expanded instead.# If UPDATE_COPYRIGHT_USE_INTERVALS=2, convert
a sequence with gaps# to the minimal containing
copyright year intervals. (See "Environment variables"# below.)
copyright year is 2 or 4 digits, and years are separated by# commas or dashes. Whitespace may appear after commas.
copyright year lists in your project.# If you have an additional project-specific rule,
copyright year list to include the current year.
copyright year range specified as YYYY-ZZZZ in this packagenote that the range specifies every single year in that closed interval.
copyright years (such as 90, 1991, 1992-2007, 2008) in a# reformatted FSF copyright statement is collapsed to a single# interval (such as 1990-
2008). If unset or set to 0, all existing
(C)" takes one of the following forms or is omitted# entirely:
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C)" to the copyright symbol
(C) (((unsigned int) (C) <= 'z' - 'a')# define FILE_SYSTEM_PREFIX_LEN(Filename) \ (_IS_DRIVE_LETTER ((Filename)[0]) &&
(Filename)[1] == ':' ? 2 : 0)# ifndef _
©);my$holder ||= 'Free Software Foundation, Inc. ';my $prefix_max = 5;my $margin = $ENV{UPDATE_COPYRIGHT_MAX_LINE_LENGTH};!$margin ||
$margin !~ m/^\d+$/ and $margin = 72;
gnu-efi 3.0.11 opensuse:gnu-efi/3.0.11-1.2/aarch64
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provided that the following conditions * are m
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 belf CFLAGS=\$SAVE\_CFLAGS fi ;;\*-so  
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gnulib 20180621~6979c25 debian:gnulib/20180621~6979c25-3

(C) add (1,#endif

(C) add (1, fputc (C, p))#else

(C)# B. (c)

(C) \_\_btowc (C)# else

(C) \_\_btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef \_LIBC# define MEMPCPY(D, S, N) \_\_mempcpy (D, S, N)# else# if HAVE\_MEMPCPY# define MEMPCPY(D

(C) CATEGORY (Cc) CATEGORY (Cf) CATEGORY (Cs) CATEGORY (Co) CATEGORY (Cn)#undef CATEGORY}

(C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define IS\_ABSOLUTE\_PATH(P) (ISLASH ((P)[0]) || HAS\_DEVICE (

(C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define IS\_PATH\_WITH\_DIR(P) \ (strchr (P, '/') != NULL ||

(C) ((C) ==# define IS\_ABSOLUTE\_PATH(P) ISLASH ((P)[0])# define IS\_PATH\_WITH\_DIR(P) (strchr (P, '/') != NULL)# define FILE\_SYSTEM\_PREFIX\_LEN(P) 0#endif

(C) ((C) ==# define IS\_PATH\_WITH\_DIR(P) (strchr (P, '/') != NULL)# define FILE\_SYSTEM\_PREFIX\_LEN(P) 0#endif

(C) ((C) ==#else# define FILE\_SYSTEM\_PREFIX\_LEN(Filename) 0

(C) ((C) ==#else /\* Unix \*/

(C) ((C) ==#endif

(C) (C)# ifdef \_LIBC# define STRLEN(S) \_\_wcslen (S)# define STRCAT(D, S) \_\_wscat (D, S)# define MEMPCPY(D, S, N) \_\_wmempcpy (D, S, N)# else# define STRLEN(S) wcslen (S)# defin

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copyright information follows, see debian/clscan/README in the gnu lib source package for information on updating this.
copyright information, for version-etc.
copyright in      $( _sc_search_regex )
copyright in      $( _sc_search_regex )
copyright is ok.
copyright line is 72.#      Set UPDATE_COPYRIGHT_MAX_LINE_LENGTH to use a different length.#      5. Set UPDATE_COPYRIGHT_HOLDER if the
copyright holder is other#      than "Free Software Foundation,
Copyright: none (in the public domain)
copyright: prereq# By default, exclude all variants of COPYING; you can also# add exemptions (such as ChangeLog.* for rotated change logs)# in the
file .x-update-copyright.
copyright program gets found.PATH=$abs_aux_dir:$PATHexport PATH
copyright(self):      ""Return formatted string which contains copyright.      The special __copyright__ variable is used (type is str).""
Copyright:' should be corrected where
copyright      symbol suitable for this locale, and
copyright target rule# in maint.mk from gnu lib's maintainer-makefile module.
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1compare - $TMP <<EOF || exit 1
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1compare - $TMP <<EOF || exit 1      Undisturbed
text.dnl Undisturbed text.
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1tr @ '\015' > $TMP-exp <<\EOF
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare - $TMP-stderr <<EOF || exit 1$TMP: warning: copyright statement not
foundEOFcompare - $TMP <<EOF || exit 1
copyright $TMP.compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1compare - $TMP.star <<EOF || exit 1
copyright $TMP.compare /dev/null $TMP-stdout || exit 1compare - $TMP-stderr <<EOF || exit 1$TMP.4: warning: copyright statement not found$TMP.5:
warning: copyright statement not foundEOFcompare
COPYRIGHT/' | tr -d diff -c $TMP - || ERR=1
Copyright: unknown
copyrightupdate-copyright:
copyright      year.
copyright year intervals in a reformatted FSF copyright statement#      are expanded instead.#      If UPDATE_COPYRIGHT_USE_INTERVALS=2, convert
a sequence with gaps#      to the minimal containing
copyright year intervals. (See "Environment variables"#      below.)
copyright year is 2 or 4 digits, and years are separated by#      commas or dashes. Whitespace may appear after commas.
copyright year lists in your project.# If you have an additional project-specific rule,
copyright year list to include the current year.
copyright years (such as 90, 1991, 1992-2007, 2008) in a#      reformatted FSF copyright statement is collapsed to a single#      interval (such as 1990-
2008). If unset or set to 0, all existing
(C)" takes one of the following forms or is omitted#      entirely:
(C)      tests whether C is a directory separator character. */#if defined _WIN32 || defined __CYGWIN__ || defined __EMX__ || defined __DJGPP__
/* Native Windows, Cygwin, OS/2, DOS */
(C)      tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C)      tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if (defined
_WIN32 && !defined __CYGWIN__) || defined
(C)" to the copyright symbol
(C) (((unsigned int) (C)
<= 'z' - 'a')# define FILE_SYSTEM_PREFIX_LEN(Filename) \      (_IS_DRIVE_LETTER ((Filename)[0]) &&
(Filename)[1] == ':' ? 2 : 0)# ifndef _
©);my$holder ||= 'Free Software Foundation, Inc. ';my $prefix_max = 5;my $margin = $ENV{UPDATE_COPYRIGHT_MAX_LINE_LENGTH};!$margin ||
$margin !~ m/^\^d+$/ and $margin = 72;
GNU libiberty 20190907 ubuntu:libiberty/20190907-1
(C)      (((C)
(C)      (((C)#define CC3(OP)      (((OP)>>8)&0x3)#define SBIT(OP)      ((OP)&0xF)#define MMR(OP)      ((OP)&0x7F)#define MMRX(OP)
((((OP)>>4)&0xF)+16)#define MMRY(OP)      (((
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COPYRIGHT      2#define NT_HP_VERSION      3#define NT_HP_SRCFILE_INFO      4#define NT_HP_LINKER      5#define
NT_HP_INSTRUMENTED      6#define NT_HP_UX_OPTIONS      7
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 (C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 1/0 - SUBF3 Alias: i, i3\*/#define  
 S\_CLASS\_INSN(name, opcode, level) \  
 (C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 2/0 - ADDF3, MPYF3 Alias: i,  
 i3\*/#define SC\_CLASS\_INSN(name, opcode,

```

(C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) Instr: 1/0 - CMPF3 Alias: i, i3*/#define S2_CLASS_INSN(name, opcode,
level) \ { name, opcode|0x20000000, 0
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(C) AC_TRY_LINK( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
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(C) AC_TRY_LINK( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS="$SAVE_CFLAGS" fi ;;sp
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GNU Libtool 2.4.6 debian:libtool/2.4.6-12  
 (C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test yes != "\$lt\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -

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belf CFLAGS=$SAVE_CFLAGS fi :.*-*so
(C)/!b go :more \./!{ N s|\n# | | b more
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copyright build-aux/useless-if-before-free build-aux/vc-list-files doc/gendocs_template doc/gendocs_template_min m4/00gnulib.m4 m4/gnulib-
common.m4 top/GNUMakefile top/README-release
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Copyright (C) 2014 Free Software Foundation, Inc.
Copyright (C) in_vc_files=$(sample-test)
copyright: # Code from module useless-if-before-free: # Code from module vc-list-files:]
copyright{copyright \def\dots{...}% \def\enddots{...}% \def\equiv{==}% \def\error{error}% \def\euro{euro}% \def\expansion{==>}%
\def\geq{>=}% \def\guillemetleft{<<}% \def\guillemetri
Copyright @copyright\in_vc_files=$(texi)
copyright date in # Do the same for the $(sample-test) and the main doc/.texi file.sc_copyright_check:
copyright} \DeclareUnicodeCharacter \DeclareUnicodeCharacter{00AB}{\guillemetleft} \DeclareUnicodeCharacter{00AD}{\-}
\DeclareUnicodeCharacter{00AE}{\registeredymbol} \DeclareUnicodeCharact
copyright \definedummyword\registeredymbol \definedummyword\dots \definedummyword\enddots \definedummyword\entrybreak
\definedummyword\equiv \definedummyword\error \definedummyword\euro
copyright-env := UPDATE_COPYRIGHT_FORCE=1 UPDATE_COPYRIGHT_USE_INTERVALS=1
copyright-env) xargs
Copyright
copyright} \gdef \gdef^^ab{\guillemetleft} \gdef^^ac{\notin} \gdef^^ad{\-} \gdef^^ae{\registeredymbol} \gdef^^af{\=} %
\gdef^^b0{\textdegree} \gdef^^b1{\pms} \gdef^^b2{\^2}
copyright in $( _sc_search_regexp)
copyright in $( _sc_search_regexp)
copyright: prereq# By default, exclude all variants of COPYING; you can also# add exemptions (such as ChangeLog.* for rotated change logs)# in the
file .x-update-copyright.
copyrightupdate-copyright:
copyright year lists in your project.# If you have an additional project-specific rule,
GNU Libtool 2.4.6 debian:libtool/2.4.6-13/all
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;;*-so
(C)/!b go :more /\.!{ N s|\n# || b more
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GNU Libtool 2.4.6 debian:libtool/2.4.6-6/all
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;;*-so
(C)/!b go :more /\.!{ N s|\n# || b more
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(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;;*-*so
(C)/!b go :more \./!{ N s|\n# || b more
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Copyright 1985, 1986, 1988, 1990, 1991, 1992, 1993, 1994, 1995,
copyright in $(_sc_search_regexp)
Copyright (C) 2000, 2004, 2009, 2011-2015 Free Software Foundation, Inc. Written by Gary V. Vaughan, 2000
Copyright 2003-2015 Free Software Foundation, Inc.
copyright date in# Do the same for the $(sample-test) and the main doc/.texi file.sc_copyright_check:
copyright} \gdef \gdef^ab{\guillemetleft} \gdef^ac{${\not$} \gdef^ad{\-} \gdef^ae{\registeredsymbol} \gdef^af{\=} %
\gdef^b0{\textdegree} \gdef^b1{\$pm$} \gdef^b2{${^2$}
copyright in $(_sc_search_regexp)
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(C)  AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then  # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;;*-.*so
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(C)/!b go  :more  \./!{  N  s|\n# ||  b more
Copyright  \  $$ (export VC_LIST_EXCEPT_DEFAULT=COPYING
copyrightupdate-copyright:
copyright{copyright \def\dots{...}% \def\enddots{...}% \def\equiv{==}% \def\error{error}% \def\euro{euro}% \def\expansion{==>}%
\def\geq{>=}% \def\guillemetleft{<<}% \def\guillemetri
copyright} \DeclareUnicodeCharacter \DeclareUnicodeCharacter{00AB}{\guillemetleft} \DeclareUnicodeCharacter{00AD}{\}
\DeclareUnicodeCharacter{00AE}{\registeredsymbol} \DeclareUnicodeCharact
copyright build-aux/useless-if-before-free build-aux/vc-list-files doc/gendocs_template doc/gendocs_template_min m4/00gnulib.m4 m4/gnulib-
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belf CFLAGS=$SAVE_CFLAGS fi ;;*-.*so
(C)!b go :more \./!{ N s|\n# || b more
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(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;;*-.*so
(C)!b go :more \./!{ N s|\n# || b more
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(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;;*-.*so
(C)!b go :more \./!{ N s|\n# || b more
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belf CFLAGS=\$SAVE\_CFLAGS fi ;\*-\*so  
(C)!b go :more /\.!{ N s|\n# || b more  
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(C) __btowc (C)# else
(C) btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef _LIBC# define MEMPCPY(D, S, N) __mempcpy (D, S,
N)# else# if HAVE_MEMPCPY# define MEMPCPY(D
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define
FILE_SYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) ==#else# define FILE_SYSTEM_PREFIX_LEN(Filename) 0
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(C) (C)# ifdef _LIBC# define STRLEN(S) __wcslen (S)# define STRCAT(D, S) __wscat (D, S)# define MEMPCPY(D, S, N) __wmempcpy (D, S,
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(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C) (((unsigned int) (C) <= 'z' - 'a')# define FILE_SYSTEM_PREFIX_LEN(Filename) \ (_IS_DRIVE_LETTER ((Filename)[0]) &&
(Filename)[1] == ':' ? 2 : 0)# ifndef _
GNUPG 1.4.10 unknown:undefined
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(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_PATH_WITH_DIR(P) \ (strchr (P, '/') != NULL ||
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)#endif
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\gdef^^ae{\registered} \gdef^^af{=} } % \gdef^^b0{\te
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*
copyright string * 15: Short copying conditions (with LFs) * 16: Long copying conditions (with LFs) * 17: Optional printable OS name * 18:
Optional thanks list (with LFs) * 19: Bu
Copyright String auf stderr ausgeben
copyright string if( (s=strusage(14)) ) printf("%s\n", s ); /* Licence string. */ if( (s=strusage(10)) ) printf("%s\n", s ); /* copying
conditions */ if( (s=strusage(15))
copyright string in the executable of your product. */

```



```

copyright string in the executable of your product. */struct internal_state {int dummy;}; /* for buggy compilers */
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C) tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if defined
_WIN32 || defined __WIN32__ || defined __C
GnuPG Made Easy (GPGME) 1.13.1 debian:pgpme1.0/1.13.1-2
(C) AC_LINK_IFELSE( AC_LANG_POP) if test "x$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need
for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
(C)/!b go :more /\./!{ N s\n# // b more
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(C) AC_LINK_IFELSE( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need
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Optional thanks list (with LFs) * 19: Bug
copyright string for your usage() you may also hardcode it here. */#ifndef GNUPG_MAJOR_VERSION
Copyright string. if ((s=strusage (14))) writestrings (0, s, "\n", NULL); /* Licence string. */ if( (s=strusage (10)) ) writestrings (0, s, "\n", NULL);
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copyright string to stderr
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(C)# B. (c)
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISSLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISSLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define
FILE_SYSTEM_PREFIX_LEN(P) 0#endfif
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copyright: # Code from module update-copyright-tests: # Code from module useless-if-before-free:

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Copyright (C)", then a list of copyright years,# and then the name of the copyright holder.
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copyright date in# Do the same for the $(sample-test) and the main doc/.texti file.sc_copyright_check:
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copyright-env) xargs
Copyright \ $(export VC_LIST_EXCEPT_DEFAULT=COPYING
copyright{ } \gdefchar \gdefchar^^ab{\guillemetleft{ }} \gdefchar^^ac{\ensuremath{\not{}} \gdefchar^^ad{\-\} \gdefchar^^ae{\registered\symbol{ }}
\gdefchar^^af{\=\{ }} % \gdefchar^^b0{\textdeg
copyright goes to the FSF.
copyright information, for version-etc.
copyright in $( _sc_search_regexp)
copyright in $( _sc_search_regexp)
copyright line is 72.# Set UPDATE_COPYRIGHT_MAX_LINE_LENGTH to use a different length.# 5. Set UPDATE_COPYRIGHT_HOLDER if the
copyright holder is other# than "Free Software Foundation,
copyright: prereq# By default, exclude all variants of COPYING; you can also# add exemptions (such as ChangeLog.* for rotated change logs)# in the
file .x-update-copyright.
copyright program gets found.PATH=$abs_aux_dir:$PATHexport PATH
copyright symbol suitable for this locale, and
copyright target rule# in maint.mk from gnulib's maintainer-makefile module.
copyright-tests
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1compare - $TMP <<EOF || exit 1
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1compare - $TMP <<EOF || exit 1 Undisturbed
text.dnl Undisturbed text.
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1tr @ '\015' > $TMP-exp <<\EOF
copyright $TMP 1>compare /dev/null $TMP-stdout || exit 1compare - $TMP-stderr <<EOF || exit 1$TMP: warning: copyright statement not
foundEOFcompare - $TMP <<EOF || exit 1
copyright $TMP.compare /dev/null $TMP-stdout || exit 1compare /dev/null $TMP-stderr || exit 1compare - $TMP.star <<EOF || exit 1
copyright $TMP.compare /dev/null $TMP-stdout || exit 1compare - $TMP-stderr <<EOF || exit 1$TMP.4: warning: copyright statement not found$TMP.5:
warning: copyright statement not foundEOFcompare
copyrightupdate-copyright:
copyright \# verify \# version-etc-fsf \# wcrtoomb \# wctob \# xalloc
copyright year.
copyright year intervals in a reformatted FSF copyright statement# are expanded instead.# If UPDATE_COPYRIGHT_USE_INTERVALS=2, convert
a sequence with gaps# to the minimal containing
copyright year intervals. (See "Environment variables"# below.)
copyright year is 2 or 4 digits, and years are separated by# commas or dashes. Whitespace may appear after commas.
copyright year lists in your project.# If you have an additional project-specific rule,
copyright year list to include the current year.
copyright years (such as 90, 1991, 1992-2007, 2008) in a# reformatted FSF copyright statement is collapsed to a single# interval (such as 1990-
2008). If unset or set to 0, all existing
(C)" takes one of the following forms or is omitted# entirely:
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C)" to the copyright symbol
(C) (((unsigned int) (C) <= 'z' - 'a')# define FILE_SYSTEM_PREFIX_LEN(Filename) \ (_IS_DRIVE_LETTER ((Filename)[0]) &&
(Filename)[1] == ':' ? 2 : 0)# ifndef _
©);my$holder ||= 'Free Software Foundation, Inc.';my $prefix_max = 5;my $margin = $ENV{UPDATE_COPYRIGHT_MAX_LINE_LENGTH};!$margin ||
$margin !~ m/\^d+$/ and $margin = 72;
GNU tar 1.32 gnu:tar:1.32
(C) add (1,#endif
(C) add (1, fputc (C, p))#else
(C) __btowc (C)# else
(C) btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef _LIBC# define MEMPCPY(D, S, N) __mempcpy (D, S,

```



```

N)# else# if HAVE_MEMPCPY# define MEMPCPY(D
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define
FILE_SYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) ==#else# define FILE_SYSTEM_PREFIX_LEN(Filename) 0
(C) ((C) ==#endif
(C) (C)# ifdef _LIBC# define STRLEN(S) __wcslen (S)# define STRCAT(D, S) __wcscat (D, S)# define MEMPCPY(D, S, N) __wmempcpy (D, S,
N)# else# define STRLEN(S) wcslen (S)# defin
(C) is nonzero if the unsigned char C can safely be given as an argument to <ctype.h> macros like `isspace'. */#if STDC_HEADERS
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copyright goes to the FSF.
copyright information, for version-etc.
copyright symbol suitable for this locale, and
copyright year.
COPYRIGHT YEARS
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C)" to the copyright symbol
(C) (((unsigned int) (C) <= 'z' - 'a')# define FILE_SYSTEM_PREFIX_LEN(FileName) \ (_IS_DRIVE_LETTER ((FileName)[0]) &&
(FileName)[1] == ':' ? 2 : 0)# ifdef _
GNU tar 1.32 opensuse:tar/1.32-1.2/aarch64
(C) add (1,#endif
(C) add (1, fputc (C, p))#else
(C) __btowc (C)# else
(C) btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef _LIBC# define MEMPCPY(D, S, N) __mempcpy (D, S,
N)# else# if HAVE_MEMPCPY# define MEMPCPY(D
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define
FILE_SYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) ==#else# define FILE_SYSTEM_PREFIX_LEN(FileName) 0
(C) ((C) ==#endif
(C) (C)# ifdef _LIBC# define STRLEN(S) __wcslen (S)# define STRCAT(D, S) __wcscat (D, S)# define MEMPCPY(D, S, N) __wmempcpy (D, S,
N)# else# define STRLEN(S) wcslen (S)# defin
(C) is nonzero if the unsigned char C can safely be given as an argument to <ctype.h> macros like `isspace'. */#if STDC_HEADERS
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copyright goes to the FSF.
copyright information, for version-etc.
copyright symbol suitable for this locale, and
copyright year.
COPYRIGHT YEARS
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C)" to the copyright symbol
(C) (((unsigned int) (C)
<= 'z' - 'a')# define FILE_SYSTEM_PREFIX_LEN(FileName) \
(_IS_DRIVE_LETTER ((FileName)[0]) &&
(FileName)[1] == ':' ? 2 : 0)# ifndef _
GNU tar 1.32 opensuse:tar/1.32-3.4/arch64
(C) add (1,#endif
(C) add (1, fputc (C, p))#else
(C) __btowc (C)# else
(C) __btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef _LIBC# define MEMPCPY(D, S, N) __mempcpy (D, S,
N)# else# if HAVE_MEMPCPY# define MEMPCPY(D
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \
&& (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define
FILE_SYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) ==#else# define FILE_SYSTEM_PREFIX_LEN(FileName) 0
(C) ((C) ==#endif
(C) (C)# ifdef _LIBC# define STRLEN(S) __wcslen (S)# define STRCAT(D, S) __wcscat (D, S)# define MEMPCPY(D, S, N) __wmempcpy (D, S,
N)#
else# define STRLEN(S) wcslen (S)# defin
(C) is nonzero if the unsigned char C can safely be given as an argument to <ctype.h> macros like 'isspace'. */#if STDC_HEADERS
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 copyright goes to the FSF.  
 copyright information, for version-etc.  
 copyright symbol suitable for this locale, and  
 copyright year.  
 COPYRIGHT YEARS  
 (C) tests whether C is a directory separator character. IS\_ABSOLUTE\_PATH(P) tests whether P is an absolute path. If it is not,  
 it may be concatenated to a direct  
 (C)" to the copyright symbol  
 (C) (((unsigned int) C) <= 'z' - 'a')# define FILE\_SYSTEM\_PREFIX\_LEN(Filename) \ (\_IS\_DRIVE\_LETTER ((Filename)[0]) &&  
 (Filename)[1] == ':' ? 2 : 0)# ifndef \_  
 GnuTLS 3.6.14 gnu:gnutls:3.6.14  
 (C) AC\_LINK\_IFELSE( AC\_LANG\_POP)) if test yes != "\$lt\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
 belf CFLAGS=\$SAVE\_CFLAGS fi ;:\*-\*so  
 (C) | ('a# define HAS\_DEVICE(Filename) \ (\_IS\_DRIVE\_LETTER ((Filename)[0]) && (Filename)[1] == ':')# define  
 FILE\_SYSTEM\_PREFIX\_LEN(Filename) (HAS\_DEVICE (Filename) ? 2 : 0)# ifdef \_\_CYGWIN\_\_#  
 (C)!/b go :more \./!{ N s|\n# | | b more

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(C) ((C) ==# define HAS_DEVICE(Filename) ((void) (Filename), 0)# define FILE_SYSTEM_PREFIX_LEN(Filename) ((void) (Filename), 0)# define
FILE_SYSTEM_DRIVE_PREFIX_CAN_BE_RELATIVE 0# define IS_ABSOLU
(C) ((C) == /* Internal macro: Tests whether a character is a drive letter. */# define _IS_DRIVE_LETTER(C) \ (((C) >= 'A' && (C) <= 'Z') || ((C) >=
'a' && (C) <= 'z')) /* Help the compiler o
(C)" if AC_PROG_CC has    dnl not yet been invoked.    _AC_COMPILE_IFELSE(    [AC_LANG_PROGRAM([[    #ifdef __clang__    barfbar
#endif
]],[[ ]])
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Copyright \(\C\) in_vc_files=$(sample-test) \
Copyright (C) Jeremy Allison 2012
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Copyright)); coerce_it(VOIDP( coerce_it(VOIDP(&(opts->pzFullVersion))); coerce_it(VOIDP(&(opts->pzUsageTitle)));
coerce_it(VOIDP(&(opts->pzExplain))); coerce_i
copyright{copyright \do\dots{...}% \do\enddots{...}% \do\equiv{==}% \do\error{error}% \do\euro{euro}% \do\expansion{==>}%
\do\geq{>=}% \do\guillemetleft{<<}% \do\guillemetright{>>}%
Copyright @copyright\in_vc_files=$(texi) \
Copyright (C) Stefan Metzmacher 2012
copyright date in# Do the same for the $(sample-test) and the main doc/.texi file.sc_copyright_check:
copyright{}}% \DeclareUnicodeCharacter \DeclareUnicodeCharacter{00AB}{\guillemetleft{}}% \DeclareUnicodeCharacter{00AC}{\ensuremath\lnot}%
\DeclareUnicodeCharacter{00AD}{\-\}% \DeclareUnicode
copyright \definedummyword\registersymbol \definedummyword\dots \definedummyword\enddots \definedummyword\entrybreak
\definedummyword\equiv \definedummyword\error \definedummyword\euro
copyright-env) xargs
Copyright \ $$ (export VC_LIST_EXCEPT_DEFAULT=COPYING
copyright{}} \gdefchar \gdefchar^^ab{\guillemetleft{}} \gdefchar^^ac{\ensuremath\lnot} \gdefchar^^ad{\-\} \gdefchar^^ae{\registersymbol{}}
\gdefchar^^af{\={}} % \gdefchar^^b0{\textdeg
Copyright (genshellopt_opt_strs
copyright in $_sc_search_regexp)
copyright in $_sc_search_regexp)
copyright note, * depending on whether the layout is GNU standard form or not. */ if ( (od->fOptState & OPTST_ARG_OPTIONAL) && (od-
>optArg.argString != NULL) && (od->optA
copyright\n" "\t'n' - version and full copyright notice\n0"/ * 1060 */ "%s error: the '%s' and '%s' options conflict\n0"/ * 1107 */ "%s: The '%s'
option has been disabled.\0"/ * 1146
copyright\n" "\t'n' - version and full copyright notice\n");#line 58 "../check.c" puts_(""%s error: the '%s' and '%s' options conflict\n");#line 187
"../find.c" puts_(""%s: The '%s' op
Copyright != NULL) fputs(opts->pzCopyright, fp);
Copyright != NULL) fputs(o->pzCopyright, fp);
copyright: prereq# By default, exclude all variants of COPYING; you can also# add exemptions (such as ChangeLog.* for rotated change logs)# in the
file .x-update-copyright.
Copyright */ puts_("genshellopt 1\n\
copyright text
Copyright; /// the full copyright notice char const * const pzCopyNotice; /// a string with the program name, project name and version
char const * const pzFullVersio
copyrightupdate-copyright:
copyright-year: $(AM_V_at)$(SED) -i "s/\`"2000-[0-9]{4,}\`"/\`"2000-`date +%Y`"/g" src/args-std.def.in $(AM_V_at)$(SED) -i "s/ 2001-[0-9]{4,}\`" /
2001-`date +%Y` /g" doc/gnutls.texi
Copyright year in tools and docs
copyright year lists in your project.# If you have an additional project-specific rule,
copyright year range specified as YYYY-ZZZZ in this package
(C) tests whether C is a directory separator character. HAS_DEVICE(Filename) tests whether Filename contains a
device
GnuTLS 3.6.14 opensuse:gnutls-debugsource/3.6.14-1.1/aarch64
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(C) AC_LINK_IFELSE( AC_LANG_POP) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;;*-so
(C) add (1,#endif
(C) add (1, fputc (C, p))#else
(C)!b go :more /\.!{ N s\n# | | b more
(C)((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define
FILE_SYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) ==#else# define FILE_SYSTEM_PREFIX_LEN(Filename) 0
(C) ((C) ==#endif
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Copyright)); coerce_it(VOIDP(
coerce_it(VOIDP(&(opts->pzFullVersion))); coerce_it(VOIDP(&(opts->pzUsageTitle)));
coerce_it(VOIDP(&(opts->pzExplain))); coerce_i
copyright{copyright \defglyph\dots{...}% \defglyph\enddots{...}% \defglyph\equiv{==}% \defglyph\error{error}% \defglyph\euro{euro}%
\defglyph\expansion{==>}% \defglyph\geq{>=}% \defgly
Copyright @copyright\in_vc_files=$(texti) \
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copyright date in# Do the same for the $(sample-test) and the main doc/.texi file.sc copyright_check:
copyright{}}% \DeclareUnicodeCharacter \DeclareUnicodeCharacter{00AB}{\guillemetleft{}}% \DeclareUnicodeCharacter{00AC}{\ensuremath\lnot}%
\DeclareUnicodeCharacter{00AD}{\-\}% \DeclareUnicode
copyright \definedummyword\registersymbol \definedummyword\dots \definedummyword\enddots \definedummyword\entrybreak
\definedummyword\equiv \definedummyword\error \definedummyword\euro
copyright-env = UPDATE_COPYRIGHT_USE_INTERVALS=1
copyright-env) xargs
Copyright \ $$ (export VC_LIST_EXCEPT_DEFAULT=COPYING
copyright{}} \gdefchar \gdefchar^^ab{\guillemetleft{}} \gdefchar^^ac{\ensuremath\lnot} \gdefchar^^ad{\-\} \gdefchar^^ae{\registersymbol{}}
\gdefchar^^af{\=\{}} % \gdefchar^^b0{\textdeg
Copyright (gensheloft_opt_strs
copyright in $( _sc_search_regexp)
copyright in $( _sc_search_regexp)
copyright note, * depending on whether the layout is GNU standard form or not. */ if ( (od->fOptState & OPTST_ARG_OPTIONAL) && (od-
>optArg.argString != NULL) && (od->optA
copyright\n" "\t'n' - version and full copyright notice\n0"/ * 1060 */ "%s error: the '%s' and '%s' options conflict\n0"/ * 1107 */ "%s: The '%s'
option has been disabled.\0"/ * 1146
copyright\n" "\t'n' - version and full copyright notice\n");#line 58 "../check.c" puts(_("%s error: the '%s' and '%s' options conflict\n"));#line 217
"../find.c" puts(_("%s: The '%s' op
Copyright != NULL) fputs(opts->pzCopyright, fp);
Copyright != NULL) fputs(o->pzCopyright, fp);
copyright: prereq# By default, exclude all variants of COPYING; you can also# add exemptions (such as ChangeLog.* for rotated change logs)# in the
file .x-update-copyright.
Copyright */ puts_("gensheloft 1\n\
copyright text
Copyright; /// the full copyright notice char const * const pzCopyNotice; /// a string with the program name, project name and version
char const * const pzFullVersio
copyrightupdate-copyright:
copyright year lists in your project.# If you have an additional project-specific rule,
copyright year range specified as YYYY-ZZZZ in this package
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C) ((unsigned int) (C) <= 'z' - 'a')# define FILE_SYSTEM_PREFIX_LEN(FileName) \ (_IS_DRIVE_LETTER ((FileName)[0]) &&
(FileName)[1] == ':' ? 2 : 0)# ifndef _
GNU troff 1.22.4 opensuse:groff-full/1.22.4-7.4/aarch64
(C) ((C) == DIRECTORY_SEPARATOR)#endif
(C) ((C) ==#endif
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written by James Clark.

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 <string>  
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Copyright: %s\n", STR(rss->channel.copyright)); } else fprintf(stderr, "RSS 0.91 or 2.0 content expected\n");
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copyright) docwrite1(soap, "<tr><td class= if (rss->channel.__size < maxitems) maxitems = rss->channel.__size; for (i = 0; i <
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*dc__date;// RSS 2.0 dc schema element};
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 (C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':'# define  
 IS\_PATH\_WITH\_DIR(P) \ (strchr (P, '/') != NULL ||  
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(C) tests whether C is a directory separator character. IS\_PATH\_WITH\_DIR(P) tests whether P contains a directory specification. \*/#if defined \_\_WIN32 || defined \_\_WIN32\_\_ || defined \_\_C

(C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define

IS\_ABSOLUTE\_PATH(P) (ISLASH ((P)[0]) || HAS\_DEVICE (

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GTK 2.62.6 opensuse:glib2/2.62.6-1.3/s390x

(C) ((C) == DIRECTORY\_SEPARATOR)#endif

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guint64 ui64; const gchar \*s, \*save;  
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 copyright; char  
 copyright){ char  
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 copyright: \* \* Copyright information for the program. \*/ props[PROP\_COPYRIGHT] = g\_param\_spec\_string ("copyright",  
 Copyright (C) year name of author  
 copyright-format/1.0/  
 copyright);GDK\_AVAILABLE\_IN\_ALL  
 copyright); g\_free (about->comments);  
 copyright (GtkAboutDialog  
 copyright (GtkAboutDialog  
 copyright (GtkAboutDialogGDK\_AVAILABLE\_IN\_ALL  
 copyright (GtkAboutDialog{ g\_return\_val\_if\_fail (GTK\_IS\_ABOUT\_DIALOG (about), NULL);  
 COPYRIGHT: gtk\_about\_dialog\_set\_copyright (about, g\_value\_get\_string (value)); break; case PROP\_LOGO:  
 copyright); gtk\_label\_set\_markup (GTK\_LABEL (about->copyright\_label), copyright\_string); g\_free (copyright\_string);



```

COPYRIGHT: g_value_set_string (value, about->copyright); break; case PROP_COMMENTS:
Copyright information for the program"), NULL, GTK_PARAM_READWRITE | G_PARAM_EXPLICIT_NOTIFY);
copyright notice and this permission notice shall be included * in all copies or substantial portions of the Software. * * THE SOFTWARE IS PROVIDED
"AS IS", WITHOUT WARRANTY OF ANY KIND, EXPRESS
copyright = NULL;
copyright != NULL) { copyright_string = g_markup_printf_escaped ("<span size=\"small\">%s</span>",
Copyright Owen Taylor 4/9/97
copyright remains in the source code and all documentation * * which in turn borrows parts of the algorithm from uwm
copyright © SOUND RECORDING COPYRIGHT { 0x0afc, 0x2038 }, /* caret ^ CARET */ { 0x0afd, 0x201a }, /* singlelowquotemark
, SINGLE LOW-9 QUOTATION MARK */ { 0x0afe,
copyright string. *
Copyright string"),
copyright string. The string is owned by the about * dialog and must not be modified. */const char *
copyright string to display in the about dialog. * This should be a short string of one or two lines. */void
Copyright: unspecified
©, "name": "copyright" },
gtk-doc 1.32 debian:gtk-doc/1.32-2
(C) AC_LINK_IFELSE( AC_LANG_POP] if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ; *-so
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 (C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test yes != "\$lt\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
 belf CFLAGS=\$SAVE\_CFLAGS fi ;:\*-\*so  
 (C)/!b go :more \./!{ N s|\n# | | b more  
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 GtkHash v1.2 debian:gtkhash/1.2-1  
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 AC\_CACHE\_CHECK([for CFPreferencesCopyAppValue], [gt\_cv  
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 belf CFLAGS=\$SAVE\_CFLAGS fi ;;\*-so  
 gtkterm 0.99.4 debian:gtkterm/0.99.4-2  
 (C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define  
 IS\_ABSOLUTE\_PATH(P) (ISLASH ((P)[0]) || HAS\_DEVICE (P))  
 (C) ((C) ==# define IS\_ABSOLUTE\_PATH(P) ISLASH ((P)[0])# define IS\_PATH\_WITH\_DIR(P) (strchr (P, '/') != NULL)#endif  
 (C) ((C) ==# define IS\_ABSOLUTE\_PATH(P) ISLASH ((P)[0])#endif  
 (C) ((C) == DIRECTORY\_SEPARATOR)#endif  
 (C) ((C) ==#endif  
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 (C) tests whether C is a directory separator character. IS\_ABSOLUTE\_PATH(P) tests whether P is an absolute path. If it is not,  
 it may be concatenated to a direct  
 guile-gnutls 3.7.9 debian:guile-gnutls/3.7.9-1  
 (C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test yes != "\$lt\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
 belf CFLAGS=\$SAVE\_CFLAGS fi ;;\*-\*so  
 (C)/!b go :more /\.!{ N s|\n# || b more  
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 (C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test yes != "\$lt\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
 belf CFLAGS=\$SAVE\_CFLAGS fi ;\*-so  
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(C) tests whether C is a directory separator character. IS\_ABSOLUTE\_PATH(P) tests whether P is an absolute path. If it is not, it may be concatenated to a direct

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(C) (((unsigned int) (C) <= 'z' - 'a')# define FILE\_SYSTEM\_PREFIX\_LEN(Filename) \ (\_IS\_DRIVE\_LETTER ((Filename)[0]) && (Filename)[1] == ':' ? 2 : 0)# ifndef \_

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 (C)# B. (c)



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(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define
FILE_SYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) ==#else# define FILE_SYSTEM_PREFIX_LEN(Filename) 0
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copyright in \$_sc_search_regexp)
copyright in \$_sc_search_regexp)
copyright line is 72.# Set UPDATE_COPYRIGHT_MAX_LINE_LENGTH to use a different length.# 5. Set UPDATE_COPYRIGHT_HOLDER if the
copyright holder is other# than "Free Software Foundation,
copyright: prereq# By default, exclude all variants of COPYING; you can also# add exemptions (such as ChangeLog..* for rotated change logs)# in the
file .x-update-copyright.
Copyright: Sverre Jarp (HP Labs) 2001-2002 * * See deflate.c for c-version * Version 2 - Optimize the outer loop
copyright target rule# in maint.mk from gnulib's maintainer-makefile module.
copyrightupdate-copyright:
copyright \# utimens \# verify \# xalloc \# year2038 \# yesno
copyright year intervals in a reformatted FSF copyright statement# are expanded instead.# If UPDATE_COPYRIGHT_USE_INTERVALS=2, convert
a sequence with gaps# to the minimal containing

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copyright year intervals. (See "Environment variables"# below.)
copyright year is 2 or 4 digits, and years are separated by# commas or dashes. Whitespace may appear after commas.
copyright year lists in your project.# If you have an additional project-specific rule,
copyright year list to include the current year.
copyright year range specified as YYYY-ZZZZ in this packagenote that the range specifies every single year in that closed interval.
copyright years (such as 90, 1991, 1992-2007, 2008) in a# reformatted FSF copyright statement is collapsed to a single# interval (such as 1990-
2008). If unset or set to 0, all existing
(C)" takes one of the following forms or is omitted# entirely:
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C) ((unsigned int) (C) <= 'z' - 'a')# define FILE_SYSTEM_PREFIX_LEN(FileName) \ (_IS_DRIVE_LETTER ((FileName)[0]) &&
(FileName)[1] == ':' ? 2 : 0)# ifndef _
©);my$holder ||= 'Free Software Foundation, Inc.';my $prefix_max = 5;my $margin = $ENV{UPDATE_COPYRIGHT_MAX_LINE_LENGTH};!$margin ||
$margin !~ m/^\d+$/ and $margin = 72;
HarfBuzz 2.6.4 opensuse:harfbuzz/2.6.4-150200.3.6.1/x86_64
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;:*-*so
(C)!/b go :more /\.!{ N s|\n# | | b more
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(C) @f  //!< we could simply set  //!< @code  //!< monadic_compose(g, f)(x) = joker(transform(f(x), g))  //!< @endcode  //!<  //!< and we would be
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(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_PATH_WITH_DIR(P) \ (strchr (P, '/') != NULL ||
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISSLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)#endif
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISSLASH ((P)[0])#endif
(C) ((C) ==# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define FILE_SYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) == DIRECTORY_SEPARATOR)#endif
(C) ((C) ==#endif
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(C) tests whether C is a directory separator character. IS\_ABSOLUTE\_PATH(P) tests whether P is an absolute path. If it is not, it may be concatenated to a direct

(C) tests whether C is a directory separator character. IS\_PATH\_WITH\_DIR(P) tests whether P contains a directory specification. `*/#if defined __WIN32 || defined __WIN32__ || defined __C`

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COPYRIGHT 2020 STMicroelectronics \* \* @file LoRaMacCrypto.c \* @author MCD Application Team \* @brief LoRa MAC layer cryptography implementation  
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COPYRIGHT 2020 STMicroelectronics \* \* @file LmHandler.c \* @author MCD Application Team \* @brief LoRaMAC Layer handling definition  
COPYRIGHT 2020 STMicroelectronics \* \* @file FragDecoder.c \* @author MCD Application Team \* @brief Fragmentation Decoder definition  
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 (C) COPYRIGHT 2020 STMicroelectronics \* @file Applications/LoRaWAN\_FUOTA/LoRaWAN\_End\_Node/readme.txt \* @author MCD Application Team \* @brief This application is a Fuota demo of a LoRa  
 (C) COPYRIGHT 2017 STMicroelectronics \* @file readme.txt \* @brief Secure Engine Core binary generation. \* SE Core binary exports SE

CallGate function entry point for \*  
 (C) COPYRIGHT 2020 STMicroelectronics \* @file Applications/LoRaWAN/LoRaWAN\_End\_Node/readme.txt \* @author MCD Application Team \* @brief This application is a simple demo application soft  
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 definition  
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 for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;  
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 ipset 6.34 opensuse:ipset/6.34-1.1/aarch64  
 (C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test x"\$lt\_cv\_cc\_needs\_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need  
 for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;  
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CFLAGS=\$SAVE\_CFLAGS fi ;:\*-so  
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 new string matching iptables match \* - add functionality  
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 belf CFLAGS=\$SAVE\_CFLAGS fi ;;\*-\*so  
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"../json_tokener.h"int main(void){ json_object *json;
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 "../json\_tokener.h"int main(void){ json\_object \*json;  
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kbd v2.2.0 debian:kbd/2.2.0-1
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belf CFLAGS=$SAVE_CFLAGS fi ;;*-*so
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krb5 1.17.1 fedora:krb5/1.17.1-3.fc32/armv7hl
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Copyright = 0; UINT len = 0; TCHAR sname_version[] = TEXT("FileVersion"); TCHAR sname_copyright[] = TEXT("LegalCopyright"); TCHAR

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COPYRIGHT 1088#define IDC\_STATIC\_NOTICE 1089#define IDC\_STATIC\_RENEW 1090#define IDD\_PASSWORD  
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copyright-file") == 0)      r = get_str_opt(a,      &(iso9660->copyright_file_identifier),      COPYRIGHT_FILE_SIZE,
key, value);      iso9660->opt.copyright_file = r == ARCHIVE_OK;      return (r);      }
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(C) AC_LINK_IFELSE( AC_LANG_POP) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -

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(C)/!b go :more \./!{ N s|\n# | | b more
(C)(FUNCTION, SIGNATURE)# -----# Don't include <ctype.h> because on OSF/1 3.0 it includes# <sys/types.h> which includes
<sys/select.h> which contains a# prototype for
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Copyright File", 0, D_CHAR);    if (r != ARCHIVE_OK)    return (r); /* Abstract File Identifier */ r = set_file_identifier(bp, 740, 776, vdc, a, vdd,
&(iso9660->abstract_file_identifier), "Ab
copyright-file") == 0)    r = get_str_opt(a,    &(iso9660->copyright_file_identifier),    COPYRIGHT_FILE_SIZE,
key, value);    iso9660->opt.copyright_file = r == ARCHIVE_OK;    return (r);    }
Copyright File Identifier    r = set_file_identifier(bp, 703, 739, vdc, a, vdd,    &(iso9660->copyright_file_identifier),
copyright-file",    KEY_STR, iso9660->copyright_file_identifier.s); if (iso9660->opt.iso_level != OPT_ISO_LEVEL_DEFAULT)
set_option_info(&info, &opt, "iso-level",    KEY_INT, iso9660->opt.iso
Copyright Filename.    * This file shall be described in the Root Directory * and containing a copyright statement.    */    unsigned int
copyright_file:1;#define OPT_COPYRIGHT_FILE_DEFAULT    0    /* Not s
copyright-file=<value>    * Type : string, max 37 bytes    * Default: Not specified
copyright/licensing statement at the beginning of the file. If any donot, please let me know and I will rectify it. The following is
copyright; please read the individual source files for details:
copyright status of the individual files;the actual statements in the files are controlling.
copyright <value>    *
libassuan 2.5.3 openssl:libassuan/2.5.3-1.9/armv6hl
(C)    AC_LINK_IFELSE(    AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then    # this is probably gcc 2.8.0, egcs 1.0 or newer; no need
for -belf    CFLAGS="$SAVE_CFLAGS" fi ;;
(C)/!b go :more    \./!{    N    s\n# //    b more
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 \def\geq{>=}% \def\guillemetleft{<<}% \def\guillemetri  
 copyright} \DeclareUnicodeCharacter{00AB}{\guillemetleft} \DeclareUnicodeCharacter{00AD}{\-}  
 \DeclareUnicodeCharacter{00AE}{\registeredsymbol} \DeclareUnicodeCharact  
 copyright \definedummyword\registeredsymbol \definedummyword\dots \definedummyword\enddots \definedummyword\entrybreak  
 \definedummyword\equiv \definedummyword\error \definedummyword\euro  
 copyright} \gdef \gdef \gdef^^ab{\guillemetleft} \gdef^^ac{\\$ \not\$} \gdef^^ad{\-} \gdef^^ae{\registeredsymbol} \gdef^^af{\={}} %  
 \gdef^^b0{\textdegree} \gdef^^b1{\\$ \pm\$} \gdef^^b2{\\$ ^2\$}  
 libassuan 2.5.3 ubuntu:libassuan/2.5.3-7ubuntu2  
 (C) AC\_LINK\_IFELSE( AC\_LANG\_POP) if test x"\$lt\_cv\_cc\_needs\_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need  
 for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;  
 (C)!b go :more \./!{ N s/\n# // b more  
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 \definedummyword\equiv \definedummyword\error \definedummyword\euro  
 copyright-format/1.0/Upstream-Name: libassuanUpstream-Contact: Werner Koch <wk@gnupg.org>Source: <https://gnupg.org/ftp/gcrypt/libassuan/>  
 copyright} \gdef \gdef^^ab{\guillemetleft} \gdef^^ac{\$\notin}\$ \gdef^^ad{\ -} \gdef^^ae{\registeredsymbol} \gdef^^af{\=} %  
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 libatomic-ops v7.6.10 opensuse:libatomic\_ops/7.6.10-2.3/armv6hl  
 (C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test yes != "\$lt\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
 belf CFLAGS=\$SAVE\_CFLAGS fi ;;\*-\*so  
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BOOST\_IOSTREAMS\_RESTRICT undefined, defines the \*  
(C) \* (y = 1. / sin(D)); y \*= D \* sin(lp\_lat); } else x = y = 0.; if (this-  
>m\_proj\_parm.mode ==  
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(C) @f //! we could simply set //! @code //! monadic\_compose(g, f)(x) = joker(transform(f(x), g)) //! @endcode //! //! and we would be happy. It turns out that `flatten` is  
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boost::iostreams::detail::absolute\_path, used for \* deb  
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(C) @f //! @f\$ g : A \to M(B) @f\$, the signature is //! @f\$ //! \mathit{monadic\\_compose} //! : (B \to M(C)) \times (A \to M(B)) \to (A \to M(C)) //! @f\$. //! /  
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 //! To compose them, we obviously can't  
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(C)#else #define BOOST\_SERIALIZATION\_COLLECTION\_TRAITS\_HELPER\_WCHAR(C) \  
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(C) @f //! we could simply set //! @code //! monadic_compose(g, f)(x) = joker(transform(f(x), g)) //! @endcode //! //! and we would be
happy. It turns out that `flatten` is
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NULL pointer, then,// citing the tmpnam(3S)  
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_LINUX_BALLOON_COMPACTION_H#include <linux/pagemap.h>#include <linux/page-flags.h
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ALSA driver
  Copyright (C) 2005 Andreas Oberitter <obi@linuxtv.org> ** based on pluto2.c 1.10 - http://instinct-wp8.no-ip.org/pluto/ * by Dany Salman

```

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Copyright (C) 2004-2005 David Brownell \* \* Periodic scheduling is based on Roman's OHCI code  
Copyright (C) 2007 Chase Douglas <chasedouglas@gmail>  
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AM335x.  
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Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Driver is originally developed by Pavel Sokolov  
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mgr.h>#include <linux/io.h>#include <linux/module.h  
Copyright (c) 2015 Sabrina Dubroca, Hannes Frederic Sowa  
Copyright (C) 2016-2017 Glider bvba \* \* This code drives an LCD module (/dev/lcd), and a keypad (/dev/keypad) \* connected to a parallel printer port.  
Copyright (C) 2016 Cavium Inc. \*/#include <linux/device.h>#include <linux/dma-mapping.h>#include <linux/interrupt.h>#include  
<linux/mmc/mmc.h>#include <linux/module.h>#include <linux/of.h>#include  
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Copyright (C) 2011 Linaro Limited. \* Written by: Vincent Guittot \* \* based on arch/sh/kernel/topology.c  
(C) \* in woken\_wake\_function() such that either we see the wait condition \* being true or the store to wq\_entry->flags in woken\_wake\_function()  
\* follows ours in the coherence order. \*/ smp\_s  
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\*\*\*\*\*  
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Removed race in async swapping. 14.4.1996. Bruno H  
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<unistd.h>#include <sys/syscall.h>  
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<linux/kernel.h>#include <linux/signal.h>#include <linux  
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Motorola Linux kernel: \*  
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Copyright (c) 2001 - 2008 Paul Mundt <lethal@linux-sh.org> \* \* This driver is mostly based on the excellent amifb and vfb sources. It uses \* an odd  
scheme for converting hardware values to/from f  
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Copyright (C) 2007 MIPS Technologies, Inc. \* written by Ralf Baechle (ralf@linux-mips.org) \*/#include <linux/kernel.h>#include  
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kswapd\_ctl limits, and swap out as many pages as neede  
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<jic23@kernel.org> for valuable inputs. \* Support for HMC5883 and HMC58  
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Copyright (C) 2002,2003 Suresh Siddha <suresh.b.siddha@intel.com> \*/#ifndef \_\_ASM\_IA64\_GCC\_INTRIN\_H#define \_\_ASM\_IA64\_GCC\_INTRIN\_H  
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Copyright (C) 2002 Richard Hirst (rhirst with parisc-linux.org)  
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<linux/slab.h>#include <linux/kobject.h>#include <linux/dm-ioct.h>#inclu  
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(C) 2000,2001,2002,2003,2004 Omnikey AG \*  
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Copyright (C) 1998 \* Daniel Pirkel <daniel.pirkel@email.cz> \* Charles University, Faculty of Mathematics and Physics \* \* ext2 - inode (block) bitmap caching inspired

Copyright (C) 2011 LAPIS Semiconductor Co., Ltd. \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/kernel.h>#include <linux/module.h>#include <linux/pci.h>#include <linux/delay.h>#in

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Spinlocks: Mohamed Abbas  
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using prep/chrp/pmac functions by Dan (dmalek@jlc.  
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 Copyright (c) 2017-2018 Covalent IO, Inc. http://covalent.io#include <stddef.h>#include <string.h>#include <linux/bpf.h>#include  
 <linux/if\_ether.h>#include <linux/if\_packet.h>#include <linux/ip.  
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 <linux/if\_ether.h>#include <linux/if\_vlan.h>#include <lin  
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 <unistd.h>#include <sys/syscall.h>#include <sched.h>#include <sys/wa  
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 dummy\_cs.c \* driver of the Linux PCMCIA Card Services package  
 COPYRIGHT; ucontrol->value.iec958.status IEC958\_AES1\_CON\_ORIGINAL; return 0;}  
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 \*/  
 Copyright (C) 2002 Russell King. \*/#include <linux/module.h>#include <linux/serio.h>#include <linux/errno.h>#include <linux/interrupt.h>#include  
 <linux/ioport.h>#include <linux/device.h>#inclu

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<linux/net.h>#include <linux/completion.h>#include <  
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Copyright (C) 1999 Niibe Yutaka \*/  
Copyright (C) 2007 John Williams \* \* Reasonably optimised generic C-code for memcpy on Microblaze \* This is generic C code to do efficient, alignment-aware memcpy.  
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Added MPI26\_SCSIIO\_IOFLAGS\_ESCAPE\_PASSTHROUGH. \*

Added MPI2\_SEP\_REQ\_SLOTSTATUS\_DEV\_OFF a

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<linux/timer.h>#include <net/ax25.h>#include <linux/skbuff.h>  
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<linux/io.h>#include <asm/pci-bridge.h>  
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(C) 2015-2016 Mathieu Poirier <mathieu.poirier@linaro.org> \* \* Guaranteed timing and support for various packet type coming from the \* generic STM  
API by Chunyan Zhang  
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<linux/sched.h>#include <linux/sunrpc/addr.h>  
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<linux/uwb/umc.h>#include <linux/usb.h>  
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<linux/io.h>#include <linux/pci.h>  
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<linux/sched/task\_stack.h> /\* task\_stack\_\*(), ... \*/#include <linux/kdebug.h>  
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Copyright (C) 2006 Thomas Bogendoerfer \*/#include <linux/delay.h>#include <linux/init.h>#include <linux/interrupt.h>#include <linux/irq.h>#include

```

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  Copyright (c) by Jaroslav Kysela <perex@perex.cz> * Routines for control of MPU-401 in UART mode * * Modified for the Aureal Vortex based
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<linux/xattr.h>#include <linux/mount.h>#include
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<linux/genalloc.h>#include <linux/io.h>#include <linux/mm.h>#include <
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Copyright (C) by Kai Svahn <kai.svahn@nokia.com> * * Changes for interrupt handling and clean-up by * Tony Lindgren <tony@atomide.com> and
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 <linux/kernel.h>#include <sys/syscall.h>#include <asm/  
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\*cdo = cdi->ops;  
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<linux/ioport.h>#include <linux/sched.h>#include <linux/slab.h>#in  
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```

<linux/ssb/ssb_regs.h>#include <linux/bcma/bcma.h>#include <linux/bcma
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Copyright (C) 2004, 06 Ralf Baechle <ralf@linux-mips.org> */#include <linux/interrupt.h>#include <linux/kernel.h>#include <linux/sched.h>#include
<linux/kernel_stat.h>#include <asm/io.h>#includ
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Copyright (c) 2019 Facebook#define STACK_MAX_LEN 600/* clang will not unroll the loop 600 times. * Instead it will unroll it to the amount it deemed *
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Copyright 1999-2007 H. Peter Anvin - All Rights Reserved  
Copyright (C) 2005 IBM \* \* Entry and exit function for SPU-side of the context restore \* sequence. Sets up an initial stack frame, then branches to \*

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by Henning Karlsen  
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with an index size of 2^64. A sparsebit arr  
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<linux/socket.h>#include <linux/in.h>#include <linux/kernel.h>#include <  
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 migration without losing interrupts for iosapic \* archi  
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 \*/extern void sh\_bios\_console\_write(const char \*  
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<linux/module.h>#include <linux/timer.h>#include <linux/memcontrol.h
(C) * STORE * smp_wmb() (B) smp_mb() (D) * STORE ->data_head STORE ->data_tail * }
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Jon Sorensen (jds@kom.au.dk)  
 (C) 2006-2012 Patrick McHardy <kaber@trash.net> \* \* TODO: - NAT to a unique tuple, not to TCP source port \* (needs netfilter tuple reservation)  
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 Copyright 2002 OProfile authors \* @remark Read the file COPYING \* \* @author John Levon  
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 Copyright (C) 1993 Pascal Haible, Bruno Haible \* \* sysv/inode.c  
 Copyright (C) 2019, Dell Inc \* \* The PCIe Link Bandwidth Notification provides a way to notify the \* operating system when the link width or data rate changes. This \* capability is required for a  
 COPYRIGHT) snd\_iprintf(buffer, " Non-Copyright"); if (digi1 & AC\_DIG1\_NONAUDIO) snd\_iprintf(buffer, " Non-Audio"); if (digi1 & AC\_DIG1\_PROFESSIONAL) snd\_iprintf(buffer, " Pro"); if (digi1 & A  
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 Copyright (C) 2002, 2004, 2007 by Ralf Baechle <ralf@linux-mips.org> \*/#ifndef \_\_ASM\_MIPS\_MACH\_IP22\_WAR\_H#define  
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(C) 1993, Hamish Macdonald, see mvme147.c for more info \*  
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Exynos5 power-domain dependent clocks  
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Copyright (C) 2006 Hartmut Rick <linux@rick.claranet.de> \* \* Derived from lm78.c and other chip drivers.  
Copyright (C) 2003 Hewlett-Packard Co \* David Mosberger <davidm@hpl.hp.com>: updated for new timer-interpolation infrastructure \*/

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<paul.mundt@nokia.com>.  
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COPYRIGHT); } return pci\_register\_driver()  
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documentation started by NeilBrown <neilb@cse.unsw.edu.au  
Copyright (C) 2000 Takashi iwai <tiwai@suse.de> \*/  
Copyright (C) 2013 Tom Gundersen <teg@jklm.no> \* \* This code registers /sys/firmware/efi{,/,efivars} when EFI is supported, \* allowing the efivarfs to  
be mounted or the efivars module to be loaded.

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 chip->spdif\_bits[0] = SPCS  
 Copyright (C) 2013-15 Synopsys, Inc. (www.synopsys.com)  
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 Copyright unasserted) \* AN = 0 (Audio data) \* P = 0 (Consumer) \*/ emu->spdif\_bits[0] = emu->spdif\_bits[1] =  
 emu->spdif\_bits[2] = SPCS\_CLKACCY\_1000PPM  
 Copyright (C) 1997 Geert Uytterhoeven \* Based on linux/driver/video/pm2fb.c:  
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 detection \* 11/12/01 D.Mosberger Convert get\_cpuinfo() to seq\_fi  
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IEC958\_AES1\_CON\_CATEGORY; ucontrol->value.iec958.status[3] = IEC958\_AES3\_CON  
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<linux/ctype.h>#include <linux/delay.h>#include <linux/firmware.h>  
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Copyright (c) 2011 Cambridge Silicon Radio Limited, a CSR plc group company. \*/#include <linux/bitops.h>#include <linux/log2.h>#include

```

<linux/hrtimer.h>struct sirfsoc_uart_param {    const char *u
    Copyright (c) 2018 Amlogic, inc. * Author: Qiufang Dai <qiuqiang.dai@amlogic.com> */#include <linux/clock-provider.h>#include
<linux/platform_device.h>#include <linux/reset-controller.h>#include <
    Copyright (C) 2018 Linus Walleij * * This module binds to the "arm,vexpress-muxfpga" device on the * Versatile Express configuration bus and sets up
which CLCD instance * gets muxed out on the DVI
    Copyright 2011-2012 Wolfson Microelectronics PLC. * Author: Mark Brown <broonie@opensource.wolfsonmicro.com> */
    Copyright (C) 2016 Gwenhael Goavec-Merou
    Copyright (c) 2019 Facebook#define STACK_MAX_LEN 600#define NO_UNROLL/* clang will not unroll at all. * Total program size is around 2k insns */
    Copyright 2010 Paul Mackerras, IBM Corp. <paulus@au1.ibm.com> */
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    Copyright (c) 2017 Huawei Corporation.*/static struct hnae3_client client;
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    Copyright (C) 2019 Dell Inc * by Narendra K <Narendra.K@dell.com> * * System firmware advertises the address of the RC12 Table via * an EFI
Configuration Table entry. This code retrieves the RC12
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<linux/delay.h>#include <linux/tty.h>#include <linux/t
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<linux/bitmap.h>#include <linux/clk.h>#include <linux/coresight.h>#include <li  
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 <linux/hrtimer.h>#include <linux/jiffies.h>#includ  
 (C) 10#define AC97\_GP\_DRSS\_78 0x0400 /\* LR 7+8 \*/  
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<linux/bio.h>#include <linux/blkdev.h>#include <linux/kmemle  
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 mt9m111 sensor driver  
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 <linux/kernel.h>#include <linux/major.h>#include <linux  
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<net/sock.h>#include <net/af\_vsock.h>  
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understand certain things  
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<asm/export.h>#include <asm/fpregdef.h>#include <asm/mipsregs.h>#i  
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Copyright (C) 2006 Linus Torvalds \* \* Trace facility for suspend/resume problems, when none of the \* devices may be working.  
Copyright 2017 IBM Corp.#ifndef \_\_CHECKER\_\_#define CREATE\_TRACE\_POINTS#include "trace.h"  
Copyright (C) 2003-2004 Oleksij Rempel <bug-track@fisher-privat.net> \* Jean Delvare <jdelvare@suse.de> \* \* The MAX6642 is a  
sensor chip made by Maxim. \* It reports up to t  
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Copyright (C) 2006, Sascha Hauer, Pengutronix  
Copyright (C) 2000 Silicon Graphics, Inc. \*/#ifndef \_ASM\_SN\_IO\_H#define \_ASM\_SN\_IO\_H  
Copyright Management Information (UDF 2.50 3.3.4.5.1.2)struct DVDCopyrightImpUse { \_\_le16 headerChecksum; uint8\_t  
CGMSInfo; uint8\_t dataType; uint8\_t protectionSystemInfo[4];} \_\_packed;  
Copyright (C) 2008 Henrik Rydberg (rydberg@euromail.se)  
Copyright (C) 1999, 2000 Silicon Graphics, Inc. \*/#include <linux/pci.h>#include <asm/paccess.h>#include <asm/pci/bridge.h>#include  
<asm/sn/arch.h>#include <asm/sn/intr.h>#include <asm/sn/sn0/h  
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<errno.h>#include <stdio.h>#include <sys/stat.h>  
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 <linux/errno.h>#include <linux/interrupt.h>#include <  
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Copyright (C) 1991, 1992 Linus Torvalds * * table of configured filesystems
Copyright (C) 2010 Tejun Heo <tj@kernel.org> * * This is the generic async execution mechanism. Work items as are * executed in process
context. The worker pool is shared and * automatically ma
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<linux/fs.h>#include <asm/sgialib.h>
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<linux/in.h>#include <linux/kernel.h>#include <linux/tim
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<time.h>#include <bpflib/bpflib.h>#include <bpflib/bpflib.h>#inclu
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Copyright (c) 1999 Jakub Jelinek (jakub@redhat.com) * * and additional input from James Simmon's port of Hannu Mallat's tdfx * driver.
Copyright message. * 0.0.17 * Implement Mic and Line in Capture. * 0.0.18 * Add support for mute control on SB Live 24bit (cards w/ SPI DAC) *
* This code was initially based on code fr
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Copyright 2002-2003 by Armin Schindler (mac@melware.de)
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 Copyright (c) 2004 Peter Nelson <rufus-kernel@hackish.org> \* \* Based on the work of:  
 Copyright 2009 Andy Green <andy@warmcat.com> \* \* S3C64XX SROM definitions  
 Copyright (c) 2014-2016 Christoph Hellwig. \*/#include <linux/exportfs.h>#include <linux/iomap.h>#include <linux/genhd.h>#include  
 <linux/slab.h>#include <linux/pr.h>  
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<linux/debugfs.h>#include <linux/list.h>#include <asm  
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actually ENABLE or DISABLE the thing \* once af  
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driver  
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Copyright (C) 2007-2008 Steven Rostedt <srostedt@redhat.com> \* \* future possible enhancements:  
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Copyright (C) 1999,2000 Tigran Aivazian <tigran@veritas.com> \* \* Make the file block allocation algorithm understand the size \* of the underlying  
block device.  
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to support extcon \* (originally switch class is supported)  
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Copyright 1999 by Ralf Spachmann (mel@melware.de)  
Copyright IBM Corp. 2016 \*/#include <linux/kernel.h>#include <asm/processor.h>#include <asm/facility.h>#include <asm/lowcore.h>#include

```

<asm/scdp.h>
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Linus Walleij <linus.walleij@linaro.org>
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(C) " DRIVER_AUTHOR "\n";
Copyright (c) 2013 Samsung Electronics Co., Ltd. * http://www.samsung.com * Author: Andrzej Pietrasiewicz
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Copyright (C) 2000 MIPS Technologies, Inc. * written by Carsten Langgaard, carsten@mips.com */#include <asm/asm.h>#include
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multi session handling. * 1995 Mark Dob
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Copyright (C) 1999-2007 Helge Deller <deller@gmx.de> * * Very basic HP Human Interface Loop (HIL) driver. * This driver handles the keyboard on

```

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(C) 2005-2006 Red Hat Inc \* Alan Cox <alan@lxorguk.ukuu.org.uk> \* \* The MPIIX is different enough to the PIIX4 and friends that we  
give it \* a separate driver. The old ide/pci code handles thi  
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<asm/export.h>#include <asm/regdef.h>  
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Copyright (C) 2008, 2009 Wind River Systems \* written by Ralf Baechle <ralf@linux-mips.org> \*/#include <linux/compiler.h>#include

```

<linux/vmalloc.h>#include <linux/init.h>#include <linux/kernel
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contiguous.h>#include <linux/init.h>#include <linux/genalloc.h>#incl  
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Copyright (C) 2009, 2011 Wolfgang Grandegger <wg@grandegger.com>  
Copyright (c) 2010 Jarod Wilson <jarod@wilsonet.com> \* \* This driver was based on the work of Greg Wickham and Adrian \* Dewhurst. It was  
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 <linux/sched/task\_stack.h>#include <linux/stacktrace.h>#inc  
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Copyright (C) 2010 Samsung Electronics \* Author: Michal Nazarewicz <mina86@mina86.com> \* \* Based on inode.c (GadgetFS) which was:  
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State counting code by Tero Kristo <tero.kristo@nok  
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<sound/dmaengine\_pcm.h>  
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<linux/smp.h>#include <asm/irq\_regs.h>#include <asm/tim  
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KOSAKI Motohiro, \* Leonid Moiseichuk, Mel Gorman, Minchan Kim an  
Copyright (c) by Chris Rankin \* \* This driver was written in part using information obtained from \* the OSS/Free SoundScape driver, written by Hannu  
Savolainen.  
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Copyright 2007, Red Hat Inc. Peter Zijlstra \* GPLv2 \* \* see http://programming.kicks-ass.net/kernel-patches/vma\_lookup/btree.patch  
Copyright (C) 1999 Nicolas Pitre <nico@fluxnic.net> \* \* SA1100 specific tweaks. This is merged into head.S by the linker.  
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<getopt.h>#include <stddef.h>#include <libelf.h>#include  
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<linux/export.h>#include <asm/branch.h>#include <asm/cpu.h  
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 Copyright 2008 DENX Software Engineering, Stefan Roese <sr@denx.de> \* \* L2 cache routines cloned from arch/ppc/syslib/ibm440gx\_common.c which  
 is:

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 Copyright (C) 1996 David S. Miller (davem@davemloft.net) \* \* with a lot of changes to make this thing work for R3000s \* Tx39XX R4k style caches  
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 Copyright (C) 2000-2002 Inside Out Networks  
 Copyright (c) 2004 Red Hat, Inc., James Morris <jmorris@redhat.com>\*/#ifndef \_LINUX\_XATTR\_H#define \_LINUX\_XATTR\_H  
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 aeb@cw.nl - May 1994 \* Restrict VT switching  
 Copyright (C) 1994, 1995 Waldorf Electronics \* Written by Ralf Baechle and Andreas Busse  
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 via sysfs  
 Copyright (C) Terry Dawson VK2KJT (terry@animats.net) \*/#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include  
 <linux/in.h>#include <linux/kernel.h>#include <linux/ti  
 Copyright (C) 2000, 2002-2003 Hewlett-Packard Co \* David Mosberger-Tang <davidm@hpl.hp.com> \* \* Kernel unwind support.



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 <linux/irq.h>#include <linux/spinlock.h>#inc  
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esi_h
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Copyright 2006 Johannes Berg <johannes@sipsolutions.net> * * This is a driver for the toonie codec chip. This chip is present * on the Mac Mini and is
nothing but a DAC.
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COPYRIGHT (1 << 5)#define m_AUDIO_STATUS_ADDITION (3 << 2)#define m_AUDIO_STATUS_CLK_ACCURACY (2 << 0)#define
v_AUDIO_STATUS_NLPCM(n) ((n & 1) << 7)#define AUDIO_N_H 0x3f#define AUDIO_N_M 0
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Copyright (C) 2009 Renesas Technology Corp.  
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Copyright IBM Corp. 2018 \* Author(s): Hendrik Brueckner <brueckner@linux.ibm.com> \* Thomas Richter <tmricht@linux.ibm.com> \*/#ifndef  
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<linux/slab.h>#include <linux/sched/signal.h>#include <linu  
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<linux/kernel.h>#include <linux/interrupt.h>#include <linux/fs.h>#inc  
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(C) SK" ;#endif  
Copyright (C) 2018 Richard Cochran <richardcochran@gmail.com> \*/#ifndef \_LINUX\_MII\_TIMESTAMP\_H#define \_LINUX\_MII\_TIMESTAMP\_H  
Copyright (c) 2017 Facebook#include <stddef.h>#include <stdbool.h>#include <string.h>#include <linux/pkt\_cls.h>#include <linux/bpf.h>#include  
<linux/in.h>#include <linux/if\_ether.h>#include <  
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Copyright (C) 2014 Linaro Ltd. \* Author: Rob Herring <robh@kernel.org> \* \* Based on 8250 earlycon:  
(C) 1999 Rusty Russell (rusty@rustcorp.com.au). \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt  
Copyright (c) 2018 Broadcom Inc  
Copyright (c) 2017-2018 Covalent IO, Inc. http://covalent.io#include <stdio.h>#include <stdlib.h>#include <sys/socket.h>#include  
<sys/ioctl.h>#include <sys/select.h>#include <netinet/in.h>#incl  
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Copyright (C) 1996 Paul Mackerras <paulus@cs.anu.edu.au> \* Low-level exception handlers, MMU support, and rewrite.  
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<sailer@ife.ee.ethz.ch>, \* Abramo Bagnar  
Copyright (C) 2011 Johan Hovold <jhovold@gmail.com> \* \* state machine code inspired by code from Tim Ruetz  
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Copyright (c) 2012 Bryan Schumaker <bjschuma@netapp.com> \*/#include <linux/init.h>#include <linux/module.h>#include  
<linux/nfs4\_mount.h>#include <linux/nfs\_fs.h>#include "delegation.h"#include  
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(C) 1999-2001 Michal Ludvig <michal@logix.cz>  
Copyright (c) 2007 Jiri Slaby <jirislaby@gmail.com> \* \* This code is loosely based on the Linux serial driver, written by \* Linus Torvalds,  
Theodore T'so and others.  
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Copyright (C) 2015-2016 Russell King \* \* Network PHYs can appear on I2C buses when they are part of SFP module. \* This driver exposes these PHYs to  
the networking PHY code, allowing \* our PHY driv  
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Copyright (C) 2011 Nobuhiro Iwamatsu \*  
Copyright (C) 1999 Jakub Jelinek (jakub@redhat.com)  
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Copyright 2013-15 Maxim Integrated Products \*/#include <linux/delay.h>#include <linux/i2c.h>#include <linux/module.h>#include  
<linux/regmap.h>#include <linux/slab.h>#include <linux/cdev.h>#include  
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Copyright (C) 2006 Filip Zyzniewski <filip.zyzniewski@tefnet.pl> \* based on HP Jornada 56x touchscreen driver by Alex Lange  
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<linux/perf\_event.h>#include <asm/kvm\_hyp.h>  
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(C) 1993 Ray Burr - Amiga FFS filesystem. \* \* Please send bug reports to: hjw@zvw.de  
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Copyright (C) 2011 Tobias Klauser <tklauser@distanz.ch> \* \* Based on cpuinfo.c from microblaze  
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Copyright IBM Corp. 2019 \* Author(s): Hendrik Brueckner <brueckner@linux.ibm.com> \*/#define KMSG\_COMPONENT "cpum\_cf\_common"#define  
pr\_fmt(fmt) KMSG\_COMPONENT ": " fmt  
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the property of their respective owners.  
Copyright (C) 2005-2006 Intel Corporation \* Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com> \* \* The HWA driver is a simple layer that forwards  
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<linux/platform\_device.h>#include <linux/delay.h>#include <lin  
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<linux/preempt.h>#include <linux/export.h>#include <linux/string  
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Copyright (C) 2006 Red Hat, Inc., Ingo Molnar <mingo@redhat.com> \*/#include <linux/rwsem.h>#include <linux/mutex.h>#include  
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Copyright (C) 1996 Mike Shaver (shaver@zeroknowledge.com) \*/#include <linux/mm.h>#include <linux/sysctl.h>#include <linux/init.h>#include  
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Copyright (C) 1997 David S. Miller (davem@caip.rutgers.edu) \* \* To put things into perspective, consider sparc64 with a few PCI controllers. \* Each type  
would have an own structure, with instances  
Copyright (C) 2011-2013 Gabor Juhos <juhosg@openwrt.org> \* \* This file was derived from: include/asm-mips/cpu-features.h  
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Advanced RISC Machine (ARM) \* Language C or ARM Assembly  
Copyright (C) Jonathan Naylor G4KLX (g4klx@g4klx.demon.co.uk) \*/#include <linux/types.h>#include <linux/slab.h>#include <linux/socket.h>#include  
<linux/timer.h>#include <net/ax25.h>#include <li  
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driver is based on the \* Rockchip version from rock  
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copyright information. \* 03-16-15 02.00.10 Updated for MPI v2.6. \* Added MPI2\_SATA\_PT\_REQ\_PT\_FLAGS\_FPDMA. \* -----

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Copyright 2000, 2001, 2002, 2003  
Copyright (C) 1996 David S. Miller (davem@caip.rutgers.edu) \* \* Calls to memset returns initial %0. Calls to bzero returns 0, if ok, and \* number of  
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 <linux/string.h>#include <linux/kernel.h>#include <li  
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 <linux/interrupt.h>#include <linux/export.h>#in  
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 <linux/module.h>#include <linux/init.h>  
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 <linux/init.h>#include <linux/serial\_core.h>  
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 Copyright (C) 2006-2008 Michael Hennerich, Analog Devices Inc. \* \* Description: AD7877 based touchscreen, sensor (ADCs), DAC and GPIO driver \*  
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 Copyright (C) 2006-2007 Bernhard Kaindl <bk@suse.de> \* \* Derived from drivers/ieee1394/ohci1394.c and arch/x86/kernel/early-quirks.c \* this file  
 has functions to:  
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 <linux/irqdomain.h>#include <linux/interrupt.h>#include  
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 <linux/mtd/partitions.h>#include <linux/etherdevice.h>#include  
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Copyright 2014 IBM Corp. \* Author(s): Alexander Yarygin <[yarygin@linux.vnet.ibm.com](mailto:yarygin@linux.vnet.ibm.com)> \*/  
Copyright 2015 Linaro Ltd. \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/slab.h>#include <linux/sizes.h>#include <linux/cdev.h>#include <linux/fs.h>#include <linux/idr.h>  
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<[cornelia.huck@de.ibm.com](mailto:cornelia.huck@de.ibm.com)> \* \* Hotplug & misc device support: Joc  
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<[simon.kallweit@intefo.ch](mailto:simon.kallweit@intefo.ch)> \* \* Bosch C\_CAN controller is compliant to CAN  
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Copyright (c) 2016 Samsung Electronics Co., Ltd. \* Author : Chanwoo Choi <[cw00.choi@samsung.com](mailto:cw00.choi@samsung.com)> \* \* This driver support Exynos Bus frequency feature by using \* DEVFREQ framework and is based on d  
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Copyright (C) 2013 Luis Alves <[ljalvs@gmail.com](mailto:ljalvs@gmail.com)> July, 6th 2013 First release based on cx24116 driver by:  
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\_\_le32 predecessorVolDescSeqLocation; \_\_le16 flags; uint8\_t reserve  
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<linux/string.h>  
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Copyright (c) 1999-2000 Takashi iwai <tiwai@suse.de> \*/  
Copyright (C) 2003, 04, 07 Ralf Baechle (ralf@linux-mips.org) \*/#ifndef \_\_ASM\_MACH\_SIBYTE\_CPU\_FEATURE\_OVERRIDES\_H#define  
\_\_ASM\_MACH\_SIBYTE\_CPU\_FEATURE\_OVERRIDES\_H  
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(C) Copyright 1999 Gregory P. Smith \* \* PCI Bus Glue  
Copyright IBM Corp. 2000, 2008 \* Author(s): Utz Bacher <utz.bacher@de.ibm.com> \* Jan Glauber <jang@linux.vnet.ibm.com> \* 2.6 cio  
integration by Cornelia Huck <cornelia.huck@de.ibm.com> \*/#i  
Copyright (C) 2002-2003 MontaVista Software, Inc. \* \* Update for Linux-2.6 and cache flush improvements  
(c) Copyright 2000 Oleg Drokin <green@crimea.edu> \* \* 27/11/2000 Initial release  
Copyright (c) 1999 ITConsult-Pro Co. <info@itc.hu>  
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 Copyright (c) 2014 Intel Corporation. All rights reserved  
 (C) Copyright 1999 Gregory P. Smith (from usb-ohci.c)  
 Copyright (C) 2009 Steven Rostedt <ststedt@redhat.com> \*/#include <linux/ring\_buffer.h>#include <linux/completion.h>#include  
 <linux/kthread.h>#include <uapi/linux/sched/types.h>#include <linux/  
 Copyright (C) 1991, 1992 Linus Torvalds \* 1995 Jay Estabrook \* \* A note on using in\_atomic() in here: We can't handle console \*  
 calls from non-schedulable context due to our USB-dependend  
 Copyright 1999-2000 Jeff Garzik \* \* Contributors: \*  
 Copyright (C) 2013 Lubomir Rintel <lkundrak@v3.sk> \*  
 Copyright (C) 1998, 1999, 2001, 2003 Hewlett-Packard Co \* David Mosberger-Tang <davidm@hpl.hp.com>  
 Copyright (C) 2004 Alex Aizman  
 Copyright (C) 2005-2006 Intel Corporation \* Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com> \* \* How transfers work: get a buffer, break it up  
 in segments (segment \* size is a multiple of the  
 Copyright (C) 2018 \* Author: Jeff LaBundy <jeff@labundy.com> \* \* 7-bit I2C slave address: 0x13  
 Copyright (C) 2001 IBM Corp.  
 Copyright (c) International Business Machines Corp., 2000,2002  
 Copyright (C) 2010 Henrik Rydberg <rydberg@euromail.se> \* \* Based on hdaps.c driver:  
 Copyright (C) 1996,1997 Jakub Jelinek (jj@sunsite.mff.cuni.cz) Pete Zaitcev <zaitcev@yahoo.com> endian fixes for cross-compiles, 2000.  
 Copyright (C) 2003 by James.Bottomley@HansenPartnership.com \*/  
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 Copyright (C) 2005 Mike Isely <isely@pobox.com> \*/#ifndef \_\_PVRUSB2\_IOREAD\_H#define \_\_PVRUSB2\_IOREAD\_H  
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 (C) Carnegie Mellon University  
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 (C) INESC 1999 \*/  
 Copyright (C) 1994 by Alan Cox (Modularised it) \* LPCAREFUL, LPABORT, LPGETSTATUS added by Chris Metcalf, metcalf@lcs.mit.edu \* Statistics and  
 support for slow printers by Rob Janssen, rob@knoware.n  
 Copyright (C) 1994-2000 Algorithmics Ltd. \* \* Kevin D. Kissell, kevink@mips.com and Carsten Langgaard, carstenl@mips.com  
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 COPYRIGHT | IEC958\_AES0\_CON\_EMPHASIS\_5015; ctrl->ch\_status[1] = IEC958\_AES1\_CON\_DIGDIGCONV\_ID; ctrl->ch\_status[2]  
 = 0x00; ctrl->ch\_status[3] = IEC958\_AES3\_CON\_FS\_44100 | IEC958\_AES3  
 Copyright (C) 2009, Red Hat Inc.  
 Copyright (C) 2015 EMAC Inc. \* \* Author: Lauri Leukkunen <lauri.leukkunen@nokia.com> \* based on TSC2301 driver by Klaus K. Pedersen  
 <klaus.k.pedersen@nokia.com>  
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 Copyright 2012 bct electronic GmbH  
 Copyright (C) 2007, 2008 Oracle Corporation \* Written by: Martin K. Petersen <martin.petersen@oracle.com> \*/  
 Copyright (c) 2017 Microsemi Corporation \*/#include <asm/machine.h>#include <asm/prom.h>  
 Copyright (C) 2004 Matthias Badaire  
 Copyright (C) William M. Brack \* \* Refactored and updated to the latest v4l core frameworks: \*  
 Copyright (C) 1995, 1996, 2001 Ralf Baechle  
 Copyright (C) 2013 Samsung Electronics Co., Ltd. \* Author: Jacek Anaszewski <j.anaszewski@samsung.com> \* \* IIO features supported by the driver: \*  
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(IEC958\_AES1\_CON\_ADC\_COPYRIGHT\_ID|0x60)#define IEC958\_AES1\_CON\_SOLIDMEM\_MASK0x0f#define IEC958\_AES1\_CON\_SOLID  
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Copyright (C) 2013 Samsung Electronics Co., Ltd. \* \* Authors: Sylwester Nawrocki <s.nawrocki@samsung.com> \*      Younghwan Joo  
<yhwan.joo@samsung.com>  
Copyright (c) 2011 Andrei Warkentin <andriew@motorola.com>  
Copyright (C) 2009 Devin Heitmueller <dheitmueller@kernellabs.com>  
Copyright (C) 1996 Olaf Kirch \* 04 Aug 1998 Ion Badulescu <ionut@cs.columbia.edu> \*      FIFO's need special handling in NFSv2 \*/  
Copyright (C) 2001 Will Dyson (will@cs.earlham.edu) \*  
Copyright (c) 2003 Intracom S.A. \* by Pantelis Antoniou <panto@intracom.gr> \*  
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Copyright (C) 2013,2016 Samsung Electronics Co., Ltd.  
Copyright (C) 2015 Renesas Electronics Corporation \* \* based on rcar-dmac.c  
Copyright (C) 1996,1997 Thomas K. Dyas (tdyas@eden.rutgers.edu) \* \* --- Notes from Thomas's original driver --- \* This is the lowlevel driver for the  
AMD7930 audio chip found on all \* sun4c machin  
Copyright 2012 Analog Devices Inc. \*/  
Copyright (c) 2006 Jing Min Zhao <zhaojingmin@users.sourceforge.net>  
Copyright (c) 2014 Mediatek Inc.  
Copyright (C) 2015 Linaro, Ltd \* Author: Andy Green <andy.green@linaro.org> \* \* This driver only deals with S2 interface (BT)  
Copyright (C) 2010 Samsung Electronics \* \* Author: Pawel Osciak <pawel@osciak.com>  
Copyright (C) 1998-2009 Texas Instruments. All rights reserved  
Copyright (C) 2007 OnStor, Inc. \* \* This driver is intended to make the second of two hardware watchdogs \* on the Sibyte 12XX and 11XX SoCs  
available to the user. There are two \* such devices ava  
Copyright 2004 The Unichrome project.  
Copyright 2008 Freescale Semiconductor Inc. \* Port to powerpc added by Kumar Gala \*  
Copyright (C) 2003 Hewlett-Packard Co \*      David Mosberger-Tang <davidm@hpl.hp.com> \* \* 25-Sep-03 davidm      Implement fsys\_rt\_sigprocmask().  
\* 18-Feb-03 louisk      Implement fsys\_gettimeofday(). \* 28-Feb-  
Copyright (C) 2005-2007 by Texas Instruments \* Some code has been taken from usb6010.c \* Copyrights for that are attributable to:  
Copyright (c) 2009 by Krzysztof Helt \* Routines for control of MPU-401 in UART mode \* \* MPU-401 supports UART mode which is not capable generate  
transmit \* interrupts thus output is done via po  
Copyright 2010 Javier Martin <javier.martin@vista-silicon.com> \* \* Based on mach-pcm038.c, mach-pca100.c, mach-mx27ads.c and others.  
Copyright (C) 1999, 2007 David S. Miller (davem@davemloft.net) \*/  
Copyright (c) 2010, Applied Micro Circuits Corporation  
Copyright (c) by Jaroslav Kysela <perex@perex.cz> \* Routines for control of SoundBlaster cards - MIDI interface \*  
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Copyright 1999 Silicon Integrated System Corporation      Revision: 1.08.10 Apr. 2 2006  
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Copyright (C) IBM Corporation, 2011 \* Derived from copypower7.s by Anton Blanchard <anton@au.ibm.com> \* Author - Balbir Singh  
<bsingharora@gmail.com> \*/#include <asm/ppc\_asm.h>#include <asm/  
(C) 2005, 2006 Red Hat Inc. \* \* Author: David Woodhouse <dwmw2@infradead.org> \*      Tom Sylla <tom.sylla@amd.com>  
Copyright (c) 1998 by Frank van de Pol <fvdpol@coil.demon.nl> \*/#include <linux/time.h>#include <sound/asequencer.h>  
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only.net> \*/  
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Copyright (C) 1995, 96, 97, 98, 99, 2000 by Ralf Baechle  
Copyright (C) 2005 Dell Inc. \* \* Remote BIOS Update (rbu) driver is used for updating DELL BIOS by \* creating entries in the /sys file systems on Linux  
2.6 and higher \* kernels. The driver support  
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Copyright (C) 2008 Wind River Systems, Inc. \*/  
Copyright (C) 2004 - 2005, 2009-2010 Nokia Corporation \* Written by Tuukka Tikkanen <tuukka.tikkanen@elektrobit.com> \* \* Modified to use omap  
shared clock framework by \* Tony Lindgren <tony@ato  
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(C) 2008 Krzysztof Piotr Oledzki <ole@ans.pl>  
Copyright 2008 Juergen Beisert, kernel@pengutronix.de \* \* This contains hardware definitions that are common between i.MX21 and \* i.MX27.  
Copyright (C) 1999, 2004 Silicon Graphics, Inc.  
Copyright (C) 1996-2005 Paul Mackerras. \* \* Updates for PPC64 by Peter Bergner & David Engebretsen, IBM Corp. \* Updates for SPARC64 by David S.  
Miller \* Derived from PowerPC and Sparc prom.h files  
Copyright (C) 2006 Kaiwan N Billimoria <kaiwan@designergraphix.com>  
Copyright (C) 2005 Luben Tuikov <luben\_tuikov@adaptecc.com> \* \* Luben Tuikov: Some register value updates to make it work with the window \*  
agnostic register r/w functions. Some register correction  
Copyright 2009 Analog Devices Inc.  
Copyright (C) 2002 Roman Zippel <zippel@linux-m68k.org> \* Released under the terms of the GNU GPL v2.0. \* \* Introduced single menu mode (show  
all sub-menus in one large tree). \* 2002-11-06 Petr Ba  
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Copyright 2007, Michael Wu <flamingice@sourmilk.net>  
Copyright (c) 2007 Simtec Electronics \* Ben Dooks <ben@simtec.co.uk> \* <http://armlinux.simtec.co.uk/> \* \* S3C2443 clock register definitions  
Copyright (C) 1996, 1997, 1998, 2003 by Ralf Baechle \*/#ifndef \_\_ASM\_MACH\_GENERIC\_FLOPPY\_H#define \_\_ASM\_MACH\_GENERIC\_FLOPPY\_H  
Copyright (C) 1999 Silicon Graphics, Inc. \* \* Cache error handler  
Copyright (C) 2011 Netlogic Microsystems. \*/#ifndef \_\_ASM\_NETLOGIC\_IRQ\_H#define \_\_ASM\_NETLOGIC\_IRQ\_H  
Copyright (C) Fuzhou Rockchip Electronics Co.Ltd \* Author:Mark Yao <mark.yao@rock-chips.com> \* \* based on exynos\_drm\_drv.h  
Copyright 2007,2014 Andrea Merello <andrea.merello@gmail.com> \* \* Based on the r8180 driver, which is:  
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Copyright (c) 2013 Dialog Semiconductor \* \* Author: Adam Thomson <Adam.Thomson.Opensource@diasemi.com>  
Copyright (c) 2017, The Linux Foundation. All rights reserved  
Copyright (C) 1995, 1996 by Andreas Busse \* \* NOTE: Some of the argument checking could be removed when \* things have settled down. Also, instead  
of returning 0xffffffff \* on failure of vdma\_alloc  
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Copyright (C) 1997 Michael AK Tesch \* written with much help from Jon Howell \* \* Updated for 3D RAGE PRO and 3D RAGE Mobility by Geert

Uytterhoeven

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Copyright (C) 2016 Marek Vasut <marex@denx.de>
Copyright (C) 1998-2000 Jens Axboe */#ifndef _IDE_CD_H#define _IDE_CD_H
Copyright (C) 2005-2009 Rodolfo Giometti <giometti@linux.it>
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Copyright (C) 2013 Texas Instruments * * Module Author: Mugunthan V N <mugunthanvnm@ti.com>
Copyright (c) 2003 Ron Lee (ron@debian.org) * cPad driver for kernel 2.4 *
Copyright (c) 2013 Broadcom Corporation */#ifndef WL_BTCOEX_H#define WL_BTCOEX_H_
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Copyright (C) 2009 Daniel Mack <daniel@caiaq.de> * * Based on code for mobots boards,
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Copyright (C) 1993 Paul B. Monday * * sysv/inode.c
Copyright (C) 2001,2004 Grant Grundler <grundler@parisc-linux.org>*** ** Lots of stuff stolen from arch/alpha/kernel/smp.c** ...and then parisc stole
from arch/ia64/kernel/smp.c. Thanks David! :^)
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Copyright 2004 Antonino A. Daplas <adaplas @pol.net> * * Based partly on rivafb-i2c.c
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Copyright (C) 1992 Drew Eckhardt * Linux scsi disk driver by * Drew Eckhardt <drew@colorado.edu> * * Modified by Eric Youngdale
eric@andante.org to * add scatter-gather, multip
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Copyright (C) 2014, Intel Corporation * Authors: Rafael J. Wysocki <rafael.j.wysocki@intel.com> * Mika Westerberg
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<surenb@google.com>
Copyright (C) 2003, 2004 Richard Curnow * * Provides a debugfs file that lists out the ASIDs currently associated * with the processes.
Copyright (C) Fuzhou Rockchip Electronics Co.Ltd * Author:Mark Yao <mark.yao@rock-chips.com> * * based on exynos_drm_drv.c
Copyright (C) 1998 Olivier A. D. Lebaillif * * Email: olivier.lebaillif@ifrsys.com
Copyright (C) 2005-2010 MontaVista Software, Inc. * * TODO * Look into engine reset on timeout errors. Should not be required.
Copyright (c) 2016 Alexandrov Stanislav <neko@nya.ai> */
Copyright (C) 2012 Atmel * * Author: Bo Shen <voice.shen@atmel.com>
Copyright (C) International Business Machines Corp., 2002,2008
Copyright IBM Corp. 2019 * Author(s): Hendrik Brueckner <brueckner@linux.ibm.com> * Thomas Richer <tmricht@linux.ibm.com> */#define
KMSG_COMPONENT "cpum_cf_diag"#define pr_fmt(fmt) KMSG_C
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```

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 Copyright (c) 2014, NVIDIA CORPORATION. All rights reserved  
 Copyright (C) 2006 \* Simon Schulz (ark3116\_driver <at> auctionant.de) \* \* ark3116 \* - implements a driver for the arkmicro ark3116 chipset  
 (vendor=0x6547, \* productid=0x0232) (used in a datac  
 Copyright (C) 2008 SGI  
 Copyright 2018 Advanced Micro Devices, Inc. \*/  
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 Copyright: Copyright (c) 1996 John Shifflett, GeoLog Consulting \*/static inlineint round\_period(unsigned int period)  
 Copyright (C) 2010, Baruch Siach <baruch@tkos.co.il>  
 Copyright (C) 2013 Oskar Andero <oskar.andro@gmail.com>  
 Copyright (c) 2016 MediaTek Inc. \* Author: Garlic Tseng <garlic.tseng@mediatek.com> \*/  
 Copyright (C) 2010 Alberto Panizzo <maramaopercheseimorto@gmail.com> \* \* Based on ov772x, ov9640 drivers and previous non merged  
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Copyright 2009-2010 Freescale Semiconductor, Inc. \* \* Simple memory allocator abstraction for QorIQ (P1/P2) based Cache-SRAM  
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Copyright (C) Alexander Popov, Promcontroller 2014 \* \* Written by Piotr Ziecik <kosmo@semihalf.com>. Hardware description \* (defines, structures and  
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 <linux/device.h>#include <linux/module.h>#include <linux/slab.h>#in  
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 MAX6650 chip. It should \* also work with the MAX6651. It d  
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 using the bog standard 8250 support.  
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<linux/module.h>#include <linux/cpufeature.h>#in  
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<linux/delay.h>#include <linux/clk.h>#include <linux/s  
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<linux/uwb/umc.h>#include <linux/usb.h>  
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4/12 - 2002 [rob@osinv  
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Copyright (C) 2002, 2006 David S. Miller (davem@davemloft.net) \* \* Rewrote buffer handling to use CIRC(Circular Buffer) macros. \* Maxim  
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<linux/mm.h>#include <linux/kbuild.h>#include <linux/suspend.h>#i  
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Copyright (C) 2010 - 2011 Samsung Electronics Co., Ltd. \* Contact: Sylwester Nawrocki, <s.nawrocki@samsung.com> \* \* Initial register configuration  
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<linux/if\_packet.h>#include <linux/ip.h>#include <linux/ipv6.h>#inclu  
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<linux/ftrace.h>#include <linux/sched/rt.h>#include <linux/sche  
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Copyright (C) 2003 by Ralf Baechle \*/#include <linux/export.h>#include <linux/init.h>#include <linux/mm.h>#include <asm/fixmap.h>#include  
<asm/pgtable.h>#include <asm/pgalloc.h>#include <asm/t  
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 \_ASM\_IA64\_MACHVEC\_H#define \_ASM\_IA64\_MACHVEC\_H  
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 <linux/smp.h>#include <linux/string.h>#include <linux/types.h>  
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<linux/mm.h>#include <linux/proc_fs.h>
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handlers  
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direct.h>#include <linux/dma-noncoherent.h>#include <linux  
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 Copyright (C) 1992, 1993 Krishna Balasubramanian \* Many improvements/fixes by Bruno Haible. \* Replaced `struct shm\_desc' by `struct  
 vm\_area\_struct', July 1994. \* Fixed the shm swap deallocation (s  
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 Copyright (C) 1998-2001 Hewlett-Packard Co \* Stephane Eranian <eranian@hpl.hp.com> \* David Mosberger-Tang <davidm@hpl.hp.com> \* \*  
 6/10/99: Updated to bring in sync with x86 version to facilitate  
 Copyright (C) 2013 - 2015 Renesas Electronics Corporation  
 Copyright (c) 2010 - Mauro Carvalho Chehab \* - Ported the driver to use rc-core \* - IR raw event decoding is now done at rc-core \* - Code  
 almost re-written \*/  
 Copyright (C) 2006 Thomas Bogendoerfer (tsbogend@alpha.franken.de) \*/#ifndef \_\_ASM\_SNI\_H#define \_\_ASM\_SNI\_H  
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 Copyright: (C) 2007 MontaVista Software, Inc., <source@mvista.com> \*  
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 Copyright (c) 2007 Xceive Corporation  
 Copyright (C) 1999 Benjamin Reed. All Rights Reserved  
 Copyright (C) 2006 Olof Johansson <olof@lixom.net> \* \* Dynamic DMA mapping support, pSeries-specific parts, both SMP and LPAR.  
 Copyright © 2014 Jussi Kivilinna <jussi.kivilinna@mbnet.fi> \* \* CBC & ECB parts based on code (crypto/cbc.c,ecb.c) by:  
 Copyright 2001 Patrick Mochel <mochel@osdl.org> \*/#ifndef \_ASM\_X86\_SUSPEND\_64\_H#define \_ASM\_X86\_SUSPEND\_64\_H  
 Copyright (C) 2006, 2007 Atmel Corporation  
 Copyright (C) 2007 Jens Axboe <jens.axboe@oracle.com> \* \* Scatterlist handling helpers.  
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 Copyright (C) 1995-2000 Russell King.  
 Copyright (C) 1996, 2006, 2008 David S. Miller (davem@davemloft.net) \* \* A lot of this driver was directly stolen from Erik H. Moe's PCI \* Qlogic ISP  
 driver. Mucho kudos to him for this code.  
 Copyright (C) 2001, 2006, 2008 MontaVista Software, <source@mvista.com> \* Copied and modified Carsten Langgaard's time.c \* \* Carsten Langgaard,  
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 Copyright (C) 2008 Henrik Kurelid <henrik@kurelid.se>  
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 SUPPORT\_SYSRQ#endif#include <linux/kernel.h>#include <linux/s  
 (C) 2010 Thomas Renninger <trenn@suse.de>  
 Copyright (c) 2019 Facebook \*//\* ----- START-EXPECTED-OUTPUT ----- \*/struct padded\_implicitly { int a; long int b; char c;  
 Copyright (C) 2006 Takashi YOSHII \* \* Based on arch/sh/kernel/cpu/sh3/clock-sh7709.c  
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 Copyright (C) 1999 Phil Blundell



Copyright (c) 2008-2009 Eurotech S.p.A. <info@eurotech.it> \* \* This module supports the PCA954x and PCA954x series of I2C multiplexer/switch \*  
chips made by NXP Semiconductors. \* This includes the  
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Copyright (C) 2008-2010 Nokia Corporation \* \* Paul Walmsley  
Copyright (c) 2014 Broadcom Corporation \*/#ifndef BRCMFMAC\_COMMON\_H#define BRCMFMAC\_COMMON\_H  
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Copyright (C) 2008-2011, Red Hat Inc, Ingo Molnar <mingo@redhat.com> \* \* Improvements and fixes by: \*  
Copyright (C) 2010 Cavium Networks, Inc. \*/#include <linux/dma-direct.h>#include <linux/scatterlist.h>#include <linux/memblock.h>#include  
<linux/export.h>#include <linux/swiotlb.h>#include <lin  
Copyright (C) 2014 Sukadev Bhattiprolu, IBM Corporation.  
Copyright (c) 2016 Petr Kulhavy <petr@barix.com> \* \* This file is part of the Inventra Controller Driver for Linux.  
(C) Copyright IBM Deutschland Entwicklung GmbH 2005 \* \* Author: Christian Krafft <krafft@de.ibm.com>  
Copyright (C) 2008 Texas Instruments. \*/#ifndef \_MACH\_DAVINCI\_MUX\_H#define \_MACH\_DAVINCI\_MUX\_H  
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Copyright (C) 2006-2009, Uri Shkolnik  
(C) 1999 David A. Hinds  
Copyright (C) 1999 by Silicon Graphics, Inc. \*/#include <linux/kernel.h>#include <linux/mm.h>  
Copyright (c) 2019 Jeff Layton \* \* I/O and data path helper functionality.  
Copyright (C) 2015 Stephen Chandler Paul <thatslyude@gmail.com>  
Copyright (C) 1992 - 1997, 2000-2003 Silicon Graphics, Inc. All rights reserved  
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Copyright (C) 2009 Florian Fainelli <florian@openwrt.org> \*/  
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Copyright (c) 2003 Nils-Erik Mattsson, Joacim Haggmark, Magnus Erixson  
Copyright (C) 2011 - CompuLab, Ltd. \* Author: Mike Rapoport <mike@compuLab.co.il> \*  
Copyright 2009 Benjamin Herrenschmidt, IBM Corp \*benh@kernel.crashing.org \*/  
Copyright (C) 1998 Philip Blundell <philb@gnu.org> \* \* This file contains the HP300-specific initialisation code. It gets \* called by setup.c.  
Copyright(c) 2009-2014 Realtek Corporation.  
Copyright (C) 1996 Paul Mackerras \* PPC44x/36-bit changes by Matt Porter (mporter@mvista.com) \* \* Derived from "arch/i386/mm/init.c"  
Copyright IBM Corp. 2013 \* Author(s): Ralf Hoppe (rhoppe@de.ibm.com) \* \* This file provides a Linux "misc" character device for access to an \*  
assigned HMC drive CD/DVD-ROM. It works as f  
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Copyright (C) 2009 Intel corporation \* \* Authors: Wu Fengguang <fengguang.wu@intel.com>  
Copyright (C) 1995, 1999, 2001, 2002 by Ralf Baechle \*/#ifndef \_UAPI\_ASM\_ERRNO\_H#define \_UAPI\_ASM\_ERRNO\_H  
Copyright IBM Corp. 1999, 2000 \* Author(s): Martin Schwidefsky (schwidefsky@de.ibm.com), \* Thomas Spatzier (tspat@de.ibm.com) \* \*  
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(C) 2001 \* by Karsten Merker <merker@linuxtag.org> \* and Maciej W. Rozycki <macro@ds2.pg.gda.pl> \* Later on, I had access to the device's  
documentation (referenced below). \*/  
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Copyright (c) 2007 Atheros Corporation.";  
(C) 2002 by Brian J. Murrell <netfilter@interlinux.bc.ca> \* based on HW's ip\_contrack\_irc.c as well as other modules  
Copyright (C) 2018, 2020 Intel Corporation \* \* Transmit and frame generation functions.  
Copyright (c) 2000-2006, Ericsson AB  
(C) 2001 - 2007 Tensilica Inc. \* Kevin Chea <kchea@yahoo.com> \* Marc Gauthier <marc@linux-xtensa.org> \* Chris Zankel <chris@zankel.net> \*  
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Copyright (c) (2005-2007) Imagination Technologies Limited.  
Copyright (C) 2008 Cavium Networks <support@caviumnetworks.com> \*/#ifndef \_\_ASM\_MIPS\_MACH\_CAVIUM\_OCTEON\_WAR\_H#define  
\_\_ASM\_MIPS\_MACH\_CAVIUM\_OCTEON\_WAR\_H  
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Copyright (C) 1995-2005 Dell Inc.  
Copyright 2016 Free Electrons NextThing Co. \* Quentin Schulz <quentin.schulz@free-electrons.com> \* \* This driver is based on a previous  
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Copyright (C) 2010 ST-Ericsson AB \* Mian Yousaf Kaukab <mian.yousaf.kaukab@stericsson.com> \* \* Based on omap2430.c  
Copyright (C) 2015 Chris Diamand <chris@diamand.org>  
Copyright (C) 1996, 1999, 2003, 2006, 2008 David S. Miller (davem@davemloft.net) \*/  
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Copyright (C) 2007 Mattia Dongili <malattia@linux.it> \*  
Copyright (c) 2007 Oracle Corporation. All rights reserved  
Copyright 2011 Intel Corp. \* Author: Huang Ying <ying.huang@intel.com> \* \* Under normal circumstances, when a hardware error occurs, the error \*  
handler receives control and processes the error.  
Copyright (C) 2009 Paul Mundt \* \* Based on arch/sh/kernel/cpu/sh4/setup-sh7750.c: \*  
Copyright (C) 2008 Nokia Corporation \* Kalle Jokiniemi \*  
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Copyright (c) 2007-2011 Nicira, Inc.  
Copyright (C) 2009 Renesas Solutions Corp. \* Kuninori Morimoto <morimoto.kuninori@renesas.com> \* \* Based on ov772x driver, mt9m111 driver,  
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(C) Copyright 2006-2007, IBM Corporation \* \* Author: Arnd Bergmann <arndb@de.ibm.com>  
(C) Copyright 1995 1996 Linus Torvalds \*/  
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Copyright (c) 2009 Tias Guns  
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Copyright (C) 2010 John Crispin <john@phrozen.org> \*/#include <linux/io.h>#include <linux/export.h>#include <linux/init.h>#include  
<linux/kernel.h>#include <linux/types.h>#include <linux/clock.h>  
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 Copyright (C) 2011 Magnus Damm  
 Copyright (C) 2020 Intel Corporation \* \* Authors:  
 Copyright (c) 2009 Don Prince <dhprince.devel@yahoo.co.uk> \* \* Controls for Octave Shift Up/Down, Channel, and \* Sustain Duration available via  
 sysfs.  
 Copyright 2010-2012 Solarflare Communications Inc. \*/#ifndef \_VFDI\_H#define \_VFDI\_H  
 Copyright (c) 2004 Simtec Electronics \* Ben Dooks <ben@simtec.co.uk> \* \* S3C2410 - NAND device controller platform\_device info  
 Copyright (C) 2011 Freescale Semiconductor, Inc. All Rights Reserved  
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 Copyright (C) 2017 Priit Laes <plaes@plaes.org>  
 Copyright (C) 2002 Pete Zaitcev <zaitcev@yahoo.com> \* \* The algorithm to search a zero bit string is geared towards its application. \* We expect a  
 couple of fixed sizes of requests, so a rotating c  
 Copyright 2016 Noralf Tr  
 Copyright (C) 2007, 2008 Eric Van Hensbergen, IBM Corporation \* \* Based on virtio console driver  
 Copyright (c) 2008-2014 Silicon Graphics, Inc. All Rights Reserved  
 Copyright IBM Corp. 1999, 2017 \*/#ifndef \_ASM\_S390\_SETUP\_H#define \_ASM\_S390\_SETUP\_H  
 Copyright 2009,2012-2013 Freescale Semiconductor Inc.  
 Copyright (C) 1996, 98, 99, 2000, 01 Ralf Baechle \* \* Multi-arch abstraction and asm macros for easier reading:  
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 Copyright (C) 2015 Jens Kuske <jenskuske@gmail.com> \* \* Based on clk-simple-gates.c, which is:  
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 Copyright (C) 1997 Theodore Ts \*/  
 Copyright (C) 2007 Ralf Baechle (ralf@linux-mips.org) \*/#include <linux/init.h>#include <linux/platform\_device.h>#include  
 <linux/serial\_8250.h>#include <linux/rtc/ds1685.h>  
 Copyright (C) 2014 Texas Instruments Incorporated - http://www.ti.com \* \* Author: Jyri Sarha <jsarha@ti.com>

Copyright 2010 \* by Konrad Rzeszutek Wilk <konrad.wilk@oracle.com> \* \* This code provides a IOMMU for Xen PV guests with PCI passthrough.  
Copyright (C) Tomi Manninen OH2BNS (oh2bns@sral.fi) \*/  
Copyright (C) 2008 Intel Corp  
Copyright (c) 2020 Facebook#include <stddef.h>#include <linux/bpf.h>#include <bpf/bpf\_helpers.h>  
Copyright (c) 2014-2015 QLogic Corporation  
Copyright (C) 2009 Samsung Electronics \* Kim Kyuwon <q1.kim@samsung.com> \* \* Based on pxa27x\_keypad.c  
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Copyright (C) 2005 R Sharada (sharada@in.ibm.com)  
Copyright (C) 2006 Jim Cromie \* \* This is a clocksource driver for the Geode SCx200's 1 or 27 MHz \* high-resolution timer. The Geode SC-1100 (at  
least) has a buggy \* time stamp counter (TSC), whi  
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Copyright (c) 2001 by Stefan Eilers, \* Hansjoerg Lipp <hjlipp@web.de>, \* Tilman Schmidt <tilman@imap.cc>. \*  
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Copyright (C) 2017 David Lechner <david@lechnology.com> \* \* Based on: drivers/clk/keystone/gate.c  
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Copyright (c) 2016 Victor Vlasenko <victor.vlasenko@sysgears.com>  
Copyright (C) 2011 - 2015 UNISYS CORPORATION \* All rights reserved  
Copyright (C) 2005 MIPS Technologies, Inc. \*/#include <linux/compiler.h>#include <linux/errno.h>#include <linux/init.h>#include  
<linux/opprofile.h>#include <linux/smp.h>#include <asm/cpu-info.h>  
Copyright (C) 2006-2008 Red Hat, Inc., Markus Armbruster <armbru@redhat.com> \* \* Based on linux/drivers/input/mouse/sermouse.c  
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Copyright (c) 2012 Dmitry Eremin-Solenikov \* \* Based on the AMD RNG driver:  
Copyright 2012 Calxeda, Inc. \* \* Based on arch/arm/plat-mxc/cpuidle.c: #v3.7  
Copyright (c) 2001 by Stefan Eilers \* and Hansjoerg Lipp <hjlipp@web.de>. \* \* This driver was derived from the USB skeleton driver by \*  
Greg Kroah-Hartman <greg@kroah.com>  
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Copyright 2001 by Peter Sprenger (sprenger@moving-bytes.de)  
(C) 1998 James Banks  
Copyright (c) 2019 Facebook \*//\* ----- START-EXPECTED-OUTPUT ----- \*/enum e1 {A = 0, B = 1,  
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(c) Copyright 1998 Alan Cox <alan@lxorguk.ukuu.org.uk>  
Copyright (c) 2012 Savoir-faire Linux Inc. \* Vivien Didelot <vivien.didelot@savoirfairelinux.com> \* \* For further information, see the  
Documentation/hwmon/ads7828 file.  
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Copyright (C) 1998 Gleb Raiko \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/sched.h>#include <linux/smp.h>#include  
<linux/mm.h>

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Andrew Morton, \* Steven Rostedt, Peter Zijlstra for sugg  
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"itd1000.h"#include "cx24113.h"#include "cx24123.h"#inc  
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```

<mkochano@pld.org.pl> * Christoph Bartelmus <liirc@bartelmus.de> * m
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Copyright : %s\n", (status snd_iprintf(buffer, "Emphasis : %s\n", emphasis[(status & SPCS_EMPHASISMASK) >> 3]);
snd_iprintf(buffer, "Mode : %i\n", (status
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<linux/interrupt.h>#include <linux/kernel_stat.h>#include <lin
(C) 2005 Red Hat <alan@lxorguk.ukuu.org.uk> * * This is a fairly generic MWDMA controller. It has some limitations * as it requires timing reloads
on PIO/DMA transitions but it is otherwise
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Bultje <rbultje@ronald.bitfreak.net> * - moved ove
Copyright (c) 2019 Facebook *//* ----- START-EXPECTED-OUTPUT ----- */struct S { int S; int U;
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information communicated between the * PROM, SYMMON, an
copyright = copyright
Copyright (c) 2000 Jaroslav Kysela <perex@perex.cz> * * This is modified (by Sasha Khapyorsky <sashak@alsa-project.org>) version * of ALSA ICH
sound driver intel8x0.c .
Copyright (C) 2001-2003 Hewlett-Packard Co * David Mosberger-Tang <davidm@hpl.hp.com> */
Copyright (C) 2020 VMware, Inc., Palo Alto, CA., USA * * PTP clock driver for VMware precision clock virtual device.
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Copyright (C) 1999, 2000 Tim Waugh <tim@cyberelk.demon.co.uk> * * This file should only be included by drivers/parport/parport_pc.c.
Copyright (C) 2015, 2019 International Business Machines Inc. * * Author: Marcelo Henrique Cerri <mhcerri@br.ibm.com>
Copyright (c) 2005 Keir Fraser * * From: xen-unstable 10676:af9809f51f81a3c43f276f00c81a52ef558afda4
Copyright (C) 2015-2017 Renesas Electronics Corporation
Copyright (c) 2016-2018 Christoph Hellwig. */#include <linux/module.h>#include <linux/compiler.h>#include <linux/fs.h>#include <linux/iomap.h>
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Torvalds, Theodore Ts'o.  
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Copyright (c) 1999 Silicon Graphics, Inc. \*/#ifndef \_ASM\_BCACHE\_H#define \_ASM\_BCACHE\_H  
Copyright (C) 1997 Paul Mackerras. \*\* pmc-control.c: Console support for PowerMac "control" display adaptor.  
(C) protects the consumer from speculatively loading the data before \* the producer pointer actually has been read. If we do not have this \* barrier,  
some architectures could load old data as specul  
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Copyright (C) Jonathan Naylor G4KLX (g4klx@g4klx.demon.co.uk) \*/#include <linux/errno.h>#include <linux/types.h>#include  
<linux/socket.h>#include <linux/slab.h>#include <linux/in.h>#include <li  
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Copyright (C) 2010 John Crispin <john@phrozen.org>  
Copyright (C) 2014 Davidlohr Bueso. \*/#include <linux/sched/signal.h>#include <linux/sched/task.h>#include <linux/mm.h>#include  
<linux/vmcache.h>#include <asm/pgtable.h>  
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<linux/spinlock.h>#include <linux/interrupt.h>  
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 <linux/kernel\_stat.h>#include <linux/seq\_file.h>#include <linux/trace.h>  
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 COPYRIGHT); new new |= ((ucontrol->value.iec958.status[3] & IEC958\_AES3\_CON\_FS) << 24); if ((new &  
 IEC958\_AES0\_CON\_EMPHASIS) == IEC958\_AES0\_CON\_EMPHASIS\_5015) val |= 1<<3;  
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 Copyright (c) 2001 by James.Bottomley@HansenPartnership.com\n" "NCR D700:\n"); banner = 0; } /\* now do the bus  
 related transforms \*/ irq = mca\_device\_transform\_irq(mca\_dev, irq); base\_a  
 Copyright (C) 2004, 2005 Dominik Brodowski <linux@brodo.de>  
 Copyright 2002 Andi Kleen <ak@suse.de> \*/#include <linux/export.h>#include <linux/uaccess.h>#include <linux/highmem.h>  
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 Copyright (C) Hans-Joachim Hetscher DD8NE (dd8ne@bnv-bamberg.de) \*/#include <linux/errno.h>#include <linux/types.h>#include  
 <linux/socket.h>#include <linux/in.h>#include <linux/kernel.h>#includ  
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 Copyright (c) 2006-2008 Nick Kossifidis <mickflemm@gmail.com>  
 Copyright (c) 2006-2008 Dominik Kuhlen <dkuhlen@gmx.net> \* \* TT connect S2-3650-CI Common Interface support, MAC readout  
 Copyright (c) 2008-2009 Michael Buesch <m@bues.ch>  
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 Copyright (C) 1995 Linus Torvalds \* \* Support of BIGMEM added by Gerhard Wichert, Siemens AG, July 1999  
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 Copyright (C) 2013 Xilinx, Inc. \*/#include <linux/firmware.h>#include <linux/fpga/fpga-mgr.h>#include <linux/idr.h>#include  
 <linux/module.h>#include <linux/of.h>#include <linux/mutex.h>#includ  
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 Copyright (C) 2007 Andrew Victor \* \* KS8695 - GPIO control registers and bit definitions.  
 Copyright (C) 2009-2010 by Mauro Carvalho Chehab  
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 Copyright (C) 2004-2008, LINBIT Information Technologies GmbH.  
 Copyright (C) 2011 Samsung Electronics Co., Ltd. \* MyungJoo.Ham <myungjoo.ham@samsung.com> \* \* Charger Manager. \* This framework enables to  
 control and multiple chargers and to \* monitor charging  
 Copyright (C) 2015 Toradex AG. \* \* Author: Sanchayan Maity <sanchayan.maity@toradex.com>  
 Copyright (C) 2002-2005 Yoichi Yuasa <yuasa@linux-mips.org>  
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 (c) Copyright Hewlett-Packard Development Company, L.P., 2006  
 Copyright (C) 2001 Keith M Wesolowski \*/#include <linux/init.h>#include <linux/kernel\_stat.h>#include <linux/types.h>#include  
 <linux/interrupt.h>#include <linux/irq.h>#include <linux/bitops.h>  
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 Copyright (c) 2003 Adrian Sun (asun@darksunrising.com)  
 Copyright (C) 1998, 1999 Brent Baccala (baccala@freesoft.org) \* \* This is the low level driver for the DBRI & MMCODEC duo used for ISDN & AUDIO \*  
 on Sun SPARCStation 10, 20, LX and Voyager models.  
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 Copyright (C) 2007 Sylvain Munaut <mnt@246tNt.com>  
 Copyright (c) 2008 Intel Corporation \* Jesse Barnes <jesse.barnes@intel.com> \* \* Moorestown has an interesting PCI implementation:  
 Copyright (C) 2009 OMICRON electronics GmbH \* \* based nslu2-setup.c, ixdp425-setup.c:  
 (C) Peter T. Breuer (ptb@ot.uc3m.es) 2003 \* \* bitmap\_create - sets up the bitmap structure \* bitmap\_destroy - destroys the bitmap structure  
 Copyright (C) 2018 Samsung Electronics Co., Ltd \* \* Authors:  
 Copyright (c) 1999-2005 Joerg Heckenbach <joerg@heckenbach-aw.de> \* \* This module is part of usbvision driver project. \* Updates to driver  
 completed by Dwaine P. Garden  
 Copyright (c) 2003, Jouni Malinen <j@w1.fi>  
 (C) Copyright TOSHIBA CORPORATION 2004-2006 \* \* This is a very generic AC97 sound machine driver for boards which \* have (AC97) audio at ACLC  
 (e.g. RBTX49XX boards).  
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<manfred@colorfullife.com>  
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Copyright (C) 2013-2014 Jens Axboe \*/#include <linux/kernel.h>#include <linux/module.h>  
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Hoyer's description of the device.  
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0xea, 0x02, 0x00, 0x00, 0xea, 0x01, 0x00, 0x00  
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(C) 2000-2001 Svenning Soerensen <svenning@post5.tele.dk>  
Copyright (C) 2006 Intel Corporation \* Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com> \* \* FIXME: docs \* FIXME: this file needs to be broken

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 <linux/module.h>#include <linux/of\_irq.h>#include <  
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 Copyright (c) 2002-2003, Jouni Malinen <jkmaline@cc.hut.fi> \* \* Adaption to a generic IEEE 802.11 stack by James Ketrenos \*  
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 Copyright (C) 2011 The AC100 Kernel Team <ac100@lists.lauchpad.net> \* \* Authors: Pierre-Hugues Husson <phhusson@free.fr> \* Ilya Petrov  
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 <cornelia.huck@de.ibm.com> \* \* Hotplug & misc device support: Joc  
 Copyright (c) 2009 Gabor Juhas <juhosg@openwrt.org>  
 Copyright (c) 2008 Texas Instruments \*/#include <linux/kernel.h>#include <linux/string.h>#include <linux/errno.h>#include <linux/unistd.h>#include  
 <linux/interrupt.h>#include <linux/init.h>#in  
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 Copyright (C) 2009 Wind River Systems, \* written by Ralf Baechle <ralf@linux-mips.org> \*/#include <linux/module.h>#include <linux/init.h>#include  
 <linux/slab.h>#include <linux/io.h>#include <  
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 Copyright (C) 2008-2009 Ralph Metzler <rjkm@metzlerbros.de> \* Modifications for new nGene firmware, \* support for  
 EEPROM-copying, \*  
 (C) Copyright 2003, 2004 Jamie Lokier \* \* Robust futex support started by Ingo Molnar  
 Copyright IBM Corp. 1999, 2012 \* Author(s): Martin Schwidewsky (schwidewsky@de.ibm.com), \* Hartmut Penner (hp@de.ibm.com), \*  
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Semiconductor. It reports up to two temperatures (its own plus up  
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Copyright (c) 2004 Andras BALI <drewie@freemail.hu> \* \* Heavily based on lm75.c by Frodo Looijaard <frodol@dds.nl>. The LM77 \* is a temperature  
sensor and thermal window comparator with 0.5 deg  
Copyright (c) 2005 Herbert Xu <herbert@gondor.apana.org.au> \* \* Portions derived from Cryptoapi, by Alexander Kjeldaas <astor@fast.no> \* and  
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"hashmap.h"  
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<linux/module.h>#include <linux/phy.h>#include <li  
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(C) Copyright 2000-2002 David Brownell <dbrownell@users.sourceforge.net> \* \* [ Initialisation is based on Linus' ] \* \* [ uhci code and gregs ohci  
fragments ]  
Copyright (C) 2008 Silicon Graphics, Inc. (SGI) \* \* 99/10/01 davidm Make sure we pass zero for reserved parameters. \* 00/03/07 davidm  
Updated pal\_cache\_flush() to be in sync with PAL v2.6. \* 00/03  
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 <prasad@linux.vnet.ibm.com> \* Frederic Weisbecker <fwei  
 Copyright (C) 2007 Antti Palosaari <crope@iki.fi> \* \* TODO: \* - add smart card reader support for Conditional Access (CA) \*  
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 Copyright (C) 2006-2009 by Openmoko, Inc.// Authors: Harald Welte <laforge@openmoko.org>// Andy Green <andy@openmoko.org>//  
 Werner Almesberger <werner@openmoko.org>// All rights  
 Copyright (C) 1996 Paul Mackerras. \* \* Apr. 21 2002 - BenH Rework bus reset code for new error handler \* Add delay after  
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 Copyright (C) 2017 Tony Lindgren <tony@atomide.com> \* \* Some parts based on earlier Motorola Linux kernel tree code in \* board-mapphone-usb.c  
 and cpcap-usb-det.c:  
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 Copyright (c) 2005 Stefano Brivio <st3@riseup.net>  
 Copyright 2015, Red Hat, Inc. and/or its affiliates. \* \* Author:  
 COPYRIGHT)) sbits if (val & AC\_DIG1\_LEVEL) sbits |= (IEC958\_AES1\_CON\_ORIGINAL << 8); sbits |= val  
 & (0x7f << 8); } return sbits;  
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Copyright (C) 1999-2001 Marcus Niemann  
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Copyright (c) 2010, ST-Ericsson \* Author: Dmitry Tarnyagin <dmitry.tarnyagin@lockless.no> \* \* Based on CW1200 UMAC WSM API, which is  
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<linux/err.h>#include <linux/platform\_device.h>#i  
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<linux/slab.h>#include <linux/ieee80211.h>#include <net/cfg8  
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<linux/swapops.h>#include <linux/init.h>#include <linux/page  
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driver

```
COPYRIGHT))          val          if (sbits & (IEC958_AES1_CON_ORIGINAL << 8))          val |= AC_DIG1_LEVEL;          val |= sbits & (IEC958_AES1_CON_CATEGORY << 8);          }          return val;
```

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Copyright IBM Corp. 2016 \* Author(s): Harald Freudenberger <freude@de.ibm.com> \* \* Currently there are two tracepoint events defined here. \* An  
s390\_zcrypt\_req request event occurs as soon as the  
copyright = True  
Copyright (c) 2001 Richard Russon <ntfs@flatcap.org>  
Copyright (c) by Francisco Moraes <fmoraes@nc.rr.com> \*/#include <linux/delay.h>#include <linux/init.h>#include <linux/interrupt.h>#include  
<linux/pci.h>#include <linux/slab.h>#include <linux/m  
Copyright (C) 1999 Vladimir Gurevich <vgurevic@cisco.com> \* Bare & Hare Software, Inc. \* Based on include/asm-  
m68knommu/MC68332.h  
Copyright: Intel Corp 2005  
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Copyright 2001 MontaVista Software Inc.  
Copyright 2015 Carnegie Mellon University \* \* Expose entries from QEMU's firmware configuration (fw\_cfg) device in \* sysfs (read-only, under  
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Copyright (c) by Francisco Moraes <fmoraes@nc.rr.com> \*/#include <linux/delay.h>#include <linux/init.h>#include <linux/interrupt.h>#include  
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 Copyright (C) 1999-2000, 2003 Hewlett-Packard Co \* David Mosberger-Tang <davidm@hpl.hp.com> \* \* A simple API for unwinding kernel stacks.  
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 Copyright (c) 2005, 2007, 2008 Ian Molton  
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 Copyright (C) 2000 Kazumoto Kojima  
 Copyright (C) 2008 Christian Pellegrin <chripell@evolware.org> \* \* Notes: the MAX3100 doesn't provide an interrupt on CTS so we have \* to use polling  
 for flow control. TX empty IRQ is unusable, sin  
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<linux/irq.h>  
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Copyright (C) 2012-2016 Alexander Shiyon <shc\_work@mail.ru> \* \* Based on max3100.c, by Christian Pellegrin <chripell@evolware.org> \* Based on  
max3110.c, by Feng Tang <feng.tang@intel.com> \* Bas  
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Copyright (C) 2001 M. R. Brown  
Copyright (C) 2017 Linus Walleij <linus.walleij@linaro.org> \* \* This SoC has an elaborate flash control register, so we need to \* detect and set it up  
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 <linux/serial\_core.h>#include <linux/errno.h>#include <linux/ioport.h>#i  
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 \_\_ASM\_POWERPC\_REG\_BOOKE\_H\_\_  
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 Copyright (c) 2018 Facebook#include <stddef.h>#include <linux/bpf.h>#include <linux/types.h>#include <bpf/bpf\_helpers.h>  
 Copyright 2007, 2008 Wolfson Microelectronics PLC. \* \* Based on OLPC Battery Driver  
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 Management Controller) via interrupts. \* Unlike IPMI i  
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 Copyright (C) 2005-2007 Atmel Corporation \*/#ifndef \_\_LINUX\_USB\_GADGET\_USBA\_UDC\_H\_\_#define \_\_LINUX\_USB\_GADGET\_USBA\_UDC\_H\_\_



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little-endian byte-swapping/bitmaps by \*  
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Copyright (c) 2009-2010 Samsung Electronics Co., Ltd. \* Pawel Osciak, <pawel@osciak.com> \* Marek Szyprowski, <m.szyprowski@samsung.com> \* \*  
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<linux/errno.h>#include <linux/init.h>#include <linux/kern  
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<alexey.y.starikovskiy@intel.com> \* 2006  
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copyright(struct comedi\_device{ struct jr3\_block \_\_iomem \*block = dev->mmio; struct jr3\_sensor \_\_iomem \*sensor0 = &block[0].sensor;  
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Copyright (C) 2006 Atsushi Nemoto <anemo@mba.ocn.ne.jp> \*/#include <linux/sched.h>#include <linux/sched/debug.h>#include  
<linux/sched/task\_stack.h>#include <linux/stacktrace.h>#include <linux/ex  
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bits  
Copyright (C) 2004-2005 Richard Purdie <rpurdie@rpsys.net> \* \* Sharp's bootloader doesn't pass any kind of machine ID \* so we have to figure out the  
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Copyright (C) 2010 Tom Zanussi <tzanussi@gmail.com>  
Copyright 2010 PMC-Sierra, Inc, derived from irq\_cpu.c \* \* This file define the irq handler for MSP CIC subsystem interrupts.  
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Copyright IBM Corp. 2008, 2020 \* \* Author(s): Carsten Otte <cotte@de.ibm.com> \* Christian Borntraeger <borntraeger@de.ibm.com> \*  
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(C) 2003 Marc Zyngier <maz@wild-wind.fr.eu.org> \* \* This code is released under the GPL version 2.  
Copyright (C) 2016 IBM Corporation \* \* Based on kexec-tools' kexec-elf-ppc64.c, fs2dt.c. \* Heavily modified for the kernel by \* Thiago Jung Bauermann  
<bauerman@linux.vnet.ibm.com>.  
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Copyright(c) 2015-2018 Intel Corporation.  
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Copyright 2019 Analog Devices Inc. \*/#include <linux/kernel.h>#include <linux/bitfield.h>#include <linux/delay.h>#include <linux/errno.h>#include  
<linux/init.h>#include <linux/module.h>#includ  
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electrons.com>  
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Copyright 2009 Geert Uytterhoeven \* \* Based on the A3000 TOD code in arch/m68k/amiga/config.c  
Copyright 2016 Naveen N. Rao <naveen.n.rao@linux.vnet.ibm.com> \* IBM Corporation \*/#include <linux/kprobes.h>#include  
<linux/ptrace.h>#include <linux/hardirq.h>#include <linux/preempt.h>#inc  
Copyright (C) 2017 Linus Walleij <linus.walleij@linaro.org> \* \* Includes portions of the SL2312/SL3516/Gemini PATA driver  
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Copyright (C) 2000 RidgeRun, Inc.  
Copyright (C) 2017 Axis Communications AB \* \* Driver for Texas Instruments' ADC084S021 ADC chip. \* Datasheets can be found here:  
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Copyright (C) 2013, Obsidian Research Corp. \* Jason Gunthorpe <jgunthorpe@obsidianresearch.com> \* Device driver for ATMEL I2C TPMs. \* \* Teddy  
Reed determined the basic I2C command flow, unlike ot  
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<linux/delay.h>#include <linux/module.h>#include <linux/i2c.h>#  
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Copyright 2015 Solarflare Communications Inc. \*/#include <linux/etherdevice.h>#include <linux/pci.h>#include <linux/module.h>#include

```

"net_driver.h"#include "ef10_sriv.h"#include "efx.h"#incl
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Copyright (C) 2010 Texas Instruments Incorporated - http://www.ti.com * * Contact: Ohad Ben-Cohen <ohad@wizery.com>
Copyright 2006 Johannes Berg <johannes@sipsolutions.net> * * This is a driver for the pcm3052 codec chip (codenamed Onyx) * that is present in
newer Apple hardware (with digital output).
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```

```

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~~~~~
Copyright (c) 2018 Politecnico di Torino#define MAP_TYPE BPF_MAP_TYPE_STACK
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Machine Check exception handling *
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PCI168 AP(W) version (PCI rev. 10, subsystem ID 180

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 (C) Texas Instruments, Inc.  
 Copyright (C) 2006 Ralf Baechle <ralf@linux-mips.org> \*/#include <linux/console.h>#include <linux/init.h>#include <linux/interrupt.h>#include  
 <linux/mc146818rtc.h>#include <linux/param.h>#inclu  
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 Copyright (C) 1995-2000 Ralf Baechle  
 Copyright (C) 2017, EETS GmbH, Felix Brack <fb@ltec.ch> \* \* This driver implements a lcd device for the ORISE OTM3225A display \* controller. The  
 control interface to the display is SPI and the disp  
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 Copyright (c) 2016-2018 Mellanox Technologies. All rights reserved  
 Copyright (c) International Business Machines Corp., 2002,2008 \* Author(s): Steve French (sfrench@us.ibm.com) \* \* Error mapping routines from  
 Samba libsmb/errormap.c  
 Copyright 2000 VA Linux Systems, Inc., Sunnyvale, California.  
 Copyright (C) 2007 Atmel Corporation. \* \* Watchdog Timer (WDT) - System peripherals registers. \* Based on AT91SAM9261 datasheet revision D.  
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 Copyright 2001, 2003-2005 MontaVista Software Inc. \* Author: MontaVista Software, Inc. \* ahennessy@mvista.com \* source@mvista.com  
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 Copyright 2014 Bart Tanghe <bart.tanghe@thomasmore.be>  
 Copyright (C) 1993, 2000 Linus Torvalds \* \* Delay routines, using a pre-computed "loops\_per\_jiffy" value.  
 Copyright (C) 2005 Russell King. \* Data taken from include/asm-i386/serial.h \*/#include <linux/module.h>#include <linux/init.h>#include  
 <linux/serial\_8250.h>  
 Copyright (C) 1993, 1994, 1995, 1999 Eric Youngdale \* \* Linux scsi disk driver \* Initial versions: Drew Eckhardt \* Subsequent  
 revisions: Eric Youngdale \* Modificati  
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 Copyright IBM Corp. 2007 \* \* Authors: Hollis Blanchard <hollisb@us.ibm.com>  
 Copyright 2019, Gustavo Romero, Michael Neuling, IBM Corp. \* \* This test will spawn two processes. Both will be attached to the same \* CPU (CPU 0).  
 The child will be in a loop writing to FP registre  
 Copyright (C) 2014 Imagination Technologies Ltd. \* \* Mnemonic names for arguments to memcpy/\_copy\_user  
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<linux/interrupt.h>#include <linux/pm.h>#inc  
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 <linux/irqchip.h>#include <linux/irqchip/arm-gic.  
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 <linux/spinlock.h>#include <linux/in.h>#include <linux/k  
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 <linux/platform\_device.h>#include <linux/sched.h>#include <linux/slab.  
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 Copyright (C) 2008, Guennadi Liakhovetski <kernel@pengutronix.de> \* \* This driver provides an interface between platform-specific camera \* busses  
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 pr\_fmt(fmt) KMSG\_COMPONENT ": " fmt  
 Copyright (c) 2015 Axentia Technologies AB \* Author: Peter Rosin <peda@axentia.se> \* \* Datasheet:  
 http://www.microchip.com/downloads/en/DeviceDoc/22096b.pdf  
 Copyright (C) 2000 Vinh Truong (vinh.truong@eng.sun.com) \* VT - Add all ioctl commands and environment status definitions \* VT - Add application  
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copyright (C) 1992 Michael K. Johnson \* Interrupt support added 1993 Nigel Gamble \* Removed 8255 status defines from inside \_\_KERNEL\_\_ Marcelo  
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<subhransu.s.prusty@intel.com> * ~~~~~
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services provided by firmware.
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Copyright (C) 1992 Eric Youngdale * Simulate a host adapter with 2 disks attached. Do a lot of checking * to make sure that we are not getting blocks
mixed up, and PANIC if * anything out of t
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<linux/fs.h>#include <linux/mm.h>#include <linux/mman.h>  
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<linux/mm.h>#include <linux/i2c.h>#include <linux/interrupt.h>#i  
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Fuzzey \*/  
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<penmount <at> seed.net.tw>
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Sedji Gaouaou * Based on the bttv driver for Bt848 wit
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<linux/kprobes.h>#include <linux/uaccess.h>#include <linux/hardirq.h>#in
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Copyright (C) 1999 Don Dugger <don.dugger@intel.com> * * 11/24/98 S.Eranian added ia64_set_iva() * 12/03/99 D. Mosberger implement
thread_saved_pc() via kernel unwind API * 06/16/00 A. Mallick add
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<rkroll@exploits.org> * * Converted to the radio-isa f
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Copyright(c) 2002-2010 Exar Corp. *****/#include
<linux/ethtool.h>#include <linux/slab.h>#include <linux/pci.h>#include <l
Copyright (c) 2007-2008, Realsil Wlan\n"); RT_TRACE(TRACE_INIT, "Initializing module"); RT_TRACE(TRACE_INIT, "Wireless extensions version
%d", WIRELESS_EXT); rtl8192_proc_module_init();return usb_r
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<linux/screen\_info.h>#include <linux/memblock.h>#incl  
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Copyright (C) 2002 Dave Engebretsen <engebret@us.ibm.com>, IBM \* Rework to support virtual processors \* \* Type of int is used as a full 64b word  
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copyright (C) 2005, Chuck Lever <cel@netapp.com> \*/  
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<linux/interrupt.h>#include <linux/platform\_dev  
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Copyright (c) 1996 Paul Mackerras. \*/#include <stdarg.h>#include <linux/types.h>#include <linux/errno.h>#include <linux/kernel.h>#include  
<linux/delay.h>#include <linux/adb.h>#include <linux/c  
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\*/#include <linux/init.h>#include <linux/string.h>#include <linux/string.h>  
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Copyright (c) 2002,2003 by Takashi Iwai <tiwai@suse.de> \* \* STRATEGY \* for playback, we send series of "chunks", which size is equal with the \* IBL  
size, typically 126 samples. at each end of c  
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Copyright (C) 2016 Linaro Ltd. <ard.biesheuvel@linaro.org>  
Copyright (c) 2008 Herbert Xu <herbert@gondor.apana.org.au> \* \* Updated RFC4106 AES-GCM testing. \* Authors: Aidan O'Mahony  
(aidan.o.mahony@intel.com) \* Adrian Hoban <adrian.hoban@intel.com>  
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Copyright 2015 Linaro Ltd. \*/#undef TRACE\_SYSTEM#define TRACE\_SYSTEM greybus  
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Copyright: Copyright (c) 1996 John Shifflett, GeoLog Consulting \* Modified by Russell King for 8MHz WD33C93A \*/static struct sync\_xfer\_tbl {  
unsigned int period\_ns; unsigned char reg\_value;  
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Copyright 2011-2012 Freescale Semiconductor, Inc \* \* Author: Dipen Dudhat <Dipen.Dudhat@freescale.com>  
Copyright (c) 2004 Ray Lehtiniemi  
(C) CC Computer Consultants GmbH, 2007 \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/in.h>#include <linux/in6.h>#include  
<linux/ip.h>#include <linux/ipv6.h>#include <linux/jhash.h>  
Copyright (c) 2009-2010 Stephane Chatty \*/  
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Copyright (c) 2002 Pete Zaitcev (zaitcev@yahoo.com) \* - Incorporating suggestions made by Linus Torvalds and Dave Miller \*/  
Copyright © 2007-2008 Andres Salomon <dilinger@debian.org> \*/#include <sound/core.h>#include <sound/info.h>#include  
<sound/control.h>#include <sound/ac97\_codec.h>#include <linux/gpio.h>  
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Copyright (C) 2004 Kay Sievers <kay.sievers@vrfy.org> \* \* This file is release under the GPLv2  
Copyright (c) 2016 Tom Haynes <loghyr@primarydata.com> \* \* The following implements a super-simple flex-file server \* where the NFSv4.1 mds is  
also the ds. And the storage is \* the same. I.e., wri  
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<linux/unistd.h>#include <linux/interrupt.h>#include <linux  
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Copyright (c) 2014 Applied Micro Circuits Corporation. \* \* Author: Tanmay Inamdar <tinamdar@apm.com>.  
Copyright (c) 2019 Facebook#include <test\_progs.h>static int libbpf\_debug\_print(enum libbpf\_print\_level level, const char \*format,  
va\_list args){ if (level != LIBBPF\_DEBUG) { vprintf(fo  
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(C) 2007 sgi. Christoph Lameter. \* \* Virtual memory maps allow VM primitives pfn\_to\_page, page\_to\_pfn, \* virt\_to\_page, page\_address() to be implemented as a base offset \* calculation without memor  
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Copyright 1999 Gregory P. Smith \*/#include <linux/acpi.h>#include <linux/clock.h>#include <linux/dma-mapping.h>#include <linux/err.h>#include  
<linux/kernel.h>#include <linux/hrtimer.h>#include <  
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Copyright (c) 2011 Jiri Kosina \* \* This module has been updated to add support for Asus i2c touchpad.  
Copyright (c) 2019, Google Inc. \* Author - Rajat Jain \* \* This code instantiates platform devices for intel\_pmc\_core driver, only \* on supported platforms  
that may not have the ACPI devices in the  
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Copyright (C) 2004 Justin Thiessen <jthiessen@penguincomputing.com> \* \* Chip details at: \*  
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Copyright 2006 Johannes Berg <johannes@sipsolutions.net> \* \* This file contains the GPIO control routines for \* direct (through feature calls) access to  
the GPIO \* registers.  
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Copyright (C) 2000, 2001, 2002 David A. Schlee <ds@schlee.org> \* \* PCMCIA support code for this driver is adapted from the dummy\_cs.c \* driver of  
the Linux PCMCIA Card Services package.  
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<linux/in.h>#include <linux/kernel.h>#include <linux/ji
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(C) SK" ;#define LINT_USE(x)#else#define LINT_USE(x) (x)=(x)#endif#define MS2BCLK(x) ((x)*12500L)
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paride_register(&bpck6);}
Copyright (C) 2005-2007 by Texas Instruments */#include <linux/device.h>#include <linux/interrupt.h>#include <linux/platform_device.h>#include
<linux/slab.h>#include "musb_core.h"
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Copyright (c) 2011, Google, Inc.
Copyright (C) Mario Six, Guntermann * * Written by Piotr Ziecik <kosmo@semihalf.com>. Hardware description * (defines, structures and comments)
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Copyright 2010 Wolfram Sang <w.sang@pengutronix.de>
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Copyright (C) 2011 Sylwester Nawrocki <s.nawrocki@samsung.com> * * Based on Omnivision OV7670 Camera Driver
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Copyright (c) 2003 by James.Bottomley@HansenPartnership.com\n" "NCR Q720:\n"); banner = 0; } io_base =

```

mca\_device\_transform\_ioport(mca\_dev, io\_base);  
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Copyright (C) 2000, 2002 Maciej W. Rozycki \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/mm.h>#include <linux/memblock.h>#include <linux/types.h>  
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Copyright (c) 2015, Intel Corporation. \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/init.h>#include <linux/acpi.h>#include  
<linux/thermal.h>#include "int340x\_thermal\_zone  
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Copyright (C) 2018 Red Hat, Inc. \*/#include <linux/errno.h>#include <linux/kernel.h>#include <linux/list.h>#include <linux/rcupdate.h>#include  
<linux/slab.h>#include <linux/sched.h>#include <l  
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Copyright 2005, Embedded Alley Solutions, Inc. \* Matt Porter <mporter@embeddedalley.com>  
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Copyright (C) 1996 David S. Miller (davem@davemloft.net) \*/#include <linux/bug.h>#include <linux/init.h>#include <linux/kernel.h>#include  
<linux/types.h>#include <linux/string.h>  
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Copyright (c) 2017 Lucas Stach, Pengutronix  
Copyright (C) 2016 Marek Vasut <marex@denx.de> \* \* i.MX23/i.MX28/i.MX6SX MXSFB LCD controller driver.  
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Andreas Schwab  
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Copyright(c) 2015 Bryan O \* \* IMR self test. The purpose of this module is to run a set of tests on the \* IMR API to validate it's sanity. We check for  
overlapping, reserved \* addresses and setup/  
Copyright (C) 1999-2001 Grant Grundler \*/#include <linux/eisa.h>#include <linux/init.h>#include <linux/module.h>#include <linux/kernel.h>#include  
<linux/pci.h>#include <linux/types.h>  
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Copyright (c) 2010 Mika Laitio <lamikr@pilppa.org> \* \* This driver will read and write the value of 4 counters to w1\_slave file in \* sys filesystem. \*  
Inspired by the w1\_therm and w1\_ds2431 driver  
Copyright (C) 2002, 2003, 2005 Maciej W. Rozycki \*/#ifndef \_\_ASM\_MIPS\_DEC\_KN01\_H#define \_\_ASM\_MIPS\_DEC\_KN01\_H  
Copyright (C) 2002, 2004, 2007 by Ralf Baechle <ralf@linux-mips.org> \*/#ifndef \_\_ASM\_MACH\_GENERIC\_WAR\_H#define

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(C) 2009-2010 Bartłomiej Zolnierkiewicz \* \* Based on  
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this \* project.  
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Copyright (c) 2014-2016 Christoph Hellwig. \*/#include <linux/sunrpc/svc.h>#include <linux/blkdev.h>#include <linux/nfs4.h>#include  
<linux/nfs\_fs.h>#include <linux/nfs\_xdr.h>#include <linux/pr.h  
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Copyright (C) 1995 \*/#ifndef \_ASM\_X86\_FLOPPY\_H#define \_ASM\_X86\_FLOPPY\_H  
Copyright (C) 2009 Steven Rostedt <srostedt@redhat.com> \*/#include <linux/stringify.h>#include <linux/kallsyms.h>#include  
<linux/seq\_file.h>#include <linux/uaccess.h>#include <linux/trace.h>#i  
Copyright (C) 1999 Stephane Eranian <eranian@hpl.hp.com> \* \* Based on skeleton from the drivers/char/rtc.c driver by P. Gortmaker  
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<linux/interrupt.h>#include <linux/kernel\_stat.h>#include <linux/proc\_f  
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Copyright (C) 2005 Trond Myklebust \* \* NFSv4-specific filesystem definitions and declarations  
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memory. This gets called from \_\_alloc\_pages  
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Richard Zidlicky \* 68060 fixes by Roman Hodek \* 68  
Copyright (C) 2013, Seth Jennings, IBM \* \* Concepts based on zcache internal zbud allocator by Dan Magenheimer.  
Copyright (C) 2005-2006 Micronas USA Inc.static int go7007\_usb\_vendor\_request(struct go7007 \*go, u16 request, u16 value, u16 index, void  
\*transfer\_buffer, int length, int in){ struct go7007\_usb \*u  
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constructing the SMB PDUs themselves  
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Copyright (c) 2015-2016 HGST, a Western Digital Company. \*/#ifndef \_NVME\_FABRICS\_H#define \_NVME\_FABRICS\_H 1  
Copyright (C) 2018-2019 Jens Axboe \*/#include <stdio.h>#include <fcntl.h>#include <string.h>#include <stdlib.h>#include <unistd.h>#include  
<assert.h>#include <errno.h>#include <inttypes.h>#i  
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Card (Grundig tuner) by \* Michael Dreher <michael@5dot1.

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erlangen.de> \* - Queues are allocated dynamically via ioctl. \*  
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<laforge@openmoko.org>  
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 Copyright (C) 2019 Facebook \* \* One challenge of controlling IO resources is the lack of trivially \* observable cost metric. This is distinguished from CPU  
 and memory where \* wallclock time and t  
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 Copyright (c) 2009 Philippe Vachon <philippe@cowpig.ca> \*  
 Copyright (C) 2013-2015 Corentin LABBE <clabbe.montjoie@gmail.com> \* \* Support AES cipher with 128,192,256 bits keysize. \* Support MD5 and  
 SHA1 hash algorithms. \* Support DES and 3DES  
 Copyright (c) 2001 La Monte H.P. Yarroll \* \* These functions manipulate an sctp event. The struct ulpevent is used \* to carry notifications and data to

the ULP (sockets).

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Copyright (c) 2003 Gerd Knorr  
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Copyright (C) 2004 Amaury Demol for DiBcom \* \* Acknowledgements  
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Copyright (C) 2003,2004 \* Neil Whelchel (koyama@firstlight.net) \* \* See Documentation/usb/usb-serial.txt for more information on using this \* driver  
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Copyright 2008 Simtec Electronics \* Ben Dooks <ben@simtec.co.uk> \* http://armlinux.simtec.co.uk/ \* \* S3C64XX - PM core support for

arch/arm/plat-s3c/pm.c

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pfm\_mont\_pmc\_check(struct task\_struct \*task, pfm\_

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Copyright (C) 2008 <srinivasa.deevi at conexant dot com> Based on cx88 driver  
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Copyright (C) 2008 Florian Fainelli <florian@openwrt.org> \* \* This file was derived from: include/asm-mips/cpu-features.h  
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Copyright (C) 2008 Jiri Hladky <hladky\_dot\_jiri\_at\_gmail\_dot\_com> \* \* The \_\_delay function must \_NOT\_ be inlined as its execution time \* depends wildly on alignment on many x86 processors. Th  
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Copyright © 2014 Jussi Kivilinna <jussi.kivilinna@iki.fi> \* \* This file is based on sha1\_generic.c and sha1\_ssse3\_glue.c:  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Search routines for btrees  
Copyright (C) 2000-2002 Toshiba Corporation \* \* Serial driver for TX3927/TX4927/TX4925/TX4938 internal SIO controller  
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Copyright (C) 2010, Arnaldo Carvalho de Melo <acme@redhat.com> \*/#include <sys/types.h>#include <sys/time.h>#include <time.h>#include <dirent.h>#include <errno.h>#include <unistd.h>#include "b  
Copyright (C) 1997, 99, 2001 - 2004 Ralf Baechle <ralf@linux-mips.org> \*/#include <linux/extable.h>#include <linux/spinlock.h>#include <asm/branch.h>#include <linux/uaccess.h>  
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Copyright (C) 2009, Arnaldo Carvalho de Melo <acme@redhat.com> \*/#include "builtin.h"#include "perf.h"#include "util/build-id.h"#include "util/debug.h"#include "util/dso.h"#include <subcmd/page  
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Copyright (C) 2005, 2006 by Ralf Baechle (ralf@linux-mips.org)  
Copyright (C) 1999, 2000 Silicon Graphics, Inc. \* \* Changed system calls macros \_\_syscall5 - \_\_syscall7 to push args 5 to 7 onto  
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Copyright (C) 2008, Paulius Zaleckas <paulius.zaleckas@teltonika.lt> \* \* Based on earlier work by  
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Copyright (c) 2017 Ruslan Bilovol <ruslan.bilovol@gmail.com> \* \* This file holds USB constants and structures defined \* by the USB\_DEVICE\_CLASS  
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Speck64) is fairly straightforward; the math \* is j  
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Copyright (C) 1999, 2000 Niibe Yutaka \*  
Copyright (C) 2004 IBM Corporation, Rusty Russell. \* \* Creation is done via kthreadd, so that we get a clean environment \* even if we're invoked from  
userspace (think modprobe, hotplug cpu, \* etc.  
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 Copyright(c) 2007 Chris Snook <csnook@redhat.com> \* \* Derived from Intel e1000 driver  
 Copyright IBM Corp. 2004, 2005 \* Interface implementation for communication with the z/VM control program \* Version 1.0 \* Author(s): Christian  
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 Copyright (C) 1991, 1992 Linus Torvalds \* \* 1997-11-28 Modified for POSIX.1b signals by Richard Henderson  
 Copyright (C) 2008 Steven Toth <stoth@linuxtv.org> \*/static int mxl5005s\_reset(struct dvb\_frontend \*fe)  
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 for STMicroelectronics. \*/  
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 Copyright (C) 2000 Frank Mori Hess <fmhess@users.sourceforge.net> \* \* COMEDI - Linux Control and Measurement Device Interface  
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 Copyright status#define AC97\_SC\_PRE 0x0008 /\* Preemphasis status \*/#define AC97\_SC\_CC\_MASK 0x07f0 /\* Category Code mask  
 \*/#define AC97\_SC\_CC\_SHIFT 4#define AC97\_SC\_L 0x0800 /\* Generation Level  
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 <linux/miscdevice.h>#include <linux/module.h>#include <linux  
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 (C) \* Notes: To enter USB suspend mode \* Command Mode \*/struct usb\_suspend\_parm { u32 action; /\* 1: sleep, 0:resume \*/  
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 (C) 2007 SoftwareBitMaker  
 Copyright (C) 2008-2009 Frederic Weisbecker <fweisbec@gmail.com> \* \* Based on the code from trace\_branch.c which is  
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<sys/stat.h>#include <sys/time.h>#include <sys/resource.h>  
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/dev/zero mmapping support, Feb 2000, Kanoj Sarcar <  
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Copyright 2008 Simtec Electronics \* Ben Dooks <ben@simtec.co.uk> \* http://armlinux.simtec.co.uk/ \* \* S3C64XX - GPIO memory port register  
definitions  
Copyright (C) 2012 Andrew Lunn <andrew@lunn.ch> \* \* Based on the code from: \*  
Copyright (C) 2007 David S. Miller (davem@davemloft.net) \*/#include <linux/kernel.h>#include <linux/pci.h>#include <linux/slab.h>#include  
<linux/init.h>#include <linux/msi.h>#include <linux/exp

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<linux/context\_tracking.h>#include <linux/cpu\_pm.h>#include  
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(C) 2005 Finn Thain \* \* Converted to DMA API, added zero-copy buffer handling, and \* (from the mac68k project) introduced dhd's support for 16-bit  
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Quatech's site:  
(C) 1999 by Joshua M. Thompson (funaho@jurai.org) \* Based on via-cuda.c by Paul Mackerras. \* \* 1999-07-01 (jmt) - First implementation for new  
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<rothwell@interlan.net> for the test equipment, h  
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 <cpu/registers.h>  
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<linux/console.h>#include <linux/screen\_info.h>#include <linux/platfo  
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belkin\_sa.[ch]). All of the information about the devic  
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<linux/mfd/syscon.h>#include <linux/of\_address.h>#include <linux/of\_d  
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<linux/percpu.h>#include <linux/input.h>  
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 then the Wax ASIC is used. To complicate matters, the  
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 COPYRIGHT (1 << 2)#define HDMI\_I2S\_LINEAR\_PCM (0 << 1)#define HDMI\_I2S\_NO\_LINEAR\_PCM (1 << 1)#define  
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 <linux/sched.h> /\* schow\_regs(), force\_sig() \*/#include  
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 lib.h>#include <linux/idr.h>#include <net/tcp.h>#include <s  
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 on kernel/rcu/torture.c.  
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 Copyright (C) 2009 Marek Vasut <marek.vasut@gmail.com> \* \* Based on ov772x camera driver:  
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Copyright (C) 2008 Erik Andr  
Copyright 2016, Rashmica Gupta, IBM Corp. \* \* This traverses the kernel virtual memory and dumps the pages that are in \* the hash pagetable, along  
with their flags to \* /sys/kernel/debug/kernel\_ha  
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Copyright (C) 1991, 1992, 1995 Linus Torvalds \* \* This file contains the m68k-specific time handling details. \* Most of the stuff is located in the machine  
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Copyright (C) 2004 IBM \*/  
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<linux/blkdev.h>#include <linux/elevator.h>#include <lin  
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code is based on the uncore PMUs like arm-cci and arm-cc  
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driver  
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copyright information. \* 03-16-15 02.00.26 Updated for MPI v2.6. \* Added MPI2\_EVENT\_ACTIVE\_CABLE\_EXCEPTION and \*  
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Copyright (C) 2003 Hewlett-Packard Co \* David Mosberger-Tang <davidm@hpl.hp.com> \*/#include <linux/init.h>#include <linux/string.h>  
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odd IO ports on 16-bit devices. These \* devices  
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 Copyright (c) 2004, Intel Corporation \*  
 Copyright (C) 2000, 2001, 2003 Broadcom Corporation \*/#include <linux/cache.h>#include <linux/delay.h>#include <linux/init.h>#include  
 <linux/interrupt.h>#include <linux/smp.h>#include <linux/sp  
 Copyright 2015 Intel Deutschland GmbH \*/#include <net/mac80211.h>#include "ieee80211\_i.h"#include "trace.h"#include "driver-ops.h"  
 Copyright 2015 Samuel Pitoiset  
 Copyright (C) 1997 Dan Malek (dmalek@jlc.net). \* \* This file contains the low-level support and setup for the \* PowerPC platform, including trap and  
 interrupt dispatch. \* (The PPC 8xx embedded  
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 <linux/moduleparam.h>#include <sound/core.h>#include  
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 Portions of said code are  
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 <linux/delay.h>#include <linux/log2.h>#include <linu  
 (C) Nadia Yvette Chambers, April 2004 \*/#include <linux/list.h>#include <linux/init.h>#include <linux/mm.h>#include <linux/seq\_file.h>#include  
 <linux/sysctl.h>#include <linux/highmem.h>#include  
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Copyright (c) 1996-2000 Jonathan A. Buzzard (jonathan@buzzard.org.uk) \* \* Thanks to Juergen Heinzl <juergen@monocerus.demon.co.uk> for the  
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Copyright IBM Corp. 2008 \* Author(s): Jan Glauber <jang@linux.vnet.ibm.com> \*/#include <linux/kernel.h>#include <linux/slab.h>#include  
<linux/export.h>#include <asm/qdio.h>  
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Copyright (C) 2012, Red Hat, Inc. Rafael Aquini <aquini@redhat.com> \*/#include <linux/mm.h>#include <linux/slab.h>#include  
<linux/export.h>#include <linux/balloon\_compaction.h>  
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Copyright (c) 2018 Facebook#include <test\_progs.h>  
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the sparc64 perf event code and the x86 code. Pe  
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byte-swapping/bitmaps by \* David S. M  
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Copyright (c) 1998-2000 Harald Koerfgen \*/#include <asm/asm.h>#include <asm/cachectl.h>#include <asm/export.h>#include  
<asm/fpregdef.h>#include <asm/mipsregs.h>#include <asm/offsets.h>#inc  
Copyright (C) 2007 Conexant Systems, Inc. \* \* - islmcv driver  
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Added CPU Hotplug Support \* \* 2005-10-07 Keith Owens <kaos@sgi.com>  
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 <linus.walleij@stericsson.com> \*/#include <linux/module.h>#include <lin  
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 /\* internal vendor ID write enable, model 039? \*/#define CM\_SETRETRY 0x00100000 /  
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Copyright (C) 2003 Linus Torvalds \* \* Mon Mar 5, 2007: Davide Libenzi <davidel@xmailserver.org> \* Changed ->read() to return a siginfo structure instead of signal number. \* Fixed lockin  
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 Copyright (C) 2007 SGI, \* Christoph Lameter <cl@linux.com> \* Generalized, added support for multiple lists and \* constructors /  
 destructors. \*/#include <linux/kernel.h>  
 Copyright (C) 2001-2002 Thomas Bogendoerfer <tsbogend@alpha.franken.de> \* \* TODO:  
 Copyright IBM Corp. 2005, 2011 \* Author(s): Jan Glauber (jang@de.ibm.com) \*/#include <crypto/internal/hash.h>#include <linux/init.h>#include  
 <linux/module.h>#include <linux/cpufeature.h>#incl  
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 Copyright (C) 2007-2009 Motorola, Inc. \* \* Rewritten for the real register offsets instead of enumeration \* to make the defines usable with Linux kernel  
 regmap support  
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 COPYRIGHT)) bits if ((diga->status[0] & IEC958\_AES0\_CON\_EMPHASIS) == IEC958\_AES0\_CON\_EMPHASIS\_NONE)  
 bits |= 0x40; if (!(diga->status[1] & IEC958\_AES1\_CON\_ORIGINAL)) bits |= 0x80; if ((d  
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 <linux/mmc/mmc.h>#include <linux/mmc/slot-gpio.h>#includ  
 Copyright Darryl Miles G7LED (dlm@g7led.demon.co.uk) \*/#include <linux/module.h>#include <linux/moduleparam.h>#include  
 <linux/capability.h>#include <linux/errno.h>#include <linux/types.h>#inclu  
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```

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 Copyright (C) 1999, 2000 Silicon Graphics, Inc. \*/#include <linux/cpu\_pm.h>#include <linux/hardirq.h>#include <linux/init.h>#include <linux/highmem.h>#include <linux/kernel.h>#include <linux/li  
 Copyright (c) 2014 Christoph Hellwig. \*/#include "xfs.h"#include "xfs\_shared.h"#include "xfs\_format.h"#include "xfs\_log\_format.h"#include "xfs\_trans\_resv.h"#include "xfs\_mount.h"#include "xfs\_

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 Copyright (C) 2008 David S. Miller <davem@davemloft.net> \*/#include <linux/kernel.h>#include <linux/interrupt.h>  
 Copyright (C) 2006 Paul Mackerras, IBM Corp. <paulus@samba.org> \*/  
 Copyright (c) 2016 Joachim Eastwood <manabian@gmail.com> \* \* Based on the imx ocotp driver,  
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Copyright (c) 2004-2005, Intel Corporation \* \* API Version History \* 1.0.x -- Initial version \* 1.1.x -- Added radiotap, QoS, TIM, libipw\_geo APIs, \*  
various structure changes, and crypt  
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Copyright (C) 2006 Arnd Bergmann <arnd@arndb.de>, IBM Corp. \*/#include <linux/console.h>#include <linux/module.h>#include  
<linux/slab.h>#include <linux/delay.h>#include <linux/serial\_core.h>#in  
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Copyright (c) 2012, Jeff Layton <jlayton@redhat.com>  
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(C) 2000 Nicolas Pitre <nico@fluxnic.net> \*/#include <linux/module.h>#include <linux/types.h>#include <linux/ioport.h>#include  
<linux/kernel.h>#include <linux/init.h>#include <linux/errno.h>#i  
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Copyright (C) 2010-2013 Felix Fietkau <nbd@openwrt.org> \*/#include <linux/netdevice.h>#include <linux/types.h>#include <linux/skbuff.h>#include  
<linux/debugfs.h>#include <linux/random.h>#include  
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Copyright (C) 2006 Intel Corporation \* Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com>  
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\_UAPI\_ASM\_IA64\_UNISTD\_H  
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Copyright: (C) 2003 Monta Vista Software, Inc. \* \* Note 1: This driver is made separate from the already too overloaded \* 8250.c because it needs  
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regardless of whether \* they've been read or no  
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Copyright (C) 2007 Novell Inc. \*/#include <linux/kobject.h>#include <linux/string.h>#include <linux/sysfs.h>#include <linux/module.h>#include  
<linux/init.h>  
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 Copyright (C) 2011 Renesas Solutions Corp. \* Kuninori Morimoto <kuninori.morimoto.gx@renesas.com> \*/#include <linux/io.h>#include  
 <linux/list.h>#include <linux/module.h>#include <linux/platform\_  
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 Copyright (c) 2002-2003, Jouni Malinen <j@w1.fi>  
 Copyright (C) 2013 Linaro Ltd. \* \* Based on arch/x86/include/asm/hugetlb.h  
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 billm@suburbia.net | |  
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 <arun.sharma@intel.com> \* \* 12/07/98 S. Eranian added pt\_regs & switch\_st  
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 copyright); i copy[i] = (char)(get\_u16(&sensor0->copyright[i]) >> 8); copy[i] = '\0';  
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 Copyright IBM Corp. 2014, 2018  
 Copyright (c) 2011 by Jarod Wilson <jarod@redhat.com> \* based heavily on the work of Stephen Cox, with additional \* help from RedRat Ltd. \* \* This  
 driver began life based an an old version of th  
 (C) 2017 Red Hat GmbH \* Author: Florian Westphal <fw@strlen.de> \* \* split from xt\_connlimit.c:  
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 by \* Benjamin Herrenschmidt <benh@kernel.crashin>  
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 Copyright (C) 2001 MIPS Technologies, Inc. \*/#include <linux/errno.h>#include <asm/asm.h>#include <asm/asmmacro.h>#include  
 <asm/irqflags.h>#include <asm/mipsregs.h>#include <asm/regdef.h>#incl  
 Copyright (C) 2012 Ralf Baechle <ralf@linux-mips.org> \*/#include <asm/asm.h>#include <asm/export.h>#include <asm/regdef.h>  
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 (c) Copyright 2001 Red Hat Inc. <alan@redhat.com> \* \* based on the old aacraid driver that is.. \* Adaptec aacraid device driver for Linux.  
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 Copyright (c) 2004 Mellanox Technologies Ltd. All rights reserved  
 (C) Copyright 2005 Greg Kroah-Hartman <gregkh@suse.de> \* \* notifier functions originally based on those in kernel/sys.c \* but fixed up to not be so  
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 (C) Copyright David Gibson, IBM Corp. 2001-2003. \* \* Portions taken from hfa384x.h.  
 Copyright (C) 1991, 1992 Linus Torvalds \* \* This file contains the interface functions for the various \* time related system calls: time, stime,  
 gettimeofday, settimeofday, \* adjtime  
 Copyright (c) 2015 Jamal Hadi Salim <jhs@mojatatu.com>  
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"percpu\_freelist.h"  
Copyright (C) 1999-2002 Andre Hedrick <andre@linux-ide.org> \* 865/865R fixes for Macintosh card version from a patch to the old \* driver  
by Thibaut VARENE <varenet@parisc-linux.org> \* When setting  
Copyright (c) 2002-2005, Jouni Malinen <j@w1.fi> \* \* This file is to be included into hostap.c when S/W AP functionality is \* compiled.  
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Copyright (C) 2000, 2001 Keith M Wesolowski \*/#include <linux/kernel.h>#include <linux/pci.h>#include <linux/types.h>#include <asm/ip32/mace.h>  
Copyright (C) 2010 Samsung Electronics  
Copyright (C) 2003 Agilent Technologies \*/  
Copyright (c) 1996 Paul Mackerras <paulus@cs.anu.edu.au> \* Low-level exception handlers, MMU support, and rewrite.  
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Copyright (C) 2012 Jean-Francois Moine <http://moinejf.free.fr>  
Copyright (C) 2003 Fenghua Yu <fenghua.yu@intel.com> \* - Change pt\_regs\_off() to make it less dependent on pt\_regs structure. \*/  
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 <linux/etherdevice.h>#include <linux/ethtool.h>#include <linux/  
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 <asm/processor.h>#include <asm/smp.h>  
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 nondebug inlines them)  
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 p2pdma.h>#include <rdma/mr\_pool.h>#include <rdma/rw.h>  
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 Copyright 1995 Linus Torvalds \*/#include <linux/mm.h>#include <linux/fs.h>#include <linux/list.h>#include <linux/highmem.h>#include  
 <linux/compiler.h>#include <linux/uaccess.h>#include <linux/  
 Copyright 2012 Michael Ellerman, IBM Corporation.  
 Copyright (C) 1992 Remy Card (card@masi.ibp.fr) \* \* xenix/alloc.c  
 Copyright (C) 2007, 2008, 2009, 2010, 2011 Cavium Networks \*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/pci.h>#include  
 <linux/interrupt.h>#include <linux/time.h>#include <  
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<linux/of\_address.h>#include <linux/platform\_device.h>  
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 <stddef.h>#include <init.h>#include <elf\_user.h>#incl  
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 Copyright (c) 2001-2002 by David Brownell  
 Copyright (C) 1995, 1996 Gero Kuhlmann and me. \* \* BOOTP rewritten to construct and analyse packets itself instead \* of misusing the IP layer.  
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 Copyright (C) 2012 Rafael J. Wysocki <rjw@sisk.pl> \* \* This code is based on the analogous interface allowing user space to \* manipulate wakelocks on

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```
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"driver-ops.h"
Copyright (C) 2005 Thomas Hergenbahn <thomas.hergenbahn@suse.de>
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<linux/ctype.h>#include <linux/errno.h>#include <linux/unistd.h>#inclu
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<linux/netdevice.h>#include <linux/of.h>#include <linux/of_mdio.h>
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Copyright (C) 2000 Takashi Iwai <tiwai@suse.de> * * Routines for control of EMU WaveTable chip
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 <k.opasiak@samsung.com> \*/  
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 BRCMF\_TRACEPOINT\_H\_  
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 Youngdale  
 Copyright (C) 2010 Dan Williams <dcbw@redhat.com> \* \* Devices listed here usually provide a CDC ACM port on which normal modem \* AT commands  
 and PPP can be used. But when that port is in-use by PP  
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 "rmi\_driver.h"#include "rmi\_2d\_sensor.h"  
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 Copyright 2015 Advanced Micro Devices, Inc.#include "dm\_services.h"#include "dc.h"#include "dc\_link\_dp.h"#include "dm\_helpers.h"#include  
 "opp.h"#include "dsc.h"#include "resource.h"  
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 code.  
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Copyright (C) 2005 Benjamin Herrenschmidt <benh@kernel.crashing.org>, \* IBM Corporation \* \* Based on pSeries\_iommu.c:  
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(C) 1:M \* ... -->|(A)|--> | +-----+ +---+ +-->| 1 | +---+ \* ... -->| | +-->|(D) 1:3 |-----+ +---+ \* pll1.N -->| N |

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IEC958\_AES2\_CON\_CHANNEL\_UNSPEC; cs[3] = IEC958\_AES3\_CON\_CLOCK\_1000PPM | IEC958\_AE  
copyright); ixgbev\_f\_wq = create\_singlethread\_workqueue(ixgbev\_driver\_name); if (!ixgbev\_f\_wq) { pr\_err("%s: Failed to create  
workqueue\n", ixgbev\_driver\_name); return -ENOMEM; }  
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Copyright (c) by Francisco Moraes <fmoraes@nc.rr.com> \* Driver EMU10K1X chips \* \* Parts of this code were adapted from audigy.c driver which is  
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 (C) 2001,2002,2006 by Jan-Benedict Glaw <jbglaw@lug-owl.de> \* \* This driver is a modified version of Erik Mouw's example proc \* interface, so: thank  
 you, Erik! He can be reached via email at \* <J.  
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 Copyright (C) 2007, 2014 Maciej W. Rozycki \*/#include <linux/delay.h>#include <linux/export.h>#include <linux/param.h>#include  
 <linux/smp.h>#include <linux/stringify.h>  
 Copyright (C) 2002 Atom Create Engineering Co., Ltd.  
 Copyright Intel Corp. 2018#include <linux/init.h>#include <linux/module.h>#include <linux/moduleparam.h>#include <linux/nd.h>#include  
 "pmem.h"#include "pfn.h"#include "nd.h"#include "nd-core.h  
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 (C) pxor GHASH\_MUL %xmm8,%xmm13,%xmm9,%xmm10,%xmm11,%xmm5,%xmm6 # final GHASH computation movdq  
 SHUF\_MASK(%rip),%xmm10 PSHUFB\_XMM %xmm10,%xmm8  
 Copyright (C) 2006 Kaiwan N Billimoria <kaiwan@designergraphix.com> \* \* The LM70 communicates with a host processor via an SPI/Microwire Bus \*  
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 Copyright (C) 2000 MIPS Technologies, Inc. \* written by Carsten Langgaard, carstenl@mips.com \*/#include <asm/asm.h>#include  
 <asm/export.h>#include <asm/asm-offsets.h>#include <asm/mipsregs.h>  
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Copyright 1998-2000 Jonathan Teh Soon Yew) * if(temp<169) * return double(temp)*0.427-32.08; * else if(temp>=169 && temp<=202) *
return double(temp)*0.582-58.16; * else * return double(temp)*
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<linux/pci.h>#include <linux/types.h>#include <as  
Copyright (C) 1996 Paul Mackerras \* Amiga/APUS changes by Jesper Skov (jskov@cygnus.co.uk). \* \* Derived from "arch/i386/mm/init.c"  
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<hkoerfg@web.de>, which itself is derived from \* "HP300 Topcat frameb  
Copyright 2019 IBM Corp.#include <linux/idr.h>#include "ocxl\_internal.h"  
COPYRIGHT 0xd4#define CX2341X\_ENC\_SET\_EVENT\_NOTIFICATION 0xd5#define CX2341X\_ENC\_SET\_NUM\_VSYNC\_LINES  
0xd6#define CX2341X\_ENC\_SET\_PLACERHOLDER 0xd7#define CX2341X\_ENC\_MUTE\_VIDEO 0xd9#define CX  
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 \* interface for 64-bit kernels. \* Authors: Adri  
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 Leonard Zubkoff and David Miller  
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 <linux/initrd.h>#include <linux/console.h>#include <linux/root\_dev.  
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 Copyright (C) 2013-2017 Wolfram Sang <wsa@the-dreams.de> \*/  
 Copyright 2005 Stanislav Marek \* email: pisa@cmp.felk.cvut.cz \* \* Calculates proper bit-timing parameters for a specified bit-rate \* and sample-  
 point, which can then be used to set the bit-ti  
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Copyright (C) 2004 David Brownell \*/  
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mod example from relay-examples by Tom Zan  
(C) Copyright IBM Corp. 2005 \* device driver to exploit watchdog RTAS functions \* \* Authors : Utz Bacher <utz.bacher@de.ibm.com>  
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<linux/swap.h>#include <linux/hugetlb.h>  
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driver,  
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 Copyright (C) 2015, Imagination Technologies Ltd. \* Authors: Matt Redfearn (matt.redfearn@mips.com) \*/#include <asm/bootinfo.h>#include  
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 (C) 2003 Christoph Jungegger <disdos@traum404.de> \* \* Some of this code is borrowed from orinoco\_plx.c  
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 Copyright (C) 2004, 2009, 2010 Texas Instruments Incorporated \* Author: Aurelien Jacquot (aurelien.jacquot@jaluna.com) \* \* Updated for 2.6.34 by:  
 Mark Salter (msalter@redhat.com)  
 Copyright (C) 2010, Guennadi Liakhovetski <g.liakhovetski@gmx.de> \*/#include <linux/clk.h>#include <linux/delay.h>#include <linux/err.h>#include  
 <linux/hdmi.h>#include <linux/irq.h>#include <li  
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 Author: Rui Prior (rprior@inescn.pt) \* P  
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 <linux/moduleparam.h>#include <linux/init.h>#include <linux/delay.h>#inclu  
 (C) 1996,1998 by Thomas Bogendoerfer (tsbogend@alpha.franken.de) \* \* This driver is based on work from Andreas Busse, but most of \* the code is  
 rewritten.  
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 | |  
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 Copyright (C) 2002-2003 Hewlett Packard Co \* Stephane Eranian <eranian@hpl.hp.com> \*/static int pfm\_mck\_pmc\_check(struct task\_struct  
 \*task, pfm\_context\_t \*ctx, unsigned int cnum, uns  
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\*\*\*\*\*/##ifndef \_\_AU88X0\_WT\_H#define \_\_AU88X0\_WT\_H  
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Copyright IBM Corp. 2019 Author(s): Hendrik Brueckner <brueckner@linux.ibm.com> \* Thomas Richter <tmricht@linux.ibm.com> \*/##ifndef S390\_CPUMCF\_KERNEL\_H#define S390\_CPUMCF\_KERNEL\_H  
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Copyright (C) 2017-2018, Intel Corporation  
(C) 1999 Andrea Arcangeli <andrea@suse.de> (dynamic inode allocation) \*/#include <linux/export.h>#include <linux/fs.h>#include  
<linux/mm.h>#include <linux/backing-dev.h>#include <linux/hash.h>#  
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<linux/vmalloc.h>#include <linux/syscalls.h>#includ  
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 <linux/export.h>#include <asm/cacheflush.h>#includ  
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 Copyright (C) 2015 \*/  
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 <linux/init.h>  
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 Copyright (C) 2008-2014 Mathieu Desnoyers  
 Copyright (c) 2019 Facebook \*//\* ----- START-EXPECTED-OUTPUT ----- \*/struct packed\_trailing\_space { int a; short b;} \_\_attribute\_\_((packed));  
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 Copyright (C) 2015 HiSilicon Co., Ltd. http://www.hisilicon.com \* \* Authors: Zhou Wang <wangzhou1@hisilicon.com> \* Dacai Zhu  
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 <stddef.h>#include <linux/i2c.h>#include <asm/pmac\_low\_i2c.h>#inc  
 Copyright 2012 Red Hat \* \* Authth: Matthew Garrett \* Dave Airlie  
 Copyright IBM Corp. 2005, 2012 \* Author(s): Michael Holzheu <holzheu@de.ibm.com> \* Heiko Carstens <heiko.carstens@de.ibm.com> \*  
 Volker Sameske <sameske@de.ibm.com> \*/  
 Copyright 2019, IBM Corporation. \*  
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 <linux/netdevice.h>#include <linux/rbtree.h>#include <linux  
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 <linux/smp.h>#include <linux/percpu.h>#include <linux/  
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 \*/#include <linux/module.h>#include <linux/init.h>#include  
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 (C) 2006-2012 \*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/ipv6.h>#include <linux/netfilter.h>#include  
 <linux/netfilter\_ipv6.h>#include <linux/export.h>#include <net/addrc  
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 (C) 1995 by Andreas Busse (andy@waldorf-gmbh.de) \* \* A driver for the onboard Sonic ethernet controller on Mips Jazz \* systems (Acer Pica-61, Mips  
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 the \* !CONFIG\_DEBUG\_MUTEXES case. Most of them a  
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 as10x\_bus\_adapter\_t;struct as102\_dev\_t;  
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```

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<linux/kernel.h>#include <linux/interrupt.h>#include <linux/fs.h>#include
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<linux/export.h>#include <linux/string.h>#include
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<linux/log2.h>#include <linux/list.h>#include <linux/slab.h>#include <
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2000, new mktime * 1998-12-20 Updated NTP code ac
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Arnd Bergmann (arndb@de.ibm.com) \* Martin Schwidefsky (schwidefsky  
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Copyright (C) 2018 STMicroelectronics \* Author(s): Amelie Delaunay <amelie.delaunay@st.com>. \*/#include <linux/bitfield.h>#include  
<linux/clk.h>#include <linux/delay.h>#include <linux/io.h>#include  
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Copyright (C) 2010 Red Hat, Inc., Peter Zijlstra \* \* Provides a framework for enqueueing and running callbacks from hardirq \* context. The enqueueing is NMI-safe.  
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Fabien Dessenne <fabien.dessenne@st.com>  
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Copyright (C) 2001 Tim Waugh <twaugh@redhat.com> \* \* Multi-function PCI cards are supposed to present separate logical \* devices on the bus. A common thing to do seems to be to just use \* one lo  
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Copyright (C) 2000, 2003 Hewlett-Packard Co \* David Mosberger-Tang <davidm@hpl.hp.com> \* \* 03/05/07 davidm Switch from PCI-DMA to generic device DMA API. \* 00/12/13 davidm Rename to swiotlb.c and  
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Copyright (C) 1999, 2000 Silicon Graphics, Inc. \*/#include <linux/kernel.h>#include <linux/sched.h>#include <linux/interrupt.h>#include  
<linux/kernel\_stat.h>#include <linux/param.h>#include <li  
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(C) Copyright 1995 - 1997 Marco van Wieringen - ELM Consultancy B.V. \* \* Plugged two leaks. 1) It didn't return acct\_file into the free\_filps if \* the file  
happened to be read-only. 2) If the acc  
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Copyright (c) by Jaroslav Kysela <perex@perex.cz>, \* Creative Labs, Inc. \* Definitions for EMU10K1 (SB Live!) chips \*/#ifndef  
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12:13:13 \* from the pcmcia package. \* The initial developer  
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 <bigeazy@linutronix.de>  
 Copyright 2018 Google Inc. \* Author: Eric Dumazet (edumazet@google.com) \* \* Reference program demonstrating tcp mmap() usage, \* and  
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 <uapi/linux/if\_vlan.h>#include <uapi/linux/ip.h>#include <uapi  
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(net/netfilter/nf\_contrack\_ftp.c):  
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optimum numerator/denominator pairs for \* pll config  
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<linux/memblock.h>#include <linux/proc\_fs.h>#include <lin  
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2002/12/09 Fix rotating register handling (off-by-1 error,  
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<linux/trace.h>#include <linux/slab.h>#include <linux/fs.h>

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Copyright (c) 2019 Facebook#include <stdbool.h>#include <stddef.h>#include <linux/bpf.h>#include <linux/ptrace.h>#include  
<bpf/bpf\_helpers.h>#include <bpf/bpf\_tracing.h>  
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<bpf/bpf\_endian.h>#include <bpf/bpf\_tracing.h>  
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driver
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unsigned long end\_brk;#ifdef CONFIG\_BINFMT\_ELF FDPIC uns  
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Copyright (C) 2019 IBM Corporation <nayna@linux.ibm.com> \* \* This code exposes secure variables to user via sysfs

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Copyright (C) 2005-2007 David Brownell  
Copyright (C) 2015 Davidlohr Bueso. \* \* Block a bunch of threads and let parallel waker threads wakeup an \* equal amount of them. The program  
output reflects the avg latency \* for each individual  
(C) Copyright 2009 Intel Corporation \* Author: Jacob Pan (jacob.jun.pan@intel.com) \* \* Shared with ARM platforms, Jamie Iles, Picochip 2011  
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Copyright (C) 2016-2017 Linaro Ltd., Rob Herring <robh@kernel.org> \*/#include <linux/kernel.h>#include <linux/serdev.h>#include  
<linux/tty.h>#include <linux/tty\_driver.h>#include <linux/poll.h>  
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Copyright (C) 2007 Jean Delvare <jdelvare@suse.de>  
Copyright (C) 2000 Takashi Iwai <tiwai@suse.de> \* \* Generic memory management routines for soundcard memory allocation  
Copyright (C) 1995-2000 Simon G. Vogl With some changes from:  
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Copyright 1999 Ben Pfaff <pfaffben@debian.org> and Petr Vandrovec <VANDROVE@vc.cvut.cz> * Based on VGA info at
http://www.goodnet.com/~tinara/FreeVGA/home.htm
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Copyright (c) 2010-2011 Samsung Electronics Co., Ltd. * http://www.samsung.com/ * * HDMI register header file for Samsung TVOUT driver
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copyright not found\n");#endif /* Validate checksum */ for (i = s = 0; i < EEPROM_CHECKSUM; i++) s += e[i]; s &= 0xFF;if (s !=
e[EEPROM_CHECKSUM]) { printk(KERN_ERR DEV_LABEL "(itf %d): EEPR
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"Hisilicon Network Subsystem"
Copyright (C) 1991-1998 Linus Torvalds * * Thanks to Branko Lankester, lankeste@fwi.uva.nl, who found a bug * in the early extended-partition
checks and added DM partitions
Copyright (C) 2002 * Tapio Laxström (tapio.laxstrom@iptime.fi) */#include <linux/kernel.h>#include <linux/errno.h>#include <linux/slab.h>#include
<linux/module.h>#include <linux/usb.h>#includ
COPYRIGHT );#endif
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0xb0000000)
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Copyright (C) 2016 Renesas Electronics Corp.
Copyright (C) 1993 Bruno Haible * * SystemV/Coherent directory handling functions
Copyright IBM Corp. 2001, 2009 * * Author(s):
Copyright (C) 2015 Ulrich Hecht * * based on
Copyright (C) 2004 - 2006 Mike Christie
Copyright (C) 2001 MIPS Technologies, Inc. */#ifndef _ASM_REBOOT_H#define _ASM_REBOOT_H
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```

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\_VXGE\_ETHTOOL\_H#define\_VXGE\_ETHTOOL\_H  
Copyright IBM Corp. 2013 \* Author(s): Ralf Hoppe (rhoppe@de.ibm.com) \*  
Copyright (C) 2003, 07 Ralf Baechle \*/#ifndef \_\_ASM\_MACH\_IP27\_CPU\_FEATURE\_OVERRIDES\_H#define  
\_\_ASM\_MACH\_IP27\_CPU\_FEATURE\_OVERRIDES\_H  
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Copyright (C) 2016 Intel Deutschland GmbH \*/#include <linux/kernel.h>#include <linux/bitops.h>#include <linux/types.h>#include  
<linux/netdevice.h>#include <linux/export.h>#include <asm/unalign  
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copyright text 2001jul05 change message format to improve OVERRUN case\*/  
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(C) 2000 Red Hat. GPL \* \* 10/10/2000 Nicolas Pitre <nico@fluxnic.net> \* - completely revamped method functions so they are aware and \*

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 Copyright (C) 2013 Linaro Limited. \* Written by: Yi Li (yi.li@linaro.org) \* \* based on arch/ia64/include/asm/dmi.h  
 Copyright (C) 1995 - 2000 by Ralf Baechle \*/#include <linux/context\_tracking.h>#include <linux/signal.h>#include <linux/sched.h>#include  
 <linux/interrupt.h>#include <linux/kernel.h>#include <li  
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 change |= snd\_ac97\_update\_bits\_nolock(ac97, AC97\_CXR\_AUDIO\_MISC, AC97\_CXR  
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 <linux/module.h>#include <linux/io.h>#include <linux/g  
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 <linux/kernel\_stat.h>#include <linux/interrupt.h>#include <linux/ftrace.h  
 Copyright (c) 2014-2016 Christoph Hellwig. \*/#include <linux/sunrpc/svc.h>#include <linux/exportfs.h>#include <linux/iomap.h>#include  
 <linux/nfs4.h>  
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 F. Padovan, February 2009  
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 <linux/string.h>#include <sound/core.h>#include <s  
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 (C) and server (S): \* \* Setup of client connection:  
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 Copyright (C) 1999-2000 Thomas Sailer (sailer@ife.ee.ethz.ch) \* \* This file implements the usbfs/x/y files, where \* x is the bus number and y the  
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 Copyright (C) 2010 Hector Martin <hector@marcansoft.com>  
 Copyright (c) 2005 - James Bottomley <James.Bottomley@steeleye.com> \* \* The basic idea here is to allow any "device controller" (which \* would most  
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 release \* Jun 1997 2.1.43+ changes \* Changed for 2.1.19 modules \* Jan 1997 Initial  
 Proper page locking in readpage \* Change

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(2628/1.25=2102.4) // +2.5V \* voltagefactor[2]=1.67/2628; (262  
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<linux/err.h>#include <linux/slab.h>#include <linux/strin  
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<linux/mtd/mtd.h>#include <linux/mtd/map.h>#include <linux/of\_p  
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AC97 secondary) *      added 64x/128x oversampl

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(C) in bits ( vmovq arg4, %xmm1 vpslldq \$8, %xmm15, %xmm15 # xmm15 = len(A)|| 0x0000000000000000

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Copyright (c) 2016-2018 Christoph Hellwig. \*/#include <linux/module.h>#include <linux/compiler.h>#include <linux/fs.h>#include  
<linux/iomap.h>#include "trace.h"  
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Copyright (c) 2019 Facebook \*/\* ----- START-EXPECTED-OUTPUT ----- \*/struct s1 {};  
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Copyright (C) 2013, Arnaud EBALARD <arno@natisbad.org> \* \* This work is based on a basic version for 2.6.31 kernel developed \* by Olivier Mouchet  
for LaCie. Updates and correction have been \* perf  
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Copyright (C) 2009 Lemote Inc. \* Author: Hu Hongbing <huhb@lemote.com> \* Wu Zhangjin <wuzhangjin@gmail.com> \*/#include  
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Copyright (C) 2010--2012 Nokia Corporation \* Contact: Sakari Ailus <sakari.ailus@iki.fi> \* \* Based on smiapp driver by Vimarsh Zutshi \* Based on  
jt8ev1.c by Vimarsh Zutshi \* Based on smia-sensor.  
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"0014.0401.2010"  
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Copyright 2009 Integrated Device Technology, Inc. \* Alex Bounine <alexandre.bounine@idt.com> \* - Added Port-Write/Error Management initialization  
and handling \*  
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 (c) Copyright 1995 Alan Cox <alan@lxorguk.ukuu.org.uk> \* \* 14-Dec-2001 Matt Domsch <Matt\_Domsch@dell.com> \* Added nowayout  
 module option to override CONFIG\_WATCHDOG\_NOWAYOUT \* Added t  
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Copyright (C) 2002-2003 Hewlett Packard Co \* Stephane Eranian <eranian@hpl.hp.com> \*/static int pfm\_ita\_pmc\_check(struct task\_struct  
\*task, pfm\_context\_t \*ctx, unsigned int cnum, uns  
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<linux/smp.h>#include <linux/mm.h>#include <linux/bitops.h>#include  
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 Copyright 2013 Qtechnology A/S  
 Copyright (C) 1995-2003 Russell King \* 2001-2002 Keith Owens \* \* Generate definitions needed by assembly language modules. \* This  
 code generates raw asm output which is post-pro  
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 Copyright (C) 2006 David S. Miller (davem@davemloft.net) \*/#include <linux/kernel.h>#include <linux/errno.h>#include <linux/thread\_info.h>#include  
 <linux/perf\_event.h>  
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 (C) 1996-1998 by Thomas Sailer, HB9JNX/AE4WA \*/#ifndef \_HDLCDRV\_H#define \_HDLCDRV\_H  
 Copyright (C) 1992,1993 | W. Metzenthien, 22 Parker St, Ormond, Vic 3163, | | Australia. E-mail  
 billm@vaxc.cc.monash.edu.au | |  
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 <linux/unistd.h>#include <linux/interrupt.h>#include <linux  
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 <tony@atomide.com> \* and Dirk  
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(1<<7)  
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 COPYRIGHT; switch (bits case 0x10: diga->status[0] |= IEC958\_AES0\_CON\_EMPHASIS\_NONE; break; case 0x00: diga->status[0] |= IEC958\_AES0\_CON\_EMPHASIS\_5015; break; } if (!(bits & 0x80)) diga-  
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 < asm/unaligned.h >#include < trace/events/scsi.h >  
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 < linux/netfilter\_ipv6/ip6\_tables.h >#include < linux/slab.h >#include < linux/module.h >#include < linux/init.h >#include < linux/dmaengine.h >#include  
 < linux/slab.h >#include < sound/pcm.h >#include < sound/pcm\_params.h >  
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Copyright (C) 2010-17 Digital Devices GmbH\n"); stat = ddb\_init\_ddbridge(); if (stat < 0) return stat; stat =

```

pci_register_driver(&ddb_pci_driver); if (stat < 0) ddb_exit_ddbridge(0, stat);
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Copyright (C) 2010 Jean-Francois Moine (http://moinejf.free.fr) * *Notes: * t613 + tas5130A * * Focus to light do not balance well as in win. *
Quality in win is not good, but its kinda better.
Copyright (C) 1996 Paul Mackerras. */#ifndef _MESH_H#define _MESH_H
Copyright (C) 2016 Renesas Electronics Corp. * * This file is based on the drivers/pinctrl/sh-pfc/pfc-r8a7796.c
Copyright (C) 2010-2012 Synopsys, Inc. (www.synopsys.com) * * vineetg: July 10th 2012 * -Decoupled the driver from arch/arc * +Using
platform_get_resource() for irq/membase (thx to bfin_uart.c
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COPYRIGHT)) val |= ((new >> 8) & 0xff) << 4; // category + original switch ((new >> 24) & 0xff) { case
IEC958_AES3_CON_FS_44100: val |= 0<<12; break; case IEC958_AES3_CON_FS_48000: val
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<linux/wait.h>#include <linux/sched.h>#include <linux/smp.h>
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<linux/trace.h>
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```

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<linux/pci.h>#include <linux/platform_device.h>#include <lin
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Copyright (C) 2018, Red Hat, Inc. \* \* Tests for Enlightened VMCS, including nested guest state.  
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 Copyright (c) 2016 Yusuke Fujimaki <usk.fujimaki@gmail.com> \* \* This module based on hid-ortek by  
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 Copyright (C) 2017 William Breathitt Gray  
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 Copyright 2002 Andy Grover <andrew.grover@intel.com>  
 Copyright 2009-2011 VMware, Inc., Palo Alto, CA., USA  
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 Copyright (C) 2007 David Gibson, IBM Corporation.  
 Copyright (c) 2008 Loc Ho <lho@amcc.com>  
 Copyright (C) 2001-2010 Guan Xuetao  
 (C) 1999 Dave Airlie, University of Limerick, Ireland <airlied@linux.ie> \* \* Rewrite of larges part of the code in order to stabilize TCP stuff. \* Fix  
 behaviour when socket buffer is full.  
 Copyright (C) 1998, 1999 Russell King, Phil Blundell \*/#include <linux/ioport.h>#include <linux/kernel.h>#include <linux/init.h>#include  
 <linux/screen\_info.h>#include <linux/io.h>#include <linu  
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 Copyright (c) 2006 Marek Vasut \* \* Based on Newton keyboard driver for Linux \* by Justin Cormack  
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 Copyright (C) 2003 IBM Corp. \* \* Original driver for 2.2.x by anonymous  
 Copyright (C) 2005 Mike Isely <isely@pobox.com> \*/#ifndef \_\_PVRUSB2\_STD\_H#define \_\_PVRUSB2\_STD\_H  
 Copyright (C) 2006 Michael Hanselmann (linux-kernel@hansmi.ch) \* \* Thanks to Caskey L. Dickson for his work with acdctl.  
 Copyright (C) 2011 Wind River Systems, \* written by Ralf Baechle (ralf@linux-mips.org) \*/#include <linux/bug.h>#include <linux/kernel.h>#include  
 <linux/mm.h>#include <linux/memblock.h>#includ  
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 Copyright (c) 2011 David George <david.george@ska.ac.za> \* \* based on adm1021.c \* some credit to Christoph Scheurer, but largely a rewrite  
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 Copyright IBM Corp. 1999, 2002 \* \* /proc interface for the dasd driver.  
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 (c) Copyright 2007 Hewlett-Packard Development Company, L.P. \* Contributed by Peter Keilty <peter.keilty@hp.com> \* \* fsyscall gettimeofday  
 data  
 Copyright 2006 - Florian Fainelli <florian@openwrt.org> \* \* Control the Cobalt Qube/RaQ front LED  
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 Benoit Cousson (b-cousson@ti.com)  
 Copyright (C) 2011 Wistron Co.Ltd \* Joseph Lai <joseph\_lai@wistron.com> and trimmed down by \* Alan Cox <alan@linux.intel.com> in turn based on  
 bma023.c. \* Device behaviour based on a misc driver po  
 Copyright (C) 2012 Michael Buesch <m@bues.ch> \* \* Derived from FC0012 tuner driver:  
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 Copyright (c) 2004, 2010 Freescale Semiconductor, Inc \* \* Author: Andy Fleming \* Maintainer: Kumar Gala <galak@kernel.crashing.org>  
 Copyright (C) 2009 Wind River Systems, \* written by Ralf Baechle <ralf@linux-mips.org> \*/#include <linux/init.h>#include <linux/sched.h>#include  
 <linux/notifier.h>#include <linux/ptrace.h>  
 Copyright (C) 2018 Davidlohr Bueso. \* \* Benchmark the various operations allowed for epoll\_ctl(2). \* The idea is to concurrently stress a single epoll  
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 Copyright 2005 Stephane Marchesin.  
 Copyright (C) 2003 Benedikt Spranger, Pengutronix  
 Copyright (C) 2010 Johan Hovold (jhovold@gmail.com) \* \* This program is largely derived from work by the linux-usb group \* and associated  
 source files. Please see the usb/serial files for \* i  
 Copyright (c) 2005-2007 Stefano Brivio <stefano.brivio@polimi.it>  
 Copyright 1995-2002, Russell King \*/#include <linux/module.h>#include <linux/ioport.h>#include <linux/blkdev.h>#include <linux/init.h>  
 Copyright (c) 1998 Corey Thomas (corey@world.std.com)  
 Copyright (C) 1997 David S. Miller (davem@caip.rutgers.edu) \*/  
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 Copyright (c) 2004-2005 Richard Purdie \* \* Based on Sharp's 2.4 kernel patches/lubbock.c  
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 Copyright © 2002-2003, Greg Ungerer (gerg@snapgear.com) \* \* Based heavily on the nftlmount.c code which is:  
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 Copyright (C) 2009 Texas Instruments \* Added OMAP4 support - Santosh Shilimkar <santosh.shilimkar@ti.com> \* \* Support functions for the OMAP  
 internal DMA channels.  
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 Copyright (C) 2004 Jean-Frederic Clere.  
 Copyright (C) 2013 Cavium, Inc. \*/#ifndef \_\_ASM\_MACH\_PARAVIRT\_CPU\_FEATURE\_OVERRIDES\_H#define  
 \_\_ASM\_MACH\_PARAVIRT\_CPU\_FEATURE\_OVERRIDES\_H  
 Copyright (C) 2014 Imagination Technologies Ltd. \*  
 Copyright (C) 2009 Siemens AG \* Written by: Dmitry Eremin-Solenikov <dmitry.baryshkov@siemens.com> \* \* spi\_atusb.c is  
 Copyright (c) 2014 by Chris J Arges <chris.j.arges at canonical.com> \* \* Many codes borrowed from audio.c by \* Alan Cox (alan at  
 lxorguk.ukuu.org.uk) \* Thomas Sailer (sailer at ife.ee.et  
 Copyright status#define AC97\_ALC650\_PRE 0x0038 /\* Preemphasis status \*/#define AC97\_ALC650\_PRE\_SHIFT 3#define  
 AC97\_ALC650\_MODE 0x00C0 /\* Preemphasis status \*/#define  
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 Copyright 2016 Heiner Kallweit <hkallweit1@gmail.com> \* Based on drivers/hid/hid-thingm.c and \* drivers/usb/misc/usblcd.c \*/  
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 Copyright (C) 2011, Guennadi Liakhovetski <g.liakhovetski@gmx.de> \* \* Based on the MT9V032 driver and Bastian Hecht's code.  
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 Copyright (C) 2017-2018 Red Hat, Inc. All rights reserved  
 Copyright (c) 1998, 1999 Ky  
 Copyright (C) 2015-2016 Free Electrons  
 Copyright (c) 2020 Facebook#include <test\_progs.h>  
 (C) 1992, 1993 Eric Youngdale \* \* Rock Ridge Extensions to iso9660  
 Copyright (C) 2013 Daniel Tang <tangrs@tangrs.id.au> \* \* Based off drivers/usb/chipidea/ci\_hdrc\_msm.c  
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 \* \* TI OPP supply driver that provides override into the  
 Copyright (C) 2003 Thomas Charbonnel (thomas@undata.org)  
 Copyright: (C) 2011 Texas Instruments, Inc. \* \* Based on twl4030-vibra.c by Henrik Saari <henrik.saari@nokia.com> \* Felipe Balbi  
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 Copyright (C) 2003 - 2007 Paul Mundt  
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 Copyright IBM Corp. 2017 \* \* Author(s): Dong Jia Shi <bjsdjsi@linux.vnet.ibm.com> \* Xiao Feng Ren <renxiaof@linux.vnet.ibm.com>  
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 copyright: \* Copyright(c) 2013, Google Inc. \*/#ifndef \_ANALOGIX\_I2C\_DPTX\_H#define \_ANALOGIX\_I2C\_DPTX\_H  
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 Copyright (C) 2017-2018 Western Digital Corporation or its \* affiliates. \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/uio.h>#include  
 <linux/falloc.h>#include <linux/file.h>#incl  
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Copyright IBM Corp. 2009, 2015 \* \* Author(s): Heiko Carstens <heiko.carstens@de.ibm.com>,  
Copyright (C) 1999-2003 Ralph Metzler  
Copyright (C) 2004 Thiemo Seufer \* \* Hairy, the userspace application uses a different argument passing \* convention than the kernel, so we have to  
translate things from o32 \* to ABI64 calling con  
Copyright (C) 2015 FUJITSU LIMITED \* Author: Taku Izumi <izumi.taku@jp.fujitsu.com> \* \* This code introduces new boot option named  
"efi\_fake\_mem" \* By specifying this parameter, you can add arbit  
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Copyright (c) 2005 Maarten Deprez <maartendeprez@users.sourceforge.net>  
Copyright IBM Corporation 2007 \*/#include <sys/types.h>#include <sys/stat.h> /\* for mkdir() \*/#include <stdio.h>#include <errno.h>#include  
<string.h>#include <stdlib.h>#include <stdint.h>#inc  
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Copyright 2006 Johannes Berg <johannes@sipsolutions.net> \*/#include <linux/module.h>#include "alsa.h"  
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(c) Copyright 2006-2011 Wim Van Sebroeck <wim@iguana.be>.  
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Copyright (C) 2003 Kirk Reiser. \* \* this code is specifically written as a driver for the speakup screenreview \* package and is not a general device driver.  
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<linux/io.h>#include <linux/irqchip.h>#include <linux/irqchip/vers  
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Copyright (C) 2003 Geert Uytterhoeven \* \* Loosely based on drivers/pci/pci-sysfs.c  
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Copyright (C) 2018 IBM Corporation. \*/#include "ptrace.h"#include "child.h"  
Copyright (c) 2013-2017 Hisilicon Limited.  
Copyright (C) 1995. Send bug reports for the \* VFAT filesystem to <chaffee@cs.berkeley.edu>. Specify \* what file operation caused you trouble  
and if you can duplicate \* the problem, send  
Copyright (C) 2018 Renesas Electronics Corp. \*/#ifndef \_\_DT\_BINDINGS\_CLOCK\_R8A77990\_CPG\_MSSR\_H\_\_#define  
\_\_DT\_BINDINGS\_CLOCK\_R8A77990\_CPG\_MSSR\_H\_\_  
Copyright (c) 2011-2013, NVIDIA Corporation.  
Copyright(c) 2003-2011 Intel Corporation"#define DRV\_AUTHOR "ilw@linux.intel.com">  
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Copyright (c) by Scott McNab <sdm@fractalgraphics.com.au> \* \* Trident 4DWave-NX memory page allocation (TLB area) \* Trident chip can handle  
only 16MByte of the memory at the same time.  
Copyright (C) 2007 Jan D. Louw <jd.louw@mweb.co.za>  
Copyright (C) 2007 MIPS Technologies, Inc. \*/#include <linux/fs.h>#include <linux/fcntl.h>#include <linux/kernel.h>#include  
<linux/linkage.h>#include <linux/export.h>#include <linux/sched.h>#i  
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Copyright (c) 2004 James Courtier-Dutton <James@superbug.demon.co.uk> \* Driver CA0106 chips. e.g. Sound Blaster Audigy LS and Live 24bit \*  
Version: 0.0.25 \* \* FEATURES currently supported:  
Copyright (C) 2016 IBM Corporation \* \* Based on kexec-tools' kexec-elf-exec.c and kexec-elf-ppc64.c. \* Heavily modified for the kernel by \* Thiago  
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futex2, N at a time.  
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<linux/mutex.h>#include <linux/nvmem-provider.h>#include  
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Copyright (C) 2015 Imagination Technologies, Inc. \* written by Ralf Baechle <ralf@linux-mips.org> \*/#ifndef  
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Copyright (C) 2002, 2003, 2004 Dell Inc. \* by Matt Domsch <Matt\_Domsch@dell.com> \* \* structures and definitions for the int 13h, ax={41,48}h \*  
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Dr. Cliff Click: \*  
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COPYRIGHT); snd\_emu10k1x\_ptr\_write(chip, SPCS1, 0, chip->spdif\_bits[1] = SPCS\_CLKACCY\_1000PPM |  
SPCS\_SAMPLERATE\_48 | SPCS\_CHANNELNUM\_LEFT | SPCS\_SOURCENUM\_UNSPEC |  
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(c) Copyright 2000 kernel concepts <nils@kernelconcepts.de> \* developed for \* Jentro AG, Haar/Munich  
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driver supports many devices and the idea is to let  
Copyright (c) 2000 by Takashi Iwai <tiwai@suse.de> \* \* Based on nm256\_audio.c OSS driver in linux kernel. \* The original author of OSS nm256 driver  
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Copyright (c) 2004 Texas Instruments. \* 1. Modified to support OMAP1610 32-KHz watchdog timer \* 2. Ported to 2.6 kernel \*  
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```

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  Copyright 2008 Juergen Beisert, kernel@pengutronix.de */#include <linux/gpio/driver.h>#include <linux/io.h>#include <linux/interrupt.h>#include
<linux/platform_device.h>#include <linux/bitops.h>
  Copyright (C) 2006 - 2008 Lemote Inc.
  Copyright (c) 2001-2008 LSI Corporation. *
  Copyright (C) 2004 Cavium Networks */#ifndef __ASM_MACH_CAVIUM_OCTEON_CPU_FEATURE_OVERRIDES_H#define
__ASM_MACH_CAVIUM_OCTEON_CPU_FEATURE_OVERRIDES_H
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```

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Copyright (c) 2019 MediaTek Inc. \*/  
Copyright 2009 Larry Finger <Larry.Finger@lwfinger.net> \* \* Based on the LED handling in the r8187 driver, which is:  
Copyright (c) 2008 Jonathan Cameron  
Copyright 2005-2006 Openedhand Ltd.// Author: Richard Purdie <rpurdie@openedhand.com>  
Copyright (C) 2020, Google LLC. \*/#include <stdlib.h>#include <ctype.h>#include <limits.h>#include <assert.h>#include "test\_util.h"  
Copyright 2010-2011 Freescale Semiconductor, Inc.  
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Copyright (C) 2009 by Jan Weitzel Phytex Messtechnik GmbH <armlinux@phytec.de>  
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Copyright (c) 2008, Swedish Institute of Computer Science. \* All rights reserved  
Copyright (C) 2001 Stefan Gmeiner <riddlebox@freesurf.ch> \* start merging tpcfg and gpm code to a xfree-input module \* adding some  
changes and extensions (ex. 3rd and 4th button) \*  
Copyright (c) Siemens AG, 2011, 2012  
Copyright (C) 1999 by Ralf Baechle \*/#ifndef \_ASM\_IA64\_SN\_KLCONFIG\_H#define \_ASM\_IA64\_SN\_KLCONFIG\_H  
Copyright (C) 2002 Thibaut Varene <varenet@parisc-linux.org> \*  
Copyright 2012, Fabio Baltieri <fabio.baltieri@gmail.com> \* \* Based on ledtrig-timer.c by Richard Purdie <rpurdie@openedhand.com>  
Copyright (C) 2004-2005 Richard Purdie \* - omap\_ts.[hc], ads7846.h, ts\_osk.c  
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McKenney <paulmck@linux.ibm.com> Hierarchical versio  
Copyright (c) 2016 Icenowy Zheng <icenowy@aosc.xyz> \* \* Based on ccu-sun8i-h3.h, which is:  
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Copyright (C) 2010 Gra \* \* based on twl4030\_bci\_battery.c by TI  
(C) 2000 David Brown <usb-storage@davidb.org> \* \* This driver was developed with information provided in FREECOM's USB \* Programmers Reference  
Guide. For further information contact Freecom \* (ht  
Copyright (C) 2008 Intel Corp. \* \* Author: Sarah Sharp \* Some code borrowed from the Linux EHCI driver.  
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Copyright IBM Corp. 2004, 2009 \* character device driver for reading z/VM system service records, \* Version 1.0 \* Author(s): Xenia Tkatschow  
<xenia@us.ibm.com> \* Stefan Weinhuber <wein@de.ibm.com>  
Copyright (C) 2003 \* Ethan Benson <erbenson@alaska.net> \* partially derived from linux/fs/ext2/ioctl.c  
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Copyright (C) 2006 Alain <alain@knauff.lu > \* \*Based on small lzma deflate implementation/Small range coder \*implementation for lzma.  
Copyright (C) 2018 IVI Foundation, Inc. \* \* This file holds USB constants defined by the USB Device Class \* and USB488 Subclass Definitions for Test  
and Measurement devices \* published by the USB-  
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(C) Copyright 2000 Linuxcare Canada, Inc.  
(c) Copyright 2001 Scott Jennings <linuxdrivers@oro.net> \* 9/27 - 2001 [Initial release] \* \* Additional fixes Alan Cox \* - Fixed  
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Copyright (C) 2003, 2004, 2005 Andriy Skulysh  
(C) 2012-2013 by Pablo Neira Ayuso <pablo@netfilter.org>  
Copyright (c) 2009-2011, Dan Magenheimer, Oracle Corp. \* \* This code complements the cleancache and frontswap patchsets to optimize \* support for  
Xen Transcendent Memory ("tmem"). The policy it im  
Copyright (c) 2006-2010 Patrick McHardy <kaber@trash.net> \*/#include <linux/module.h>#include <linux/moduleparam.h>#include  
<linux/types.h>#include <linux/kernel.h>#include <linux/in.h>#include  
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\* \* For more information, please consult  
(c) Copyright 2005 4G Systems <info@4g-systems.biz> \* \* Release 0.01. \* Author: Michael Stickel michael.stickel@4g-systems.biz  
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Carstens <heiko.carstens@de.ibm.com>, \*/#ifndef \_\_ASM\_SMP\_H#define  
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(C) 1999 Rt-Control, Inc\n");  
(C) Copyright IBM Corp. 2001, 2003  
Copyright © 2012 NetCommWireless \* Iwo Mergler <Iwo.Mergler@netcommwireless.com.au> \* \* Test for multi-bit error recovery on a NAND page This  
mostly tests the \* ECC controller / driver.  
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<marvin24@gmx.de>  
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 -----  
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 Copyright (C) 2009-2011 ST-Ericsson AB \* Author: Martin Persson <martin.persson@stericsson.com> \* Author: Linus Walleij <linus.walleij@linaro.org>  
 \* \* The DB3350 design and control registers are o  
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 Copyright (C) 2008 Paul Hays <Paul.Hays@cattail.ca>  
 Copyright (c) 2001 La Monte H.P. Yarroll \* \* These are the definitions needed for the sctp\_ulpevent type. The \* sctp\_ulpevent type is used to carry  
 information from the state machine \* upwards to  
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 <linux/mm.h>  
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 Copyright (C) 2006-2008 Analog Devices Inc. \*/  
 Copyright (C) 2010-2011 ST-Ericsson \* \* Implements u8500 semaphore handling for protocol 1, no interrupts.  
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 Copyright (c) Steven Rostedt <ststedt@redhat.com> \*  
 Copyright 2016 NXP \*  
 Copyright (C) 2005 Dirk Behme \*/#include <linux/types.h>#include <linux/hwmon.h>#include <linux/err.h>#include <linux/sched.h>#include  
 <linux/delay.h>#include <linux/input.h>#include <linux/in  
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 timer framework support by Timo Teras \* \* Some part  
 Copyright (C) Amit Shah <amit.shah@redhat.com>, 2009, 2010, 2011 \*/#ifndef \_UAPI\_LINUX\_VIRTIO\_CONSOLE\_H#define  
 \_UAPI\_LINUX\_VIRTIO\_CONSOLE\_H#include <linux/types.h>#include <linux/virtio\_types.h>  
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 Copyright (c) by Jaroslav Kysela <perex@perex.cz> \* Routines for control of YMF724/740/744/754 chips \*/  
 Copyright (C) 2000 Deep Blue Solutions Ltd. \*/  
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 Copyright (C) 2014 by Renesas Electronics Corporation \* \* Because most IP blocks can only detect one I2C slave address anyhow, this \* driver does not  
 support simulating EEPROM types which take more

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Copyright (C) 2015-2016 Glider bvba \* \* Based on clk-rcar-gen3.c  
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driver \* by Imre Deak.  
Copyright (C) 1998 Eddie C. Dost (ecd@skynet.be)  
Copyright (C) 2005 Mike Isely <isely@pobox.com> \*/#ifndef \_\_PVRUSB2\_HDW\_INTERNAL\_H#define \_\_PVRUSB2\_HDW\_INTERNAL\_H  
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Copyright (C) 2014 H Hartley Sweeten <hsweeten@visionengravers.com> \* \* Based on 8253.h and various subdevice implementations in comedi  
drivers.  
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<linux/types.h>#include <asm/sni.h>  
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@author Andi Kleen \* @author Robert Richter <robert.ric  
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Copyright (C) 2002 Peter Hettkamp <peter.hettkamp@htp-tel.de> \* \* large parts based on the bttv driver  
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Copyright (c) 2011 Bryan Schumaker <bjschuma@netapp.com> \* \* Uses debugfs to create fault injection points for client testing  
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Copyright 2008-2011 DENX Software Engineering GmbH \* Author: Heiko Schocher <hs@denx.de> \* \* based on code from:  
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Copyright 2012 Maarten Lankhorst  
Copyright 2007 Dension Audio Systems Ltd.// Author: Zoltan Devai  
(C) 2004 jornada 720 kbd driver by Alex Lange <chicken@handhelds.org> \*/#include <linux/device.h>#include <linux/errno.h>#include  
<linux/interrupt.h>#include <linux/input.h>#include <linux/ker  
Copyright (c) 1993, 1994 Motorola Inc.All rights reserved  
Copyright (C) 2013-2017 Oracle Corporation \* This file is based on ast\_fb.c  
Copyright (C) 1997, 2007 David S. Miller (davem@davemloft.net) \* \* These routines maintain argument size conversion between 32bit and 64bit \*  
environment.  
Copyright (C) 2007 David S. Miller <davem@davemloft.net> \*/  
Copyright (c) 2015 Cambridge Silicon Radio Limited, a CSR plc group company.  
(C) 1995-2001 Russell King v0.04\n";  
Copyright (C) 2007 Ralf Baechle <ralf@linux-mips.org> \*/#include <linux/clockchips.h>#include <linux/interrupt.h>#include <linux/percpu.h>#include  
<linux/smp.h>#include <linux/irq.h>  
Copyright 2001 by Micro Solutions, Inc., DeKalb IL.\n",pi->device); printk("%s: BACKPACK %s, Micro Solutions BACKPACK Drive at 0x%x\n", pi-  
>device,BACKPACK\_VERSION,pi->port); printk("%s: Unit: %d  
Copyright (C) 2004 Texas Instruments \* \* This driver is based on the Linux io\_ti driver, which is  
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Copyright (c) by Jaroslav Kysela <perex@perex.cz> \* DRAM access routines \*/  
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Copyright (C) 2004-2007 James Smart, Emulex Corporation \* Rewrite for host, target, device, and remote port attributes, \* statistics, and service  
functions... \* Add vports, etc \*  
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Copyright (C) 2010 Marcelo Roberto Jimenez <mroberto@cpti.cetuc.puc-rio.br>  
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Copyright (C) 2013 Aeroflex Gaisler \* \* This driver supports the APBPS2 PS/2 core available in the GRLIB \* VHDL IP core library.  
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Copyright (C) 2018, Richtek Technology Corporation \* \* Richtek RT1711H Type-C Chip Driver  
Copyright (c) 2016 BayLibre, SAS. \* Author: Neil Armstrong <narmstrong@baylibre.com> \*/#include <linux/platform\_device.h>#include  
<linux/mfd/syscon.h>#include "clk-regmap.h"#include "meson-aoclk  
Copyright (C) 05/1998 Peter Maydell <pmaydell@chiark.greenend.org.uk> \* \* Most of this stuff was obtained by looking at other LANCE drivers, \* in  
particular a2065.[ch]. The AMD C-LANCE datasheet wa  
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Copyright (c) 2001 La Monte H.P. Yarroll \* \* These are the definitions needed for the sctp\_ulpq type. The \* sctp\_ulpq is the interface between the  
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Copyright (C) 2000 Matthew Wilcox (willy a debian . org)  
Copyright (C) 2004-2008 Jean Delvare <jdelvare@suse.de> \* Based on the lm90 driver. \* \* The LM63 is a sensor chip made by National  
Semiconductor. It measures \* two temperatures (its own and one e  
Copyright (c) 2018 Covalent IO, Inc. http://covalent.io#undef SOCKMAP#define TEST\_MAP\_TYPE BPF\_MAP\_TYPE\_SOCKHASH  
Copyright (C) 1998 V. Roganov and G. Raiko \* \* Code is derived from Ultra/PCI PSYCHO controller support, see that \* for author info.  
Copyright (C) 2014-2015 SUSE Linux GmbH \* Written by: Hannes Reinecke <hare@suse.de> \* Modified by: Damien Le Moal  
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 <linux/moduleparam.h>#include <linux/io.h>#include <s  
 Copyright (C) 2006, Timesys Corp., Thomas Gleixner <tglx@timesys.com> \* \* This file contains macros used solely by rtmutex.c. Debug version.  
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 Copyright (C) 2013 Oliver Smith <oliver@8.c.9.b.0.7.4.0.1.0.0.2.ip6.arpa>  
 Copyright (C) 2011 taskit GmbH \* \* Author: Christian Glindkamp <christian.glindkamp@taskit.de>  
 Copyright 2008 Thomas Bogendoerfer <tsbogend@alpha.franken.de> \* Mxier part taken from mace\_audio.c:  
 Copyright (C) 1995-1999 Gary Thomas, Paul Mackerras, Cort Dougan. \*/#ifndef \_ASM\_POWERPC\_PPC\_ASM\_H#define \_ASM\_POWERPC\_PPC\_ASM\_H  
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 based on pci-exynos.c and pcie-designware.c  
 Copyright (c) 2009 QLogic Corporation. All rights reserved  
 Copyright (C) 2003 IBM. \*/#include <linux/object.h>#include <linux/string.h>#include <linux/pci.h>#include <linux/pci\_hotplug.h>#include  
 "rpaphp.h"#include "rpadlpar.h"#include "../pci.h"  
 (C) (diagram), 9.2 \* Upon reception of the SHUTDOWN COMPLETE chunk the endpoint will verify \* that it is in SHUTDOWN-ACK-SENT state, if it is not  
 the chunk should be \* discarded. If the endpoint is  
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 Copyright 2000 Hewlett Packard (Paul Bame, bame@puffin.external.hp.com)  
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 ~~~~~  
 Copyright (C) 2005-2006 Andrey Volkov <avolkov@varma-el.com>, \* Varma Electronics Oy  
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 Copyright 2001, 2003-2005 MontaVista Software Inc.  
 Copyright message. \* 0.0.19 \* Finally fix support for SB Live 24 bit. SB0410 and SB0413. \* The output codec needs resetting, otherwise all output is  
 muted. \* 0.0.20 \* Merge "pci\_disabl  
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 (C) 2010 Bartłomiej Zolnierkiewicz \* \* based upon  
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 \_\_ASM\_MIPS\_PMC\_SIERRA\_WAR\_H  
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 <jedu@slimlogic.co.uk> \* Author: Arnaud Deconinck <a-deconinck@ti.com>  
 Copyright (C) 2011 ST Microelectronics  
 Copyright (C) 2012 Guenter Roeck <linux@roeck-us.net> \* \* Derived from w83627ehf driver  
 (C) 1992, 1993 Linus Torvalds, (C) 1997 Ingo Molnar \* \* moved some of the old arch/i386/kernel/irq.h to here. VY  
 Copyright 2009 Ben Skeggs  
 Copyright (C) 2008 Steven Rostedt <srostedt@redhat.com> \*/#include <linux/kallsyms.h>#include <linux/seq\_file.h>#include  
 <linux/spinlock.h>#include <linux/irqflags.h>#include <linux/uaccess.h>#  
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<benh@kernel.crashing.org>  
Copyright (c) 2005 Dirk Opfer \* \* Based on code written by Sharp/Lineo for 2.4 kernels  
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Copyright (c) Ahmet \* \* Major (almost complete) rewrite:  
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Copyright (C) 2011 Renesas Solutions Corp. \* Kuninori Morimoto <kuninori.morimoto.gx@renesas.com> \*/#ifndef RENESAS\_USB\_PIPE\_H#define RENESAS\_USB\_PIPE\_H  
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Copyright (c) by James Courtier-Dutton <James@superbug.co.uk> \* Added support for Audigy 2 Value. \* Added EMU 1010 support. \*  
General bug fixes and enhancements. \* \* BUGS: \* --  
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Copyright 2014 Linaro Ltd. \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/slab.h>#include <linux/usb.h>#include  
<linux/usb/hcd.h>  
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(C) 2004 Kaleb Lember <kaleb@smartlink.ee> \* \* Author: David Woodhouse <dwmw2@infradead.org> \* Additional Diskonchip 2000 and Millennium support by Dan Brown <dan\_brown@ieee.org> \* Diskonchip Mill  
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Copyright 2013 Red Hat, Inc. \* Author: Daniel Borkmann <dborkman@redhat.com> \* Chetan Loke <loke.chetan@gmail.com> (TPACKET\_V3 usage example) \* \* A basic test of packet socket's TPACKET\_V1  
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Tosatti \*/#ifndef LINUX\_LP\_H#define LINUX\_LP\_H  
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Adapters.
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COPYRIGHT); snd_emu10k1x_ptr_write(chip, SPCS2, 0, chip->spdif_bits[2] = SPCS_CLKACCY_1000PPM |
SPCS_SAMPLERATE_48 | SPCS_CHANNELNUM_LEFT | SPCS_SOURCENUM_UNSPEC |
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Copyright (C) 2016 BayLibre SAS * * Author:
Copyright (c) 2014 Broadcom Corporation */#ifdef CONFIG_OFvoid brcmf_of_probe(struct device *dev, enum brcmf_bus_type bus_type,
struct brcmf_mp_device *settings);#elsestatic void brcmf_of_p
Copyright 2012-2017 Solarflare Communications Inc. */
Copyright (C) 2008 Openmoko, Inc.
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```



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Copyright 2017, Gustavo Romero, Breno Leitao, Cyril Bur, IBM Corp.  
(C) Adam Belay. \*/#include <linux/module.h>#include <linux/pci.h>#include <linux/pnp.h>#include <linux/string.h>#include  
<linux/kernel.h>#include <linux/serial\_core.h>#include <linux/bitops.h>  
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Copyright 2001, 2002 Red Hat. \* \* Mmmm, chocolate vanilla mocha...  
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(C) IP\_VS\_DBG\_ADDR((C)->af, ntohs((C)->cport), IP\_VS\_DBG\_ADDR((C)->af, &((C)->vaddr)), \  
ntohs((C)->vport), \ IP\_VS\_DBG\_ADDR((C)->daf, &((C)->daddr)), \ ntohs((C)->dport), \

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Copyright 2001 David Brownell  
(C) Copyright 2005 - 2010 Paul Mundt \* \* Re-map IO memory to kernel address space so that we can access it. \* This is needed for high PCI addresses  
that aren't mapped in the \* 640k-1MB IO memory  
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Copyright (c) 2017 Cadence// Cadence PCIe host controller driver.// Author: Cyrille Pitchen <cyrille.pitchen@free-electrons.com>  
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<amit.kucheria@verdurent.com>  
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Copyright (C) 2010 Texas Instruments Inc.  
Copyright (c) 2010 Samsung Electronics Co., Ltd. \* http://www.samsung.com \* \* PATA driver for Samsung SoCs. \* Supports CF Interface in  
True IDE mode. Currently only PIO mode has been \* implement  
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<linux/io.h>#include <linux/notifier.h>#include

(C) 2005-2006 Harald Welte <laforge@gnumonks.org> \* - add support for poll() \* - driver cleanup \* - add waitqueues \* - adhere to linux kernel coding style and policies \* - support 2.6.13 "ne  
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Copyright (C) 2002 Hewlett-Packard Company  
(C) Copyright 2002 Hewlett-Packard Company \* \* Bus glue for Toshiba Mobile IO(TMIO) Controller's OHCI core  
(C) 1999, 2000 Silicon Graphics, Inc. \*/#include <linux/bcd.h>#include <linux/clockchips.h>#include <linux/init.h>#include <linux/kernel.h>#include <linux/sched.h>#include <linux/sched\_clock.h>  
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Copyright (c) 2005 Dirk Opfer \* \* Based on Sharp's 2.4 kernel patches  
Copyright (C) 2014 Akihiro Tsukada <tskd08@gmail.com> \*/#include <linux/dma-mapping.h>#include <linux/kernel.h>#include <linux/pci.h>  
Copyright (c) 1997 Dan Malek (dmalek@jlc.net). \*/#define BD\_SC\_EMPTY ((ushort)0x8000) /\* Receive is empty \*/#define BD\_SC\_READY ((ushort)0x8000) /\* Transmit is ready \*/#define BD\_SC\_WRAP ((ushort)  
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Copyright (c) 2011, Intel Corporation. \* \* Author: Mathias Nyman <mathias.nyman@linux.intel.com> \* Based on intel\_pmic\_gpio.c  
Copyright (C) 2003 Winfried Ritsch (IEM) \* based on hdsp.h from Thomas Charbonnel (thomas@undata.org) \*  
Copyright 2017 IBM Corp. #ifndef \_OCXL\_CONFIG\_H #define \_OCXL\_CONFIG\_H  
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Copyright IBM Corp. 2007-2010 Mel Gorman <mel@csn.ul.ie> \*/ #include <linux/cpu.h> #include <linux/swap.h> #include <linux/migrate.h> #include  
<linux/compaction.h> #include <linux/mm\_inline.h> #inclu  
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Copyright (C) 2009 - 2011 Renesas Solutions Corp. \* Kuninori Morimoto <morimoto.kuninori@renesas.com> \* Paul Mundt <paul.mundt@renesas.com>  
\* \* Based on SH7785 Setup  
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Copyright (C) 2004, 2010 Nokia Corporation \* Written by Timo Teras <ext-timo.teras@nokia.com> \* \* Converted to x\_tables and forward-ported to  
2.6.34 \* by Luciano Coelho <luciano.coelho@nokia.com>  
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by:  
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Copyright (C) 2016-2017 Christoph Hellwig. \*/ #include <linux/interrupt.h> #include <linux/kernel.h> #include <linux/slab.h> #include  
<linux/cpu.h> #include <linux/sort.h>  
Copyright(C) 2015-2018 Linaro Limited. \* \* Author: Tor Jeremiassen <tor@ti.com> \* Author: Mathieu Poirier <mathieu.poirier@linaro.org>  
Copyright 2008/2009 Benjamin Herrenschmidt, IBM Corp \* <benh@kernel.crashing.org> \* \* Some bits and pieces from  
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Copyright (C) STMicroelectronics SA 2014 \* Author: Fabien Dessenne <fabien.dessenne@st.com> for STMicroelectronics. \*/ #include <linux/seq\_file.h>  
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Copyright (C) 2017 Linaro Ltd. \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/debugfs.h>#include <linux/device.h>#include  
<linux/platform\_device.h>#include <linux/qrtr.h>#  
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Copyright (C) 2000 Philipp Rumpf (prumpf@tux.org) \* \* Cache and TLB management  
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Copyright(c) 2008 John W. Linville <linville@tuxdriver.com> \* \* Portions copied from old ieee80211 component, w/ original copyright \* notices below: \*  
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Copyright (C) 1998 Harald Koerfgen \* \* Rewritten extensively for controller-driven IRQ support \* by Maciej W. Rozycki.  
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(C) 2001-2002 Andras Kis-Szabo <kisza@sch.bme.hu>  
Copyright (C) 2013 Imagination Technologies Ltd. \*/#include <linux/kernel.h>#include <linux/fs.h>#include <linux/syscalls.h>#include  
<linux/moduleloader.h>#include <linux/atomic.h>#include <lin  
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cleaned up and \* made readable and Linux style by Wolfgang Zuleger <w  
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Copyright (C) 2002-2003 Hewlett-Packard Co \* David Mosberger-Tang <davidm@hpl.hp.com> \*/#ifndef \_ASM\_IA64\_INTRINSICS\_H#define  
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 Copyright (C) 2016 \* Author: Ruslan Bilovol <ruslan.bilovol@gmail.com> \* \* Sound card implementation was cut-and-pasted with changes \* from  
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operations initialization. \*  
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copyright of the respective owners.\*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/module.h>#include <linux/string.h>#include  
<linux/slab.h>#include <linux/delay.h>#inc  
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Copyright (C) 2008 Cambridge Silicon Radio Ltd. \*/#include <linux/slab.h>#include <linux/kernel.h>#include <linux/debugfs.h>#include  
<linux/seq\_file.h>#include <linux/export.h>  
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partition setup  
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allocation can be done.  
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Copyright (C) 1993 Pascal Haible, Bruno Haible \* \* [sysv/ialloc.c](http://sysv/ialloc.c)  
Copyright (C) Intel Corporation 2017 \*/#include <linux/kernel.h>#include <linux/string.h>#include <linux/ratelimit.h>#include  
<linux/mmu\_context.h>#include <asm/desc\_defs.h>#include <asm/desc.h>  
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Copyright: 2011 Raufeld GmbH \* Author: Johannes Stezenbach <js@sig21.net> \*/#ifndef \_\_LINUX\_SND\_\_STA32X\_H#define  
\_\_LINUX\_SND\_\_STA32X\_H  
(C) Copyright 2004 Linus Torvalds \*/#include <linux/pci.h>#include <linux/mm.h>#include <linux/export.h>#include <asm/io.h>#include <asm/pci-  
bridge.h>#include <asm/isa-bridge.h>  
Copyright (C) 2009 STMicroelectronics \* \* I2C master mode controller driver, used in Nomadik 8815 \* and Ux500 platforms.  
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(C) \* A(W1) \* C(C) \* \* Which would create W1->C->W1 dependencies, even though there is no \* actual deadlock  
possible. There are two solutions, using a \* read-recursive acquire on the wor  
Copyright (C) 2005 Ilya A. Volynets-Evenbakh <ilya@total-knowledge.com> \* swiped from i386, and cloned for MIPS by Geert, polished by Ralf. \* IP32  
changes by Ilya.  
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addresses (or in the future maybe other \* high-level  
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 Copyright (c) 2006 Rick Koch <n1gp@hotmail.com>  
 Copyright (C) 1992 Krishna Balasubramanian \* \* Removed all the remaining kernel mess \* Catch the -EFAULT stuff properly \* Use GFP\_KERNEL for  
 messages as in 1.2 \* Fixed up the unchecked user spac  
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 Copyright (C) 1998, 1999, 2002-2003 Hewlett-Packard Co \* David Mosberger-Tang <davidm@hpl.hp.com> \*/#include <linux/types.h>  
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 Copyright (C) 2009, Dongsoo Nathaniel Kim <dongsoo45.kim@samsung.com>  
 Copyright (C) 2016 Martin Blumenstingl <martin.blumenstingl@googlemail.com> \* \* Some devices (like the Cisco Meraki Z1 Cloud Managed Teleworker  
 Gateway) \* need to be able to initialize the PCIe wif  
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Copyright (C) 1998-2001 David Mosberger-Tang <davidm@hpl.hp.com> \* \* 00/03/06 D. Mosberger Fixed to return proper return value (bug found by \*  
by Andreas Schwab <schwab@suse.de>).  
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COPYRIGHT | IEC958\_AES0\_CON\_EMPHASIS; value->value.iec958.status[1] = IEC958\_AES1\_CON\_CATEGORY |  
IEC958\_AES1\_CON\_ORIGINAL; return 0;}  
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from MIPS.  
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Copyright (C) 2013 Huawei Ltd. \* Author: Jiang Liu <liuj97@gmail.com> \* \* Based on arch/arm/include/asm/jump\_label.h  
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examining the MSFT drivers' debug output \* (insanely lef  
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(C) 2001 Dimitromanolakis Apostolos <apdim@grecian.net> \* \* Based in the radio Maestro PCI driver. Actually it uses the same chip \* for radio but different pci controller.  
Copyright (c) 2008 Tomasz Grobelny <tomasz@grobelny.oswiecenia.net>  
Copyright 2015 Maciej Szmigiero <mail@maciej.szmigiero.name> \* \* Based on ad7414.c:  
Copyright 2012 Red Hat, Inc.  
Copyright (C) 2015, Huawei Inc. \*/#ifndef \_\_BPF\_PROLOGUE\_H#define \_\_BPF\_PROLOGUE\_H  
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Copyright (C) 2014-2015 Texas Instruments, Inc. \* \* Dave Gerlach <d-gerlach@ti.com> \* Suman Anna <s-anna@ti.com>  
Copyright (C) 2007 Marvell International Ltd.  
Copyright (c) 2012 Samsung Electronics Co., Ltd. \* http://www.samsung.com \* \* Register definition file for Samsung G-Scaler driver  
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Copyright (C) 2016 Intel Corporation \* Author: Lv Zheng <lv.zheng@intel.com> \*  
Copyright (C) 2002 Dell Inc.  
Copyright (C) 2011 ST-Ericsson SA \* Written on behalf of Linaro for ST-Ericsson \* This interface is used in the core to keep track of pins. \* \* Author:  
Linus Walleij <linus.walleij@linaro.org>  
(C) 1999 Thomas Bogendoerfer (tsbogend@alpha.franken.de) \* \* based on code from Oliver Jowett <oliver@jowett.manawatu.gen.nz>  
Copyright (C) 2015-2017 Socionext Inc.  
Copyright (C) 1998 Franz Sirl \* \* Frame buffer structure from:  
Copyright (c) 2017, Mellanox Technologies. All rights reserved  
Copyright (C) 2017 Hauke Mehrtens <hauke@hauke-m.de> \*/#ifndef \_DT\_BINDINGS\_MIPS\_LANTIQ\_RCU\_GPHY\_H#define  
\_DT\_BINDINGS\_MIPS\_LANTIQ\_RCU\_GPHY\_H  
Copyright (C) 2006-2013 Texas Instruments. \*  
Copyright (C) 2013 Davidlohr Bueso <davidlohr@hp.com> \* \* futex-hash: Stress the hell out of the Linux kernel futex uaddr hashing.  
Copyright (C) 2003 Maciej W. Rozycki  
Copyright (C) 1996-2003 Russell King  
Copyright (C) 2004 by Ralf Baechle \* \* The cpustart method is a PMC-Sierra's function to start the secondary CPU. \* Stock PMON 2000 has the smpfork, semlock and semunlock methods instead.  
Copyright (c) 2016 HiSilicon Limited.  
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Copyright 2008 Embedded Alley Solutions, Inc All Rights Reserved  
Copyright (c) 2006, Ian Campbell, XenSource Inc. \*/#include <linux/types.h>#include <linux/cache.h>#include <linux/export.h>  
Copyright (C) 2002 Maciej W. Rozycki \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/sched.h>#include  
<linux/sched/debug.h>#include <linux/sched/signal.h>#include <asm/traps.  
Copyright (c) 2006, 2007 Cisco Systems, Inc. All rights reserved  
Copyright (c) 2017 Chao Yu <chao@kernel.org>  
COPYRIGHT; ucontrol->value.iec958.status IEC958\_AES1\_CON\_ORIGINAL; ucontrol->value.iec958.status[3] =  
IEC958\_AES3\_CON\_FS; return 0;}  
Copyright (C) 2014 Zi Shen Lim <zlim.lnx@gmail.com>  
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Copyright (c) 2000-2002 Vojtech Pavlik \* \* Based on the work of:  
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Copyright (C) 2018 Intel Corporation \* Author: Rafael J. Wysocki <rafael.j.wysocki@intel.com> \* \* The idea of this governor is based on the observation  
that on many systems \* timer events are two  
Copyright 1999 Derrick J Brashear (shadow@dementia.org) \* \* Driver layout based loosely on tga6b.c, see that file for credits.  
Copyright 2016 Jonathan Cameron <jic23@kernel.org>  
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Copyright 2007 Dension Audio Systems Ltd. \* Author: Zoltan Devai \* \* Based on the WM87xx drivers by Liam Girdwood and Richard Purdie  
Copyright IBM Corp. 1999 \* Author(s): Hartmut Penner (hp@de.ibm.com), \* Martin Schwidefsky (schwidefsky@de.ibm.com) \* \* Derived  
from "include/asm-i386/processor.h"  
Copyright (C) 2016 Icenowy Zheng <icenowy@aosc.xyz>  
Copyright (C) 2006 Compulab, Ltd. \* Mike Rapoport <mike@compulab.co.il> \* - Creation of driver \* \* Based on pxa6b.c  
Copyright (C) 2010 Mauro Carvalho Chehab  
Copyright 1993 United States Government as represented by the Director, National Security Agency.  
Copyright (c) 2004-2007 Reyk Floeter <reyk@openbsd.org>  
(C) Copyright Al Viro 2002,2003 \* \* NOTE: data structure needs to be changed. It works, but for large dev\_t \* it will be too slow. It is isolated, though,  
so these changes will be \* local to tha  
Copyright (C) IBM Corporation, 2011 \* \* Author: Anton Blanchard <anton@au.ibm.com>  
Copyright © 2014 Intel Corporation \* Daniel Vetter <daniel.vetter@ffwll.ch> \* \* Author: Rickard E. (Rik) Faith <faith@valinux.com> \* Author: Gareth  
Hughes <gareth@valinux.com>  
Copyright: Monta Vista Software, Inc.  
Copyright (C) 1998 Pete Zaitcev <zaitcev@yahoo.com> \*/  
Copyright (C) 2006 Benjamin Herrenschmidt, IBM Corporation \* \* Provide default implementations of the DMA mapping callbacks for \* directly mapped  
busses.  
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Copyright (C) 1998 V. Roganov and G. Raiko \*/  
Copyright (C) 2005 Mike Isely <isely@pobox.com> \*/#ifndef \_\_PVRUSB2\_UTIL\_H#define \_\_PVRUSB2\_UTIL\_H  
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Copyright (c) 2003-2005 Silicon Graphics, Inc. All rights reserved  
Copyright IBM Corp. 2004 2005  
Copyright (C) 2012 Anssi Hannula <anssi.hannula@iki.fi> \* \* This keymap is for a variant that has a distinctive scrollwheel instead of \* up/down buttons  
(tested with P/N 40009936 / 20018268), repor  
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Copyright (c) 2013 Coraid, Inc. See COPYING for GPL terms./ \* \* aonet.c \* Ethernet portion of AoE driver  
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Copyright (C) 2008-2010 Nokia Corporation \* Contact: Felipe Balbi <felipe.balbi@nokia.com> \* \* This gadget driver borrows from serial.c which is: \*  
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Copyright (C) 2008 Texas Instruments, Inc. \* Rajendra Nayak <rnayak@ti.com> \*  
 Copyright (C) 2013-2016, Linaro Ltd. \* Author: Hanjun Guo <hanjun.guo@linaro.org> \*\* Reads the ACPI SRAT table to figure out what memory belongs to which CPUs.  
 Copyright 2006 Endreia Technologies Inc. \*\* Based on pxa2xx-pcm.c by: \*  
 Copyright (C) 2004 Konrad Eisele (eiselekd@web.de, konrad@gaisler.com) Gaisler Research  
 Copyright (c) 2002, 2006, 2008 David S. Miller (davem@davemloft.net) \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/init.h>#include <linux/input.h>#include <linux/of\_device.h>  
 Copyright (C) 2005 Tower Technologies \* based on nslu2-io.c:  
 Copyright (c) 2006 Oliver Neukum <oliver@neukum.name> \*\* USB Printer Device Class driver for USB printers and printer cables  
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 Copyright (C) 2007 Carlos Corbacho <carlos@strangeworlds.co.uk>  
 Copyright (C) 2017 Chen-Yu Tsai \* Chen-Yu Tsai <wens@csie.org> \*\* Based on pinctrl-sun50i-a64-r.c  
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 Copyright (C) 2011 Becky Bruce, Freescale Semiconductor \*\* Based on the IA-32 version:  
 Copyright (C) 2012 Google, Inc. \*/#include <linux/dma-mapping.h>#include <linux/err.h>#include <linux/genalloc.h>#include <linux/io.h>#include <linux/mm.h>#include <linux/scatterlist.h>#include  
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 Copyright (C) 2007 Red Hat, Inc., Ingo Molnar <mingo@redhat.com> \*\* Interactivity improvements by Mike Galbraith  
 Copyright (c) 2011 Stefan Achatz <erazor\_de@users.sourceforge.net> \*/  
 (C) ordering is still observed by the pmu driver. \*/ if (!rb->aux\_overwrite) { aux\_tail = READ\_ONCE(rb->user\_page->aux\_tail);  
 handle->wakeup = rb->aux\_wakeup + rb->aux\_watermark; if (aux\_head  
 Copyright (C) 2009 Wind River Systems, \* written by Ralf Baechle <ralf@linux-mips.org> \*/#include <linux/init.h>#include <linux/irqflags.h>#include <linux/notifier.h>#include <linux/prefetch.h>  
 Copyright (C) 2003 - 2012 Paul Mundt  
 Copyright (C) 2003 by Ralf Baechle \*/#ifndef \_\_ASM\_PREFETCH\_H#define \_\_ASM\_PREFETCH\_H  
 Copyright 2012 Steffen Trumtrar <s.trumtrar@pengutronix.de> \*\* description of display timings  
 COPYRIGHT) { snd\_iprintf(buffer, "permitted\n"); } else { snd\_iprintf(buffer, "protected\n"); }  
 snd\_iprintf(buffer, "Emphasis: "); if ((status[0] & IEC958\_AES0\_CON\_EMPHASIS) != IEC958\_AE  
 Copyright (c) 1998-2001 Vojtech Pavlik \*/  
 Copyright (C) 2017 Oracle Corporation  
 Copyright (C) 2015, 2017-2018 \* Author: Matt Ranostay <matt.ranostay@konsulko.com> \*\* TODO: interrupt mode, and signal strength reporting  
 Copyright (c) 2007 Anton Vorontsov <avorontsov@ru.mvista.com>  
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 Copyright (C) 2002 Maxim Yevtyushkin <max@linuxmedialabs.com> \* \* Based on adv7176 driver by: \*  
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 Copyright (C) 2007 Nobuhiro Iwamatsu \* \* Hitachi UL SolutionEngine 7724 Support.  
 Copyright (C) 2006-2007, Advanced Micro Devices, Inc.  
 Copyright (C) 2009 Valentin Sidikov  
 Copyright (C) 1995 \*/#ifndef \_\_ASM\_POWERPC\_FLOPPY\_H#define \_\_ASM\_POWERPC\_FLOPPY\_H#endif \_\_KERNEL\_\_  
 Copyright (c) 2005-2006 Mauro Carvalho Chehab <mchehab@kernel.org>// SAA7111, SAA7113 and SAA7118 support  
 (C) Copyright 2002 Alan Cox <alan@lxorguk.ukuu.org.uk> \*/#include <linux/interrupt.h>#include <linux/kernel.h>#include <linux/types.h>  
 Copyright (C) IBM Corporation, 2004. All rights reserved  
 Copyright 2005-2009 Freescale Semiconductor, Inc.  
 Copyright (C) 2008-2009 Deep Root Systems, LLC \*/  
 Copyright (C) 2018 Gateworks Corporation \*/#include <linux/delay.h>#include <linux/hdmi.h>#include <linux/i2c.h>#include <linux/init.h>#include  
 <linux/interrupt.h>#include <linux/kernel.h>#inc  
 Copyright © 2004 Ferenc Havasi <havasi@inf.u-szeged.hu>, \* University of Szeged, Hungary \* \* Created by Arjan van de Ven  
 <arjan@infradead.org>  
 (C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Inode handling routines  
 Copyright (c) 2020 Facebook#define MAX\_STACK (512 - 3 \* 32)  
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 Copyright 2018 David Lechner <david@lechnology.com> \* \* Based on mi0283qt.c:  
 copyright; for (;;) while (\*++c != '\$') if (\*c == '\0') exit(0); while (\*++c != '\$')  
 putc(\*c, stderr); putc('\n', stderr); }  
 Copyright (C) 2013 Tom Zanussi <tom.zanussi@linux.intel.com> \*/  
 Copyright (C) 2004-2007 John Williams <john.williams@petalogix.com> \* \* derived from arch/v850/kernel/ptrace.c  
 Copyright (C) 2001 Grant Grundler (Hewlett Packard)  
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 Copyright (C) 2019 IBM Corporation \* Author: Nayna Jain \* \* This file initializes secvar operations for PowerPC Secureboot  
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 Copyright (c) 1997, 1998, 2000, 2001 Justin T. Gibbs.  
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 (C) 2012 Christoph Lameter <cl@linux.com> \*/#include <linux/slab.h>

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Copyright (C) 2010 ARM Ltd., Will Deacon <will.deacon@arm.com> \*/#define pr\_fmt(fmt) "hw perfevents: " fmt  
Copyright (C) 2002,2004 MARA Systems AB <http://www.marasystems.com>  
Copyright (C) 2012 Sven Schnelle <svens@stackframe.org> \*/  
Copyright (C) 2008, Guennadi Liakhovetski, DENX Software Engineering <lg@denx.de>  
(C) 2014 by Pablo Neira Ayuso <pablo@netfilter.org> \*\* Based on code from ebt\_log from: \*  
Copyright (C) 2000 MIPS Technologies, Inc. \*/#ifndef \_ASM\_SWITCH\_TO\_H#define \_ASM\_SWITCH\_TO\_H  
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Copyright 2004 Paul Mackerras <paulus@samba.org> \*\* This is the power management code for ATI radeon chipsets. It contains \* some  
dynamic clock PM enable/disable code similar to what X.org does,  
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Copyright (C) 2010 Marc Zyngier <maz@misterjones.org> \*\* based on fsg-setup.c:  
Copyright (C) 2005 Nokia Corporation \* Written by Tony Lindgren <tony@atomide.com> \*  
Copyright (C) SAN People \* \* Parallel I/O Controller (PIO) - System peripherals registers.  
Copyright(c) 2016 Linaro Ltd. All rights reserved  
Copyright (C) 2007 Dave Airlie <airlied@linux.ie>  
Copyright (C) 2010 Devin Heitmueller <dheitmueller@kernellabs.com> \*/#include <linux/kernel.h>#include <linux/errno.h>#include  
<linux/slab.h>#include <linux/module.h>#include <linux/mm.h>#inclu  
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module is part of usbvision driver project. \* Updates t  
Copyright (C) 2002 by Peter Schildmann <peter.schildmann@web.de>  
Copyright 2004-2009 Simtec Electronics \* Ben Dooks <ben@simtec.co.uk> \* \* S3C - I2C Controller platform\_device info  
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Copyright (C) 2009 Zhang Le <r0bertz@gentoo.org> \* \* reference: /proc/cpuinfo, \* arch/mips/kernel/cpu-probe.c(cpu\_probe\_legacy), \*  
arch/mips/kernel/proc.c(show\_cpuinfo), \* loongson2f user manual.  
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Copyright 2008 Juergen Beisert, kernel@pengutronix.de \* \* This contains i.MX27-specific hardware definitions. For those \* hardware pieces that are  
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copyright + flash\_ver images, 0 for failure.static u32 chk\_cfg(u8 \*cfg, u32 length, u32 \*flash\_ver){ u16 \*pw = (u16 \*)cfg - 1; u32 sz = 0; u32 len = length;

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Copyright (C) 2004 Jens Axboe <axboe@kernel.dk> \* \* Helper functions for setting/querying io priorities of processes. The \* system calls closely mimmick getpriority/setpriority, see the man page fo  
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Copyright (C) 2005 - 2006 Mike Christie  
Copyright (C) 2006,2007 Thomas Bogendoerfer (tsbogend@alpha.franken.de) \* \* i8259 parts ripped out of arch/mips/kernel/i8259.c  
(C) State timer broadcast controlstatic void lapic\_timer\_state\_broadcast(struct acpi\_processor \*pr, struct acpi\_processor\_cx \*cx,  
int broadcast){ int state = cx - pr->power.st  
Copyright (c) by Jaroslav Kysela <perex@perex.cz> \* Routines for control of ESS ES1688/688/488 chip \*/  
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Copyright message. \* 0.0.17 \* Add iec958 file in proc file system to show status of SPDIF in. \* 0.0.18 \* Implement support for Line-in capture on  
SB Live 24bit. \* \* This code was initia  
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Copyright (C) IBM Corporation, 2002, 2004 \* \* 2002-Oct Created by Vamsi Krishna S <vamsi\_krishna@in.ibm.com> Kernel \* Probes initial  
implementation ( includes suggestions from \* Rusty Russell).  
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Copyright (c) 2004 James Courtier-Dutton <James@superbug.demon.co.uk> \* Driver CA0106 chips. e.g. Sound Blaster Audigy LS and Live 24bit \*  
Version: 0.0.18 \* \* FEATURES currently supported:

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<http://sourceforge.net/projects/garmin-gps/>  
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Copyright (C) 2010 Wang Lei. All Rights Reserved  
(C) 1999 Thomas Bogendoerfer (tsbogend@alpha.franken.de) \* \* 11/26/2000 -- disabled the existing code because it didn't work for \* me in 2.4.  
Replaced with a significantly more primitive version  
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Copyright (C) 2005 Maciej W. Rozycki  
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Copyright (C) 2019 IBM Corporation \* Author: Nayna Jain \* \* - loads keys and hashes stored and controlled by the firmware.  
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~~~~~  
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copyright information. \* Added MPI2\_BIOSPAGE1\_OPTIONS\_ADVANCED\_CONFIG. \* Added AdapterOrderAux fields to BIOS  
Page 3. \* 03-16-15 02.00.31 Updated for MPI v  
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Copyright (c) 2004 Erik Rigtorp <erikki@linux.nu> <erik@rigtorp.com> \* \* This driver works with Elektor magazine USB Interface as published in \*  
issue #291. It should also work with the original s  
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Copyright (C) 2009 David S. Miller (davem@davemloft.net) \*/#include <linux/kernel.h>#include <linux/export.h>#include <linux/init.h>#include  
<linux/irq.h>  
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Copyright (C) 2010 ST-Ericsson SA \* \* This is a generic driver for ARM AMBA-type serial ports. They \* have a lot of 16550-like features, but are not  
register compatible. \* Note that although they  
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Copyright IBM Corp. 1999 \* Author(s): Martin Schwidefsky (schwidefsky@de.ibm.com) \* \* Derived from "include/asm-i386/current.h"  
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\_\_ASM\_MIPS\_MACH\_IP28\_WAR\_H  
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Copyright unasserted) \* AN = 0 (Audio data) \* P = 0 (Consumer) \*/ def\_bits = SPCS\_CLKACCY\_1000PPM |  
SPCS\_SAMPLERATE\_48 | SPCS\_CHANNELNUM\_LEFT | SPCS  
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driver by Aaro Koskinen.  
Copyright (C) 2009 Theodore Kilgore <kilgota@auburn.edu> \* \* Support for the control settings for the CIF cameras is  
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VXGE\_MAIN\_H#define VXGE\_MAIN\_H  
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Copyright IBM Corp. 2016 \* Author(s): Martin Schwidefsky <schwidefsky@de.ibm.com> \* \* Adjunct processor bus, queue related code.  
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Copyright (C) 2008 Red Hat Inc, Steven Rostedt <srostedt@redhat.com> \* \* - Added format output of fields of the trace point. \* This was based off  
of work by Tom Zanussi <tzanussi@gmail.com>.  
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copyright) \*/static inline int in\_8(const volatile unsigned char{int ret};  
Copyright (C) 1992 Doug Evans \* \* coh/inode.c  
Copyright (C) 2012 Intel Corp (tianyu.lan@intel.com) \* \* move struct usb\_hub to this file.  
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Copyright (C) 2003 by Ralf Baechle \*/#include <linux/init.h>#include <linux/mm.h>#include <linux/memblock.h>#include <linux/highmem.h>#include  
<asm/fixmap.h>#include <asm/pgtable.h>#include <a  
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Copyright (C) 2001, 2002, 2003 Patrick Mochel \* \* Based on kobject.h:  
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 <linux/in.h>#include <linux/kernel.h>#include <linux/mod  
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 randomize.h>#include <linux/errno.h>#include <linux/mm.h  
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<asm/scfp.h>#include "compressed/decompressor.h"#include "boo  
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<asm/fpregdef.h>#include <asm/mipsregs.h>#include <asm/a  
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mq.h>#include <linux/elevator.h>#include <linux/bio.  
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```

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    */#ifndef ssc_h#define ssc_h
    Copyright (C) 2006 Red Hat, Inc., Ingo Molnar <mingo@redhat.com> */#include <linux/sched/task_stack.h>#include <linux/sched/debug.h>#include
    <linux/sched.h>#include <linux/kernel.h>#include <lin
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    Copyright (C) 2003 Ralf Baechle * 6/2004 pf */#ifndef __ASM_MACH_IP28_CPU_FEATURE_OVERRIDES_H#define
    __ASM_MACH_IP28_CPU_FEATURE_OVERRIDES_H
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    Copyright (c) 2019 Facebook#include <test_progs.h>#include "fexit_test.skel.h"
    (C) Copyright 2008, 2012 Intel Corporation
    Copyright (C) 1991, 1992 Linus Torvalds * * GK 2/5/95 - Changed to support mounting root fs via NFS * Added initrd & change_root: Werner
    Almesberger & Hans Lermen, Feb '96 * Moan early if gc
  
```

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(C) Copyright IBM Corporation 2006 \* \* Author: David Erb (djerb@us.ibm.com) \* Modifications:  
Copyright (C) 2008 <srinivasa.deevi at conexant dot com> Based on em28xx driver  
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libbsd 0.10.0 unknown:undefined

(C) AC\_LINK\_IFELSE( AC\_LANG\_POP)) if test yes != "\$lt\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
belf CFLAGS=\$SAVE\_CFLAGS fi ;:\*-so

(C)!b go :more \./!{ N s|n# | | b more

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libcap-ng 0.7.10 openseuse:libcap-ng-python/0.7.10-1.3/armv6hl

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belf CFLAGS=\$SAVE\_CFLAGS fi ;\*-.\*so

(C)!b go :more \./!{ N s|n# || b more

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(C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test x"\$lt\_cv\_cc\_needs\_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;  
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(C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test yes != "\$lt\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
belf CFLAGS=\$SAVE\_CFLAGS fi ;;\*-so  
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libdb 5.3.28 fedora:libdb/5.3.28-9.fc22/armv7hl
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(C) 3 } else { set abandoned false }
(C) 3 process_msgs $envlist
(C) 3 process_msgs $envlist $envs(A) test force noarchive_timeout # Using small log file size, push into the second log file.
(C) 3 process_msgs $envlist error_check_good update.msg.sent \ $rep085_update_req_count [incr upd_before] error_check_good
no.page.msg $rep085_page_msg_count $pg_before if { !$env_private } {
(C) 3 proc_msgs_once $envlist
(C) 3 replclear 1 set succeeded [catch { process_msgs $envlist } ret] switch $result { internal_init { error_check_good
initd $succeeded 0
(C) AC_LINK_IFELSE( AC_LANG_POP) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need
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belf CFLAGS="$SAVE_CFLAGS" fi ;;sp
(C) "berkdb_env -create -txn $verbargs -home $clientdir -errpfx CLIENT -rep_client \ -rep_transport \[list 2 rep089_send\]" set clientenv
[eval $env_cmd(C)]
(C) "berkdb_env_noerr -create \ $c_logargs $envargs $verbargs -home $clientdir $repmemargs \ $privargs $c_txnargs -errpfx CLIENT -
rep_client \ -rep_transport \[list 2 replsend\]"
(C) "berkdb_env_noerr -create $c_logargs -errpfx ENV.C $verbargs $repmemargs \ -lock_detect default $privargs \ -home
$clientdir -rep_transport \[list 2 replsend\]" set env [eval
(C) "berkdb_env_noerr -create $c_logargs -home $clientdir $verbargs $repmemargs $privargs \ -rep_transport \[list 2 replsend\]"
(C) "berkdb_env_noerr -create $c_logargs -lock_detect default $verbargs $repmemargs \ $privargs \ -home $clientdir -rep_transport \[list 2
replsend\]"
(C) "berkdb_env_noerr -create $c_txnargs $c_logargs -home $clientdir -errpfx CLIENT $privargs \ -rep_client -rep_transport \[list 2 replsend\]"
(C) "berkdb_env_noerr -create \ $c_txnargs $c_logargs $verbargs -rep_client -errpfx CLIENT \ $privargs -home $clientdir $repmemargs \
-rep_transport \[list 2 replsend\]"
(C) "berkdb_env_noerr -create -home $c_txnargs $c_logargs -rep_client $verbargs -errpfx CLIENT \ -rep_transport \[list 2 replsend\]"
(C) "berkdb_env_noerr -create -lock_max_objects $max_locks -lock_max_locks $max_locks \ -home $clientdir -errpfx CLIENT $verbargs
$repmemargs \ $privargs \ -rep_client -rep_transpo
(C) "berkdb_env_noerr -create -lock_max_objects $max_locks -lock_max_locks $max_locks \ -home $clientdir -errpfx CLIENT $verbargs
$repmemargs -rep -thread"
(C) "berkdb_env_noerr -create -log_max 1000000 $envargs $c_logargs $recargs $repmemargs \ -home $clientdir -errpfx CLIENT $c_txnargs
-rep_client \ -rep_transport \[list 2 replsend\]" s
(C) "berkdb_env_noerr -create -log_max 1000000 $envargs $c_logargs $recargs $verbargs \ -home $clientdir -errpfx CLIENT $c_txnargs -
rep_client \ -rep_transport \[list 2 replsend\]" set
(C) "berkdb_env_noerr -create -log_max 1000000 $envargs -home $clientdir $c_logargs \ -errfile /dev/stderr -errpfx CLIENT -txn -rep_client \
-rep_transport \[list 2 replsend\]" set env
(C) "berkdb_env_noerr -create -private -home -txn $c_logargs -errpfx CLIENT $verbargs $repmemargs \ -rep_client -rep_transport \[list 2
replsend\]"
(C) "berkdb_env_noerr -create \ $verbargs $repmemargs $privargs \ -home $clientdir -errpfx CLIENT -txn nosync -rep_client \ -rep_transport
\[list 2 replsend\]" set clientenv [eval $env
(C) "berkdb_env_noerr -home -errpfx CLIENT -lock -log -txn" set clientenv [eval $env_cmd(C)]
(C)/!b go :more \./!{ N s/\n# // b more
(C) case_operand(X) case_explist(Y) case_else(Z) END(E). A.pExpr = sqlite3PEExpr(pParse, TK_CASE, X, Z, 0); if( A.pExpr ){ A.pExpr->x.pList = Y;
sqlite3ExprSetHeight(pParse, A.pExpr); }e
(C) (((c=C))>=0x42#endif
(C) (((c=C))>=0x42#endif#endif /* SQLITE_AMALGAMATION */
(C) (((c=C)#endif#ifdef SQLITE_EBCDICconst char sqlite3IsEbcidicIdChar[] = { /* x0 x1 x2 x3 x4 x5 x6 x7 x8 x9 xA xB xC xD xE xF */ 0, 0, 1, 1, 1, 1,
1, 1, 1, 1, 0, 0, 0, 0, 0, 0, /* 4x */

```

```

(C) close $envs(A) close $envs(B) close
(C) close $envs(B) close $envs(A) close replclose $testdir/MSGQUEUEUDIR
(C) close $envs(B) close replclose $testdir/MSGQUEUEUDIR}
(C) close replclose $testdir/MSGQUEUEUDIR}
(C) close replclose $testdir/MSGQUEUEUDIR error_check_good envclose [$e close] 0
(C) close set envs(B) [eval $env_B_cmd -recover -rep_master] set envs(A) [eval $env_A_cmd -recover -rep_client]
(C) close set envs(C) [eval $env_C_cmd -rep_client -recover] if { !$env_private } {
(C) close set envs(C) [eval $env_C_cmd -rep_client -recover] } join_failure { error_check_bad no_autoinit
$ succeeded 0 error_check_good join_fail \ [is_substr $ret DB_REP_JOIN_FAIL
(C) (C)->txn#define NOWAIT_FLAG(txn) \ ((txn) != NULL && F_ISSET((txn), TXN_NOWAIT) ? DB_LOCK_NOWAIT : 0)#define
IS_REAL_TXN(txn) \ ((txn) != NULL && !F_ISSET(txn, TXN_FAMILY))#define IS_SU
(C) $envs(C) process_msgs $envlist rep_verify $dirs($master) $envs($master) \ $dirs($test_client) $envs($test_client) $init_test
(C) JOIN. { X = sqlite3JoinType(pParse,&A,&B,&C); }
(C) last if { $new_last_log > $last_client_log } { set stop 1 }
(C) log_flush exec $util_path/db_archive -d -h $dirC }
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"SQLITEODBCU.DLL\0"#else VALUE "OriginalFile
Copyright 2004-2011 <chw@ch-werner.de>0" VALUE "LegalTrademarks", "\0" VALUE "OriginalFilename", "SQLITE3ODBC.DLL\0"
VALUE "PrivateBuild", "\0" VALUE
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code has been tested against that, and is
Copyright Oracle 1997, 2013\0" VALUE "OriginalFilename", "libdb53.dll\0" VALUE "ProductName", "Oracle libdb\0" VALUE
"ProductVersion", "5.3.28\0" END
copyright to this source code.
copyright to this source code.*//* First off, code is included that follows the "include" declaration** in the input grammar file. */#include <stdio.h>
copyright to this source code.*//* First off, code is include which follows the "include" declaration** in the input file. */#include <stdio.h>
copyright to this source code. In place of * a legal notice, here is a blessing: *
copyright to this source code. In place of * a legal notice, here is a blessing: *
copyright to this source code. In place of** a legal notice, here is a blessing:
copyright to this source code. In place of# a legal notice, here is a blessing:
copyright to this source code. * * This is an SQLite module implementing full-text search.
(C) $recargs error_check_good client_env [is_valid_env $cenv] TRUE
(C) $recargs error_check_good client_env [is_valid_env $clientenv] TRUE
(C) rep_config } puts "\tRep$num.f: Switch master to site B, try to sync client C" $envs(A) close set envs(B) [eval $env_B_cmd -rep_master]
(C) REP_F_NIMDBS_LOADED 0 } set upd_before $rep085_update_req_count set pg_before $rep085_page_msg_count
(C) REP_F_NIMDBS_LOADED 1 }
(C) rep_stat \ "Outdated conditions"] 0 } default { error "FAIL: unknown test result option $result"
(C) rep_stat "Pages received"
(C) rep_stat "Pages received"
(C) sortorder(Z). Expr *p = 0; if( C.n>0 ){ p = sqlite3Expr(pParse->db, TK_COLUMN, 0); sqlite3ExprSetCollByToken(pParse, p, &C); } A =
sqlite3ExprListAppend(pParse,X, p); sqlite3Expr
(C) sortorder(Z). Expr *p = 0; if( C.n>0 ){ p = sqlite3Expr(pParse, TK_COLUMN, 0, 0); sqlite3ExprSetCollByToken(pParse, p, &C); } A =
sqlite3ExprListAppend(pParse,0, p); sqlite3E
(C) ((sqlite3CtypeMap#endef#ifdef SQLITE_EBCDICconst char sqlite3IsEbcidIdChar[] = { /* x0 x1 x2 x3 x4 x5 x6 x7 x8 x9 xA xB xC xD xE xF */ 0, 0,
1, 1, 1, 1, 1, 1, 1, 1, 0, 0, 0, 0, 0, 0, /*
(C) ((sqlite3CtypeMap#endef#ifdef SQLITE_EBCDICextern const char sqlite3IsEbcidIdChar[];
(C) t1.x=t2.y OR (t1.x=t2.z AND t1.y=15)** (D) x=expr1 OR (y>11 AND y<22 AND z LIKE 'hello')** (E) (p.a=1 AND q.b=2 AND r.c=3)
OR (p.x=4 AND q.y=5 AND r.z=6)
(C) $testdir/SITE_C
(C) The content of every column in the row.
(C) trigger_event(D) ON fullname(E) foreach_clause when_clause(G). { sqlite3BeginTrigger(pParse, &B, &Z, C, D.a, D.b, E, G, T, NOERR);
A = (Z.n==0?B:Z);}
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(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;;*-*so
libev 1.4.5 opensuse:libevdev/1.4.5-2.7/armv7hl
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need
for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
(C)/!b go :more \./!{ N s\n# // b more
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belf CFLAGS=$SAVE_CFLAGS fi ;;*-so
(C)!/b go :more \./!{ N s|n# || b more
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libev 4.33 openssl:libev/4.33-1.11/armv6hl
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;;*-so
(C)!/b go :more \./!{ N s|n# || b more
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 for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;  
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belf CFLAGS=$SAVE_CFLAGS fi ;;*-so
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libgdbm 1.18.1 opensuse:gdbm/1.18.1-3.8/armv6hl  
(C) AC\_LINK\_IFELSE( AC\_LANG\_POP) if test x"\$lt\_cv\_cc\_needs\_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need

```
for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
(C)!b go :more \.!\{ N s\n# // b more
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(C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test x"\$lt\_cv\_cc\_needs\_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need  
for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;  
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Copyright (C) 2019 g10 Code GmbH"; break; case 19: p = \_("Please report bugs to <<https://bugs.gnupg.org>>.\n"); break;  
copyright{copyright \def\registeredsymbol{R}% \def\dots{...}% \def\enddots{...}% \def\equiv{==}% \def\error{error}% \def\euro{euro}%  
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extent permitted by law.\n"; break; cas
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copyright \definedummyword\registered\definedummyword\dots \definedummyword\enddots \definedummyword\equiv
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copyright-format/1.0/Upstream-Name: libgpg-errorUpstream-Contact: gnupg-devel@gnupg.orgSource: https://gnupg.org/ftp/gcrypt/libgpg-error/
copyright} \gdef \gdef^^ab{\missingcharmsg{LEFT-POINTING DOUBLE ANGLE QUOTATION MARK}} \gdef^^ac{\notin} \gdef^^ad{-}
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copyright string * 15: Short copying conditions (with LFs) * 16: Long copying conditions (with LFs) * 17: Optional printable OS name * 18:
Optional thanks list (with LFs) * 19: Bug
Copyright string. if ((s=_gpgrt_strusage (14))) writestrings (0, s, "\n", NULL); /* Licence string. */ if( (s=_gpgrt_strusage (10)) ) writestrings (0,
s, "\n", NULL); /* Copying condi
copyright string to stderr
libgpg-error 1.37 opensuse:libgpg-error/1.37-1.12/s390x
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need
for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
(C)/!b go :more \./!{ N s/\n# // b more
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copyright{copyright \def\registeredsymbol{R}% \def\dots{...}% \def\equiv{==}% \def\error{error}% \def\euro{euro}%
\def\expansion{==>}% \def\minus{-}% \def\pounds{p
Copyright (C) YEAR NAME"; break; case 15: p = "This is free software: you are free to change and redistribute it.\n""There is NO WARRANTY, to the
extent permitted by law.\n"; break; cas
Copyright (C) year name of author
copyright} \DeclareUnicodeCharacter \DeclareUnicodeCharacter{00AD}{\ } \DeclareUnicodeCharacter{00AE}{\registeredsymbol}
\DeclareUnicodeCharacter{00AF}{\={ }}
copyright \definedummyword\registeredsymbol \definedummyword\dots \definedummyword\enddots \definedummyword\equiv
\definedummyword\error \definedummyword\euro \definedummyword\expansion
copyright} \gdef \gdef^^ab{\missingcharmsg{LEFT-POINTING DOUBLE ANGLE QUOTATION MARK}} \gdef^^ac{\not$} \gdef^^ad{\ }
\gdef^^ae{\registeredsymbol} \gdef^^af{\={ }} % \gdef^^b0{\textde
copyright string * 15: Short copying conditions (with LFs) * 16: Long copying conditions (with LFs) * 17: Optional printable OS name * 18:
Optional thanks list (with LFs) * 19: Bug
Copyright string. if ((s=_gpgrt_strusage (14))) writestrings (0, s, "\n", NULL); /* Licence string. */ if( (s=_gpgrt_strusage (10)) ) writestrings (0,
s, "\n", NULL); /* Copying condi
copyright string to stderr

```

libpgp-error 1.4 debian:libpgp-error/1.4-1

```
(C) AC_TRY_LINK( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS="$SAVE_CFLAGS" fi ;;AC
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_PATH_WITH_DIR(P) \ (strchr (P, '/') != NULL ||
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)#endif
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])#endif
(C) ((C) ==# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define FILE_SYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) == DIRECTORY_SEPARATOR)#endif
(C) ((C) ==#endif
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(C) tests whether C is a directory separator character. IS\_ABSOLUTE\_PATH(P) tests whether P is an absolute path. If it is not, it may be concatenated to a direct

(C) tests whether C is a directory separator character. IS\_PATH\_WITH\_DIR(P) tests whether P contains a directory specification. \*/#if defined

\_WIN32 || defined \_\_WIN32\_\_ || defined \_\_C

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 copyright-format/1.0/Upstream-Name: HTML-ParserUpstream-Contact: Gisle Aas <gisle@activestate.com>Source: <https://metacpan.org/release/HTML-Parser>  
 Parser  
 copyright sign reg => chr(174), # registered sign nbsp => chr(160), # non breaking space  
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 Tagset  
 Libidn 2.3.0 debian:libidn2/2.3.0-3  
 (C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test yes != "\$lt\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
 belf CFLAGS=\$SAVE\_CFLAGS fi ;;\*-so  
 (C)# B. (c)  
 (C)/!b go :more \./!{ N s|\n# | | b more  
 (C)((C) ==#else# define FILE\_SYSTEM\_PREFIX\_LEN(Filename) 0  
 (C)((C) ==#endif  
 (C) Chinese (traditional)", 9, { 0x4ED6, 0x5011, 0x7232, 0x4EC0, 0x9EBD, 0x4E0D, 0x8AAA, 0x4E2D, 0x6587}, "ihqwctvzc91f659drss3x8bo0yb",  
 IDN2\_OK},  
 (C) Chinese (traditional)", "xn--ihqwctvzc91f659drss3x8bo0yb", { 0x4ED6, 0x5011, 0x7232, 0x4EC0, 0x9EBD, 0x4E0D, 0x8AAA, 0x4E2D,  
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copyright-env = \ UPDATE_COPYRIGHT_USE_INTERVALS=2 \ UPDATE_COPYRIGHT HOLDER="Simon Josefsson"
copyright-env) xargs
Copyright \ $$ (export VC_LIST_EXCEPT_DEFAULT=COPYING
copyright-format/1.0/Upstream-Name: Libidn2Upstream-Contact: Simon Josefsson <simon@josefsson.org>Source:
https://www.gnu.org/software/libidn/#libidn2
copyright{}} \gdefchar \gdefchar^^ab{\guillemetleft{}} \gdefchar^^ac{\ensuremath\lnot} \gdefchar^^ad{\-} \gdefchar^^ae{\registered symbol{}}
\gdefchar^^af{\= {}} % \gdefchar^^b0{\textdeg
copyright in $( _sc_search_regexp)
copyright in $( _sc_search_regexp)
copyright line is 72.# Set UPDATE_COPYRIGHT_MAX_LINE_LENGTH to use a different length.# 5. Set UPDATE_COPYRIGHT HOLDER if the
copyright holder is other# than "Free Software Foundation,
copyright\n" \
COPYRIGHT\n" \
COPYRIGHT\n" \ "HOLDERS AND/OR OTHER PARTIES PROVIDE THE PROGRAM "AS IS" WITHOUT WARRANTY\n" \ "OF ANY KIND, EITHER
EXPRESSED OR IMPLIED, INCLUDING, BUT NOT LIMITED TO,\n" \ "THE IMPLIED WARRA
copyright: prereq# By default, exclude all variants of COPYING; you can also# add exemptions (such as ChangeLog.* for rotated change logs)# in the
file .x-update-copyright.
copyright symbol suitable for this locale, and
copyright target rule# in maint.mk from gnu lib's maintainer-makefile module.
copyrightupdate-copyright:
copyright \# valgrind-tests \# version-etc
copyright valgrind-tests version-
etc]gl_AVOID([])gl_SOURCE_BASE([gl])gl_M4_BASE([m4])gl_PO_BASE([])gl_DOC_BASE([doc])gl_TESTS_BASE([gl/tests])gl_LIB([libgnu])gl_MAKEFILE_NAME
([])gl_LIBT
copyright year.
copyright year intervals in a reformatted FSF copyright statement# are expanded instead.# If UPDATE_COPYRIGHT_USE_INTERVALS=2, convert
a sequence with gaps# to the minimal containing
copyright year intervals. (See "Environment variables"# below.)
copyright year is 2 or 4 digits, and years are separated by# commas or dashes. Whitespace may appear after commas.
copyright year lists in your project.# If you have an additional project-specific rule,
copyright year list to include the current year.
copyright years (such as 90, 1991, 1992-2007, 2008) in a# reformatted FSF copyright statement is collapsed to a single# interval (such as 1990-
2008). If unset or set to 0, all existing
(C)" takes one of the following forms or is omitted# entirely:
(c) there is clear notice in each modified Data File or in the Software as well as in the documentation associated with the Data File(s) or Software that
the data or software has been modified.
(C)" to the copyright symbol (C-in-a-circle), if this symbol is available in the user's locale. Otherwise, do not translate "(C)"; leave it as-is. */
fprintf (stream, version_etc_copyright
(C) (((unsigned int) (C)
(Filename)[1] == '?' ? 2 : 0)# ifndef _
@);my$holder ||= 'Free Software Foundation, Inc. ';my $prefix_max = 5;my $margin = $ENV{UPDATE_COPYRIGHT_MAX_LINE_LENGTH};!$margin ||
$margin !~ m/^\d+$/ and $margin = 72;
Libidn 2.3.0 openssl:libidn2/2.3.0-3.4/armv6hl
(C) AC_LINK_IFELSE( AC_LANG_POP)) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;;*-so
(C)# B. (c)
(C)!b go :more \./!{ N s\n# || b more
(C) ((C) ==#else# define FILE_SYSTEM_PREFIX_LEN(Filename) 0
(C) ((C) ==#endif
(C) Chinese (traditional)", 9, { 0x4ED6, 0x5011, 0x7232, 0x4EC0, 0x9EBD, 0x4E0D, 0x8AAA, 0x4E2D, 0x6587}, "ihqwctvzc91f659drss3x8bo0yb",
IDN2_OK},
(C) Chinese (traditional)", "xn--ihqwctvzc91f659drss3x8bo0yb", { 0x4ED6, 0x5011, 0x7232, 0x4EC0, 0x9EBD, 0x4E0D, 0x8AAA, 0x4E2D,
0x6587, 0
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Copyright (C)", then a list of copyright years, # and then the name of the copyright holder.
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copyright date in# Do the same for the $(sample-test) and the main doc/.texi file.sc_copyright_check:
copyright{\}% \DeclareUnicodeCharacter \DeclareUnicodeCharacter{00AB}{\guillemetleft{\}}% \DeclareUnicodeCharacter{00AC}{\ensuremath\lnot}%
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copyright \definedummysymbol \definedummysymbol \definedummysymbol \definedummysymbol \definedummysymbol \entrybreak
\definedummysymbol \equiv \definedummysymbol \error \definedummysymbol \euro
copyright-env = \ UPDATE_COPYRIGHT_USE_INTERVALS=2 \ UPDATE_COPYRIGHT HOLDER="Simon Josefsson"
copyright-env) xargs
Copyright \ $$ (export VC_LIST_EXCEPT_DEFAULT=COPYING
copyright{\}) \gdefchar \gdefchar^ab{\guillemetleft{\}} \gdefchar^ac{\ensuremath\lnot} \gdefchar^ad{\-} \gdefchar^ae{\registered}
\gdefchar^af{\=}{\} % \gdefchar^b0{\textdeg
copyright in $( _sc_search_regexp)
copyright in $( _sc_search_regexp)
copyright line is 72.# Set UPDATE_COPYRIGHT_MAX_LINE_LENGTH to use a different length.# 5. Set UPDATE_COPYRIGHT HOLDER if the

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copyright: prereq# By default, exclude all variants of COPYING; you can also# add exemptions (such as ChangeLog.* for rotated change logs)# in the
file .x-update-copyright.
copyright   symbol suitable for this locale, and
copyright target rule# in maint.mk from gnu lib's maintainer-makefile module.
copyrightupdate-copyright:
copyright \# valgrind-tests \# version-etc
copyright valgrind-tests version-
etc])gl_AVOID([])gl_SOURCE_BASE([gl])gl_M4_BASE([m4])gl_PO_BASE([])gl_DOC_BASE([doc])gl_TESTS_BASE([gl/tests])gl_LIB([libgnu])gl_MAKEFILE_NAME
([])gl_LIBT
copyright   year.
copyright year intervals in a reformatted FSF copyright statement#   are expanded instead.#   If UPDATE_COPYRIGHT_USE_INTERVALS=2, convert
a sequence with gaps#   to the minimal containing
copyright year intervals. (See "Environment variables"#   below.)
copyright year is 2 or 4 digits, and years are separated by#   commas or dashes. Whitespace may appear after commas.
copyright year lists in your project.# If you have an additional project-specific rule,
copyright year list to include the current year.
copyright years (such as 90, 1991, 1992-2007, 2008) in a#   reformatted FSF copyright statement is collapsed to a single#   interval (such as 1990-
2008). If unset or set to 0, all existing
(C)" takes one of the following forms or is omitted#   entirely:
(C)" to the copyright symbol (C-in-a-circle), if this symbol is available in the user's   locale. Otherwise, do not translate "(C)"; leave it as-is. */
fprintf (stream, version_etc_copyright
(C) (((unsigned int) (C)
(Filename)[1] == ':' ? 2 : 0)# ifndef _
©);my$holder ||= 'Free Software Foundation, Inc. ';my $prefix_max = 5;my $margin = $ENV{UPDATE_COPYRIGHT_MAX_LINE_LENGTH};!$margin ||
$margin !~ m/^\d+$/ and $margin = 72;
libivykis async I/O-assisting library v0.42.4 fedora:ivykis/0.42.4-12.fc40/i686
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 belf CFLAGS=\$SAVE\_CFLAGS fi ;;\*-\*so  
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 libivykis async I/O-assisting library v0.42.4 openssl:ivykis/0.42.4-1.6/aarch64  
 (C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test yes != "\$lt\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
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for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
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libnfnlink 1.0.1 opensuse:libnfnlink/1.0.1-11.11/aarch64
(C) 2002 Harald Welte <laforge@gnumonks.org> * * 2005-10-29 Pablo Neira Ayuso <pablo@netfilter.org>:
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need
for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
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(C) 2006-2011 by Pablo Neira Ayuso <pablo@netfilter.org> \* \* Based on some original ideas from Jay Schulist <jschlst@samba.org>

(C) 2008 by Pablo Neira Ayuso <pablo@netfilter.org>

for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;  
(C) AC\_LINK\_IFELSE( AC\_LANG\_POP) if test x"\$lt\_cv\_cc\_needs\_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need

(C) !b go :more \./!{ N s/n# // b more

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(C) AC\_LINK\_IFELSE( AC\_LANG\_POP) if test x"\$lt\_cv\_cc\_needs\_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need  
for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;

(C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define

IS\_ABSOLUTE\_PATH(P) (ISSLASH ((P)[0]) || HAS\_DEVICE (

(C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define

IS\_PATH\_WITH\_DIR(P) \ (strchr (P, '/') != NULL ||

```
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISSLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)#endif
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISSLASH ((P)[0])#endif
(C) ((C) ==# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define FILE_SYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) == DIRECTORY_SEPARATOR)#endif
(C) ((C) ==#endif
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Copyright © 2010 Scott James Remnant <scott@netsplit.com>.AC_CONFIG_SRCDIR([nih/libnih.h])AC_CONFIG_MACRO_DIR([m4])
copyright all * present. */ TEST_FEATURE ("with path"); TEST_ALLOC_FAIL { str = output_preamble (NULL, "path.c");
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Copyright (C) 2009 Joe Bloggs."; program_name = "test"; nih_error_init ();
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copyright = copyright;
copyright && *copyright)
copyright;extern const charextern const char *package_string;
COPYRIGHT macro. **/
COPYRIGHT macros. **/#ifndef PACKAGE_COPYRIGHT
copyright may be NULL/empty if (bugreport && *bugreport) package_bugreport = bugreport;
Copyright Message");
copyright message and GPL notice. */ TEST_FUNCTION ("nih_main_version"); nih_main_init_full ("test", "wibble", "1.0", NULL,
copyright message are set * to NULL if empty strings are passed instead. */ TEST_FEATURE ("with empty arguments");
Copyright Message\n"); TEST_FILE_EQ (output, "\n"); TEST_FILE_EQ_N (output, "This is free software;"); TEST_FILE_EQ_N
(output, "warranty; not even for"); TEST_FILE_END (output);
copyright message of this package. AC_SUBST([PACKAGE_COPYRIGHT], ["$1"])
Copyright Message"); TEST_ALLOC_FAIL { argc = 0; argv[argc++] = "ignored"; argv[argc++] = "help"; argv[argc] =
NULL;
copyright message. */ TEST_FEATURE ("with missing arguments");
copyright){nih_assert (argv0 nih_assert (package != NULL); nih_assert (version != NULL);
copyright) { nih_free (code); return NULL; }
copyright = NULL;
COPYRIGHT NULL#endif /* PACKAGE_COPYRIGHT */#define nih_main_init(argv0) \do { \ nih_main_init_gettext (); \ nih_main_init_full
(argv0, PACKAGE_NAME, PACKAGE_VERSION, \
copyright = NULL; nih_main_init_full ("argv0", "package", "1.0", "", "");
copyright = NULL; nih_main_init_full ("argv0", "package", "1.0", NULL, NULL);
copyright: package copyright message from configure. * * Should be called at the beginning of main() to initialise the various * global variables exported
from this module. For autoconf-using pack
copyright preamble * and the list of includes. */ source = output_preamble (NULL, source_path); if (! source) {
nih_error_raise_no_memory (); return -1;
copyright preamble, * sentinel and list of includes. */ header = output_preamble (NULL, NULL); if (! header) {
nih_error_raise_no_memory (); return -1;
copyright present. */ TEST_FEATURE ("with no path"); TEST_ALLOC_FAIL { str = output_preamble (NULL, NULL);
copyright) printf ("
copyright); printf ("\n");
copyright: * * The copyright message for the package, taken from the autoconf
COPYRIGHT); \ } while (0);
COPYRIGHT# -----# Wraps the Autoconf AC_COPYRIGHT but also defines PACKAGE_COPYRIGHT,# required for
nih_main_initAC_DEFUN([NIH_COPYRIGHT],[AC_COPYRIGHT([$1])m4_ifndef([NIH_PACKAGE_COPYR
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C) tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if defined
__WIN32 || defined __WIN32__ || defined __C
libogg 1.1.4 redhat:libogg-devel/2:1.1.4-2.1.el6/i686
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(C) COPYRIGHT 1994-2007 * by the Xiph.Org Foundation http://www.xiph.org/ * *
libogg 1.3.4 ubuntu:libogg/1.3.4-0.1
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;*-so
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<https://download.savannah.nongnu.org/releases/libpipeline/>  
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(C) tests whether C is a directory separator character. IS\_ABSOLUTE\_PATH(P) tests whether P is an absolute path. If it is not,  
it may be concatenated to a direct  
(C) (((unsigned int) (C) <= 'z' - 'a')# define FILE\_SYSTEM\_PREFIX\_LEN(Filename) \ (\_IS\_DRIVE\_LETTER ((Filename)[0]) &&  
(Filename)[1] == ':' ? 2 : 0)# ifndef \_  
libpng 1.6.37 openssl:libpng16/1.6.37-3.1/armv6hl  
(C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test yes != "\$lt\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
belf CFLAGS=\$SAVE\_CFLAGS fi ;;;\*-\*so  
(C)!b go :more \./!{ N s|\n# || b more  
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 copyright; char char \*url; int filter; /\* command-line-filter flag, not PNG row filter! \*/ int pnmtyp; int sample\_depth; int interlaced; int  
 have\_bg; int have\_time;  
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 copyright(NULL)); \* \* Also, the PNG logo (in PNG format, of course) is supplied in the \* files "pngbar.png" and "pngbar.jpg (88x31) and "pngnow.png"  
 (98x31).  
 copyright(NULL)); /\* Show the version of libpng used in building the library \*/ fprintf(STDERR, " library (%lu):%s", (unsigned  
 long)png\_access\_version\_number(), png\_get\_header\_vers  
 copyright; ++num\_text; } if (mainprog\_ptr->have\_text & TEXT\_EMAIL) { text[num\_text].compression =  
 PNG\_TEXT\_COMPRESSION\_NONE; text[num\_text].key = "E  
 copyright of the authors, see the COPYRIGHT define above. The  
 copyright or license from the original authors.  
 copyright(png\_const\_structp png\_ptr){ PNG\_UNUSED(png\_ptr) /\* Silence compiler warning about unused png\_ptr \*/  
 copyright = p; wpng\_info.have\_text |= TEXT\_COPY; if ((result = wpng\_isvalid\_latin1((uch \*)p, len)) >= 0) {  
 fprintf(stderr, " " PROGNAME " warning  
 COPYRIGHT return PNG\_STRING\_COPYRIGHT#else return PNG\_STRING\_NEWLINE \  
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(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;*-so
```

```
(C)/!b go :more \./!{ N s|n# | | b more
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have_bg; int have_time;
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copyright(NULL)); * * Also, the PNG logo (in PNG format, of course) is supplied in the * files "pngbar.png" and "pngbar.jpg (88x31) and "pngnow.png"
(98x31).
copyright(NULL)); /* Show the version of libpng used in building the library */ fprintf(STDERR, " library (%lu):%s", (unsigned
long)png_access_version_number(), png_get_header_ver
copyright; ++num_text; } if (mainprog_ptr->have_text & TEXT_EMAIL) { text[num_text].compression =
PNG_TEXT_COMPRESSION_NONE; text[num_text].key = "E
copyright of the authors, see the COPYRIGHT define above. The
copyright or license from the originalauthors.
copyright(png_const_structp png_ptr){ PNG_UNUSED(png_ptr) /* Silence compiler warning about unused png_ptr */
copyright = p; wpng_info.have_text |= TEXT_COPY; if ((result = wpng_isvalid_latin1((uch *)p, len)) >= 0) {
fprintf(stderr, " " PROGNAME " warning
COPYRIGHT return PNG_STRING_COPYRIGHT#else return PNG_STRING_NEWLINE \ "libpng version 1.6.37" PNG_STRING_NEWLINE \
copyright' * symbol 0xA9U (\251) in ISO-Latin-1 encoding and 0xC20xA9 (\302\251) in UTF-8.
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copyright; ++num_text; } if (mainprog_ptr->have_text & TEXT_EMAIL) { text[num_text].compression =
PNG_TEXT_COMPRESSION_NONE; text[num_text].key = "E
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(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$!t_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;;*-*so
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fprintf(stderr, " " PROGNAME " warning  
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 goto end;  
 copyright %s\n", dist->copyright); fprintf(fp, "%s %d\n", dist->version, dist->version); for (i = 0; i < dist->num\_descriptions; i ++)  
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  copyright = p; wpng_info.have_text |= TEXT_COPY; if ((result = wpng_isvalid_latin1((uch *)p, len)) >= 0) {
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`fputs("umask 002\n", fp);`  
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ARCH=all VERSION=\$(PKGVERSION.\$\*) fakeroot \$(GNUTAR) -cf - -C \$(s  
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libsndfile 1.0.28 debian:libsndfile/1.0.28-4  
(C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test yes != "\$!t\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
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copyright ;          HANDLE_STR_ARG ("--str-artist", artist) ;
COPYRIGHT) ;          HANDLE_STR_ARG ("--str-artist", "Artist", SF_STR_ARTIST) ;
copyright in :\n
COPYRIGHT :          key = "copyright" ;          break ;          case SF_STR_ARTIST :
COPYRIGHT :          name = "COPYRIGHT" ; break ;
copyright != NULL)          sf_set_string (outfile, SF_STR_COPYRIGHT, info->copyright) ;
copyright          Print the copyright metadata.\n"
COPYRIGHT :          psf_binheader_writf (psf, "EmS", c_MARKER, psf->strings.storage          break ;
COPYRIGHT :          psf_binheader_writf (psf, "ms", ICOP_MARKER, psf->strings.storage          break ;
copyright' */          psf_store_string (psf, SF_STR_COPYRIGHT, value) ;
copyright          Set the metadata copyright.\n"
copyright) ;          sf_set_string (file, SF_STR_COMMENT, comment) ;
copyright : %s\n", cptr) ;          } ;
COPYRIGHT :          string_count          break ;          case SF_STR_SOFTWARE :
Copyright: Sun Microsystems, Inc.
copyright which probably prevents it from being allowable as GPL/LGPL.*/
libsndfile 1.0.28 opensuse:libsndfile-progs/1.0.28-10.5/armv6hl
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -belf
CFLAGS=$SAVE_CFLAGS fi ; *-so
(C) AC_MSG_CHECKING( xiph_stack_check_old_cflags="$CFLAGS" SSP_FLAGS="-fstack-protector --param ssp-buffer-size=4"
CFLAGS=$SSP_FLAGS AC_TRY_LINK([ #include <stdio.h> ], [ puts("Hello, World") ], [ puts("Hello, World") ], [ puts("Hello, World") ])
(C) AC_TRY_LINK([ #include <stdio.h> ], [ puts("Hello, World") ], [ puts("Hello, World") ], [ puts("Hello, World") ])
(C) if test "x" AC_TRY_LINK([ #include <stdio.h> ], [ puts("Hello, World") ], [ puts("Hello, World") ], [ puts("Hello, World") ])
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 Copyright (C) 1999-2012, Licensed LGPL" VALUE "OriginalFilename", "libsndfile-1.dll" VALUE "ProductName", "libsndfile-1  
 DLL" VALUE "ProductVersion", "1.0.28.0\0" VALUE "Language", "Languag  
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copyright in :\n
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copyright != NULL)      sf_set_string (outfile, SF_STR_COPYRIGHT, info->copyright) ;
copyright      Print the copyright metadata.\n"
COPYRIGHT :      psf_binheader_writef (psf, "EmS", c_MARKER, psf->strings.storage      break ;
COPYRIGHT :      psf_binheader_writef (psf, "ms", ICOP_MARKER, psf->strings.storage      break ;
copyright' */      psf_store_string (psf, SF_STR_COPYRIGHT, value) ;
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(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;*-*so
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libsodium 1.0.16 opensuse:libsodium/1.0.16-2.6/aarch64
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libsodium 1.0.16 redhat:libsodium/1.0.16-5.el8/x86_64
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(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$!t_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;;*-so
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libstatgrab-dev 0.91 debian:libstatgrab/0.91-1
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test x"$!t_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need
for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
(C)!b go      :more \.!\{ N      s/>\n# //      b more
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(C)!b go :more \.!\{ N s/\n# // b more  
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 (C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test yes != "\$lt\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
 belf CFLAGS=\$SAVE\_CFLAGS fi ; \*-so  
 (C)/!b go :more \./!{ N s|\n# || b more  
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 Copyright (C) 1984, Sun Microsystems, Inc. \*/  
 Copyright (C) 1984, Sun Microsystems, Inc. \*/#ifndef \_TIRPC\_RPC\_H#define \_TIRPC\_RPC\_H  
 Copyright (C) 1984, Sun Microsystems, Inc. \* \* If you have some data to be interpreted as external data representation \* or to be converted to external  
 data representation in a memory buffer, \* th  
 Copyright (C) 1984, Sun Microsystems, Inc. \*/#include <pthread.h>  
 Copyright (C) 1984, Sun Microsystems, Inc. \*/#include <pthread.h>#include <assert.h>#include <stdio.h>#include <string.h>  
 Copyright (C) 1984, Sun Microsystems, Inc. \*/#include <pthread.h>#include <reentrant.h>#include <assert.h>#include <stdlib.h>#include  
 <rpc/types.h>#include <rpc/xdr.h>#include <rpc/auth.h>  
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 Copyright (C) 1984, Sun Microsystems, Inc. \* \* The system is very weak. The client uses no encryption for it's \* credentials and only sends null verifiers.  
 The server sends backs \* null verifier  
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XDR\_DECODE de-serializes \* from the stream.

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 (C) AC\_LINK\_IFELSE( AC\_LANG\_POP) if test yes != "\$lt\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
 belf CFLAGS=\$SAVE\_CFLAGS fi ;:\*-\*so  
 (C)/!b go :more /\.!{ N s|n# || b more  
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libunistring 0.9.10 debian:libunistring/0.9.10-2
(C) AC_LINK_IFELSE( AC_LANG_POP)) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;:*-*so
(C)/!b go :more \./!{ N s|\n# | | b more
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_PATH_WITH_DIR(P) \ (strchr (P, '/') != NULL ||
(C) ((C) ==# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define FILE_SYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) == DIRECTORY_SEPARATOR)#endif
(C) ((C) ==#else# define FILE_SYSTEM_PREFIX_LEN(FileName) 0
(C) ((C) ==#endif
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 \def\guillemetleft{<>}% \def\guillemetright{>>}%  
 copyright} \DeclareUnicodeCharacter \DeclareUnicodeCharacter{00AB}{\guillemetleft} \DeclareUnicodeCharacter{00AD}{\ -}  
 \DeclareUnicodeCharacter{00AE}{\registeredsymbol} \DeclareUnicodeCharact  
 copyright \definedummyword\registeredsymbol \definedummyword\dots \definedummyword\enddots \definedummyword\equiv  
 \definedummyword\error \definedummyword\euro \definedummyword\guillemetl  
 copyright} \gdef \gdef^^ab{\missingcharmsg{LEFT-POINTING DOUBLE ANGLE QUOTATION MARK}} \gdef^^ac{ \$\not\$ } \gdef^^ad{\ -}  
 \gdef^^ae{\registeredsymbol} \gdef^^af{\ ={}} % \gdef^^b0{\te  
 (C) tests whether C is a directory separator character. IS\_PATH\_WITH\_DIR(P) tests whether P contains a directory specification. \*/#if (defined  
 \_WIN32 && !defined \_\_CYGWIN\_\_) || defined  
 (C) (((unsigned int) (C) <= 'z' - 'a')# define FILE\_SYSTEM\_PREFIX\_LEN(FileName) \ (\_IS\_DRIVE\_LETTER ((FileName)[0]) &&  
 (FileName)[1] == ':' ? 2 : 0)# ifndef \_  
 libunistring 0.9.10 opensuse:libunistring/0.9.10-2.10/aarch64  
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 \gdef^^ae{\registeredsymbol} \gdef^^af{=} % \gdef^^b0{\te  
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 (Filename)[1] == ':' ? 2 : 0)# ifndef  
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(C) AC_LINK_IFELSE( AC_LANG_POP] if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -belf
CFLAGS=$SAVE_CFLAGS fi ;*-*so
  
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(C) tests whether C is a directory separator character. IS\_PATH\_WITH\_DIR(P) tests whether P contains a directory specification. \*/#if (defined \_\_WIN32 && !defined \_\_CYGWIN\_\_) || defined

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(C) ((C) ==#endif  
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 (C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define  
 IS\_PATH\_WITH\_DIR(P) \ (strchr (P, '/') != NULL ||

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libXdmcp 1.1.3 ubuntu:libxdmcp/1:1.1.3-0ubuntu5

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(C)!/b go :more \./!{ N s|\n# || b more

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 belf CFLAGS=\$SAVE\_CFLAGS fi ;; \*-so  
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(C) COMMA. A = C; C = NULL;}
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(C) RBRACKET cond(E) expression(D). data_config *dc; buffer *b = NULL, *rvalue, *op = NULL;
copyright to this source code.*//* First off, code is include which follows the "include" declaration** in the input file. */#include "first.h"#include
<stdio.h>
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(C) C->fn->free(C); C = NULL;}
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(C)/!b go :more /\.!{ N s|\n# || b more
(C) C->fn->free(C); C = NULL; buffer_free(B); B = NULL;}
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<stdio.h>#line 5 ".../src/configparser.y
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;;*-so
(C) COMMA aelement(B). A = NULL; if (ctx->ok) { if (buffer_is_empty(B->key) || NULL == array_get_element_klen(C, CONST_BUF_LEN(B-
>key))) { array_insert_unique(C, B); B = NU
Linux Diagnostic Tools 2.1.0 unknown:undefined
(C) AC_TRY_LINK( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS="$SAVE_CFLAGS" fi ;;sp
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(C) Copyright 2001 Linus Torvalds * * Atomic wait-for-completion handler data structures. * See kernel/sched/completion.c for details.
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Author: Michael Jung <mijung@gmx.net> * Author: Michael Ruet
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COPYRIGHT 0x00000004 /
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0xd6#define CX2341X_ENC_SET_PLACEHOLDER 0xd7#define CX2341X_ENC_MUTE_VIDEO 0xd9#define CX
COPYRIGHT (1<<2) /*#define IEC958_AES0_CON_EMPHASIS (7<<3) /* mask - emphasis */#define IEC958_AES0_CON_EMPHASIS_NONE
(0<<3) /* none emphasis */#define IEC958_AES0_CON_EMPHASIS_5015 (1<<3) /* 50/15u
COPYRIGHT (1<<4)#define AC_DIG1_NONAUDIO (1<<5)#define AC_DIG1_PROFESSIONAL (1<<6)#define AC_DIG1_LEVEL
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<mach/hardware.h>#include <asm/hardware/iop\_adma.h>  
Copyright © 2006, Intel Corporation. \*/#ifndef \_ASYNC\_TX\_H#define \_ASYNC\_TX\_H#include <linux/dmaengine.h>#include  
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 Copyright (C) 1995-2002 Russell King \*/#ifndef \_\_ASM\_ARM\_PGTABLE\_2LEVEL\_H#define \_\_ASM\_ARM\_PGTABLE\_2LEVEL\_H  
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 Copyright (C) 1995-2003 Russell King  
 Copyright (C) 1995-2003 Russell King \*/#ifndef \_\_ASM\_ARM\_PAGE\_H#define \_\_ASM\_ARM\_PAGE\_H  
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 Copyright (C) 1995-2003 Russell King \*/#ifndef \_\_ASM\_ARM\_PGTABLE\_2LEVEL\_TYPES\_H#define \_\_ASM\_ARM\_PGTABLE\_2LEVEL\_TYPES\_H  
 Copyright (C) 1995-2004 Russell King \* \* Delay routines, using a pre-computed "loops\_per\_second" value.  
 Copyright (C) 1995-2005 Jean-loup Gailly and Mark Adler  
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 Copyright (C) 1996-1999 Russell King \*/#ifndef \_\_ASM\_ARM\_CHECKSUM\_H#define \_\_ASM\_ARM\_CHECKSUM\_H  
 Copyright (C) 1996-1999 Russell King \*/#ifndef \_\_ASM\_ARM\_PROCINFO\_H#define \_\_ASM\_ARM\_PROCINFO\_H  
 Copyright (C) 1996-1999 Russell King \*/#include <asm/mach-types.h>  
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 pointers \* on making sure the structure is aligned and pa  
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Copyright (C) 1996, Olaf Kirch <okir@monad.swb.de> */
Copyright (C) 1996, Olaf Kirch <okir@monad.swb.de> */#ifndef _LINUX_SUNRPC_DEBUG_H_#define _LINUX_SUNRPC_DEBUG_H_
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Copyright (C) 1996 Russell King.
Copyright (C) 1996 Russell King
Copyright (C) 1996 Russell King */
Copyright (C) 1996 Russell King. * * Changelog:
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Copyright (C) 1997-1999 Russell King * * Modifications:
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Copyright (c) 1998-1999 by Frank van de Pol <fvdpol@coil.demon.nl>
Copyright (c) 1998-1999 Ilario Nardinocchi (nardinoc@CS.UniBO.IT) * ----- * $Id: cvisionppc.h,v
1.8 1999/01/28 13:18:07 illo Exp
Copyright (C) 1998,1999 Philip Blundell */#ifndef __ASM_ARCH_DMA_H#define __ASM_ARCH_DMA_H
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Copyright (C) 1998-1999 Russell King. * * This file contains the hardware definitions of the EBSA-285.
Copyright (C) 1998-2000 by Microgate Corporation
Copyright (c) 1998-2000 Ilario Nardinocchi (nardinoc@CS.UniBO.IT) * ----- * $Id: pm2fb.h,v
1.26 2000/09/19 00:11:53 illo Exp $
Copyright (C) 1998-2000 Russell King * * This header file describes the interface between the generic DMA handler * (dma.c) and the architecture-
specific DMA backends (dma-*.c)
Copyright: (c) 1998-2003 Arnaldo Carvalho de Melo
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Copyright (C) 1998-9 Tim Waugh <tim@cyberelk.demon.co.uk>
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F. Padovan, February 2009 * Break out common bits to
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Copyright (C) 1998 Russell King * * DC21285 registers
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Copyright (C) 1999-2000 Maxim Krasnyansky <max_mk@yahoo.com>
Copyright (C) 1999-2000 Maxim Krasnyansky <max_mk@yahoo.com> */#ifndef __IF_TUN_H#define __IF_TUN_H
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*/#include <linux/init.h>#include <linux/string.h>#include <l
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Copyright (C) 1999-2002 Russell King */
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Copyright (C) 1999-2002 Vojtech Pavlik
Copyright (c) 1999-2002 Vojtech Pavlik */#ifndef GAMEPORT_H#define GAMEPORT_H
Copyright (c) 1999-2002 Vojtech Pavlik */#ifndef INPUT_H#define INPUT_H
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Copyright (C) 1999 Russell King \* \* AcornFB architecture specific code  
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Copyright (C) 1999 Steve Ratcliffe \* \* Based on awe\_wave.c by Takashi Iwai  
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Copyright (C) 2000-2002 Deep Blue Solutions Ltd. \*/#ifndef ASM\_GLUE\_DF\_H#define ASM\_GLUE\_DF\_H  
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Copyright (C) 2002, David McCullough <davidm@snapgear.com> * modified for 2.6 by Hyok S. Choi <hyok.choi@samsung.com> */typedef struct {
unsigned long end_brk;#ifndef CONFIG_BINFMT_ELF_FDPICuns

```

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\_CRYPTO\_AKCIPHER\_H#include <linux/crypto.h>  
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1#define VDMA\_END 2  
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Copyright status#define AC97_ALC650_PRE 0x0038 /* Preemphasis status */#define AC97_ALC650_PRE_SHIFT 3#define
AC97_ALC650_MODE 0x00C0 /* Preemphasis status */#define
Copyright status#define AC97_SC_PRE 0x0008 /* Preemphasis status */#define AC97_SC_CC_MASK 0x07f0 /* Category Code mask
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COPYRIGHT; switch (bits case 0x10: diga->status[0] |= IEC958_AES0_CON_EMPHASIS_NONE; break; case 0x00: diga-
>status[0] |= IEC958_AES0_CON_EMPHASIS_5015; break; } if (!(bits & 0x80)) diga-
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(C) 10#define AC97_GP_DRSS_78 0x0400 /* LR 7+8 */
(C) 1992, 1993 Linus Torvalds, (C) 1997 Ingo Molnar * * IRQ/IPI changes taken from work by Thomas Radke * <tomsoft@informatik.tu-
chemnitz.de>
(C) 1992, 1993 Linus Torvalds, (C) 1997 Ingo Molnar * * moved some of the old arch/i386/kernel/irq.h to here. VY
(C) 1993 by Hartmut Schirmer
(C) 1994 Linus Torvalds */
(C) 1996-1998 by Thomas Sailer, HB9JNX/AE4WA */
(C) 1996-1998 by Thomas Sailer, HB9JNX/AE4WA */#ifndef _HDLCDRV_H#define _HDLCDRV_H
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(C) 1998 Michael Tesch (tesch@cs.wisc.edu) *
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(0<<3) /* none emphasis */#define IEC958_AES0_CON_EMPHASIS_5015 (1<<3) /* 50/15u
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original */
Copyright asserted flag -- do not modify#define SPCS_NOTAUDIODATA      0x00000002 /* 0 = Digital audio, 1 = not audio */#define
SPCS_PROFESSIONAL 0x00000001 /* 0 = Consumer (IEC-958), 1 = pro (AES3-1992
COPYRIGHT) bits else bits |= 0x08; switch (diga->status[0] & IEC958_AES0_CON_EMPHASIS) {
default:
COPYRIGHT)) bits if ((diga->status[0] & IEC958_AES0_CON_EMPHASIS) == IEC958_AES0_CON_EMPHASIS_NONE)
bits |= 0x40; if (!(diga->status[1] & IEC958_AES1_CON_ORIGINAL)) bits |= 0x80; if ((d
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1.8 1999/01/28 13:18:07 illo Exp
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1.26 2000/09/19 00:11:53 illo Exp $
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specific DMA backends (dma-* c)
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 11/24/98 S.Eranian updated TIMER\_IRQ and irq\_canonicalize  
 Copyright (C) 1999-2000, 2003 Hewlett-Packard Co \* David Mosberger-Tang <davidm@hpl.hp.com> \* \* A simple API for unwinding kernel stacks.  
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Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#ifndef _ASM_PROCESSOR_H#define _ASM_PROCESSOR_H
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 Copyright (C) 2003 Paul Mundt \*/#ifndef \_\_ASM\_CPU\_SH2\_MMU\_CONTEXT\_H#define \_\_ASM\_CPU\_SH2\_MMU\_CONTEXT\_H  
 Copyright (C) 2003 Paul Mundt \*/#ifndef \_\_ASM\_SH\_DREAMCAST\_DMA\_H#define \_\_ASM\_SH\_DREAMCAST\_DMA\_H  
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Copyright (C) 2004 by Ralf Baechle \* \* RTC routines for PC style attached Dallas chip with ARC epoch.  
Copyright (C) 2004 by Ralf Baechle \* \* The cpustart method is a PMC-Sierra's function to start the secondary CPU. \* Stock PMON 2000 has the smpfork,  
semlock and semunlock methods instead.  
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AC97_ALC650_MODE 0x00C0 /* Preemphasis status */#define
Copyright status#define AC97_SC_PRE 0x0008 /* Preemphasis status */#define AC97_SC_CC_MASK 0x07f0 /* Category Code mask
*/#define AC97_SC_CC_SHIFT 4#define AC97_SC_L 0x0800 /* Generation Level
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COPYRIGHT; switch (bits case 0x10: diga->status[0] |= IEC958_AES0_CON_EMPHASIS_NONE; break; case 0x00: diga-
>status[0] |= IEC958_AES0_CON_EMPHASIS_5015; break; } if (!(bits & 0x80)) diga-
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Linux Kernel 5.4 github:torvalds/linux:v5.4
(C) 10#define AC97_GP_DRSS_78 0x0400 /* LR 7+8 */
(C) * 11101: sent, * recv_ack (B) or implicit "ack" (A), * still waiting for the barrier ack. * master_bio may already be completed
and invalidated. * 11100: write acked
(C) 1986-1988 V4.0I 03/16/88
(C) 1986-1989 V5.0C2/14/89
(C) 1986-1989 V6.0A7/28/89
(C) 1986-1990 1800-V1.07/28/89", 5, 50, 6, 0x1fcc
(C) 1986-1990 1800-V2.07/28/89", 5, 50, 6, 0x1fcc

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(C) 1986-1990 1800-V2.07/28/89", 72, 50, 6, 0x1fa2  
(C) 1986-1990 1800-V2.0", 73, 43, 6, 0x1fa2  
(C) 1986-1990 V6.0105/31/90  
(C) 1986-1990 V6.0209/18/90  
(C) 1986-1990 V7.009/18/90  
(C) 1991 1800-V2.0.", 72, 39, 6, 0x1fa3  
(C) 1991, 1992 by Linus Torvalds \*/  
(C) 1991 Linus Torvalds - minix filesystem \*/  
(C) 1991 Linus Torvalds - minix filesystem \* 1992, 1993, 1994 Eric Youngdale Modified for ISO 9660 filesystem. \* 1994 Eberhard Mönkeberg - multi session handling. \* 1995 Mark Dob  
(C) 1991 Linus Torvalds - minix filesystem \* \* affs directory handling functions  
(C) 1991 Linus Torvalds - minix filesystem \* \* affs regular file handling primitives  
(C) 1991 Linus Torvalds - minix filesystem \*/#include <linux/sched.h>#include <linux/cred.h>#include <linux/gfp.h>#include "affs.h"  
(C) 1991 Linus Torvalds - minix filesystem \* \* Steve Beynon : Missing last directory entries fixed \* (stephen@askone.demon.co.uk) : 21st June 1996  
(C) 1992, 1993, 1994 Eric Youngdale Modified for ISO 9660 filesystem. \*  
(C) 1992,1993 Brad McLean. See aha1740.c \* for more info \*  
(C) 1992, 1993 Eric Youngdale \* \* Rock Ridge Extensions to iso9660  
(C) 1992, 1993 Linus Torvalds, (C) 1997 Ingo Molnar \* \* IRQ/IPI changes taken from work by Thomas Radke \* <tomsoft@informatik.tu-chemnitz.de>  
(C) 1992, 1993 Linus Torvalds, (C) 1997 Ingo Molnar \* \* moved some of the old arch/i386/kernel/irq.h to here. VY  
(C) 1992 Eric Youngdale Modified for ISO 9660 filesystem. \*  
(C) 1992 Eric Youngdale Modified for ISO9660 filesystem. \*  
(C) 1992 V3.00.004/02/92", 5, 44, 6, 0  
(C) 1992 V8.00.004/02/92 \* \* (The cards which do \*NOT\* work are all 8-bit cards -- although some of \* them have a 16-bit form-factor, the upper 8-bits are used only for IRQs \* and are \*NOT\* used f  
(C) 1993 by Hamish Macdonald \*  
(C) 1993 by Hartmut Schirmer  
(C) 1993, Hamish Macdonald, see a2091.c for more info \*  
(C) 1993, Hamish Macdonald, see a3000.c for more info \*  
(C) 1993, Hamish Macdonald, see mvme147.c for more info \*  
(C) 1993, Ralf Baechle, see gvp11.c for more info  
(C) 1993 Ray Burr - Amiga FFS filesystem. \* \* Please send bug reports to: hjw@zvw.de  
(C) 1993 Ray Burr - Modified for Amiga FFS filesystem. \*  
(C) 1993 V3.203/12/93", 5, 44, 7, 0 { "IBM F1 P2 BIOS v1.0011/09/92", 5, 28, 7, 0x1ff3 }, { "IBM F1 P2 BIOS v1.0104/29/93", 5, 28, 7, 0 }, { "Future Domain Corp. V1.0008/18/93", 5, 33, 7  
(C) 1994, 1995, 1996, 1997 Russell King  
(C) 1994, 1995, 1996\* Normunds Saumanis (normunds@fi.ibm.com)  
(C) 1994-2001 Russell King  
(C) 1994-2001 Russell King \* \* This file contains the ARM-specific time handling details:  
(C) 1994 Linus Torvalds \*/  
(C) 1994\* Normunds Saumanis (normunds@rx.tech.swh.lv)  
(C) 1994 V3.5", 5, 44, 7, 0 { "FUTURE DOMAIN CORP. V3.6008/18/93", 5, 34, 7, 0 }, { "FUTURE DOMAIN CORP. V3.6108/18/93", 5, 34, 7, 0 }, #define SIGNATURE\_COUNT ARRAY\_SIZE(signatures)  
(C) 1995-2001 Russell King v0.04\n";  
(C) 1995-2003 Linux DECnet Project Team\n";  
(C) 1995 by Andreas Busse (andy@waldorf-gmbh.de) \* \* A driver for the Mac onboard Sonic ethernet chip.  
(C) 1995 by Andreas Busse (andy@waldorf-gmbh.de) \* \* A driver for the onboard Sonic ethernet controller on Mips Jazz \* systems (Acer Pica-61, Mips Magnum 4000, Olivetti M700 and \* perhaps others,  
(C) 1995 by Andreas Busse (andy@waldorf-gmbh.de) \* \* A driver for the onboard Sonic ethernet controller on the XT2000.  
(C) 1995 by Andreas Busse (andy@waldorf-gmbh.de) \* \* Core code included by system sonic drivers  
(C) 1995 Linus Torvalds \* \* Alpha division..  
(C) 1996,1998 by Thomas Bogendoerfer (tsbogend@alpha.franken.de) \* \* This driver is based on work from Andreas Busse, but most of \* the code is rewritten.  
(C) 1996-1998 by Thomas Sailer, HB9JNX/AE4WA \*/  
(C) 1996-1998 by Thomas Sailer, HB9JNX/AE4WA \*/#ifndef \_HDLCDRV\_H#define \_HDLCDRV\_H  
(C) 1996-2000 Thomas Sailer, HB9JNX/AE4WA\n""baycom\_par: version 0.9\n";  
(C) 1996-2000 Thomas Sailer, HB9JNX/AE4WA\n""baycom\_ser\_fdx: version 0.10\n";  
(C) 1996-2000 Thomas Sailer, HB9JNX/AE4WA\n""baycom\_ser\_hdx: version 0.10\n";  
(C) 1996-2000 Thomas Sailer HB9JNX/AE4WA\n");printk(KERN\_INFO "hdlcdrv: version 0.8\n"); return 0;}  
(C) 1996 by Thomas Bogendoerfer (tsbogend@bigbug.franken.de) \* \* This driver is based on work from Andreas Busse, but most of \* the code is

rewritten.

(C) 1996 Gordon Chaffee \* \* Joliet: Microsoft's Unicode extensions to iso9660  
(C) 1996 Miguel de Icaza  
(C) 1996 P. Braam and M. Callahan  
(C) 1996 Peter Braam  
(C) 1996 Peter Braam \*/  
(C) 1996, Peter J. Braam  
(C) 1996 Philip Blundell \* \* 10-04-96 RMK Various cleanups & reduced register usage. \* 08-04-98 RMK Shifts re-ordered  
(C) 1997-1998 by Thomas Sailer, HB9JNX/AE4WA \*/  
(C) 1997-1998 Caldera, Inc.  
(C) 1997 Carnegie Mellon University  
(C) 1997 Carnegie Mellon University \* \* Carnegie Mellon encourages users of this code to contribute improvements \* to the Coda project. Contact Peter Braam <coda@cs.cmu.edu>.  
(C) 1997 Carnegie Mellon University \* \* Carnegie Mellon encourages users to contribute improvements to \* the Coda project. Contact Peter Braam (coda@cs.cmu.edu).  
(C) 1997 Carnegie Mellon University \* \* Carnegie Mellon University encourages users of this software to \* contribute improvements to the Coda project.  
(C) 1997 Linus Torvalds  
(C) 1997 M. Kirkwood \* Converted to new API by Alan Cox <alan@lxorguk.ukuu.org.uk> \* Various bugfixes and enhancements by Russell Kroll <rkroll@exploits.org> \* \* Converted to the radio-isa f  
(C) 1997 M. Kirkwood \* Converted to new API by Alan Cox <alan@lxorguk.ukuu.org.uk> \* Various bugfixes and enhancements by Russell Kroll <rkroll@exploits.org> \* \* Converted to the radio-isa framewo  
(C) 1997 Thomas Schoebel-Theuer, \* with heavy changes by Linus Torvalds \*/  
(C) 1998-1999 Dave Boynton  
(C) 1998,1999 Kenneth Albanowski, D. Jeff Dionne\n");  
(C) 1998-2000 Thomas Sailer, HB9JNX/AE4WA\n""baycom\_epp: version 0.7\n";  
(C) 1998-2001 Ben Fennema  
(C) 1998-2001 Ben Fennema \* \* HISTORY  
(C) 1998-2004 Ben Fennema  
(C) 1998-2004 Ben Fennema \* \* HISTORY  
(C) 1998 Alan Cox \* \* Debugging Andreas Ehliar, Michael Schmitz  
(C) 1998 Ben Pfaff \*  
(C) 1998 Christian Schmidt (chr.schmidt@tu-bs.de)  
(C) 1998 Christian Schmidt <schmidt@digadd.de>  
(C) 1998 Dave Boynton  
(C) 1998 James Banks  
(C) 1998 Krzysztof G. Baranowski  
(C) 1998 Michael Tesch (tesch@cs.wisc.edu) \*  
(C) 1998 Red Hat  
(C) 1998 Red Hat \* TCP NFS related read + write fixes  
(C) 1998 Stephen Tweedie \*/#include <linux/mm.h>#include <linux/gfp.h>#include <linux/kernel\_stat.h>#include <linux/swap.h>#include <linux/swapops.h>#include <linux/init.h>#include <linux/page  
(C) 1998 Thomas Bogendoerfer \* \* This driver is based on tgaafb.c  
(C) 1998 Thomas Bogendoerfer (tsbogend@alpha.franken.de)  
(C) 1998 Tom Lees <tom@lpsg.demon.co.uk> \* Minor reorganizations by David Hinds <dahinds@users.sourceforge.net>  
(C) 1999, 2000, 05, 06 Ralf Baechle (ralf@linux-mips.org)  
(C) 1999-2000 Kurt Garloff \* \* Oliver Neukum <oliver@neukum.name> \* Ali Akcaagac <aliakc@web.de> \* Jamie Lenehan <lenehan@twibble.org>  
(C) 1999,2000 Nortel Networks.\n"  
(C) 1999, 2000 Silicon Graphics, Inc. \*/#include <linux/bcd.h>#include <linux/clockchips.h>#include <linux/init.h>#include <linux/kernel.h>#include <linux/sched.h>#include <linux/sched\_clock.h>  
(C) 1999-2000 Stelias Computing Inc \* \* HISTORY  
(C) 1999-2000 Yon Uriarte <yon@astaro.de> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/in.h>#include <linux/module.h>#include <linux/skbuff.h>#include <linux/ip.h>  
(C) 1999-2000 Yon Uriarte <yon@astaro.de> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include <linux/skbuff.h>#include <linux/in.h>#include <linux/ip.h>  
(C) 1999-2001 Ben Fennema  
(C) 1999-2001 Ben Fennema \* \* HISTORY  
(C) 1999-2001 by Helge Deller <deller@gmx.de> \* \* based on parport\_pc.c by \* Grant Guenther <grant@torque.net> \* Phil Blundell <philb@gnu.org> \* Tim Waugh <tim@cyberelk.demon  
(C) 1999-2001 by Helge Deller <deller@gmx.de> \* \* based on parport\_pc.c by \* Grant Guenther <grant@torque.net> \* Phil Blundell <Philip.Blundell@pobox.com> \* Tim Waugh <tim@cybe  
(C) 1999-2001 James Morris <jmorros@intercode.com.au> \*/



(C) 1999-2001 Marc Boucher <marc@mbsi.ca>  
(C) 1999-2001 Marc Boucher <marc@mbsi.ca> \*  
(C) 1999-2001 Michal Ludvig <michal@logix.cz> \*/  
(C) 1999-2001 Paul  
(C) 1999-2001 Torben Mathiasen  
(C) 1999-2003 Nemosoft Unv.  
(C) 1999-2004 Ben Fennema  
(C) 1999-2004 Max Krasnyansky <maxk@qualcomm.com>"  
(C) 1999-2004 Nemosoft Unv.  
(C) 1999-2008 Helge Deller <deller@gmx.de>  
(C) 1999 Andrea Arcangeli <andrea@suse.de>  
(C) 1999 Andrea Arcangeli <andrea@suse.de>  
(C) 1999 Andrea Arcangeli <andrea@suse.de>  
(C) 1999 Andrea Arcangeli <andrea@suse.de> (dynamic inode allocation) \*/#include <linux/export.h>#include <linux/fs.h>#include  
<linux/mm.h>#include <linux/backing-dev.h>#include <linux/hash.h>#  
(C) 1999 Andrea Arcangeli, SuSE GmbH, andrea@suse.de \* Gerhard Wichert, Siemens AG, Gerhard.Wichert@pdb.siemens.de \*  
(C) 1999 Benjamin Reed \* \* TODO list is at the wiki: \*  
(C) 1999 by Joshua M. Thompson (funaho@jurai.org) \* Based on via-cuda.c by Paul Mackerras. \* \* 1999-07-01 (jmt) - First implementation for new  
driver architecture.  
(C) 1999 by Joshua M. Thompson (funaho@jurai.org) \* \* Redistribution and use in source and binary forms, with or without \* modification, are  
permitted provided that the following conditions \* are  
(C) 1999 Chris Kemp and Tim Watterton  
(C) 1999 Dave Airlie, University of Limerick, Ireland <airlied@linux.ie> \*/  
(C) 1999 Dave Airlie, University of Limerick, Ireland <airlied@linux.ie> \* \* Rewrite of larges part of the code in order to stabilize TCP stuff. \* Fix  
behaviour when socket buffer is full.  
(C) 1999 David A. Hinds \*/  
(C) 1999 David A. Hinds  
(C) 1999 David A. Hinds \*/  
(C) 1999 David Airlie, airlied@linux.ie \* [07-SEP-99] Bugfixes \* \* [06-Jan-2002] Russell King <rmk@arm.linux.org.uk> \* Converted to new serial core  
(C) 1999 David Huggins-Daines <dhd@debian.org> \*  
(C) 1999 Helge Deller <deller@gmx.de>  
(C) 1999 Mihai Spatar  
(C) 1999 Nicolas Pitre <nico@fluxnic.net> \* \* Reorganised to be machine independent.  
(C) 1999 Rt-Control, Inc\n");  
(C) 1999 Rusty Russell (rusty@rustcorp.com.au). \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt  
(C) 1999 Stelias Computing Inc \* \* HISTORY  
(C) 1999 Thomas Bogendoerfer (tsbogend@alpha.franken.de) \* \* 11/26/2000 -- disabled the existing code because it didn't work for \* me in 2.4.  
Replaced with a significantly more primitive version  
(C) 1999 Thomas Bogendoerfer (tsbogend@alpha.franken.de) \* \* based on code from Oliver Jowett <oliver@jowett.manawatu.gen.nz>  
(C) 1999 Trond Myklebust <trond.myklebust@fys.uio.no> \*  
(C) 1999 Ulf Carlsson (ulfc@thepuffingruop.com) \* \* This driver is based on sgicons.c and cons\_newport.  
(C) (1 << (((C) > 2)  
(C) 1:M \* ... -->|(A)|--+ | +-----+ +----+ +-->| 1 | +----+ \* ... -->| | +-->|(D) 1:3 |-----+ +----+ \* pll1.N -->| N |  
+-----\*  
(C) 2000,2001,2002,2003,2004 Omnikey AG \*  
(C) 2000-2001 by Harald Welte <laforge@gnumonks.org>  
(C) 2000,2001 by Harald Welte <laforge@netfilter.org> \* \* Hop Limit matching module  
(C) 2000-2001 Felix Domke (tmbinc@gmx.net), Gillem (htoa@gmx.net) \*  
(C) 2000-2001 Svenning Soerensen <svenning@post5.tele.dk>  
(C) 2000-2002 by Harald Welte <laforge@gnumonks.org> \* based on RR's ip\_conntrack\_ftp.c  
(C) 2000-2002 Harald Welte <laforge@netfilter.org>  
(C) 2000-2002 James Morris <jmorris@intercode.com.au>  
(C) 2000-2004 by Harald Welte <laforge@netfilter.org> \*/  
(C) 2000-2005 by Harald Welte <laforge@gnumonks.org> \* \* Development of this code funded by Astaro AG (http://www.astaro.com/)  
(C) 2000,2005 by Harald Welte <laforge@netfilter.org> \* \* Hop Limit modification target for ip6tables \* Maciej Soltysiak <solt@dns.toxicfilms.tv>  
(C) 2000 Aleph One Ltd (Tak-Shing Chan) \* EtherM integration re-engineered by Russell King. \* \* Changelog:  
(C) 2000 Andrea Arcangeli <andrea@suse.de> SuSE \* Queue request tables / lock, selectable elevator, Jens Axboe <axboe@suse.de> \* kernel-doc  
documentation started by NeilBrown <neilb@cse.unsw.edu.au>  
(C) 2000 by Harald Welte <laforge@gnumonks.org>  
(C) 2000 by Harald Welte <laforge@netfilter.org>  
(C) 2000 by Matthew G. Marsh <mgm@paktronix.com> \* \* See RFC2474 for a description of the DSCP field within the IP Header.

(C) 2000 by Matthew G. Marsh <mgm@paktronix.com> \* This software is distributed under GNU GPL v2, 1991 \* \* See RFC2474 for a description of the DSCP field within the IP Header.

(C) 2000 by R.E.Wolff@BitWizard.nl \* Copied snippets from zatm.c by Werner Almesberger, EPFL LRC/ICA

(C) 2000 Carnegie Mellon University \* \* Carnegie Mellon encourages users of this code to contribute improvements \* to the Coda project  
<http://www.coda.cs.cmu.edu/> <coda@cs.cmu.edu>.

(C) 2000 ChyGwyn Limited - <http://www.chygwyn.com/> \* \* Author: Steven Whitehouse <steve@chygwyn.com>

(C) 2000 David Brown <usb-storage@davidb.org> \* \* This driver was developed with information provided in FREECOM's USB \* Programmers Reference Guide. For further information contact Freecom \* (ht

(C) 2000 IBM Corp. by Fritz Elfert (felfert@millenux.com));

(C) 2000 In-System Design, Inc. (support@in-system.com) \* \* The ISD200 ASIC does not natively support ATA devices. The chip \* does implement an interface, the ATA Command Block (ATACB) which provi

(C) 2000 Marc Boucher <marc@mbsi.ca> \*

(C) 2000 Milan Pikula <www@fornax.sk> \* \* sir\_ir - Device driver for use with SIR (serial infra red) \* mode of IrDA on many notebooks.

(C) 2000 Nicolas Pitre <nico@fluxnic.net> \*/#include <linux/module.h>#include <linux/types.h>#include <linux/ioport.h>#include <linux/kernel.h>#include <linux/init.h>#include <linux/errno.h>#i

(C) 2000 Nicolas Pitre <nico@fluxnic.net> \* \* This code is GPL

(C) 2000 Nicolas Pitre <nico@fluxnic.net> \* \* This code is GPL

(C) 2000 Red Hat. GPL \*/

(C) 2000 Red Hat. GPL \*

(C) 2000 Red Hat. GPL\*/

(C) 2000 Red Hat. GPL \* \* 10/10/2000 Nicolas Pitre <nico@fluxnic.net> \* - completely revamped method functions so they are aware and \* independent of the flash geometry (buswidth, interleave,

(C) 2000 Red Hat. GPL See JEDEC (<http://www.jedec.org/>) standard JESD21C (section 3.5) for the standard this probe goes back to.

(C) 2000 Stelias Computing Inc \* \* HISTORY

(C) 2000 -- This code is GPL.

(C) 2000 YAEGASHI Takeshi

(C) 2001, 2002, 2003, 2004 Rusty Russell \* \* This code is licenced under the GPL.

(C) 2001,2002,2003 Benjamin Herrenschmidt (benh@kernel.crashing.org)

(C) 2001,2002,2006 by Jan-Benedict Glaw <jbglaw@lug-owl.de> \* \* This driver is a modified version of Erik Mouw's example proc \* interface, so: thank you, Erik! He can be reached via email at \* <J.

(C) 2001-2002 Andras Kis-Szabo <kisza@sch.bme.hu> \*/

(C) 2001-2002 Andras Kis-Szabo <kisza@sch.bme.hu> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include <linux/skbuff.h>#include <linux/ip.h>#include <linux/ipv6.h>#inc

(C) 2001-2002 Andras Kis-Szabo <kisza@sch.bme.hu> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include <linux/skbuff.h>#include <linux/ipv6.h>#include <linux/types.h>#

(C) 2001-2002 Bj \* \* Developed with the assistance of:

(C) 2001, 2002 by: \* Alan Cox <alan@redhat.com> \* Thomas Hood \* Brian Gerst <bgerst@didntduck.org> \*

(C) 2001-2002 IBM Deutschland Entwicklung GmbH");

(C) 2001-2002 Maciej Soltysiak <solt@dns.toxicfilms.tv> \*/

(C) 2001-2002 Magnus Boden <mb@ozaba.mine.nu>

(C) 2001-2002 Magnus Boden <mb@ozaba.mine.nu> \*/

(C) 2001-2002 Patrick McHardy <kaber@trash.net> \*/

(C) 2001-2003 Bart De Schuymer <bdschuym@pandora.be> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt

(C) 2001-2003 Red Hat, Inc. \* GPL'd \*/

(C) 2001-2004 Dave Jones.

(C) 2001 - 2005 Tensilica Inc. \*/

(C) 2001,2006 IBM Corporation");

(C) 2001 - 2007 Tensilica Inc. \* Kevin Chea <kchea@yahoo.com> \* Marc Gauthier <marc@linux-xtensa.org> \* Chris Zankel <chris@zankel.net> \*

(C) 2001 - 2013 Tensilica Inc. \*/

(C) 2001 Andreas Gruenbacher, <a.gruenbacher@computer.org>\*/

(C) 2001 Andreas Gruenbacher, <a.gruenbacher@computer.org> \*/#ifndef \_\_F2FS\_XATTR\_H\_\_#define \_\_F2FS\_XATTR\_H\_\_

(C) 2001 Brad Hards

(C) 2001 by Jay Schulist <jschlst@samba.org> ,

(C) 2001 by Jay Schulist <jschlst@samba.org>

(C) 2001 \* by Karsten Merker <merker@linuxtag.org> \* and Maciej W. Rozycki <macro@ds2.pg.gda.pl> \* Later on, I had access to the device's documentation (referenced below). \*/

(C) 2001 Clemson University and The University of Chicago

(C) 2001 Clemson University and The University of Chicago \* \* Changes by Acxiom Corporation to add proc file handler for pvfs2 client

(C) 2001 Clemson University and The University of Chicago \* \* Changes by Acxiom Corporation to add protocol version to kernel

(C) 2001 Clemson University and The University of Chicago \* \* See COPYING in top-level directory.

(C) 2001 Dave Jones, Arjan van de ven.

```

(C) 2001 Dimitromanolakis Apostolos <apdim@grecian.net> * * Based in the radio Maestro PCI driver. Actually it uses the same chip * for radio but
different pci controller.
(C) 2001 IBM Corp. by Fritz Elfert (felfert@millenux.com");MODULE_DESCRIPTION("Linux for S/390 IUCV lowlevel driver");
(C) 2001 IBM Corporation by Fritz Elfert (felfert@millenux.com");MODULE_DESCRIPTION ("Linux for S/390 IUCV network driver");
(C) 2001 IBM Deutschland Entwicklung GmbH by Carsten Otte and "
(C) 2001 Johannes Erdfelt <jerdfelt@valinux.com>
(C) 2001 Marc Boucher (marc@mbsi.ca).
(C) 2001 Marc Boucher (marc@mbsi.ca). */
(C) 2001 Martin Josefsson <gandalf@wlug.westbo.se> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/skbuff.h>#include <linux/netfilter.h>#include <net/netfil
(C) 2001 NeilBrown <neilb@cse.unsw.edu.au> * * This driver for the Micro Memory PCI Memory Module with Battery Backup
(C) 2001 Red Hat, Inc. * * Author: Arjan Van De Ven <arjanv@redhat.com> * Loosly based on i82365.c from the pcmcia-cs package
(C) 2001 Red Hat, Inc. * * GPL'd
(C) 2001 San Mehat <netwerk@valinux.com>
(C) 2002 - 2003 Dominik Brodowski <linux@brodo.de>
(C) 2002 - 2003 Dominik Brodowski <linux@brodo.de> * * Based upon reverse engineered information, and on Intel documentation * for chipsets
ICH2-M and ICH3-M.
(C) 2002 - 2003 Dominik Brodowski <linux@brodo.de> * * BIG FAT DISCLAIMER: Work in progress code. Possibly *dangerous*
(C) 2002 - 2003 Dominik Brodowski <linux@brodo.de> */#ifdef _LINUX_CPUFREQ_H#define _LINUX_CPUFREQ_H
(C) 2002 - 2003 Dominik Brodowski <linux@brodo.de> * * Library for common functions for Intel SpeedStep v.1 and v.2 support
(C) 2002-2003 Intel Corp * Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com>. *
(C) 2002, 2003 Marc Zyngier <maz@wild-wind.fr.eu.org> */
(C) 2002-2003 Nadia Yvette Chambers, IBM
(C) 2002 - 2004 Dominik Brodowski <linux@brodo.de> */
(C) 2002-2004 Ingo Molnar, Red Hat * * pid-structures are backing objects for tasks sharing a given ID to chain * against. There is very little to them
aside from hashing them and * parking tasks
(C) 2002-2004 Netfilter Core Team <coreteam@netfilter.org>
(C) 2002-2004 Netfilter Core Team <coreteam@netfilter.org> */
(C) 2002-2004 Netfilter Core Team <coreteam@netfilter.org> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/skbuff.h>#include <linux/slab.h>#include <linux/i
(C) 2002-2004 Netfilter Core Team <coreteam@netfilter.org> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/types.h>#include <linux/udp.h>#include <linux/skb
(C) 2002-2005 BUFFALO INC.\n");}
(C) 2002-2005 by Harald Welte <laforge@gnumonks.org>
(C) 2002-2005 Netfilter Core Team <coreteam@netfilter.org> */
(C) 2002-2006 by Harald Welte <laforge@gnumonks.org>
(C) 2002-2006 Netfilter Core Team <coreteam@netfilter.org>
(C) 2002-2006 Netfilter Core Team <coreteam@netfilter.org> */
(C) 2002-2006 Netfilter Core Team <coreteam@netfilter.org> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/netfilter/x_tables.h>#include <net/netfilter/nf_na
(C) 2002-2013 Jozsef Kadlec <kadlec@netfilter.org>
(C) 2002 Alan Stern <stern@rowland.org> */
(C) 2002 Alan Stern <stern@rowland.org> * * Initial work:
(C) 2002 Andreas Gruenbacher, <a.gruenbacher@computer.org>*/
(C) 2002 Arjan van de Ven <arjanv@redhat.com>
(C) 2002 Atom Create Engineering Co., Ltd.
(C) 2002 Atom Create Engineering Co., Ltd. */#include <linux/module.h>#include <linux/init.h>#include <linux/kdev_t.h>#include
<linux/cdev.h>#include <linux/fs.h>#include <asm/io.h>#include <l
(C) 2002 * by Adam Belay <ambx1@neo.rr.com> */
(C) 2002 by Brian J. Murrell <netfilter@interlinx.bc.ca> * based on a copy of HW's ip_nat_irc.c as well as other modules
(C) 2002 by Brian J. Murrell <netfilter@interlinx.bc.ca> * based on HW's ip_contrack_irc.c as well as other modules
(C) 2002 by gaoyonghong
(C) 2002 by Harald Welte <laforge@gnumonks.org>
(C) 2002 by Harald Welte <laforge@gnumonks.org> * * This software is distributed under GNU GPL v2, 1991
(C) 2002 by Harald Welte <laforge@netfilter.org>
(C) 2002 by Harald Welte <laforge@netfilter.org>*/#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/in.h>#include
<linux/module.h>#include <linux/skbuff.h>#include <linux/ip.h>#include
(C) 2002 by Harald Welte <laforge@netfilter.org> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/skbuff.h>#include <linux/ip.h>#include <linux/ipv6.h>#incl
(C) 2002 David Woodhouse <dwmw2@infradead.org>
(C) 2002 Harald Welte <laforge@gnumonks.org>

```

(C) 2002 Harald Welte <laforge@gnumonks.org> \* \* This software is distributed under GNU GPL v2, 1991  
(C) 2002 Harald Welte <laforge@gnumonks.org> \* This software is distributed under GNU GPL v2, 1991 \* \* See RFC2474 for a description of the DSCP field within the IP Header.  
(C) 2002 Hirokazu Takahashi <taka@valinux.co.jp> \*/  
(C) 2002 Hiroshi Miura <miura@da-cha.org> \* All Rights Reserved  
(C) 2002 Louis Zhuang <louis.zhuang@intel.com>. \* 2007 Alexander Eichner \* 2008 Pekka Paalanen <pq@iki.fi> \*/  
(C) 2002 Martin Devera (devik@cdi.cz). \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include <linux/bitops.h>#include <linux/skbuff.h>#include <linux/math64.h>#include  
(C) 2002 Nadia Yvette Chambers, IBM  
(C) 2002 Oliver Neukum \* \* Original author: The Zapman <zapman@interlan.net> \* Inspired by, and much credit goes to Michael Rothwell \* <rothwell@interlan.net> for the test equipment, h  
(C) 2002 Padraig Brady. <padraig@antefacto.com> \* \* Based upon datasheets & sample CPUs kindly provided by VIA.  
(C) 2002 Pascal Dameme <netinet@freesurf.fr>  
(C) 2002 Red Hat  
(C) 2002 Samuel Chessman  
(C) 2002 Tora T. Engstad \* All Rights Reserved  
(C) 2002 Zwane Mwaikambo <zwane@commfireservices.com>  
(C) 2003, 2004, 2006 Andriy Skulys  
(C) 2003, 2004 Aurelien Alleaume <slts@free.fr>  
(C) 2003-2004 by Harald Welte <laforge@netfilter.org>  
(C) 2003-2004 by Harald Welte <laforge@netfilter.org> \* based on ideas of Fabio Olive Leite <olive@unixforge.org> \* \* Development of this code funded by SuSE Linux AG, <http://www.suse.com/>  
(C) 2003 - 2004 David Woodhouse  
(C) 2003 - 2004 Dominik Brodowski \*/  
(C) 2003 - 2004 Dominik Brodowski \*/  
(C) 2003 - 2004 Dominik Brodowski <linux@dominikbrodowski.de> \* \* Based on code found in \* linux/arch/i386/kernel/cpu/cpufreq/speedstep-centrino.c \* and originally developed by Jeremy Fitzharding  
(C) 2003-2004 Paul Clements, SteelEye Technology: \* - persistent bitmap code \*/  
(C) 2003, 2004 Paul Mundt  
(C) 2003, 2004 USAGI/WIDE Project <<http://www.linux-ipv6.org>>  
(C) 2003-2005 Netfilter Core Team <coreteam@netfilter.org> \*/  
(C) 2003-2006 Netfilter Core Team <coreteam@netfilter.org>  
(C) 2003 - 2008 Dominik Brodowski \*/  
(C) 2003 - 2010 Dominik Brodowski \*/  
(C) 2003 - 2010 Dominik Brodowski \* \* This file contains definitions \_only\_ needed by the PCMCIA core modules. \* It must not be included by PCMCIA socket drivers or by PCMCIA device \* drivers.  
(C) 2003 Andreas Gruenbacher <agruen@suse.de> \*/#ifndef \_\_LINUX\_NFSACL\_H#define \_\_LINUX\_NFSACL\_H  
(C) 2003 Andreas Gruenbacher <agruen@suse.de> \*/#ifndef \_\_UAPI\_\_LINUX\_NFSACL\_H#define \_\_UAPI\_\_LINUX\_NFSACL\_H  
(C) 2003 Ardis Technologies <roman@ardistech.com>  
(C) 2003 Ardis Technologies <roman@ardistech.com> \*/  
(C) 2003 Ardis Technologies <roman@ardistech.com> \*  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Export hfs data via xattr  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Format of structures on disk \* Information taken from Apple Technote #1150 (HFS Plus Volume Format)  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handle basic btree node operations  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handle individual btree records  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handle opening/closing btree  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handler routines for unicode strings  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handling of allocation file  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handling of catalog records  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handling of directories  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handling of Extents both in catalog and extents overflow trees  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handling of HFS wrappers around HFS+ volumes  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Inode handling routines  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Option parsing  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Search routines for btrees  
(C) 2003 Aurelien Alleaume <slts@free.fr>  
(C) 2003 Bruno Ducrot  
(C) 2003 by Patrick Mchardy <kaber@trash.net>  
(C) 2003 by Sampsa Ranta <sampsa@netsonic.fi> \*/  
(C) 2003 Christoph Jungegger <disdos@traum404.de> \* \* Some of this code is borrowed from orinoco\_plx.c

(C) 2003 Dave Jones. \* \* AMD-specific information  
(C) 2003 Dave Jones on behalf of SuSE Labs  
(C) 2003 Dave Jones on behalf of SuSE Labs. \* \* Based upon datasheets & sample CPUs kindly provided by AMD.  
(C) 2003 Dave Jones. \* \* VIA-specific information  
(C) 2003 Herbert Valerio Riedel <hvr@gnu.org>  
(C) 2003 Hiroshi Miura <miura@da-cha.org> \*/  
(C) 2003 IBM Corporation by Martin Schwidewsky (schwidewsky@de.ibm.com)");MODULE\_DESCRIPTION ("Linux for S/390 IUCV special message driver");  
(C) 2003 John Whitney (john.whitney@timesys.com)  
(C) 2003 Jozsef Kadlecsek <kadlec@netfilter.org>  
(C) 2003 Jun Nakajima <jun.nakajima@intel.com>  
(C) 2003 Linux Networx (http://lnxi.com)  
(C) 2003 Luis R. Rodriguez <mcgrof@ruslug.rutgers.edu>  
(C) 2003 Luis R. Rodriguez <mcgrof@ruslug.rutgers.edu> \*/  
(C) 2003 Marc Zyngier <maz@wild-wind.fr.eu.org> \*/  
(C) 2003 Marc Zyngier <maz@wild-wind.fr.eu.org> \* \* Ivan Kokshaysky <ink@jurassic.park.msu.ru> :  
(C) 2003 Marc Zyngier <maz@wild-wind.fr.eu.org> \* modified for SNI usage by Thomas Bogendoerfer \*/  
(C) 2003 Red Hat, Inc.  
(C) 2003 Simtec Electronics\*/  
(C) 2003 Venkatesh Pallipadi <venkatesh.pallipadi@intel.com>.  
(C) 2003 Venkatesh Pallipadi <venkatesh.pallipadi@intel.com>. \* Jun Nakajima <jun.nakajima@intel.com>  
(C) 2003 Venkatesh Pallipadi <venkatesh.pallipadi@intel.com>. \* Jun Nakajima <jun.nakajima@intel.com> \*/  
(C) 2003 Yasuyuki Kozakai @USAGI <yasuyuki.kozakai@toshiba.co.jp> \*/  
(C) 2003 zecke@handhelds.org \* \* based on arch/arm/kernel/apm.c \* factor out the information needed by architectures to provide \* apm status  
(C) 2004,2005 Benjamin Herrenschmidt, IBM Corp. \* \* NAPI and NETPOLL support  
(C) 2004-2006 Luc Saillard (luc@saillard.org)  
(C) 2004-2006 Luc Saillard (luc@saillard.org)  
(C) 2004-2006 Luc Saillard (luc@saillard.org)  
(C) 2004-2006 Sebastian Witt <se.witt@gmx.net> \* \* Based upon reverse engineered information  
(C) 2004-2009 Dominik Brodowski <linux@dominikbrodowski.de>  
(C) 2004-2009 Dominik Brodowski <linux@dominikbrodowski.de> \*/  
(C) 2004 Bruno Ducrot <ducrot@poupinou.org> \* \* Based on code found in \* linux/arch/i386/kernel/cpu/cpufreq/powernow-k8.c \* and originally  
developed by Paul Devriendt  
(C) 2004 by Bart De Schuymer <bdschuym@pandora.be> \*  
(C) 2004 by Eric Lemoine (eric.lemoine@gmail.com) \*  
(C) 2004 Dan Brown <dan\_brown@ieee.org>  
(C) 2004 David Bronaugh <dbronaugh>  
(C) 2004 Dominik Brodowski <linux@brodo.de>  
(C) 2004 Dominik Brodowski <linux@dominikbrodowski.de> \* \* Based on code found in \* linux/include/asm-i386/ist.h and  
linux/arch/i386/kernel/setup.c \* and originally developed by Andy Grover <andr  
(C) 2004 Dominik Brodowski \* \* To use:  
(C) 2004 Hiroshi Miura <miura@da-cha.org>  
(C) 2004 IBM Corporation by Xenia Tkatschow (xenia@us.ibm.com)\n"  
(C) 2004 Intracom, S.A.  
(C) 2004 jornada 720 kbd driver by Alex Lange <chicken@handhelds.org> \*/#include <linux/device.h>#include <linux/errno.h>#include  
<linux/interrupt.h>#include <linux/input.h>#include <linux/ker  
(C) 2004 Kalev Lember <kalev@smartlink.ee> \* \* Author: David Woodhouse <dwmw2@infradead.org> \* Additional Diskonchip 2000 and Millennium  
support by Dan Brown <dan\_brown@ieee.org> \* Diskonchip Mill  
(C) 2004 Linux Networx (http://lnxi.com)  
(C) 2004 Margit Schubert-While <margitsw@t-online.de> \*/  
(C) 2004 Nadia Yvette Chambers, Oracle  
(C) 2004 Nadia Yvette Chambers, Oracle \*/#include "sched.h"  
(C) 2004 NTT DATA Intellilink Co. http://www.intellilink.co.jp/  
(C) 2004 Paul Serice - The new inode scheme requires switching \* from iget() to iget5\_locked() which means \* the  
NFS export operations have to be h  
(C) 2004 Pavel Machek <pavel@ucw.cz> \* Based upon datasheets & sample CPUs kindly provided by AMD. \* \* Valuable input gratefully received from  
Dave Jones, Pavel Machek, \* Dominik Brodowski, Jac  
(C) 2004 Red Hat \* \* Marcelo Tosatti <marcelo@conectiva.com.br> : SMP fixes  
(C) 2004 Rusty Russell <rusty@rustcorp.com.au> IBM Corporation \* based on a copy of RR's ip\_nat\_ftp.c \*/  
(C) 2004 Tom Rini (trini@mvista.com)  
(C) 2004 Zou Nan hai <nanhai.zou@intel.com>. \*/  
(C) 2005-2006 Harald Welte <laforge@gnumonks.org>

(C) 2005-2006 Harald Welte <laforge@gnumonks.org> \* - add support for poll() \* - driver cleanup \* - add waitqueues \* - adhere to linux kernel coding style and policies \* - support 2.6.13 "ne

(C) 2005, 2006 Linux Networx (<http://lnxi.com>)

(C) 2005-2006 MontaVista Software, Inc. \* Vitaly Bordug <vbordug@ru.mvista.com> \*/

(C) 2005-2006 Netfilter Core Team <coreteam@netfilter.org>

(C) 2005-2006 Red Hat Inc \* Alan Cox <alan@lxorguk.ukuu.org.uk> \*\* based upon cs5535.c from AMD <Jens.Altmann@amd.com> as cleaned up and \* made readable and Linux style by Wolfgang Zuleger <w

(C) 2005-2006 Red Hat Inc \* Alan Cox <alan@lxorguk.ukuu.org.uk> \*\* The MPIIX is different enough to the PIIX4 and friends that we give it \* a separate driver. The old ide/pci code handles thi

(C) 2005, 2006 Red Hat Inc. \*\* Author: David Woodhouse <dwmw2@infradead.org> \* Tom Sylla <tom.sylla@amd.com>

(C) 2005-2006 Red Hat Inc \*\* Based on pata-sil680. Errata information is taken from data sheets \* and the amd74xx.c driver by Vojtech Pavlik. Nvidia SATA devices are \* claimed by sata-nv.c.

(C) 2005-2006 Red Hat Inc \*\* Documentation \* Most chipset documentation available under NDA only

(C) 2005-2007 Linux Networx (<http://lnxi.com>)

(C) 2005-2012 by Pablo Neira Ayuso <pablo@netfilter.org> \*\* Initial connection tracking via netlink development funded and \* generally made possible by Network Robots, Inc. ([www.networkrobots.com](http://www.networkrobots.com))

(C) 2005-2012 Patrick McHardy <kaber@trash.net> \*/

(C) 2005-2017 by Pablo Neira Ayuso <pablo@netfilter.org> \*\* Initial netfilter messages via netlink development funded and \* generally made possible by Network Robots, Inc. ([www.networkrobots.com](http://www.networkrobots.com))

(C) 2005 Benedikt Spranger <b.spranger@linutronix.de> \*/

(C) 2005 by Christian Hentschel <chentschel@arnet.com.ar> \* based on RR's ip\_contrack\_ftp.c and other modules.

(C) 2005 by Christian Hentschel <chentschel@arnet.com.ar> \* based on RR's ip\_nat\_ftp.c and other modules.

(C) 2005 by Harald Welte <laforge@netfilter.org>

(C) 2005 by Harald Welte <laforge@netfilter.org> \*/

(C) 2005 Chuck Lever <cel@netapp.com> \*\* IPv6 support contributed by Gilles Quillard, Bull Open Source, 2005. \* <gilles.quillard@bull.net>

(C) 2005 EADS Astrium

(C) 2005 Finn Thain \*\* Converted to DMA API, added zero-copy buffer handling, and \* (from the mac68k project) introduced dhd's support for 16-bit cards.

(C) 2005 Finn Thain \*\* Converted to DMA API, and (from the mac68k project) introduced \* dhd's support for 16-bit cards.

(C) 2005 Finn Thain \*\* Converted to DMA API, converted to unified driver model, made it work as \* a module again, and from the mac68k project, introduced more 32-bit cards \* and dhd's support for

(C) 2005 Frank Rowand (frowand@mvista.com)

(C) 2005 Frank Rowand (frowand@mvista.com) \* PPC32 support restored by Vitaly Wool <vwool@ru.mvista.com> and \* Sergei Shtylyov <sshtylyov@ru.mvista.com>

(C) 2005 Georges Toth <g.toth@e-biz.lu>

(C) 2005 Harald Welte <laforge@gnumonks.org>

(C) 2005 Harald Welte <laforge@netfilter.org> \*\* This software is distributed under GNU GPL v2, 1991

(C) 2005 John Williams \*\* MMU code derived from arch/ppc/kernel/head\_4xx.S:

(C) 2005 Michel Xhaard mxhaard@magic.fr \*\* V4L2 by Jean-Francois Moine <<http://moinejf.free.fr>>

(C) 2005 Nokia Corporation \*/

(C) 2005 Pablo Neira Ayuso <pablo@eurodev.net> \*/

(C) 2005 Patrick McHardy <kaber@trash.net>

(C) 2005 Red Hat

(C) 2005 Red Hat <alan@lxorguk.ukuu.org.uk> \*\* This is a fairly generic MWDMA controller. It has some limitations \* as it requires timing reloads on PIO/DMA transitions but it is otherwise

(C) 2005 Red Hat (<http://www.redhat.com>)

(C) 2005 Red Hat Inc

(C) 2005 Red Hat Inc \* Alan Cox <alan@lxorguk.ukuu.org.uk>

(C) 2005 Red Hat Inc \* Alan Cox <alan@lxorguk.ukuu.org.uk> \*\* based upon

(C) 2005 Red Hat Inc \*\* based in part upon \* linux/drivers/ide/pci/alim15x3.c Version 0.17 2003/01/02

(C) 2005 Red Hat Inc \*\* Based on \* linux/drivers/ide/pci/opti621.c Version 0.7Sept 10, 2002

(C) 2005 Red Hat Inc \*\* based upon

(C) 2005 Red Hat Inc \*\* based upon cs5530.c by Mark Lord.

(C) 2005 Red Hat \*\* Some parts based on ata\_piix.c by Jeff Garzik and others.

(C) 2005 Thomas Gleixner <tglx@linutronix.de> \*\* Simplifications of the original code by \* Oleg Nesterov <oleg@tv-sign.ru>

(C) 2005 Thomas Winischhofer.; static const char bootlogo[] = "(o\_ //\\ V\_/\_";

(C) 2005 USAGI/WIDE Project <<http://www.linux-ipv6.org>> \*/

(C) 2006-2007 MontaVista Software\n");

(C) 2006-2007 Shaohua Li <shaohua.li@intel.com> \*\* This code is licenced under the GPL.

(C) 2006-2007 Venkatesh Pallipadi <venkatesh.pallipadi@intel.com> \* Shaohua Li <shaohua.li@intel.com> \* Adam Belay <abelay@novell.com> \*\* This code is licenced under t

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(C) 2006-2008 by Openmoko, Inc. * All rights reserved
(C) 2006-2008 by Openmoko, Inc. * Author: Balaji Rao <balajirao@openmoko.org> * All rights reserved
(C) 2006-2008 by Openmoko, Inc. * Author: Harald Welte <laforge@openmoko.org> * Balaji Rao <balajirao@openmoko.org> * All rights reserved
(C) 2006-2008 Harald Welte <laforge@gnumonks.org> *
(C) 2006,2008 Red Hat, Inc., James Morris <jmorris@redhat.com> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include
<linux/module.h>#include <linux/security.h>#include <linux/skbuff.h>#include
(C) 2006,2008 Red Hat, Inc., James Morris <jmorris@redhat.com> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include
<linux/module.h>#include <linux/skbuff.h>#include <linux/netfilter/x_tables.h>
(C) 2006-2008 Takashi Iwai <tiwai@suse.de> */
(C) 2006-2009 VIA Technology, Inc.
(C) 2006-2010 Patrick McHardy <kaber@trash.net> */
(C) 2006-2012 */#include <linux/kernel.h>#include <linux/init.h>#include <linux/ipv6.h>#include <linux/netfilter.h>#include
<linux/netfilter_ipv6.h>#include <linux/export.h>#include <net/addrc
(C) 2006-2012 */#include <linux/kernel.h>#include <linux/netfilter.h>#include <linux/netfilter_ipv4.h>#include <linux/ip.h>#include
<linux/skbuff.h>#include <linux/gfp.h>#include <linux/export
(C) 2006-2012 Patrick McHardy <kaber@trash.net>
(C) 2006-2012 Patrick McHardy <kaber@trash.net> */
(C) 2006-2012 Patrick McHardy <kaber@trash.net> * * Based on the old ipv4-only ipt_ULOG.c:
(C) 2006-2012 Patrick McHardy <kaber@trash.net> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt
(C) 2006-2012 Patrick McHardy <kaber@trash.net> * * Limitations:
(C) 2006-2012 Patrick McHardy <kaber@trash.net> * * TODO: - NAT to a unique tuple, not to TCP source port * (needs netfilter tuple
reservation)
(C) 2006 by Patrick McHardy <kaber@trash.net>
(C) 2006 IBM Corp */
(C) 2006 kogiidena
(C) 2006 Michael Ellerman, IBM Corp */
(C) 2006 MontaVista Software, Inc. * Vitaly Bordug <vbordug@ru.mvista.com> */
(C) 2006 Montavista Software\n");
(C) 2006 Patrick McHardy <kaber@trash.net> */#include <linux/kernel.h>#include <linux/module.h>#include <linux/moduleparam.h>#include
<linux/textsearch.h>#include <linux/skbuff.h>#include <linu
(C) 2006 Red Hat
(C) 2006 Red Hat <alan@lxorguk.ukuu.org.uk> * * Some parts based on ata_piix.c by Jeff Garzik and others.
(C) 2006 Red Hat Inc */
(C) 2006 Red Hat Inc * Alan Cox * * Based heavily on * linux/drivers/ide/pci/cy82c693.c Version 0.40 Sep. 10, 2002
(C) 2006 Red Hat Inc * * The Opti DMA controllers are related to the older PIO PCI controllers * and indeed the VLB ones. The main differences are
that the timing *numbers are now based off PCI c
(C) 2006 Simtec Electronics
(C) 2006 Thomas Gleixner <tglx@linutronix.de>
(C) 2006 Thomas Maier <balagi@justmail.de> *
(C) 2006 Tim Small
(C) 2007, 2008, 2011, 2012 Patrick McHardy <kaber@trash.net> */
(C) 2007,2008 Dmitry Baryshkov * * Based on:
(C) 2007,2008 Jochen Friedrich <jochen@scram.de> */
(C) 2007-2008, MontaVista Software, Inc.
(C) 2007, 2008 Patrick McHardy <kaber@trash.net> */
(C) 2007,2009,2010 Bartłomiej Zolnierkiewicz * * Based in part on linux/drivers/ide/pci/pdc202xx_old.c
(C) 2007,2009 Bartłomiej Zolnierkiewicz * * Based upon linux/drivers/ide/pci/sis5513.c
(C) 2007-2010 by Jan Engelhardt <jengelh@medozas.de> * * Extracted from xt_TEE.c
(C) 2007,2011 Bartłomiej Zolnierkiewicz * * Based in part on drivers/ide/pci/aec62xx.c
(C) 2007-2012 Patrick McHardy <kaber@trash.net> */#include <linux/module.h>#include <linux/gfp.h>#include <linux/types.h>#include
<linux/skbuff.h>#include <linux/tcp.h>#include <linux/udp.h>#i
(C) 2007 aCarLab (India) Pvt. Ltd. (http://acarlab.com)
(C) 2007 Alexander Shishkin <virtuoso@slind.org>
(C) 2007 Bartłomiej Zolnierkiewicz * * based upon
(C) 2007 by Hans J. Koch <hjk@hansjkoch.de>
(C) 2007 by Hans J. Koch <hjk@hansjkoch.de> * * based on code written by John Morris <john.morris@spirentcom.com>
(C) 2007 by Holger Schurig <hs4233@mail.mn-solutions.de>
(C) 2007 by Patrick McHardy <kaber@trash.net> * * Based on the old ipv4-only ip_queue.c:
(C) 2007 by Sebastian Cla
(C) 2007 Dmitry Adamushko <dmitry.adamushko@gmail.com> * * Group scheduling enhancements by Srivatsa Vaddagiri
(C) 2007 EADS Astrium * * This driver is based on the lm75 and other lm_sensors/hwmon drivers

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(C) 2007 Hans J. Koch <hjk@hansjkoch.de>
(C) 2007 jetzbroadband (http://jetzbroadband.com)
(C) 2007 Kees Lemmens (adapted for kernel 2.6.20)
(C) 2007 Laszlo Attila Toth <panther@balabit.hu> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/kernel.h>#include
<linux/module.h>#include <linux/skbuff.h>#include <linux/netdevice
(C) 2007 Linux Networx (http://lnxi.com)
(C) 2007 Martin K. Petersen <mkp@mkp.net>
(C) 2007 Michel Benoit * * Based on rtc-at91rm9200.c by Rick Bronson
(C) 2007 Mike Galbraith <efault@gmx.de> * * Various enhancements by Dmitry Adamushko.
(C) 2007 Montavista Software, Inc. * Vitaly Bordug <vitb@kernel.crashing.org> * * Converted to of_platform_device. Renamed to i2c-cpm.c.
(C) 2007 Patrick McHardy <kaber@trash.net> */#include <linux/module.h>#include <linux/skbuff.h>#include <linux/gen_stats.h>
(C) 2007 Patrick McHardy <kaber@trash.net> */#include <linux/module.h>#include <linux/skbuff.h>#include <linux/gen_stats.h>#include
<linux/jhash.h>#include <linux/rtnetlink.h>#include <linux/ra
(C) 2007 Pavel Emelyanov <xemul@openvz.org>, OpenVZ, SWsoft Inc.
(C) 2007 Red Hat Inc * * Based upon * linux/drivers/ide/pci/cmd640.c Version 1.02 Sep 01, 1996
(C) 2007 Red Hat, Inc.\n");
(C) 2007 Red Hat Inc * * Note: The controller like many controllers has shared timings for * PIO and DMA. We thus flip to the DMA timings in dma_start
and flip back * in the dma_stop function. Thu
(C) 2007 Rodolfo Giometti\n");
(C) 2007 sgi, Christoph Lameter
(C) 2007 SGI, Christoph Lameter
(C) 2007 SGI, Christoph Lameter */#include <linux/kobject.h>
(C) 2007 sgi. Christoph Lameter. * * Virtual memory maps allow VM primitives pfn_to_page, page_to_pfn, * virt_to_page, page_address() to be
implemented as a base offset * calculation without memor
(C) 2007 SoftwareBitMaker
(C) 2007 Sukadev Bhattiprolu <sukadev@us.ibm.com>, IBM * Many thanks to Oleg Nesterov for comments and help *
(C) 2007 United Security Providers
(C) 2007 Venkatesh Pallipadi <venkatesh.pallipadi@intel.com> * Shaohua Li <shaohua.li@intel.com> * Adam Belay <abelay@novell.com>
* * This code is licenced under the GPL.
(C) 2007 www.douglaskthompson.com
(C) 2007 www.softwarebitmaker.com
(C) 2008, 2009, 2010, 2011 by Marc Kleine-Budde <kernel@pengutronix.de> */
(C) 2008, 2009 by Marc Kleine-Budde <kernel@pengutronix.de> * * Bosch C_CAN controller is compliant to CAN protocol version 2.0 part A and B. *
Bosch C_CAN user manual can be obtained from:
(C) 2008-2009 Pablo Neira Ayuso <pablo@netfilter.org> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/skbuff.h>#include <linux/jhash.h>#include <linux/ip.h>
(C) 2008-2013 Christoph Lameter <cl@linux.com> * * Optimized manipulation for memory allocated through the per cpu * allocator or for addresses of
per cpu variables.
(C) 2008 Anatolij Gustschin <agust@denx.de> * DENX Software Engineering */
(C) 2008 Korolev Alexey <akorolev@infradead.org>
(C) 2008 Krzysztof Piotr Oledzki <ole@ans.pl> */
(C) 2008 Manuel Traut <manut@linutronix.de> *
(C) 2008 Vasiliy Leonenko <vasiliy.leonenko@gmail.com> */#include <linux/module.h>#include <linux/types.h>#include <linux/kernel.h>#include
<linux/init.h>#include <linux/errno.h>#include <linux
(C) 2008 Vasiliy Leonenko <vasiliy.leonenko@gmail.com> * Many thanks to Roman Borisov for initial enabling * * TODO:
(C) 2009-2010 Bartlomiej Zolnierkiewicz
(C) 2009-2010 Bartlomiej Zolnierkiewicz * * Based on
(C) 2009-2010 Bartlomiej Zolnierkiewicz * * Some parts based on ata_piix.c by Jeff Garzik and others.
(C) 2009 Alexander Clouter <alex@digriz.org.uk> */
(C) 2009 Alexander Clouter <alex@digriz.org.uk>
(C) 2009 Bartlomiej Zolnierkiewicz * * Documentation:
(C) 2009 Daniel Mack <daniel@caiaq.de> ). *
(C) 2009 Google Inc. John(Jung-Ik) Lee <jilee@google.com> * * Per Atp867 data sheet rev 1.2, Acard. * Based in part on early ide code from * 2003-
2004 by Eric Uhrhane, Google, Inc.
(C) 2009 - Peter Feuerer peter (a) piie.net * http://piie.net * 2009 Borislav Petkov bp (a) alien8.de * * Inspired by and
many thanks to:
(C) 2009 Samsung Electronics * Author: Michal Nazarewicz (mina86@mina86.com) */
(C) 2009 Sascha Hauer, Pengutronix
(C) 2009 Siemens AG */#include <linux/fb.h>#include <linux/delay.h>#include <linux/init.h>#include <linux/interrupt.h>#include
<linux/module.h>#include <linux/pci.h>#include <linux/slab.h>#if
(C) 2009 Uwe Kleine-Koenig, Pengutronix */

```



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(C) 2010-2011 Red Hat, Inc.\*/  
(C) 2010-2011 Thomas Graf <tgraf@redhat.com>  
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \*/  
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \*\* Based on Len Brown's <lenb@kernel.org> turbostat tool.  
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \*\* Based on SandyBridge monitor. Implements the new package C-states \* (PC8, PC9, PC10) coming with a specific Haswell (family 0  
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \*\* Based on the idea from Michael Matz <matz@suse.de>  
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \*\* Ideas taken over from the perf userspace tool (included in the Linux \* kernel git repo): subcommand builtins and param parsin  
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \*\* Miscellaneous helpers which do not fit or are worth \* to put into separate headers  
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \*\* Output format inspired by Len Brown's <lenb@kernel.org> turbostat tool.  
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \*\* PCI initialization based on example code from:  
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \*\* ToDo: Needs to be done more properly for AMD/Intel specifics  
(C) 2010 Bartlomiej Zolnierkiewicz \*\* based upon  
(C) 2010 Eric Dumazet \*/  
(C) 2010 Luotao Fu <l.fu@pengutronix.de> \* All rights reserved  
(C) 2010 Michael J. Cree \*\* Somewhat based on the Sparc code, and to a lesser extent the PowerPC and  
(C) 2010 Pablo Neira Ayuso <pablo@netfilter.org> \*/  
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(C) 2010 Thomas Renninger <trenn@suse.de> \*/  
(C) 2011 Bartlomiej Zolnierkiewicz \*\* Based in part on linux/drivers/ide/pci/si82c105.c \* SL82C105/Winbond 553 IDE driver  
(C) 2011 Bartlomiej Zolnierkiewicz \*\* Documentation:  
(C) 2011 DENX Software Engineering, Anatolij Gustschin <agust@denx.de> \*/  
(C) 2011 DENX Software Engineering, Anatolij Gustschin <agust@denx.de> \*\* FIXME: this driver is used on a device-tree probed platform: it \* should be defined as a bit-banged SPI device and probed  
(C) 2011 Hans de Goede <hdegoede@redhat.com>  
(C) 2011 Hans de Goede <hdegoede@redhat.com> \*/  
(C) 2011 Intra2net AG <http://www.intra2net.com> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt  
(C) 2011 Intra2net AG <http://www.intra2net.com> \*/#include <linux/init.h>#include <linux/module.h>#include <linux/kernel.h>#include <linux/skbuff.h>#include <linux/atomic.h>#include <linux/ref  
(C) 2011 Linux Foundation, Christoph Lameter \*/  
(C) 2011 Linux Foundation, Christoph Lameter \*\* Compile with: \*  
(C) 2011 Omnibond Systems \*\* Changes by Acxiom Corporation to implement generic service\_operation()  
(C) 2011 Pablo Neira Ayuso <pablo@netfilter.org>  
(C) 2011 Patrick McHardy <kaber@trash.net> \*/  
(C) 2011 Patrick McHardy <kaber@trash.net> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/in.h>#include <linux/ip.h>#include <net/ip.h>#include <linux/module.h>#include <linux/skb  
(C) 2011 The Chromium OS Authors \*/  
(C) 2011 The Chromium OS Authors \*\* In the file "/sys/module/dm\_verity/parameters/prefetch\_cluster" you can set \* default prefetch value. Data are read in "prefetch\_cluster" chunks from the \* has  
(C) 2011 Thomas Renninger <trenn@novell.com> Novell Inc. \*/  
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(C) 2012 by Vyatta Inc. <http://www.vyatta.com> \*/  
(C) 2012 by Vyatta Inc. <http://www.vyatta.com> \*/#include <linux/init.h>#include <linux/module.h>#include <linux/kernel.h>#include <linux/rculist.h>#include <linux/rculist\_nulls.h>#include <li  
(C) 2012 Christoph Lameter <cl@linux.com> \*/#include <linux/slab.h>  
(C) 2012 Google, Inc. \* Author: Kent Overstreet <koverstreet@google.com> \*\* This implements a refcount with similar semantics to atomic\_t - atomic\_inc(), \* atomic\_dec\_and\_test() - but percpu.  
(C) 2012 Michel Lespinasse <>walken@google.com>  
(C) 2012 MontaVista Software, LLC <source@mvista.com> \*\* Based upon \* linux/drivers/ide/pci/cmd64x.c Version 1.30 Sept 10, 2002  
(C) 2012 Pablo Neira Ayuso <pablo@netfilter.org> \*\* This software has been sponsored by Vyatta Inc. <http://www.vyatta.com>

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 (C) 2013 Astaro GmbH \*/  
 (C) 2013 Daniel Borkmann <dborkman@redhat.com> \*/  
 (C) 2013 James Chapman <jchapman@katalix.com> \*/  
 (C) 2013 Viresh Kumar <viresh.kumar@linaro.org> \* \* Oct 2005 - Ashok Raj <ashok.raj@intel.com> \* Added handling for CPU hotplug \* Feb 2006 -  
 Jacob Shin <jacob.shin@amd.com> \* Fix handling for C  
 (C) 2014-2015 Pratik Patel <pratikp@codeaurora.org> \* \* Serious refactoring, code cleanup and upgrading to the Coresight upstream \* framework by  
 Mathieu Poirier  
 (C) 2014 by Pablo Neira Ayuso <pablo@netfilter.org> \*/  
 (C) 2014 by Pablo Neira Ayuso <pablo@netfilter.org> \* \* Based on code from ebt\_log from: \*  
 (C) 2014 Sittisak <sittisaks@hotmail.com> \*/  
 (C) 2015-2016 Chunyan Zhang <zhang.chunyan@linaro.org> \*/#include <asm/local.h>#include <linux/acpi.h>#include <linux/amba/bus.h>#include  
 <linux/bitmap.h>#include <linux/clk.h>#include <linux/c  
 (C) 2015-2016 Mathieu Poirier <mathieu.poirier@linaro.org> \* \* Guaranteed timing and support for various packet type coming from the \* generic STM  
 API by Chunyan Zhang  
 (C) 2015-2018 Kevin Darbyshire-Bryant <kevin@darbyshire-bryant.me.uk>  
 (C) 2015 Borislav Petkov <bp@alien8.de> \* \* This driver allows to upgrade microcode on x86 processors.  
 (C) 2015 by Il Han <corone.il.han@gmail.com> \*/  
 (C) 2015 Pengutronix, Alexander Aring <aar@pengutronix.de>  
 (C) 2015 Pengutronix, Alexander Aring <aar@pengutronix.de> \*/  
 (C) 2015 Pengutronix, Alexander Aring <aar@pengutronix.de> \* \* Authors:  
 (C) 2015 Red Hat GmbH \* Author: Florian Westphal <fw@strlen.de> \*/  
 (C) 2016 Arnaldo Carvalho de Melo <acme@redhat.com> \*/  
 (C) 2016 by Pablo Neira Ayuso <pablo@netfilter.org> \*/  
 (C) 2016 by Pablo Neira Ayuso <pablo@netfilter.org> \* \* Author: Harald Welte <hwelte@sysmocom.de> \* Pablo Neira Ayuso  
 <pablo@netfilter.org> \* Andreas Schultz <aschultz@travelping.com>  
 (C) 2016 Pengutronix, Alexander Aring <aar@pengutronix.de> \*/  
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 (C) 2017 Anju T Sudhakar, IBM Corporation.  
 (C) 2017 Helge Deller <deller@gmx.de> \*/  
 (C) 2017 Hemant K Shaw, IBM Corporation. \*/  
 (C) 2017 Hemant K Shaw, IBM Corporation. \*/#include <linux/kernel.h>#include <linux/platform\_device.h>#include <linux/of.h>#include  
 <linux/of\_address.h>#include <linux/of\_platform.h>#include <l  
 (C) 2017 Hemant K Shaw, IBM Corporation. \*/#include <linux/perf\_event.h>#include <linux/slab.h>#include <asm/opal.h>#include <asm/imc-  
 pmu.h>#include <asm/cputhreads.h>#include <asm/smp.h>#incl  
 (C) 2017 Linus Walleij <linus.walleij@linaro.org> \*/#ifdef \_GEMINI\_ETHERNET\_H#define \_GEMINI\_ETHERNET\_H  
 (C) 2017 Red Hat GmbH \* Author: Florian Westphal <fw@strlen.de> \* \* split from xt\_connlimit.c:  
 (C) 2018 Mojatau, Alexander Aring <aring@mojatau.com>  
 (C) 2018 Uladzislau Rezki (Sony) <urezki@gmail.com> \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/module.h>#include  
 <linux/vmalloc.h>#include <linux/random.h>#include <linux  
 (C) \* 2 - 1) : ((C)  
 (C) Adam Belay. \*/#include <linux/module.h>#include <linux/pci.h>#include <linux/pnp.h>#include <linux/string.h>#include  
 <linux/kernel.h>#include <linux/serial\_core.h>#include <linux/bitops.h>  
 (C) Alex Holden " " <alex@linuxhacker.org> 1998.\n", VERSION);  
 (C) and server (S): \* \* Setup of client connection:  
 (C) Architecture Software \* Developer's Manual, Volume 3: System Programming Guide, \* revision #12 in Table B-1: MSRs in the Pentium 4 and \*  
 Intel Xeon Processors, on page B-4 and B-5. \*/ fsb\_  
 (C) \* A(W1) \* C(C) \* \* Which would create W1->C->W1 dependencies, even though there is no \* actual deadlock  
 possible. There are two solutions, using a \* read-recursive acquire on the wor  
 (C) Balbir Singh, IBM Corp. 2006  
 (C) Balbir Singh, IBM Corp. 2006 \*/  
 (C) Benjamin Reed,  
 (C) bits in a HPT \* entry can be set by hardware at any time up until \* the HPTE is invalidated and the TLB invalidation \* sequence  
 has completed. This means that when \* removing a HPTE,  
 (C) \_\_BUG\_ON((unsigned long)(C))  
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 (C) by Vojtech Pavlik \*/  
 (C) (((C) << 11)  
 (C) Carnegie Mellon University  
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(C) CC Computer Consultants GmbH, 2007 \* \* based on ...  
(C) CC Computer Consultants GmbH, 2007 \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/in.h>#include <linux/in6.h>#include <linux/ip.h>#include <linux/ipv6.h>#include <linux/jhash.  
(C) CC Computer Consultants GmbH, 2008 \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include <linux/skbuff.h>#include <linux/ip.h>#include <linux/ipv6.h>#include <linux  
(C)(C) Leaguerme (Shenzhen) MicroElectronics Co., Ltd. \* \* see Documentation/media/dvb-drivers/dvb-usb.rst for more information  
(C) Copyright 1991-2000 Linus Torvalds \* \* We have a per-user structure to keep track of how many \* processes, files etc the user has claimed, in order to be \* able to have per-user limits for sys  
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(C) Copyright 1995 1996 Linus Torvalds \*/#include <linux/vmalloc.h>#include <linux/mm.h>#include <linux/sched.h>#include <linux/io.h>#include <linux/export.h>#include <asm/cacheflush.h>#includ  
(C) Copyright 1995 - 1997 Marco van Wieringen - ELM Consultancy B.V. \* \* Plugged two leaks. 1) It didn't return acct\_file into the free\_filps if \* the file happened to be read-only. 2) If the acc  
(C) Copyright 1995-1999 Tekram Technology Co., Ltd. \* \* Kurt Garloff <garloff@suse.de>  
(C) Copyright 1995-2003 by Geert Uytterhoeven <geert@linux-m68k.org> \* \* Fixes and tips by:  
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(c) Copyright 1995 Alan Cox <alan@lxorguk.ukuu.org.uk> \* \* 14-Dec-2001 Matt Domsch <Matt\_Domsch@dell.com> \* Added nowayout  
module option to override CONFIG\_WATCHDOG\_NOWAYOUT  
(c) Copyright 1995 Alan Cox <alan@lxorguk.ukuu.org.uk> \* \* 14-Dec-2001 Matt Domsch <Matt\_Domsch@dell.com> \* Added nowayout  
module option to override CONFIG\_WATCHDOG\_NOWAYOUT \* Added t  
(c) Copyright 1995 Alan Cox <alan@lxorguk.ukuu.org.uk> \* \* 14-Dec-2001 Matt Domsch <Matt\_Domsch@dell.com> \* Added nowayout  
module option to override CONFIG\_WATCHDOG\_NOWAYOUT \* Can't add  
(c) Copyright 1995 Alan Cox <alan@lxorguk.ukuu.org.uk> \* \* Release 0.10.  
(c) Copyright 1995 Alan Cox <alan@lxorguk.ukuu.org.uk> \* \* Software only watchdog driver. Unlike its big brother the WDT501P \* driver this won't always recover a failed machine.  
(c) Copyright 1995 Alan Cox <alan@lxorguk.ukuu.org.uk> \* \* The OCTEON watchdog has a maximum timeout of 2^32 \* io\_clock. \* For most systems this is less than 10 seconds, so to allow for \* softw  
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\* (C) Copyright 1995 by Geert Uytterhoeven, \* Peter De Schrijver \* \* lance.c: An AMD LANCE ethernet driver for linux.  
\* Written 1993-94 by Donald Becker.  
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(C) Copyright 1996 Linus Torvalds \* \* Address space accounting code <alan@lxorguk.ukuu.org.uk>  
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TCO timer driver for NV chipsets * based on softdog.c by Alan Cox <alan@
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<linux/ioport.h>#include <linux/sched.h>#include <linux/slab.h>#in
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4/12 - 2002 [rob@osinv
(c) Copyright 2001 Scott Jennings <linuxdrivers@oro.net> * 9/27 - 2001 [Initial release] * * Additional fixes Alan Cox * - Fixed
formatting * - Removed debug prints * - Fixed
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(C) Copyright 2002 Alan Cox <alan@lxorguk.ukuu.org.uk> */#include <linux/interrupt.h>#include <linux/kernel.h>#include <linux/types.h>#include
<linux/smp.h>
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(C) Copyright 2004 Linus Torvalds \*/#include <linux/pci.h>#include <linux/mm.h>#include <linux/export.h>#include <asm/io.h>#include <asm/pci-bridge.h>#include <asm/isa-bridge.h>

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(c) Copyright 2005 Benjamin Herrenschmidt, IBM Corp. \* <benh@kernel.crashing.org> \* \* This core code tracks the list of sensors & controls, register \* clients, and holds the ker

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Review Board proposal 1842, \* which was approved on 9/  
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data  
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(C) Copyright 2007 MIPS Technologies, Inc. \* written by Ralf Baechle <ralf@linux-mips.org> \*/#include <linux/pci.h>#include  
<linux/export.h>#include <asm/io.h>  
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<linux/serial_reg.h>#include <linux/serial_8250.h>
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_PLATFORM_MSIC_H_#define _PLATFORM_MSIC_H_
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<linux/kernel.h>#include <linux/interrupt.h>#include <linux/scatterlist
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(C) Copyright 2014-2015 Intel Corporation * * This driver is based on Intel SCU IPC driver(intel_scu_ipc.c) by * Sreedhara DS
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power management usage.

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3.0, (ITMT), * the maximum turbo frequencies of so
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expires, the hypervisor stops or bounces * the guest do
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(C) Copyright Al Viro 2002,2003 * * NOTE: data structure needs to be changed. It works, but for large dev_t * it will be too slow. It is isolated, though,
so these changes will be * local to tha
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(C) Copyright AudioScience Inc. 1996-2009*****/
(C) Copyright AudioScience Inc. 1997-2003*****/
(C) Copyright AudioScience Inc. 1997-2003*****/#define
SOURCEFILE_NAME "hpios.c"#include "hpi_internal.h"#include "hptidebug
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(C) Copyright AudioScience Inc. 1998-2003*****/
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(C) Copyright AudioScience Inc. 1998-2003*****/#define
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SOURCEFILE_NAME "hpicmn.c"
(C) Copyright AudioScience Inc. 1998-2010*/
(C) Copyright AudioScience Inc. 1998-2010*****/#define
SOURCEFILE_NAME "hpi6205.c"
(C) Copyright AudioScience Inc. 2002*****/#define
SOURCEFILE_NAME "hpimsgx.c"#include "hpi_internal.h"#include "hpi_version.
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(C) Copyright AudioScience Inc. 2007*****// * Initialise
response headers, or msg/response pairs.Note that it is valid to ju
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<linux/mount.h>#include <linux/fs.h>#include <linux/nsproxy.h>#include <u  
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 (C) Copyright TOSHIBA CORPORATION 2005-2007 \*/#include <linux/rtc.h>#include <linux/platform\_device.h>#include <linux/interrupt.h>#include <linux/module.h>#include <linux/io.h>#include <linux/g  
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 (C) David Hinds.  
 (C) David Hinds and the Linux wireless  
 (C) David S. Miller. \*/  
 (C) DENX Software Engineering. \* \* Author: Yuri Tikhonov <yur@emcraft.com>  
 (C) (diagram), 9.2 \* Upon reception of the SHUTDOWN COMPLETE chunk the endpoint will verify \* that it is in SHUTDOWN-ACK-SENT state, if it is not the chunk should be \* discarded. If the endpoint is  
 (C) " DRIVER\_AUTHOR "\n");  
 (C) DVB-S USB2.0");  
 (C) IBM Corporation, 2011. \* Author: Suzuki Poulouse <suzuki@in.ibm.com> \* \* - Based on ppc64 code - reloc\_64.S  
 (C) in bits ( MOVQ\_R64\_XMM %r12, %xmm1  
 (C) in bits ( vmovq %r12, %xmm1 vpslldq \$8, %xmm15, %xmm15 # xmm15 = len(A)|| 0x0000000000000000  
 (C) INESC 1998 \*/  
 (C) INESC 1998 \*  
 (C) INESC 1999 \*/  
 (C) \* in woken\_wake\_function() such that either we see the wait condition \* being true or the store to wq\_entry->flags in woken\_wake\_function()

```

* follows ours in the coherence order. */ smp_s
(C) IP_VS_DBG_ADDR((C)->af, ntohs((C)->cport), \ IP_VS_DBG_ADDR((C)->af, &((C)->vaddr)), \
ntohs((C)->vport), \ IP_VS_DBG_ADDR((C)->daf, &((C)->daddr)), \ ntohs((C)->dport), \
(C) Jay Lan, SGI, 2006
(C) Jean Tourrilhes.
(C) Jens Axboe <jens.axboe@oracle.com> 2008 */
(C) Jiri Kosina\n");out:
(C) Len Brown <len.brown@intel.com>\n");}
(C) Linus Torvalds * * 'traps.c' handles hardware exceptions after we have saved some state in * 'linux/arch/arm/lib/traps.S'. Mostly a debugging aid,
but will probably * kill the offending pr
(C) Linux Foundation 2008-2013 * Unified interface for all slab allocators */
(C) Nadia Yvette Chambers, April 2004 */#include <linux/list.h>#include <linux/init.h>#include <linux/mm.h>#include <linux/seq_file.h>#include
<linux/sysctl.h>#include <linux/highmem.h>#includ
(C) Neil Horman <nhorman@tuxdriver.com> */
(C) * Notes: To disconnect the current associated BSS * Command Mode */struct disconnect_parm { u32 rsvd;
(C) * Notes: To enter USB suspend mode * Command Mode */struct usb_suspend_parm { u32 action; /* 1: sleep, 0:resume */
copyright ;-) */
COPYRIGHT (0 << 2)
COPYRIGHT) ? 0 : AC97_CXR_COPYRGT; v |= new & IEC958_AES0_NONAUDIO ? AC97_CXR_SPDIF_AC3 : AC97_CXR_SPDIF_PCM;
change |= snd_ac97_update_bits_nolock(ac97, AC97_CXR_AUDIO_MISC, AC97_CXR
copyright (0 = copy inhibited) * D1: 0 = digital audio / 1 = non-digital audio * * C6 upper channel status (digital serial data description)
(CS4237B,CS4238B) * D7-D6: sample freque
COPYRIGHT 0#define DVD_CPM_COPYRIGHTED 1
COPYRIGHT (0)#define EEPROM_COPYRIGHT_LEN (44)#define EEPROM_CHECKSUM (62)#define EEPROM_CHECKSUM_REV (63)#define
EEPROM_MAC (64)#define EEPROM_MAC_REV (70)#define EEPROM_SERIAL (112)#defin
COPYRIGHT 0x00000004 /
COPYRIGHT 0x01#define DVD_STRUCT_DISCKEY 0x02#define DVD_STRUCT_BCA 0x03#define DVD_STRUCT_MANUFACT 0x04
COPYRIGHT = 0x10, AUDIO_STATUS_NONAUDIO = 0x20, AUDIO_STATUS_PROFESSIONAL = 0x40, AUDIO_STATUS_LEVEL =
0x80};
COPYRIGHT (0x1 << 2)
COPYRIGHT 0xd4#define CX2341X_ENC_SET_EVENT_NOTIFICATION 0xd5#define CX2341X_ENC_SET_NUM_VSYNC_LINES
0xd6#define CX2341X_ENC_SET_PLACEHOLDER 0xd7#define CX2341X_ENC_MUTE_VIDEO 0xd9#define CX
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Copyright 10/16/2005 Tilman Kranz <ttilde@tk-sls.de> * Creative Audio MIDI, for the CA0106 Driver * Version: 0.0.1 * * Changelog:
COPYRIGHT (1 << 2)#define HDMI_I2S_LINEAR_PCM (0 << 1)#define HDMI_I2S_NO_LINEAR_PCM (1 << 1)#define
HDMI_I2S_CONSUMER_FORMAT (0)#define HDMI_I2S_PROF_FORMAT (1)#define HDMI_I2S_CH_ST_0_CLR
COPYRIGHT (1<<2) /*#define IEC958_AES0_CON_EMPHASIS (7<<3) /* mask - emphasis */#define IEC958_AES0_CON_EMPHASIS_NONE
(0<<3) /* none emphasis */#define IEC958_AES0_CON_EMPHASIS_5015 (1<<3) /* 50/15u
COPYRIGHT (1<<4)#define AC_DIG1_NONAUDIO (1<<5)#define AC_DIG1_PROFESSIONAL (1<<6)#define AC_DIG1_LEVEL
(1<<7)
COPYRIGHT (1 << 5)#define m_AUDIO_STATUS_ADDITION (3 << 2)#define m_AUDIO_STATUS_CLK_ACCURACY (2 << 0)#define
v_AUDIO_STATUS_NLPCM(n) ((n & 1) << 7)#define AUDIO_N_H 0x3f#define AUDIO_N_M 0
copyright * 1986, 1989, 1990 by Abacus Research and Development, Inc. (ARDI) * It is used here by the permission of ARDI's president Cliff Matthews.
*/
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>device,BACKPACK_VERSION,pi->port);    printk("%s: Unit: %d
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<linux/ptrace.h>#include <linux/delay.h>
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Copyright 2002 Andi Kleen, SuSE Labs. * * This hasn't been optimized for the hammer yet, but there are likely * no advantages to be gotten from x86-64
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<linux/irq.h>#include <linux/perf\_event.h>#include <linux/ex  
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<asm/reg.h>#include <asm/cputable.h>  
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<linux/perf\_event.h>#include <linux/percpu.h>#incl  
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 <linux/interrupt.h>#include <linux/module.h>#include <linux/m  
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 <linux/percpu.h>#include <linux/hardirq.h>#include <asm/reg  
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<linux/virtio.h>#include <linux/virtio_config.h>#include <linux/virtio
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<linux/err.h>#include <linux/init.h>#include <linux/inte  
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<linux/regmap.h>#include <linux/slab.h>#include <linux/cdev.h>#inc  
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<linux/debugfs.h>#include <linux/list.h>#include <lin
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 "opp.h"#ifdef CONFIG\_DRM\_AMD\_DC\_DSC\_SUPPORT#include "dsc  
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<linux/platform\_device.h>#include <linux/spinlock.h>  
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<linux/of.h>#include <linux/platform\_device.h>#include <linux/spin  
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transaction (suspended since active is \* al  
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Copyright 2015 Linaro. \*/#include <linux/sched.h>#include <linux/device.h>#include <linux/dmaengine.h>#include <linux/dma-mapping.h>#include  
<linux/dmapool.h>#include <linux/init.h>#include <l

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<stdint.h>#include <poll.h>#include <sys/types.h>#include <  
Copyright 2015 Linaro Ltd. \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/slab.h>#include <linux/sizes.h>#include  
<linux/cdev.h>#include <linux/fs.h>#include <linux/idr.h>  
Copyright 2015 Linaro Ltd. \*/#include <linux/kernel.h>#include <linux/module.h>#include <sound/soc.h>#include <sound/pcm\_params.h>  
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Copyright 2015, Michael Neuling, IBM Corp. \* Original: Michael Neuling 19/7/2013 \* Edited: Rashmica Gupta 01/12/2015 \* \* Do some transactions, see  
if the tar is corrupted. \* If the transaction is  
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Copyright 2015, Michael Neuling, IBM Corp. \* \* Test the kernel's signal delivery code to ensure that we don't \* reclaim twice in the kernel signal delivery  
code. This can happen \* if we trigger  
Copyright 2015, Michael Neuling, IBM Corp. \* \* Test the kernel's signal return code to ensure that it doesn't \* crash when both the transactional and  
suspend MSR bits are set in \* the signal conte  
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Copyright 2015 Robert Jarzmik <robert.jarzmik@free.fr> \*/  
Copyright 2015, Sam Bobroff, IBM Corp. \* \* Test the kernel's system call code to ensure that a system call \* made from within an active HTM transaction  
is aborted with the \* correct failure code.  
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Copyright 2016-2017 NXP */#include <linux/init.h>#include <linux/module.h>#include <linux/platform_device.h>#include
<linux/etherdevice.h>#include <linux/of_net.h>#include <linux/interrupt.h>#
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<linux/mtd/partitions.h>#include <linux/mutex.h>#include <li
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Copyright 2016 Broadcom */#include <linux/clk.h>#include <linux/platform_device.h>#include <linux/device.h>#include <linux/of_mdio.h>#include
<linux/module.h>#include <linux/phy.h>#include <li
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0x5a) \* SI1145/6/6 (7-bit I2C slave address 0x60)  
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Copyright 2016, Cyril Bur, IBM Corp. \* \* Sending one self a signal should always get delivered.  
Copyright 2016, Cyril Bur, IBM Corp. \* \* Syscalls can be performed provided the transactions are suspended. \* The exec() class of syscall is unique as a  
new process is loaded.  
Copyright 2016, Cyril Bur, IBM Corp. \* \* Test the kernel's signal frame code.  
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Copyright 2016 Ezequiel Garcia <ezequiel@vanguardiasur.com.ar> \* \* Based on LED IDE-Disk Activity Trigger  
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parameters, input and output pointers.  
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Copyright 2016 Google Inc. \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/slab.h>#include <linux/sizes.h>#include  
<linux/uaccess.h>#include <linux/greybus.h>  
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<linux/interrupt.h>#include <linux/cpu.h>#include <linux  
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Copyright 2016 Linaro Ltd. */
Copyright 2016 Linaro Ltd. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt
Copyright 2016 Linaro Ltd. */#include <linux/kernel.h>#include <linux/module.h>#include <linux/pm_runtime.h>#include <sound/soc.h>#include
<sound/pcm_params.h>#include <uapi/linux/input.h>
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Copyright 2016 Naveen N. Rao <naveen.n.rao@linux.vnet.ibm.com> * IBM Corporation */#include <linux/kprobes.h>#include
<linux/ptrace.h>#include <linux/hardirq.h>#include <linux/preempt.h>#inc
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Copyright 2016 Netflix, Inc. */#include <stdio.h>#include <stdlib.h>#include <unistd.h>#include <errno.h>#include <signal.h>#include
<string.h>#include <assert.h>#include <linux/perf_event.h>
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Copyright 2016 NXP */#ifndef _FSL_DPNI_CMD_H#define _FSL_DPNI_CMD_H
Copyright 2016 NXP */#include <linux/kernel.h>#include <linux/errno.h>#include <linux/fsl/mc.h>#include "dpni.h"#include "dpni-cmd.h"
Copyright 2016 NXP Semiconductor * * Derived from mpc85xx_edac.c * Author: Dave Jiang <djiang@mvista.com>
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Copyright 2016 Peter Meerwald-Stadler <pmeerw@pmeerw.net> * * IIO driver for VEML6070 (7-bit I2C slave addresses 0x38 and 0x39)
Copyright 2016 Qtechnology A/S * 2016 Ricardo Ribalda <ricardo.ribalda@gmail.com> */#ifndef __LINUX_PLATFORM_DATA_AD5761_H__#define
__LINUX_PLATFORM_DATA_AD5761_H__
Copyright 2016 Qtechnology A/S * 2016 Ricardo Ribalda <ricardo.ribalda@gmail.com> */#include <linux/kernel.h>#include <linux/module.h>#include
<linux/spi/spi.h>#include <linux/bitops.h>#include
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Copyright 2016, Rashmica Gupta, IBM Corp. *
Copyright 2016, Rashmica Gupta, IBM Corp. * * This traverses the kernel pagetables and dumps the * information about the used sections of memory to
* /sys/kernel/debug/kernel_pagetables.
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<linux/moduleparam.h>#include <linux/init.h>#include <linux/delay.h>#inclu
Copyright 2016 Realtek Semiconductor Corp. * Author: Oder Chiou <oder_chiou@realtek.com> */
Copyright 2016 Red Hat Inc.
Copyright 2016 Red Hat, Inc. and/or its affiliates. */#include <linux/kvm_host.h>#include <linux/debugfs.h>#include "lapic.h"
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<jcsing.lee@samsung.com>  
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Copyright 2017-2018 NXP \* Dong Aisheng <aisheng.dong@nxp.com> \* \* Implementation of the SCU based Power Domains  
Copyright 2017-2018 NXP \* \* Header file containing the public API for the System Controller (SC) \* Power Management (PM) function. This includes  
functions for power state \* control, clock control,  
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Copyright 2017 Analog Devices Inc. \* Author: Lars-Peter Clausen <lars@metafoo.de> \*/  
Copyright 2017 Analog Devices Inc. \*/#include <linux/bitfield.h>#include <linux/clk.h>#include <linux/delay.h>#include <linux/device.h>#include  
<linux/err.h>#include <linux/gpio/consumer.h>#in  
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Copyright 2017 \* Author: Dong Aisheng <aisheng.dong@nxp.com> \* \* File containing client-side RPC functions for the MISC service. These \* function  
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```

Copyright 2017 Cadence Design Systems Inc. */#include <linux/memblock.h>#include <linux/stddef.h>#include <linux/kernel.h>#include
<linux/init.h>#include <linux/errno.h>#include <linux/reboot.h
Copyright 2017 Canonical Ltd. */
Copyright 2017 Canonical Ltd. ** TODO * If a task uses change_hat it currently does not return to the old * cred or task context but instead creates a
new one. Ideally the task * should return
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<krzk@kernel.org>
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Copyright 2017 Google Inc * * Provides a simple driver to control the ASPEED LPC snoop interface which * allows the BMC to listen on and save the data
written by * the host to an arbitrary LPC I/O
Copyright 2017 Google, Inc * * This driver uses the memory mapper cross-ec interface to communicate * with the Chrome OS EC about accelerometer
data or older commands. * Accelerometer access is pre
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Copyright 2017, Gustavo Romero, Breno Leitao, Cyril Bur, IBM Corp. * * Force FP, VEC and VSX unavailable exception during transaction in all * possible
scenarios regarding the MSR.FP and MSR.VEC st
Copyright 2017, Gustavo Romero, IBM Corp. * * Check if thread endianness is flipped inadvertently to BE on trap * caught in TM whilst MSR.FP and
MSR.VEC are zero (i.e. just after * load_fp and loa
Copyright 2017 Hannes Reinecke, SUSE Linux GmbH <hare@suse.com> * * Based on the original DAC960 driver,
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Copyright 2017 * * Header file containing the public System Controller Interface (SCI) * definitions.
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Copyright 2017 IBM Corp.#ifndef _ASM_PNV_OCXL_H#define _ASM_PNV_OCXL_H
Copyright 2017 IBM Corp.#ifndef _CHECKER_#define CREATE_TRACE_POINTS#include "trace.h"
Copyright 2017 IBM Corp.#ifndef _MISC_OCXL_H_#define _MISC_OCXL_H_
Copyright 2017 IBM Corp.#ifndef _OCXL_CONFIG_H_#define _OCXL_CONFIG_H_
Copyright 2017 IBM Corp.#ifndef _OCXL_INTERNAL_H_#define _OCXL_INTERNAL_H_
Copyright 2017 IBM Corp.#ifndef _UAPI_MISC_OCXL_H#define _UAPI_MISC_OCXL_H
Copyright 2017 IBM Corp.#include <asm/pnv-ocxl.h>#include <asm/opal.h>#include <asm/xive.h>#include <misc/ocxl-config.h>#include "pci.h"
Copyright 2017 IBM Corp.#include <linux/fs.h>#include <linux/poll.h>#include <linux/sched/signal.h>#include <linux/eventfd.h>#include
<linux/uaccess.h>#include <uapi/misc/ocxl.h>#include <asm/r
Copyright 2017 IBM Corp.#include <linux/interrupt.h>#include <asm/pnv-ocxl.h>#include "ocxl_internal.h"#include "trace.h"
Copyright 2017 IBM Corp.#include <linux/module.h>#include <linux/pci.h>#include <asm/mmu.h>#include "ocxl_internal.h"
Copyright 2017 IBM Corp.#include <linux/pci.h>#include <asm/pnv-ocxl.h>#include <misc/ocxl-config.h>#include "ocxl_internal.h"
Copyright 2017 IBM Corp.#include <linux/sched/mm.h>#include <linux/mutex.h>#include <linux/mm_types.h>#include
<linux/mmu_context.h>#include <asm/copro.h>#include <asm/pnv-ocxl.h>#include <misc
Copyright 2017 IBM Corp.#include <linux/sched/mm.h>#include "trace.h"#include "ocxl_internal.h"
Copyright 2017 IBM Corp.#include <linux/sysfs.h>#include "ocxl_internal.h"
Copyright 2017 IBM Corp.#include "ocxl_internal.h"
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Copyright 2017, IBM Corporation * cpufeatures is the new way to discover CPU features with /cpus/features * devicetree. This supersedes PVR based
discovery ("cputable"), and older * device tree

```

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Copyright 2017 Manivannan Sadhasivam <manivannanec23@gmail.com> \* \* IIO driver for VL6180 (7-bit I2C slave address 0x29)  
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Copyright 2017 Texas Instruments, Inc. \*/#ifndef \_\_DT\_BINDINGS\_CLK\_DRA7\_H#define \_\_DT\_BINDINGS\_CLK\_DRA7\_H  
Copyright 2017 Texas Instruments, Inc. \*/#ifndef \_\_DT\_BINDINGS\_CLK\_OMAP4\_H#define \_\_DT\_BINDINGS\_CLK\_OMAP4\_H  
Copyright 2017 Texas Instruments, Inc. \*/#ifndef \_\_DT\_BINDINGS\_CLK\_OMAP5\_H#define \_\_DT\_BINDINGS\_CLK\_OMAP5\_H  
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Copyright 2018-2019 NXP \*/  
Copyright 2018-2019 NXP \* \* Based on caamalg.c crypto API driver.  
Copyright 2018-2019 NXP \*/#ifndef DPAA2\_ETH\_DEBUGFS\_H#define DPAA2\_ETH\_DEBUGFS\_H  
Copyright 2018-2019 NXP \*/#include <linux/module.h>#include <linux/debugfs.h>#include "dpaa2-eth.h"#include "dpaa2-eth-debugfs.h"  
Copyright 2018-2019 NXP. \* \* Limitations:  
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Copyright 2018 Advanced Micro Devices, Inc. \*/  
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Copyright 2018 Analog Devices Inc. \*/#include <linux/bitfield.h>#include <linux/clk.h>#include <linux/delay.h>#include <linux/device.h>#include <linux/err.h>#include <linux/kernel.h>#include <  
Copyright 2018 Analog Devices Inc. \* \* TODO: Currently CRC is not supported in this driver  
Copyright 2018 Andrej Shadura <andrew@shadura.me>  
Copyright 2018 Angelo Dureghello <angelo@sysam.it> \*/#ifndef \_FSL\_EDMA\_COMMON\_H\_#define \_FSL\_EDMA\_COMMON\_H\_  
Copyright 2018 Anshuman Khandual, IBM Corporation. \*/  
Copyright 2018, Anton Blanchard, IBM Corp. \*/  
Copyright 2018 Arcx Inc \*/  
Copyright 2018 Arm Limited \* Author: Dave Martin <Dave.Martin@arm.com> \*/#include <linux/irqflags.h>#include <linux/sched.h>#include <linux/thread\_info.h>#include <linux/kvm\_host.h>#include <as  
Copyright 2018, Breno Leitao, Gustavo Romero, IBM Corp. \* \* A test case that creates a signal and starts a suspended transaction \* inside the signal handler.  
Copyright 2018, Breno Leitao, Gustavo Romero, IBM Corp. \* \* This test raises a SIGUSR1 signal, and toggle the MSR[TS] \* fields at the signal handler.  
With MSR[TS] being set, the kernel will \* forc  
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Copyright 2018 David Lechner <david@lechnology.com> \* \* Based on mi0283qt.c:  
Copyright 2018 DEVIALET \*/  
Copyright 2018 Google, Inc.  
Copyright 2018 Google Inc. \* Author: Eric Dumazet (edumazet@google.com) \* \* Reference program demonstrating tcp mmap() usage, \* and SO\_RCVLOWAT hints for receiver.  
Copyright 2018 Google Inc. \* Author: Soheil Hassas Yeganeh (soheil@google.com) \* \* Simple example on how to use TCP\_INQ and TCP\_CM\_INQ.

Copyright 2018 Google, Inc \* \* This driver uses the cros-ec interface to communicate with the Chrome OS \* EC about counter sensors. Counters are presented through \* iio sysfs.  
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Copyright 2018 Google LLC \* \* The corresponding platform device is typically registered in \* drivers/platform/chrome/wilco\_ec/core.c  
Copyright 2018 Google LLC \* \* The Wilco EC is similar to a typical ChromeOS embedded controller. \* It uses the same MEC based low-level communication and a similar \* protocol, but with some import  
Copyright 2018 Google LLC \* \* This is the entry point for the drivers that control the Wilco EC. \* This driver is responsible for several tasks:  
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Copyright 2018 Google LLC. \* Tomasz Figa <tfiga@chromium.org> \* \* Based on s5p-mfc driver by Samsung Electronics Co., Ltd.  
Copyright 2018 HabanaLabs, Ltd. \* All Rights Reserved  
Copyright 2018 Hannes Reinecke, SUSE Linux GmbH <hare@suse.com> \* \* Based on the original DAC960 driver, which has  
Copyright 2018 Hans de Goede <hdegoede@redhat.com> \*/  
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Copyright 2018 IBM Corp/\* \* A FSI master controller, using a simple GPIO bit-banging interface  
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Jacob Pan <jacob.jun.pan@linux.intel.com>  
Copyright © 2018 Intel Corporation \*/#include "gt/intel\_gt.h"  
Copyright 2018, Jann Horn <jannh@google.com>  
Copyright 2018 Konsulko Group \* \* Author: Matt Porter <mporter@konsulko.com>  
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Copyright 2018, Michael Ellerman, IBM Corporation.  
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Copyright 2018, Michael Ellerman, IBM Corporation. \*/#ifndef \_ASM\_POWERPC\_CODE\_PATCHING\_ASM\_H#define  
\_ASM\_POWERPC\_CODE\_PATCHING\_ASM\_H  
Copyright 2018, Michael Ellerman, IBM Corp. \* \* Test that an out-of-bounds branch to counter behaves as expected.  
Copyright 2018, Murilo Opsfelder Araujo, IBM Corporation. \*/  
Copyright 2018 Nick Piggin, Michael Ellerman, IBM Corp. \*/  
Copyright 2018 Noralf Tr  
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Copyright 2018 NXP \* Author: Dong Aisheng <aisheng.dong@nxp.com> \* \* Implementation of the SCU IPC functions using MUs (client side).  
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Copyright 2018 NXP \* \* Header file for the IPC implementation.  
Copyright 2018 NXP \*/#ifndef \_\_PTP\_QORIQ\_H\_\_#define \_\_PTP\_QORIQ\_H\_\_  
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Copyright 2018 NXP. \* \* This driver supports the fractional plls found in the imx8m SOCs  
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Copyright 2018 Pavel Machek <pavel@ucw.cz> // TI LMU LED common framework, based on previous work from// Milo Kim <milo.kim@ti.com>  
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Copyright 2018 SUSE Linux. \* Author: Coly Li <colyli@suse.de> \*/  
Copyright 2018 SUSE Linux. \* Author: Coly Li <colyli@suse.de> \*/#include <inttypes.h>#include <stdio.h>  
Copyright 2018 SUSE Linux GmbH  
Copyright 2018 Tempo Semiconductor, Inc. // Author: Steven Eckhoff <steven.eckhoff.opensource@gmail.com>  
Copyright 2018, Thiago Jung Bauermann, IBM Corporation. \*/#include <stdio.h>#include <stdbool.h>#include <semaphore.h>  
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Copyright 2019 Analog Devices Inc. \* \* https://wiki.analog.com/resources/fpga/docs/axi\_ip \* https://wiki.analog.com/resources/fpga/docs/hdl/regmap  
Copyright 2019 Analog Devices Inc. \*/#include <linux/bitfield.h>#include <linux/clk.h>#include <linux/device.h>#include <linux/err.h>#include  
<linux/gcd.h>#include <linux/kernel.h>#include <li  
Copyright 2019 Analog Devices Inc. \*/#include <linux/kernel.h>#include <linux/bitfield.h>#include <linux/delay.h>#include <linux/errno.h>#include  
<linux/init.h>#include <linux/module.h>#includ  
Copyright 2019 Annaliese McDermond \* \* Author: Annaliese McDermond <nh6z@nh6z.net>  
Copyright 2019 ARM Limited \* \* 32 Bit compat layer by Stefani Seibold <stefani@seibold.net> \* sponsored by Rohde & Schwarz GmbH & Co. KG  
Munich/Germany  
Copyright 2019 Arm Limited \* Author: Andrew Murray <Andrew.Murray@arm.com> \*/#include <linux/kvm\_host.h>#include  
<linux/perf\_event.h>#include <asm/kvm\_hyp.h>  
Copyright 2019 Arm Limited \* Authors: Mark Rutland <mark.rutland@arm.com> \* Amit Daniel Kachhap <amit.kachhap@arm.com> \*/  
Copyright 2019 ARM Ltd. \* \* Generic implementation of update\_vsyscall and update\_vsyscall\_tz.  
Copyright 2019 Cadence \* \* Authors:  
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Copyright © 2019 Collabora Ltd. \*/#ifndef \_\_PANFROST\_DRM\_H\_\_#define \_\_PANFROST\_DRM\_H\_\_  
Copyright 2019 Collabora Ltd#ifndef \_\_PANFROST\_PERFCNT\_H\_\_#define \_\_PANFROST\_PERFCNT\_H\_\_  
Copyright 2019 Collabora Ltd.#include <linux/bitfield.h>#include <linux/bitmap.h>#include <linux/delay.h>#include <linux/dma-mapping.h>#include  
<linux/interrupt.h>#include <linux/io.h>#include  
Copyright 2019 Collabora Ltd.#include <linux/delay.h>#include <linux/interrupt.h>#include <linux/io.h>#include <linux/platform\_device.h>#include  
<linux/pm\_runtime.h>#include <linux/dma-resv.h>#  
Copyright 2019 Collabora Ltd.#include <linux/devfreq.h>#include <linux/platform\_device.h>#include <linux/pm\_opp.h>#include <linux/clk.h>#include  
<linux/regulator/consumer.h>  
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 Copyright 2019 Google LLC \*/#ifndef \_UAPI\_LINUX\_FSVERITY\_H#define \_UAPI\_LINUX\_FSVERITY\_H  
 Copyright 2019 Google LLC \* \* See Documentation/ABI/testing/debugfs-wilco-ec for usage.  
 Copyright 2019 Google LLC \* \* See Documentation/ABI/testing/sysfs-class-power and \* Documentation/ABI/testing/sysfs-class-power-wilco for  
 userspace interface \* and other info.  
 Copyright 2019 Google LLC \* \* Sysfs properties to view and modify EC-controlled features on Wilco devices. \* The entries will appear under  
 /sys/bus/platform/devices/GOOG000C:00/  
 Copyright 2019 Google LLC \* \* The Wilco Embedded Controller can create custom events that \* are not handled as standard ACPI objects. These events  
 can \* contain information about changes in EC con  
 Copyright 2019 Google LLC \* \* The Wilco Embedded Controller is able to send telemetry data \* which is useful for enterprise applications. A daemon  
 running on \* the OS sends a command to the EC via  
 Copyright 2019 Google LLC \* \* This file is a rework and part of the code is ported from \* drivers/mfd/cros\_ec\_dev.c that was originally written by \* Bill  
 Richardson.  
 Copyright 2019, Gustavo Romero, Michael Neuling, IBM Corp. \* \* This test will spawn two processes. Both will be attached to the same \* CPU (CPU 0).  
 The child will be in a loop writing to FP registe  
 Copyright 2019 Hans de Goede <hdegoede@redhat.com> \*/  
 Copyright 2019, Hari Bathini, IBM Corporation. \*/  
 Copyright 2019 IBM Corp. \*/  
 Copyright 2019 IBM Corp.#include <linux/idr.h>#include "ocxl\_internal.h"  
 Copyright 2019 IBM Corp.#include <linux/module.h>#include "ocxl\_internal.h"  
 Copyright 2019 IBM Corp.#include <linux/sched/mm.h>#include "trace.h"#include "ocxl\_internal.h"  
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 Copyright © 2019 Intel Corporation \* \* mei\_hdcp.c: HDCP client driver for mei bus  
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 Copyright 2019 Linaro, Ltd., Rob Herring <robh@kernel.org>  
 Copyright 2019 Linaro, Ltd., Rob Herring <robh@kernel.org>#ifndef \_\_PANFROST\_FEATURES\_H\_\_#define \_\_PANFROST\_FEATURES\_H\_\_  
 Copyright 2019 Linaro, Ltd, Rob Herring <robh@kernel.org>#include <linux/atomic.h>#include <linux/bitfield.h>#include <linux/delay.h>#include  
 <linux/dma-mapping.h>#include <linux/interrupt.h>#in  
 Copyright 2019 Linaro, Ltd, Rob Herring <robh@kernel.org>/\* \* Register definitions based on mali\_midg\_regmap.h  
 Copyright 2019 Madhavan Srinivasan, IBM Corporation.  
 Copyright 2019, Michael Ellerman, IBM Corp.  
 Copyright 2019, Michael Neuling, IBM Corporation. \*/  
 Copyright 2019, Nick Piggin, Gautham R. Shenoy, Aneesh Kumar K.V, IBM Corp. \*/  
 Copyright 2019 NXP  
 Copyright 2019 NXP. \*/  
 Copyright 2019 NXP \*/  
 Copyright 2019 NXP \*  
 Copyright 2019 NXP \* Author: Daniel Baluta <daniel.baluta@nxp.com> \* \* Implementation of the DSP IPC interface (host side)  
 Copyright 2019 NXP. \* \* FlexSPI is a flexible SPI host controller which supports two SPI \* channels and up to 4 external devices. Each channel supports  
 \* Single/Dual/Quad/Octal mode data transfer  
 Copyright 2019 NXP \* \* Header file for the DSP IPC implementation  
 Copyright 2019 NXP \* \* Implementation of the SCU IRQ functions using MU.  
 Copyright 2019 NXP \*/#include <linux/device.h>#include <linux/debugfs.h>#include <linux/fsl/ptp\_qoriq.h>  
 Copyright 2019 NXP#include <linux/of\_mdio.h>#include "enetc\_mdio.h"  
 Copyright 2019 NXP \* \* Peng Fan <peng.fan@nxp.com>  
 Copyright 2019 Ondrej Zary \* \* Original driver by \* Rickard E. Faith, faith@cs.unc.edu  
 Copyright 2019 Ondrej Zary \* \* The initial developer of the original code is David A. Hinds \* <dahinds@users.sourceforge.net>. Portions created by  
 David A. Hinds  
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Copyright Adrian McMenamin * <adrian@mcmemon.co.uk> * 2006 */
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Copyright Alistair Pople, IBM Corporation 2014. */#include <linux/bitops.h>#include <linux/irq.h>#include <linux/irqchip.h>#include
<linux/irqdomain.h>#include <linux/interrupt.h>#include <lin
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copyright asserted, 1 = copyright not asserted#define CS8427_ORIG (1<<0) /* SCMS generation indicator, 0 = 1st generation or higher, 1
= original */
Copyright asserted flag -- do not modify#define SPCS_NOTAUDIODATA 0x00000002 /* 0 = Digital audio, 1 = not audio */#define
SPCS_PROFESSIONAL 0x00000001 /* 0 = Consumer (IEC-958), 1 = pro (AES3-1992
copyright as the source file * from which it was derived. */
Copyright AudioScience, Inc., 2003*****
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Copyright: * Ben Dooks <ben@simtec.co.uk> * Sascha Hauer <s.hauer@pengutronix.de> */
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copyright bit#define CM_DAC2SPDO 0x00200000 /* enable wave+fm_midi -> SPDIF/OUT */#define CM_INVIDWEN 0x00100000
/* internal vendor ID write enable, model 039? */#define CM_SETRETRY 0x00100000 /
COPYRIGHT) bits else bits |= 0x08; switch (diga->status[0] & IEC958_AES0_CON_EMPHASIS) {
default:
COPYRIGHT)) bits if ((diga->status[0] & IEC958_AES0_CON_EMPHASIS) == IEC958_AES0_CON_EMPHASIS_NONE)
bits |= 0x40; if (!(diga->status[1] & IEC958_AES1_CON_ORIGINAL)) bits |= 0x80; if ((d
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The Unicorn' (MOTU) *   MidiTimePiece
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Copyright by Micha */#ifndef LINUX_CB710_MMC_H#define LINUX_CB710_MMC_H
Copyright by Micha */#ifndef LINUX_CB710_SG_H#define LINUX_CB710_SG_H
Copyright by Micha */#include <linux/cb710.h>#include <linux/kernel.h>#include <linux/module.h>
Copyright by Micha */#include <linux/kernel.h>#include <linux/module.h>#include <linux/cb710.h>
Copyright by Micha */#include <linux/kernel.h>#include <linux/module.h>#include <linux/pci.h>#include <linux/delay.h>#include "cb710-mmc.h"
Copyright by Micha */#include <linux/kernel.h>#include <linux/module.h>#include <linux/pci.h>#include <linux/spinlock.h>#include
<linux/idr.h>#include <linux/cb710.h>#include <linux/gfp.h>
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generic 7990.c LANCE code. */
Copyright (C) 05/1998 Peter Maydell <pmaydell@chiark.greenend.org.uk> * * Most of this stuff was obtained by looking at other LANCE drivers, * in
particular a2065.[ch]. The AMD C-LANCE datasheet wa
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chars caused a compiler warning on HIUX (so I re
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Copyright (C) 1991, 1992, 1993, 1994 Linus Torvalds */#include <linux/mm.h>#include <asm/mmu.h>
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Removed race in async swapping. 14.4.1996. Bruno H  
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kswapdctl limits, and swap out as many pages as needed  
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Copyright (C) 1991, 1992 Linus Torvalds \* 1995 Jay Estabrook \* \* A note on using in\_atomic() in here: We can't handle console \* calls  
from non-schedulable context due to our USB-dependend  
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/dev/zero mmappping support, Feb 2000, Kanoj Sarcar <  
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 Copyright (C) 1991, 1992 Linus Torvalds \* \* ext/freelists.c  
 Copyright (C) 1991, 1992 Linus Torvalds \* \* GK 2/5/95 - Changed to support mounting root fs via NFS \* Added initrd & change\_root: Werner  
 Almesberger & Hans Lermen, Feb '96 \* Moan early if gc  
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 Copyright (C) 1991, 1992 Linus Torvalds \* \* Goal-directed block allocation by Stephen Tweedie \* (sct@redhat.com), 1993, 1998  
 Copyright (C) 1991, 1992 Linus Torvalds \*/#include <linux/fs.h>#include <linux/ext2\_fs.h>#include <linux/blockgroup\_lock.h>#include  
 <linux/percpu\_counter.h>#include <linux/rbtree.h>  
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 Copyright (C) 1991, 1992 Linus Torvalds \* \* Modified to make sys\_syslog() more flexible: added commands to \* return the last 4k of kernel messages,  
 regardless of whether \* they've been read or no  
 Copyright (C) 1991, 1992 Linus Torvalds \* \* More specifically, the first copied function was strtobool, which \* was introduced by: \*  
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 - fil  
 Copyright (C) 1991, 1992 Linus Torvalds \* \* table of configured filesystems  
 Copyright (C) 1991, 1992 Linus Torvalds \* \* This file contains the interface functions for the various time related \* system calls: time, stime,  
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 drivers, which allows all the partition code to \* b  
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 checks and added DM partitions  
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 Copyright (C) 1992, 1993 Krishna Balasubramanian \* Many improvements/fixes by Bruno Haible. \* Replaced 'struct shm\_desc' by 'struct  
 vm\_area\_struct', July 1994. \* Fixed the shm swap deallocation (s  
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 information communicated between the \* PROM, SYMMON, an  
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 Copyright (C) 1992 Drew Eckhardt \* Linux scsi disk driver by \* Drew Eckhardt <drew@colorado.edu> \* \* Modified by Eric Youngdale  
 ericy@andante.org to \* add scatter-gather, multip  
 Copyright (C) 1992 Drew Eckhardt \* SCSI disk driver header file by \* Drew Eckhardt \* \* <drew@colorado.edu>  
 Copyright (C) 1992 Eric Youngdale \* Simulate a host adapter with 2 disks attached. Do a lot of checking \* to make sure that we are not getting blocks  
 mixed up, and PANIC if \* anything out of t  
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 Copyright (C) 1992 Krishna Balasubramanian and Linus Torvalds  
 Copyright (C) 1992 Krishna Balasubramanian \* \* Removed all the remaining kernel mess \* Catch the -EFAULT stuff properly \* Use GFP\_KERNEL for  
 messages as in 1.2 \* Fixed up the unchecked user spac  
 Copyright (C) 1992 Krishna Balasubramanian \* \* Sep 1997 - Call suser() last after "normal" permission checks so we \* get BSD style process  
 accounting right. \* Occurs in sever  
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 Tosatti \*/#ifndef LINUX\_LP\_H#define LINUX\_LP\_H  
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 Tosatti \*/#ifndef UAPI\_LINUX\_LP\_H#define UAPI\_LINUX  
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 Copyright (C) 1992 Rick Sladkey \*  
 Copyright (C) 1992 Rick Sladkey \*/#include <linux/fs.h>#include <linux/file.h>#include <linux/falloc.h>#include <linux/nfs\_fs.h>#include  
 "delegation.h"#include "internal.h"#include "iostat.h"  
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Copyright (C) 1994-1999 Russell King * Moved from linux/arch/arm/kernel/debug.S by Ben Dooks*/#include <linux/amba/serial.h>
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Copyright (C) 1994-2000 Algorithmics Ltd. */#ifndef __IEEE754INT_H#define __IEEE754INT_H
Copyright (C) 1994-2000 Algorithmics Ltd. * * Kevin D. Kissell, kevink@mips.com and Carsten Langgaard, carstenl@mips.com
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<linux/interrupt.h>#include <linux/irqchip.h>#include <linux/irqdomain.h>#include <linux/delay.h>#include <linux/init.h>#include
Copyright (C) 1994 - 2000 Ralf Baechle */#include <linux/kernel.h>#include <linux/delay.h>#include <linux/init.h>#include
<linux/interrupt.h>#include <linux/kernel_stat.h>#include <linux/proc_f
Copyright (C) 1994 - 2001, 2003, 07 Ralf Baechle */#include <linux/clockchips.h>#include <linux/i8253.h>#include <linux/init.h>#include
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checked. * - Put in a better version of rea
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Copyright (C) 1996-2001 Paul Mackerras (paulus@cs.anu.edu.au) * Ben. Herrenschmidt (benh@kernel.crashing.org) * * TODO: *
Copyright (C) 1996-2001 Russell King. * * I_hate_ this partitioning mess - why can't we have one defined * format, and everyone stick to it?
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Copyright (C) 1996-2003 Russell King
Copyright (C) 1996-2003 Russell King */#ifndef __ASM_ARM_PTRACE_H#define __ASM_ARM_PTRACE_H
Copyright (C) 1996-2003 Russell King. */#include <linux/module.h>#include <linux/types.h>#include <linux/tty.h>#include
<linux/serial_core.h>#include <linux/errno.h>#include <linux/ioport.h>#i
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Copyright (c) 1996-2004 Russell King. * * Please note that this platform does not support 32-bit IDE IO.
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Copyright (C) 1996,2005 David S. Miller (davem@davemloft.net)
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Copyright (C) 1996-2005 Paul Mackerras. */#include <asm/processor.h>#include <asm/cache.h>#include <asm/errno.h>#include
<asm/ppc_asm.h>#include <asm/export.h>#include <asm/code-patching-asm.h>
Copyright (C) 1996-2005 Paul Mackerras. */#include <linux/string.h>#include <asm/udbg.h>#include <asm/time.h>#include "nonstd.h"
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 Copyright (C) 1996-98 Ingo Molnar, Gadi Oxman  
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 Copyright (C) 1996, Cygnus Software Technologies Ltd. \*/#include <linux/moduleparam.h>#include <linux/compiler.h>#include

```

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Copyright (C) 1996 David S. Miller (davem@caip.rutgers.edu) * * Calls to memset returns initial %0. Calls to bzero returns 0, if ok, and * number of
bytes not yet set if exception occurs and we we
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<linux/string.h>
Copyright (C) 1996 David S. Miller (davem@davemloft.net) */#include <linux/bug.h>#include <linux/init.h>#include <linux/kernel.h>#include
<linux/types.h>#include <linux/string.h>
Copyright (C) 1996 David S. Miller (davem@davemloft.net) */#include <linux/init.h>
Copyright (C) 1996 David S. Miller (davem@davemloft.net) */#include <linux/init.h>#include <asm/sgialib.h>
Copyright (C) 1996 David S. Miller (davem@davemloft.net) */#include <linux/init.h>#include <linux/kernel.h>
Copyright (C) 1996 David S. Miller (davem@davemloft.net) */#include <linux/init.h>#include <linux/kernel.h>#include <linux/string.h>
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added. HK
Copyright (C) 1996 David S. Miller (dm@sgi.com) * Compatibility with board caches, Ulf Carlsson */#include <linux/kernel.h>#include
<asm/sgialib.h>#include <asm/bcache.h>#include <asm/setup.h>
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<linux/spinlock.h>#include <net/ax25.h>
Copyright (C) 1996 Mike Shaver (shaver@zeroknowledge.com) */#include <linux/mm.h>#include <linux/sysctl.h>#include <linux/init.h>#include
<net/ax25.h>#include <net/netrom.h>
Copyright (C) 1996 Mike Shaver (shaver@zeroknowledge.com) */#include <linux/mm.h>#include <linux/sysctl.h>#include <linux/init.h>#include
<net/ax25.h>#include <net/rose.h>
copyright (C) 1996 okir@monad.swb.de.\n");
Copyright (C) 1996 Olaf Kirch * 04 Aug 1998 Ion Badulescu <ionut@cs.columbia.edu> * FIFO's need special handling in NFSv2 */
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Copyright (C) 1996 Paul Gortmaker
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Copyright (C) 1996 Paul Mackerras. * * Apr. 21 2002 - BenH Rework bus reset code for new error handler * Add delay after
initial bus reset *
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Copyright (C) 1996 Paul Mackerras. */#ifndef _MESH_H_#define _MESH_H
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Copyright (C) 1996 Paul Mackerras. */#include <asm/processor.h>#include <asm/errno.h>#include <asm/ppc_asm.h>#include
<asm/export.h>#include <asm/kasan.h>
Copyright (C) 1996 Paul Mackerras. */#include <linux/kernel.h>#include <linux/delay.h>#include <linux/types.h>#include <linux/string.h>#include
<linux/slab.h>#include <linux/blkdev.h>#include
Copyright (C) 1996 Paul Mackerras. */#include <stdarg.h>#include <linux/types.h>#include <linux/errno.h>#include <linux/kernel.h>#include
<linux/delay.h>#include <linux/adb.h>#include <linux/c
Copyright (C) 1996 Paul Mackerras. * * Modified to declare controllers as structures, added * client notification of bus reset and handles PowerBook *
sleep, by Benjamin Herrenschmidt.
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Copyright (c) 1996 Paul Mackerras <paulus@cs.anu.edu.au>
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<linux/module.h>#include <linux/of.h>#include
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processors don't use a \* hash table, so this file is  
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<jiang.liu@linux.intel.com> \* Enable support of hierarchical irqdomai  
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<linux/interrupt.h>#include <linux/kernel.h>#  
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MUST be kept in sync.

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Copyright (C) 1997,1998 Jakub Jelinek (jj@sunsite.mff.cuni.cz) * Heavily based on arch/sparc/kernel/irq.c. */
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Copyright (C) 1997, 1998 Ralf Baechle (ralf@gnu.org) */#include <linux/init.h>#include <linux/kernel.h>#include <linux/kdev_t.h>#include
<linux/types.h>#include <linux/console.h>#include <linux
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Copyright (C) 1997-1999 Russell King *
Copyright (C) 1997-1999 Russell King * * E and F format directory handling
Copyright (C) 1997-1999 Russell King * from: *
Copyright (C) 1997-1999 Russell King */#include <linux/buffer_head.h>#include <linux/writeback.h>#include "adfs.h"
Copyright (C) 1997-1999 Russell King */#include <linux/module.h>#include <linux/init.h>#include <linux/parser.h>#include <linux/mount.h>#include
<linux/seq_file.h>#include <linux/slab.h>#inclu
Copyright (C) 1997-1999 Russell King */#include <linux/slab.h>#include "adfs.h"#include "dir_fplus.h"
Copyright (C) 1997-1999 Russell King * * Modifications:
Copyright (C) 1997-1999 Russell King * * Structure passed to kernel to tell it about the * hardware it's running on. See Documentation/arm/setup.rst *
for more info.
Copyright (C) 1997 - 2000, 2003, 04 Ralf Baechle (ralf@linux-mips.org) */#include <linux/kernel.h>#include <linux/init.h>#include <linux/pci.h>
Copyright (C) 1997 - 2000, 2003 Ralf Baechle <ralf@linux-mips.org> */#include <linux/kernel.h>#include <linux/pci.h>#include
<linux/types.h>#include <asm/sni.h>
```

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 <linux/init.h>#include <linux/kernel.h>#include <linux/sched.h>#  
 Copyright (C) 1997-2002 by Ron Minnich <rminnich@sarnoff.com> \*/  
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 Copyright (C) 1997-2002 Inside Out Networks, Inc. \* \* Feb-16-2001 DMI Added I2C structure definitions \* May-29-2002 gkh Ported to Linux  
 Copyright (C) 1997 - 2002 Ralf Baechle (ralf@gnu.org)  
 Copyright (C) 1997-2002 Russell King \* \* ARM architecture version 4 and version 5 TLB handling functions. \* These assume a split I/D TLBs, with a write buffer.  
 Copyright (C) 1997-2002 Russell King \* \* ARM architecture version 4, Faraday variation. \* This assume an unified TLBs, with a write buffer, and branch target buffer (BTB)  
 Copyright (C) 1997-2002 Russell King \* \* ARM architecture version 4 TLB handling functions. \* These assume a split I/D TLBs, and no write buffer.  
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 Copyright (C) 1997-2002 Russell King \*/#include <asm/unaligned.h>#include "adfs.h"  
 Copyright (C) 1997-2002 Russell King \*/#include <linux/linkage.h>#include <linux/init.h>#include <asm/assembler.h>#include  
 <asm/memory.h>#include <asm/page.h>#include "proc-macros.S"  
 Copyright (C) 1997-2002 Russell King \*/#include <linux/linkage.h>#include <linux/init.h>#include <asm/assembler.h>#include <asm/page.h>#include "proc-macros.S"  
 Copyright (C) 1997-2002 Russell King \* Modified for ARMv7 by Catalin Marinas \* \* ARM architecture version 6 TLB handling functions. \* These assume a split I/D TLB.  
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 Copyright (C) 1997-2005 Russell King \*/#include <linux/module.h>#include <linux/blkdev.h>#include <linux/kernel.h>#include  
 <linux/string.h>#include <linux/ioport.h>#include <linux/proc\_fs.h>#i  
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```

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<asm/branch.h>#include <linux/uaccess.h>
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Copyright (c) 1997 Dan error_act (dmalek@jlc.net)
Copyright (c) 1997 Dan error_act (dmalek@jlc.net) * * In addition to the individual control of the communication * channels, there are a few functions
that globally affect the * communication proc
Copyright (c) 1997 Dan Malek (dmalek@jlc.net)
Copyright (C) 1997 Dan Malek (dmalek@jlc.net). */
Copyright (c) 1997 Dan Malek (dmalek@jlc.net). */#define BD_SC_EMPTY ((ushort)0x8000) /* Receive is empty */#define BD_SC_READY
((ushort)0x8000) /* Transmit is ready */#define BD_SC_WRAP ((ushort)
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Copyright (C) 1997 Dan Malek <dmalek@jlc.net> * PowerPC 8xx modifications.
Copyright (c) 1997 Dan Malek (dmalek@jlc.net) * * Right now, I am very wasteful with the buffers. I allocate memory * pages and then divide them
into 2K frame buffers. This way I know I * have b
Copyright (c) 1997 Dan Malek (dmalek@jlc.net) * * The I/O on the MPC860 is comprised of blocks of special registers * and the dual port ram for the
Communication Processor Module. * Within this sp
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control and status is available * through the
Copyright (C) 1997 Dan Malek (dmalek@jlc.net). * * This file contains the low-level support and setup for the * PowerPC platform, including trap and
interrupt dispatch. * (The PPC 8xx embedded
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Copyright (C) 1997 David S. Miller (davem@caip.rutgers.edu) * * Derived from "arch/i386/kernel/signal.c"
Copyright (C) 1997 David S. Miller (davem@caip.rutgers.edu) * * These routines maintain argument size conversion between 32bit and 64bit *
environment.
Copyright (C) 1997 David S. Miller (davem@caip.rutgers.edu) * * To put things into perspective, consider sparc64 with a few PCI controllers. * Each type
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 Copyright (C) 1997 Miguel de Icaza (miguel@nuclecu.unam.mx) \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/errno.h>#include  
 <linux/kd.h>#include <linux/selection.h>#include <  
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 Copyright (C) 1998-1999 Andrzej Krzysztofowicz, Author and Maintainer  
 Copyright (C) 1998-1999 ARM Limited. \*/#ifndef INTEGRATOR\_HARDWARE\_H#define INTEGRATOR\_HARDWARE\_H  
 Copyright (C) 1998, 1999 Brent Baccala (baccala@freesoft.org) \* \* This is the low level driver for the DBRI & MMCODEC duo used for ISDN & AUDIO \*  
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 Copyright (c) 1998-1999 by Frank van de Pol <fvdpol@coil.demon.nl> \*/#ifndef \_\_SND\_SEQ\_CLIENTMGR\_H#define \_\_SND\_SEQ\_CLIENTMGR\_H  
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```

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Copyright (c) 1998-1999 by Frank van de Pol <fvdpol@coil.demon.nl> ** MAJOR CHANGES * Nov. 13, 1999 Takashi Iwai <iwai@ww.uni-
erlangen.de> * - Queues are allocated dynamically via ioctl. *
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Copyright (C) 1998 Free Software Foundation, Inc. * * The Tiger algorithm was developed by Ross Anderson and Eli Biham. * It was optimized for 64-bit
processors while still delievering * decent pe
Copyright (C) 1998 Gabriel Paubert. */
Copyright (C) 1998 Geert Uytterhoeven
Copyright (C) 1998 Gleb Raiko
Copyright (C) 1998 Gleb Raiko */#include <linux/init.h>#include <linux/kernel.h>#include <linux/sched.h>#include <linux/smp.h>#include
<linux/mm.h>

```

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 <asm/fpregdef.h>#include <asm/mipsregs.h>#include <asm/a  
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 Copyright (C) 1998 Kenneth Albanowski <kjahds@kjahds.com>,  
 Copyright (C) 1998 Kenneth Albanowski <kjahds@kjahds.com> \* JAN/99 -- coded full program relocation (gerg@snapgear.com) \*/  
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 <evan@lineo.com> \* vz multiport fixes by Michae  
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 Copyright (C) 1998 Phil Blundell \* \* Changelog:  
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 Copyright (C) 1998 Phil Blundell \*/#include <linux/clockchips.h>#include <linux/clocksource.h>#include <linux/init.h>#include  
 <linux/interrupt.h>#include <linux/irq.h>#include <linux/sched\_cloc  
 Copyright (C) 1998 Phil Blundell \*/#include <linux/clockchips.h>#include <linux/i8253.h>#include <linux/init.h>#include <linux/interrupt.h>#include  
 <linux/irq.h>#include <linux/spinlock.h>#inc  
 Copyright (C) 1998 Phil Blundell \*/#include <linux/ioport.h>#include <linux/init.h>#include <linux/dma-mapping.h>#include <linux/io.h>  
 Copyright (C) 1998 Philip Blundell <philb@gnu.org>  
 Copyright (C) 1998 Philip Blundell <philb@gnu.org> \* \* Do the dirty work of rebooting the machine. Basically we need to undo all the \* good stuff  
 that head.S did when we started up. The caches

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 Copyright (C) 1999, 2000, 2001 Silicon Graphics, Inc. \*/#ifndef \_ASM\_PGTABLE\_64\_H#define \_ASM\_PGTABLE\_64\_H  
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 <linux/fs.h>#include <linux/mm.h>#include <linux/mman.h>  
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 \* \* 11/24/98 S.Eranian updated TIMER\_IRQ and irq\_canonicalize  
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Copyright (C) 1999, 2000 by Ralf Baechle */#ifndef _ASM_SN_KLCONFIG_H#define _ASM_SN_KLCONFIG_H
Copyright (C) 1999, 2000 by Ralf Baechle */#ifndef _ASM_SN_KLDIR_H#define _ASM_SN_KLDIR_H
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Copyright (C) 1999, 2000 by Silicon Graphics, Inc. * * PROM library functions for acquiring/using memory descriptors given to us * from the ARCS
firmware. This is only used when CONFIG_ARC_MEMORY
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Copyright (C) 1999, 2000 Kaz Kojima * * FIXME! These routines can be optimized in big endian case.
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Copyright (C) 1999, 2000 Kaz Kojima * * Original x86 implementation:
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Copyright (C) 1999, 2000 Niibe Yutaka */#ifndef __ASM_SH_PTRACE_H#define __ASM_SH_PTRACE_H
Copyright (C) 1999, 2000 Niibe Yutaka */#ifndef _UAPI__ASM_SH_PTRACE_H#define _UAPI__ASM_SH_PTRACE_H
Copyright (C) 1999, 2000 Niibe Yutaka * * In turn started from i386 version:
Copyright (C) 1999, 2000 Niibe Yutaka * * Simple interrupt handling using IMASK of SR register.
Copyright (C) 1999,2000 Philipp Rumpf
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Copyright (C) 1999-2000 Russell King * * ISA DMA primitives * Taken from various sources, including:
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Copyright (C) 1999, 2000 Silicon Graphics, Inc. * * Changed system calls macros _syscall5 - _syscall7 to push args 5 to 7 onto
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Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#ifndef _ASM_POSIX_TYPES_H#define _ASM_POSIX_TYPES_H
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Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include <linux/compiler.h>#include <linux/kernel.h>#include <linux/sched.h>#include
<linux/timer.h>#include <linux/smp.h>#include <linux/mmzone
Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include <linux/cpu_pm.h>#include <linux/hardirq.h>#include <linux/init.h>#include
<linux/highmem.h>#include <linux/kernel.h>#include <linux/li
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<linux/smp.h>#include <linux/dma-direct.h>#include <linux/plat
Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include <linux/kernel.h>#include <linux/sched.h>#include <linux/interrupt.h>#include
<linux/kernel_stat.h>#include <linux/param.h>#include <li
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device number.
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Copyright (C) 1999,2000 Walt Drummond <drummond@valinux.com> * * 00/04/19 D. Mosberger Rewritten to mirror more closely the x86 I/O *
APIC code. In particular, we now have separate * handle
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driver
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davidm Tuned to make it perform well both for cached and uncac
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 <asm/export.h>#include <asm/fpregdef.h>#include <asm/mipsregs.h>#i  
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 \*/#include <linux/init.h>#include <linux/string.h>#include <l  
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 2002/12/09 Fix rotating register handling (off-by-1 error,  
 Copyright (C) 1999-2002 Andre Hedrick <andre@linux-ide.org>  
 Copyright (C) 1999-2002 Andre Hedrick <andre@linux-ide.org> \* 865/865R fixes for Macintosh card version from a patch to the old \* driver by  
 Thibaut VARENE <varenet@paris-linux.org> \* When setting  
 Copyright (C) 1999-2002 Andre Hedrick <andre@linux-ide.org> \* \* TODO \* Testing work  
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 Copyright (C) 1999 Nicolas Pitre (full GPIO irq isolation) \* \* 2001/11/14 RMK Cleaned up and standardised a lot of the IRQs.  
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Copyright (C) 1999 Niibe Yutaka */#ifndef __ASM_CPU_SH3_MMU_CONTEXT_H#define __ASM_CPU_SH3_MMU_CONTEXT_H
Copyright (C) 1999 Niibe Yutaka */#ifndef __ASM_CPU_SH4_CACHE_H#define __ASM_CPU_SH4_CACHE_H
Copyright (C) 1999 Niibe Yutaka */#ifndef __ASM_CPU_SH4_MMU_CONTEXT_H#define __ASM_CPU_SH4_MMU_CONTEXT_H
Copyright (C) 1999 Niibe Yutaka */#include <linux/irq.h>#include <linux/interrupt.h>#include <linux/module.h>#include
<linux/kernel_stat.h>#include <linux/seq_file.h>#include <linux/trace.h>
Copyright (C) 1999 Oystein Svendsen <svendsen@pvv.org> */
Copyright (C) 1999 Paul
Copyright (C) 1999 Paul */
Copyright (C) 1999 Paul */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/kmod.h>#include <linux/module.h>#include
<linux/vmalloc.h>#include <linux/netfilter/x_tables.h>#include <lin
Copyright (C) 1999 Paul */#ifndef __LINUX_BRIDGE_EFF_H#define __LINUX_BRIDGE_EFF_H
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Copyright (C) 1999 Pavel Semerad (semerad@ss1000.ms.mff.cuni.cz) */
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Copyright (C) 1999 Ralf Baechle (ralf@gnu.org) */#ifndef __ASM_SN_SN0_ARCH_H#define __ASM_SN_SN0_ARCH_H
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Copyright (C) 1999 Roman Weissgaerber * * Author : Yoshihiro Shimoda <yoshihiro.shimoda.uh@renesas.com>
Copyright (C) 1999 Roman Weissgaerber * * The SL811HS controller handles host side USB (like the SL11H, but with * another register set and SOF
generation) as well as peripheral side USB * (like t
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Copyright (C) 1999 Russell King */#ifndef __ASM_PROC_DOMAIN_H#define __ASM_PROC_DOMAIN_H
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Copyright (C) 1999 Silicon Graphics, Inc. * * based on MIPS asm/mmu_context.h
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Copyright (C) 1999 Silicon Graphics, Inc. * * Essentially rewritten for the Xtensa architecture port.
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Copyright (C) 1999 Silicon Graphics, Inc. */#ifndef __ASM_BREAK_H#define __ASM_BREAK_H
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Copyright (C) 1999 Silicon Graphics, Inc. */#ifndef __ASM_SIGNAL_H#define __ASM_SIGNAL_H
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Copyright (C) 1999 Silicon Graphics, Inc. */#ifndef __UAPI_ASM_SIGCONTEXT_H#define __UAPI_ASM_SIGCONTEXT_H

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Copyright (C) 1999 Silicon Graphics, Inc. */#include <asm/mipsregs.h>#include <asm/regdef.h>#include <asm/stackframe.h>
Copyright (C) 1999 Silicon Graphics, Inc. */#include <linux/compiler.h>#include <linux/init.h>#include <linux/kernel.h>#include <linux/irqflags.h>
Copyright (C) 1999 Silicon Graphics, Inc. */#include <linux/init.h>#include <asm/fw/arc/types.h>#include <asm/sgialib.h>
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Copyright (C) 1999 Srinivasa Prasad Thirumalachar <sprasad@sprasad.engr.sgi.com> * * 02/01/04 J. Hall Updated Error Record Structures to conform
to July 2001 * revision of the SAL spec. * 01/
Copyright (C) 1999 Stephane Eranian <eranian@hpl.hp.com> * * Based on skeleton from the drivers/char/rtc.c driver by P. Gortmaker
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Copyright (C) 1999 Steve Ratcliffe * * Based on awe_wave.c by Takashi Iwai
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Copyright (C) 1999 SuSE GmbH, (Philipp Rumpf, prumpf@tux.org) * * 1994-07-02 Alan Modra * fixed set_rtc_mmss, fixed time.year for >=
2000, new mktime * 1998-12-20 Updated NTP code ac
Copyright (C) 1999 Takashi Iwai <tiwai@suse.de> * * Changes * 19990227 Steve Ratcliffe Made separate file and merged in latest *
midi emulation.
Copyright (C) 1999 Tetsuya Okada
Copyright (C) 1999 Tetsuya Okada * * Based on code written by Paul Gortmaker.
Copyright (C) 1999 Tetsuya Okada */#include <linux/module.h>#include <linux/mod_devicetable.h>#include <linux/kernel.h>#include
<linux/bcd.h>#include <linux/rtc.h>#include <linux/init.h>#incl
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m68knommu/MC68332.h
Copyright (C) 1999 VMware, Inc. (Regis "HPReg" Duchesne) * Made nbd_end_request() use the io_request_lock
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Copyright (C) 1999 Walt Drummond <drummond@valinux.com> * * 12/26/04 S.Siddha, G.Jin, R.Seth * Add multi-threading and multi-core
detection * 11/12/01 D.Mosberger Convert get_cpuinfo() to seq_fi
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Copyright (C) 2000, 2001, 04 Keith M Wesolowski */#include <linux/init.h>#include <linux/kernel.h>#include <linux/pci.h>#include <linux/types.h>
Copyright (C) 2000, 2001, 06 Ralf Baechle <ralf@linux-mips.org> * swiped from i386, and cloned for MIPS by Geert, polished by Ralf. */#include
<linux/dma-direct.h>#include <linux/dma-noncoherent>
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<linux/reboot.h>#include <linux/string.h>
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<linux/interrupt.h>#include <linux/smp.h>#include
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Copyright (C) 2000, 2001, 2002, 2003, 2005 Maciej W. Rozycki */#include <linux/console.h>#include <linux/export.h>#include <linux/init.h>#include
<linux/interrupt.h>#include <linux/ioport.h>#i
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Paul Antoine and Harald Koerfgen.
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Copyright (C) 2000, 2001, 2002, 2003 Broadcom Corporation */#include <linux/export.h>#include <linux/init.h>#include <linux/kernel.h>#include
<linux/reboot.h>#include <linux/string.h>
Copyright (C) 2000, 2001, 2002, 2003 Broadcom Corporation */#include <linux/kernel.h>#include <linux/init.h>#include <linux/linkage.h>#include
<linux/interrupt.h>#include <linux/spinlock.h>#inc
Copyright (C) 2000, 2001, 2002, 2003 David S. Miller (davem@redhat.com) * * Support for Apple GMAC and assorted PHYs, WOL, Power Management
Copyright (C) 2000, 2001, 2002, 2007 Maciej W. Rozycki */#include <linux/init.h>#include <linux/ioport.h>#include <linux/export.h>#include
<linux/screen_info.h>#include <linux/memblock.h>#incl
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Copyright (C) 2000, 2001, 2002 Andi Kleen SuSE Labs * * 1997-11-28 Modified for POSIX.1b signals by Richard Henderson * 2000-06-20 Pentium III
FXSR, SSE support by Gareth Hughes * 2000-2002
Copyright (C) 2000, 2001, 2002 Andi Kleen, SuSE Labs */#include <linux/kallsyms.h>#include <linux/kprobes.h>#include <linux/uaccess.h>#include
<linux/utsname.h>#include <linux/hardirq.h>#include
Copyright (C) 2000, 2001, 2002 Andi Kleen, SuSE Labs */#include <linux/sched/debug.h>#include <linux/kallsyms.h>#include
<linux/kprobes.h>#include <linux/uaccess.h>#include <linux/hardirq.h>#in
Copyright (C) 2000, 2001, 2002 Andi Kleen, SuSE Labs * * Pentium III FXSR, SSE support * Gareth Hughes <gareth@valinux.com>, May 2000
Copyright (C) 2000,2001,2002 Andi Kleen, SuSE Labs (x86-64 port) * * These routines maintain argument size conversion between 32bit and 64bit *
environment. In 2.5 most of this should be moved to a
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Copyright (C) 2000, 2001, 2002 David A. Schleef <ds@schleef.org> * * PCMCIA support code for this driver is adapted from the dummy_cs.c * driver of
the Linux PCMCIA Card Services package.
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Copyright (C) 2000, 2001, 2003 Broadcom Corporation */#include <linux/cache.h>#include <linux/delay.h>#include <linux/init.h>#include
<linux/interrupt.h>#include <linux/smp.h>#include <linux/sp
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Copyright (C) 2000,2001,2004 Broadcom Corporation */#include <linux/clockchips.h>#include <linux/interrupt.h>#include <linux/percpu.h>#include
<linux/smp.h>#include <linux/irq.h>
Copyright (C) 2000,2001,2004 Broadcom Corporation */#include <linux/clocksource.h>#include <linux/sched_clock.h>
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Copyright (C) 2000, 2001 Broadcom Corporation */#ifndef __ASM_SMP_OPS_H#define __ASM_SMP_OPS_H
Copyright (C) 2000, 2001 Broadcom Corporation */#include <linux/clockchips.h>#include <linux/interrupt.h>#include <linux/irq.h>#include
<linux/percpu.h>#include <linux/smp.h>
Copyright (C) 2000, 2001 Broadcom Corporation */#include <linux/clocksource.h>#include <linux/sched_clock.h>
Copyright (C) 2000, 2001 Broadcom Corporation */#include <linux/init.h>
Copyright (C) 2000-2001 by Harald Welte <laforge@gnumonks.org>
Copyright (C) 2000 - 2001 by Kanoj Sarcar (kanoj@sgi.com)
Copyright (C) 2000, 2001 by Nicolas Pitre */#include <linux/sched.h>#include <linux/device.h>#include <linux/dmaengine.h>#include
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Copyright (C) 2000 - 2001 by Silicon Graphics, Inc. */#include <linux/init.h>#include <linux/sched.h>#include <linux/sched/task_stack.h>#include
<linux/topology.h>#include <linux/nodemask.h>
Copyright (C) 2000 - 2001 by Silicon Graphics, Inc. */#include <linux/kernel.h>#include <linux/init.h>#include <linux/sched.h>#include
<linux/smp.h>#include <linux/mm.h>#include <linux/export.h
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Copyright (C) 2000-2001 Deep Blue Solutions
Copyright (C) 2000-2001 Deep Blue Solutions Ltd * * Contributors to the old driver include:
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<linux/interrupt.h>#include <linux/amba/bus.h>#include <linux/i
Copyright (C) 2000-2001 Deep Blue Solutions Ltd. */#include <linux/module.h>#include <linux/tty.h>#include <linux/tty_flip.h>#include
<linux/slab.h>#include <linux/sched/signal.h>#include <linu
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*/#include <asm/asmmacro.h>#include <asm/export.h>
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Copyright (C) 2000, 2001 Paolo Alberelli * * Fast PTE->TLB refill path
Copyright (C) 2000, 2001 Paolo Alberelli */#include <linux/compiler.h>#include <linux/types.h>#include <asm-generic/swab.h>
Copyright (C) 2000, 2001 Paolo Alberelli */#include <linux/rwsem.h>#include <linux/module.h>#include <linux/smp.h>#include
<linux/user.h>#include <linux/elfcore.h>#include <linux/sched.h>#inc
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 Copyright (C) 2000, 2001 Silicon Graphics, Inc. \*/#ifndef \_UAPI\_ASM\_SIGINFO\_H#define \_UAPI\_ASM\_SIGINFO\_H  
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 Copyright (C) 2000-2001 Toshiba Corporation \*/#ifndef \_\_ASM\_TXX9\_JMR3927\_H#define \_\_ASM\_TXX9\_JMR3927\_H  
 Copyright (C) 2000-2001 Toshiba Corporation \*/#include <linux/init.h>#include <asm/tx9irq.h>#include <asm/tx9/tx3927.h>  
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 addresses refer to tc.h, ioasic\_addrs.h and friends.  
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 <linux/memblock.h>#include <linux/types.h>  
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 <linux/amba/bus.h>#include <linux/io.h>#include <linux/i  
 Copyright (C) 2000-2003 Deep Blue Solutions Ltd \*/#include <linux/types.h>#include <linux/kernel.h>#include <linux/init.h>#include  
 <linux/device.h>#include <linux/export.h>#include <linux/spinl  
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 <linux/netfilter\_ipv4/ip\_tables.h>#include <linux/netdevice.h>#include <linux/skbuff.h>#i  
 Copyright (C) 2000-2004 Netfilter Core Team <coreteam@netfilter.org> \*/#include <linux/module.h>#include  
 <linux/netfilter\_ipv6/ip6\_tables.h>#include <linux/slab.h>#include <net/ipv6.h>  
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Copyright (C) 2000-2004 Russell King \* \* DMA uncached mapping support.  
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 <linux/interrupt.h>#include <linux/dma-noncoher  
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 Copyright (C) 2000 Deep Blue Solutions Ltd \*/#include <linux/clk.h>#include <linux/clocksource.h>#include <linux/clockchips.h>#include <linux/err.h>#include <linux/interrupt.h>#include <linux/i  
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 Copyright (C) 2000 MIPS Technologies, Inc. \*/#include <asm/irqflags.h>#include <asm/hazards.h>#include <linux/compiler.h>#include  
 <linux/preempt.h>#include <linux/export.h>#include <linux/string.h>  
 Copyright (C) 2000 MIPS Technologies, Inc. \*/#include <linux/compat.h>#include <linux/types.h>#include <linux/sched.h>#include  
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note */#ifndef _SPARC64_ENVCTRL_H#define _SPARC64
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 <linux/module.h>#include <linux/platform\_device.h>#include <linux  
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 <linux/kernel\_stat.h>#include <linux/interrupt.h>#include <linux/ftrace.h  
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<linux/ioport.h>#include <linux/interrupt.h>#include <linux/irq.h
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Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/linkage.h>#include <asm/asm.h>#include <asm/param.h> .text
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Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h>#include <linux/moduleloader.h>#include <linux/kernel.h>#include
<linux/mm.h>#include <linux/elf.h>#include <linux/vmalloc.h>#in
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h>#include <linux/sched.h>#include <linux/personality.h>#include
<linux/binfmts.h>#include <linux/elf.h>
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h>#include <linux/sched.h>#include <linux/sched/debug.h>#include
<linux/stacktrace.h>
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h>#include <linux/sched.h>#include <linux/string.h>#include
<linux/cryptohash.h>#include <linux/delay.h>#include <linux/in6.h>#inc
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h>#include <linux/types.h>#include <linux/kernel.h>#include
<linux/signal.h>#include <linux/sched/signal.h>#include <linux/init.h>
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Copyright (C) 2001-2010 Guan Xuetao *//* in FPGA, no GPIO support */
Copyright (C) 2001-2010 GUAN Xue-tao * * IP checksum routines
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Copyright (C) 2001-2010 GUAN Xue-tao */OUTPUT_ARCH(unicore32)ENTRY(_start)SECTIONS
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option to override CONFIG_WATCHDOG_NOWAYOUT
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Copyright (C) 2001 - 2013 Tensilica Inc. */#ifndef __XTENSA_STACKTRACE_H__#define __XTENSA_STACKTRACE_H

```

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```

```

<linux/blkdev.h>#include <linux/uid.h>#include <linux/iocontext.h>#
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Copyright (C) 2001 Keith M Wesolowski */#include <linux/init.h>#include <linux/kernel_stat.h>#include <linux/types.h>#include
<linux/interrupt.h>#include <linux/irq.h>#include <linux/bitops.h>
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*
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Upper Layer Protocol, or ULP, * and the core SCT
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<linux/fs.h>#include <linux/smp.h>#include <linux/ptrace.h>
Copyright (C) 2001 MIPS Technologies, Inc. */#include <linux/errno.h>#include <asm/asm.h>#include <asm/asmmacro.h>#include
<asm/irqflags.h>#include <asm/mipsregs.h>#include <asm/regdef.h>#incl
Copyright (C) 2001 MIPS Technologies, Inc. */#include <linux/kernel.h>#include <linux/export.h>#include <linux/pm.h>#include
<linux/types.h>#include <linux/reboot.h>#include <linux/delay.h>
Copyright (C) 2001 MIPS Technologies, Inc. */#include <linux/kernel.h>#include <linux/sched/signal.h>#include <linux/signal.h>#include
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has two banks of 32-bit Flash ROM, each 8 Megabytes i
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 \_\_ASM\_MIPS\_MACH\_IP28\_WAR\_H  
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 \_\_ASM\_MIPS\_MACH\_RM\_WAR\_H  
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 Copyright (c) 2002 - 2004 Karsten Wiese\*/  
 Copyright (C) 2002, 2004 Maciej W. Rozycki \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/linkage.h>#include  
 <linux/smp.h>#include <linux/string.h>#include <linux/types.h>  
 Copyright (C) 2002, 2004 Maciej W. Rozycki \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/string.h>#include <linux/types.h>  
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Copyright (c) 2002, 2006, 2008 David S. Miller (davem@davemloft.net) \*/#include <linux/kernel.h>#include <linux/module.h>#include  
<linux/init.h>#include <linux/input.h>#include <linux/of\_device.  
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needed. \* Find out how to get Chassis  
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<asm/sections.h>#include <asm/addrspace.h>#include <asm/setup.h>#include
Copyright (C) 2002 - 2007 Paul Mundt */#include <linux/kernel.h>#include <linux/mm.h>#include <linux/io.h>#include
<asm/mmu_context.h>#include <asm/cacheflush.h>
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<linux/sched/task.h>#include <linux/sched/task_stack.h>#include <lin
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<asm/pgtable.h>#include <asm/tlbflush.h>#include <
Copyright (C) 2002 - 2009 Paul Mundt */#include <linux/mm.h>#include <linux/init.h>#include <linux/mutex.h>#include <linux/fs.h>#include
<linux/highmem.h>#include <linux/module.h>#include <as
Copyright (C) 2002 - 2009 Paul Mundt */#include <linux/module.h>#include <linux/init.h>#include <linux/mm.h>#include <linux/pm.h>#include
<linux/tick.h>#include <linux/preempt.h>#include <lin
Copyright (C) 2002 - 2009 Paul Mundt */#include <linux/pci.h>#include <linux/io.h>#include <linux/spinlock.h>#include <asm/addrspace.h>#include
"pci-sh4.h"
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<linux/of_address.h>
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Copyright(c) 2002-2010 Exar Corp. *****/#ifndef
VXGE_CONFIG_H#define VXGE_CONFIG_H#include <linux/hardirq.h>#include <lin
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_VXGE_ETHTOOL_H#define _VXGE_ETHTOOL_H
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VXGE_MAIN_H#define VXGE_MAIN_H
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VXGE_REG_H#define VXGE_REG_H
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VXGE_TRAFFIC_H#define VXGE_TRAFFIC_H
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VXGE_VERSION_H#define VXGE_VERSION_H
Copyright(c) 2002-2010 Exar Corp. *****/#include
<linux/etherdevice.h>#include <linux/io-64-nonatomic-lo-hi.h>#include <lin
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<linux/ethtool.h>#include <linux/slab.h>#include <linux/pci.h>#include <l
Copyright(c) 2002-2010 Exar Corp. *****/#include
<linux/vmalloc.h>#include <linux/etherdevice.h>#include <linux/io-64-nonat
Copyright(c) 2002-2010 Exar Corp.\n");   DBG_PRINT(ERR_DBG, "%s: Neterion %s (rev %d)\n", dev->name,   sp->product_name, pdev-

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>revision); DBG_PRINT(ERR_DBG, "%s: Driver version %s\n", dev->name,
    Copyright(C) 2002-2010 Exar Corp.\n");    pr_info("Driver version: %s\n", DRV_VERSION);
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<linux/spinlock.h>#include <linux/kallsyms.h>
    Copyright (C) 2002 - 2010 Paul Mundt */#include <linux/mm.h>#include <linux/init.h>#include <linux/mutex.h>#include <linux/fs.h>#include
<linux/smp.h>#include <linux/highmem.h>#include <linux
    Copyright (C) 2002 - 2010 Paul Mundt */#include <linux/screen_info.h>#include <linux/ioport.h>#include <linux/init.h>#include
<linux/initrd.h>#include <linux/console.h>#include <linux/root_dev.
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<linux/sched/debug.h>#include <linux/sched/task_stack.h>#inclu
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    Copyright (C) 2002 Anton Blanchard, IBM Corp. */#ifndef _ASM_MMZONE_H_#define _ASM_MMZONE_H_#ifdef __KERNEL__
    Copyright (c) 2002 ARM Limited. */
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    Copyright (c) 2002 ARM Limited. */#include <linux/init.h>#include <linux/device.h>#include <linux/smp.h>#include <linux/io.h>#include
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Copyright (C) 2002 ARM Ltd. */#include <linux/smp.h>#include <linux/io.h>#include <linux/of_address.h>#include <linux/delay.h>
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Copyright (C) 2002 Atom Create Engineering Co., Ltd.
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Copyright (C) 2002 Benjamin Herrenschmidt (benh@kernel.crashing.org) * * Todo: - add support for the OF persistent properties
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Copyright (C) 2002 by Theodore Ts */#include <linux/types.h>#include <linux/fs.h>#include <linux/f2fs_fs.h>#include <linux/cryptohash.h>#include
<linux/pagemap.h>#include <linux/unicode.h>
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Copyright (C) 2002, David McCullough <davidm@snapgear.com> * modified for 2.6 by Hyok S. Choi <hyok.choi@samsung.com> */typedef struct {
unsigned long end_brk;#ifdef CONFIG_BINFMT_ELF_FDPICuns
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 Copyright (C) 2002 Dominik Brodowski <devel@brodo.de> \* \* TBD: \* 1. Support more than one IRQ resource entry per link device (index). \*

2. Implement start/stop mechanism and use ACPI B  
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 Copyright (C) 2002 Hewlett-Packard Company \*/#include <linux/module.h>#include <linux/init.h>#include <linux/kernel.h>#include  
 <linux/platform\_data/sa11x0-serial.h>#include <linux/platform\_devic  
 Copyright (C) 2002 Hewlett-Packard Company \*/#include <linux/module.h>#include <linux/kernel.h>#include <linux/device.h>#include  
 <linux/errno.h>#include <linux/init.h>  
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<asm/bytorder.h>#include <linux/crypto.h>#include <li  
Copyright (c) 2002 James Morris <jmorris@intercode.com.au> \*/#include <linux/types.h>#include <linux/crypto.h>#include <linux/errno.h>#include  
<linux/string.h>#include "internal.h"  
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McKinley \* 3/08/02 davidm            Some more tweaking  
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Copyright (C) 2002 Maciej W. Rozycki \*/#ifndef \_ASM\_MC146818RTC\_H#define \_ASM\_MC146818RTC\_H  
Copyright (C) 2002 Maciej W. Rozycki \*/#ifndef \_ASM\_PGTABLE\_BITS\_H#define \_ASM\_PGTABLE\_BITS\_H  
Copyright (C) 2002 Maciej W. Rozycki \*/#ifndef \_ASM\_WBFLUSH\_H#define \_ASM\_WBFLUSH\_H  
Copyright (C) 2002 Maciej W. Rozycki \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/sched.h>#include  
<linux/sched/debug.h>#include <linux/sched/signal.h>#include <asm/traps.  
Copyright (C) 2002 Maciej W. Rozycki \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/signal.h> /\* for SIGBUS \*/#include  
<linux/sched.h> /\* schow\_regs(), force\_sig() \*/#include  
Copyright (C) 2002 Maciej W. Rozycki \*/#include <linux/kernel.h>#include <linux/sched.h>#include <linux/smp.h>#include <linux/mm.h>  
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<linux/types.h>#include <linux/time.h>
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<asm/freq.h>#include <asm/io.h>
Copyright (C) 2002 M. R. Brown <mrbrown@linux-sh.org> */#include <linux/kernel.h>#include <linux/init.h>#include <linux/profile.h>#include
<linux/timex.h>#include <linux/sched.h>#include <lin
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Copyright (c) 2002 Nokia Corp. * * This is part of the SCTP Linux Kernel Implementation.
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Copyright (C) 2002 Paul Mackerras, IBM Corp. */#include <asm/ppc_asm.h>#include <asm/processor.h>
Copyright (C) 2002 Paul Mackerras, IBM Corp. */#include <asm/processor.h>#include <asm/ppc_asm.h>#include <asm/export.h>#include <asm/asm-
compat.h>#include <asm/feature-fixups.h>
Copyright (C) 2002 Paul Mackerras, IBM Corp. */#include <asm/processor.h>#include <asm/ppc_asm.h>#include <asm/export.h>#include <asm/asm-
compat.h>#include <asm/feature-fixups.h>#include <asm/k
Copyright (C) 2002 Paul Mackerras, IBM Corp. */#include "ppc_asm.h"
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Copyright (C) 2002 Paul Mundt */#ifndef __ASM_SH_BARRIER_H#define __ASM_SH_BARRIER_H
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Copyright (C) 2002 Paul Mundt */#include <linux/init.h>#include <linux/of_fdt.h>#include <linux/smp.h>#include <linux/io.h>#include
<asm/processor.h>#include <asm/cache.h>
Copyright (C) 2002 Paul Mundt */#include <linux/signal.h>#include <linux/sched.h>#include <linux/kernel.h>#include <linux/errno.h>#include
<linux/string.h>#include <linux/types.h>#include <li
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Copyright (C) 2002 Pete Zaitcev <zaitcev@yahoo.com> * * The algorithm to search a zero bit string is geared towards its application. * We expect a
couple of fixed sizes of requests, so a rotating c
Copyright (c) 2002 Pete Zaitcev (zaitcev@yahoo.com) * * We used to warn all over the code: DO NOT USE prom_printf(), * and yet people do. Anton's
banking code was outputting banks * with prom_prin
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<linux/in.h>#include <linux/kernel.h>#include <linux/ji
Copyright (C) 2002 Ralf Baechle DO1GRB (ralf@gnu.org) */#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include
<linux/in.h>#include <linux/kernel.h>#include <linux/mo
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Copyright (C) 2002 RidgeRun, Inc. * Author: Steve Johnson */#ifndef __ASOC_TI_MCBSP_H#define __ASOC_TI_MCBSP_H
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Copyright (C) 2002 Roman Zippel <zippel@linux-m68k.org> * * Introduced single menu mode (show all sub-menus in one large tree). * 2002-11-06 Petr
Baudis <pasky@ucw.cz>
Copyright (C) 2002 Roman Zippel <zippel@linux-m68k.org>.\n"
Copyright (C) 2002 Roman Zippel <zippel@linux-m68k.org> */%option nostdinit noyywrap never-interactive full ecs%option 8bit nodefault yylineno%x
ASSIGN_VAL_HELP_STRING
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Copyright (C) 2002 Russell King * * Commonly used scsi driver functions.
Copyright (C) 2002 Russell King * * Experimentation shows that on a StrongARM, it appears to be faster * to use the "invalidate whole tlb" rather than
"invalidate single * tlb" for this.
Copyright (C) 2002 Russell King. */#ifndef __ASM_ARM_THREAD_INFO_H#define __ASM_ARM_THREAD_INFO_H

```

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<linux/errno.h>#include <linux/interrupt.h>#include <
Copyright (C) 2002 Russell King. */#include <linux/module.h>#include <linux/serio.h>#include <linux/errno.h>#include <linux/interrupt.h>#include
<linux/ioport.h>#include <linux/device.h>#inclu
Copyright (C) 2002 Russell King. * Modified for nommu by Hyok S. Choi * * Module allocation method suggested by Andi Kleen.
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Copyright (C) 2002 Stuart Menefy */#include <linux/io.h>#include <linux/irq.h>#include <linux/interrupt.h>#include <linux/signal.h>#include
<cpu/irq.h>#include <asm/page.h>
Copyright (C) 2002 Stuart Menefy (stuart.menefy@st.com) */#include <asm/cache.h>#include <asm/tlb.h>#include <cpu/mmu_context.h>#include
<cpu/registers.h>
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Copyright (c) 2002 Thibaut Varene <varenet@parisc-linux.org> * * Pieces of code based on linux-2.4's hp_mouse.c & hp_keyb.c
Copyright (C) 2002 Thibaut Varene <varenet@parisc-linux.org> * * TODO: - handle processor number on SMP systems (Reporting Entity ID) *
handle message ID * - handle timestamps
Copyright (c) 2002 Thimo Seuffer */#include <linux/tty.h>#include <linux/major.h>#include <linux/init.h>#include <linux/console.h>#include
<linux/fs.h>#include <asm/setup.h>#include <asm/sgial
Copyright (C) 2002 Thomas Gleixner (tglx@linutronix.de) */
Copyright (c) 2002 Thomas Gleixner <tgxl@linutronix.de> * * Converted to platform driver by Janusz Krzysztofik <jkrzysz@tis.icnet.pl> * Partially
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Copyright (C) 2002 Trond Myklebust * * structure passed from user-space to kernel-space during an nfsv4 mount
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Copyright (C) 2002 Vivien Chappelier */#include <linux/module.h>#include <linux/init.h>#include <linux/serio.h>#include <linux/errno.h>#include
<linux/interrupt.h>#include <linux/ioport.h>#include

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Copyright (C) 2003, 04 Ralf Baechle (ralf@linux-mips.org) \*/#include <linux/kernel.h>#include <linux/mm.h>#include <linux/pci.h>#include  
<linux/init.h>#include <linux/types.h>#include <linux/dm  
Copyright (C) 2003, 06, 07 by Ralf Baechle (ralf@linux-mips.org) \*/#ifndef \_\_ASM\_CMPXCHG\_H#define \_\_ASM\_CMPXCHG\_H  
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\* \* Additions/Fixe  
Copyright (C) 2003, 06 Ralf Baechle (ralf@linux-mips.org) \*/#include <linux/bcd.h>#include <linux/i8253.h>#include <linux/init.h>#include  
<linux/irq.h>#include <linux/kernel.h>#include <linux/i  
Copyright (C) 2003, 07 Ralf Baechle \*/#ifndef \_\_ASM\_MACH\_IP22\_CPU\_FEATURE\_OVERRIDES\_H#define  
\_\_ASM\_MACH\_IP22\_CPU\_FEATURE\_OVERRIDES\_H  
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Copyright (c) 2003, 2004, 2005 Francois Romieu <romieu@fr.zoreil.com>  
Copyright (C) 2003, 2004, 2005 Paul Mundt \*/#include <linux/init.h>#include <linux/module.h>#include <linux/spinlock.h>#include  
<linux/proc\_fs.h>#include <linux/seq\_file.h>#include <linux/list  
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<linux/ptrace.h>#include <linux/stddef.h>  
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<linux/interrupt.h>#include <mach/sysasic.h>#include <mach/dma.h>#inclu
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<asm/processor.h>#include <asm/unistd.h>#incl
Copyright (C) 2003, 2004 Richard Curnow */#include <linux/sched.h>#include <linux/sched/debug.h>#include <linux/kernel.h>#include
<linux/string.h>#include <linux/errno.h>#include <linux/ptrace
Copyright (C) 2003, 2004 Richard Curnow */#include <linux/threads.h>#include <asm/processor.h>#include <asm/page.h>
Copyright (C) 2003, 2004 Richard Curnow * * Provides a debugfs file that lists out the ASIDs currently associated * with the processes.
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"stdio.h"#include "page.h"#include "ops.h"#
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electrical engineering in the Technion - Israel
Copyright (C) 2003-2005 Ben. Herrenschmidt (benh@kernel.crashing.org) * * The linux i2c layer isn't completely suitable for our needs for various *

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 <linux/device.h>#include <linux/mutex.h>#include <linux/err.h>#in  
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 <linux/interrupt.h>#include <linux/module.h>#include <linux/spinlock.h>#i  
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 <asm/assembler.h>#include <asm/asm-offsets.h>#include <asm/hwcap.h>#incl



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Copyright (C) 2003-2006, Marvell International Ltd. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt
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<net/mac80211.h>
Copyright (C) 2003-2006, Marvell International Ltd. */#include <linux/wait.h>#include <linux/timer.h>
Copyright (C) 2003 - 2006 Paul Mundt */#include <linux/init.h>#include <linux/kernel.h>#include <linux/module.h>#include
<linux/interrupt.h>#include <asm/cacheflush.h>#include <mach/sysasic.h>
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<linux/platform_device.h>#include <linux/of.h>#include <linux/of_device
Copyright (C) 2003 - 2007 Paul Mundt */#include <linux/init.h>#include <linux/io.h>#include <linux/kernel.h>#include <cpu/irq.h>
Copyright (C) 2003 - 2007 Paul Mundt */#include <linux/init.h>#include <linux/io.h>#include <linux/string.h>#include <asm/processor.h>#include
<asm/cache.h>#include <asm/tlb.h>
Copyright (C) 2003 - 2007 Tensilica Inc. */
Copyright (C) 2003-2007 Yoichi Yuasa <yuasa@linux-mips.org> */#include <linux/cpu.h>#include <linux/errno.h>#include <linux/init.h>#include
<linux/ioport.h>#include <linux/kernel.h>#include <l
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 Copyright (C) 2003-2008 Yoichi Yuasa <yuasa@linux-mips.org> \*/#include <linux/err.h>#include <linux/fs.h>#include <linux/init.h>#include  
 <linux/io.h>#include <linux/ioport.h>#include <linux/in  
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 Copyright (C) 2003-2009 Jean Delvare <jdelvare@suse.de> \* \* Heavily inspired from the lm78, lm75 and adm1021 drivers. The LM83 is \* a sensor chip  
 made by National Semiconductor. It reports up to f  
 Copyright (C) 2003-2009 Jean Delvare <jdelvare@suse.de> \* \* Inspired from the lm83 driver. The W83L785TS-S is a sensor chip made \* by Winbond.  
 It reports a single external temperature with a 1 deg  
 Copyright (C) 2003-2009 Jean Delvare <jdelvare@suse.de> \* \* The ADM1025 is a sensor chip made by Analog Devices. It reports up to 6 \* voltages  
 (including its own power source) and up to two temper  
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 Copyright (C) 2003-2009 Yoichi Yuasa <yuasa@linux-mips.org> \*/#include <linux/errno.h>#include <linux/fs.h>#include  
 <linux/gpio/driver.h>#include <linux/init.h>#include <linux/interrupt.h>#inc  
 Copyright (C) 2003-2009 Yoichi Yuasa <yuasa@linux-mips.org> \*/#include <linux/init.h>#include <linux/ioport.h>#include <linux/irq.h>#include  
 <linux/string.h>  
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 Copyright (C) 2003-2010 Jean Delvare <jdelvare@suse.de> \* \* Based on the lm83 driver. The LM90 is a sensor chip made by National \*  
 Semiconductor. It reports up to two temperatures (its own plus up  
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 Copyright (C) 2003 - 2012 Paul Mundt \* \* Based on linux/arch/i386/mm/fault.c:  
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 offsets.h>#include <asm/thread\_info.h>#include <asm/unistd.h>#  
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\_\_KERNEL\_\_  
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Copyright (C) 2003 Benjamin Herrenschmidt (benh@kernel.crashing.org)  
Copyright (C) 2003 Bernardo Innocenti <bernie@develer.com> \* Based on former asm-ppc/div64.h and asm-m68knommu/div64.h \* \* Optimization for  
constant divisors on 32-bit machines:  
Copyright (C) 2003 Bernardo Innocenti <bernie@develer.com> \* \* Based on former do\_div() implementation from asm-parisc/div64.h:  
Copyright (C) 2003 Broadcom Corporation \*/#include <linux/cache.h>#include <linux/sched.h>#include <linux/mm.h>#include <linux/smp.h>#include  
<linux/kernel.h>#include <linux/signal.h>#include  
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Copyright (c) 2003 by Karsten Wiese <annabellesgarden@yahoo.de> \*/  
Copyright (C) 2003 by Ralf Baechle \*/#ifndef \_\_ASM\_AR7\_IRQ\_H#define \_\_ASM\_AR7\_IRQ\_H  
Copyright (C) 2003 by Ralf Baechle \*/#ifndef \_\_ASM\_MACH\_EMMA2RH\_IRQ\_H#define \_\_ASM\_MACH\_EMMA2RH\_IRQ\_H  
Copyright (C) 2003 by Ralf Baechle \*/#ifndef \_\_ASM\_MACH\_GENERIC\_IRQ\_H#define \_\_ASM\_MACH\_GENERIC\_IRQ\_H  
Copyright (C) 2003 by Ralf Baechle \*/#ifndef \_\_ASM\_PREFETCH\_H#define \_\_ASM\_PREFETCH\_H  
Copyright (C) 2003 by Ralf Baechle \*/#include <linux/export.h>#include <linux/init.h>#include <linux/mm.h>#include <asm/fixmap.h>#include  
<asm/pgtable.h>#include <asm/pgalloc.h>#include <asm/t  
Copyright (C) 2003 by Ralf Baechle \*/#include <linux/init.h>#include <linux/mm.h>#include <linux/memblock.h>#include <linux/highmem.h>#include  
<asm/fixmap.h>#include <asm/pgtable.h>#include <a  
Copyright (C) 2003 by Ralf Baechle \* \* RTC routines for Malta style attached PIIX4 device, which contains a \* Motorola MC146818A-compatible Real  
Time Clock.  
Copyright (C) 2003 Carlos O \*/#ifndef \_\_PARISC64\_KERNEL\_SIGNAL32\_H#define \_\_PARISC64\_KERNEL\_SIGNAL32\_H  
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Copyright (c) 2003 Christoph Hellwig <hch@lst.de> \* - new-style, hotplug-aware pci probing and scsi registration \* \* Version : v2.00.4 Mon Nov 14  
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make this driver easier to implement.
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<linux/irqchip.h>#include <linux/of_irq.h>#include <linux/of_addr
  Copyright (c) 2003 */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include <linux/kernel.h>
  Copyright (c) 2003 Dimitromanolakis Apostolos <apostol@cs.utoronto.ca>
  Copyright (c) 2003 Dimitromanolakis Apostolos <apostol@cs.utoronto.ca> * * version 0.82: Stable / not all features work yet (no communication with
AC97 secondary) * added 64x/128x oversampl
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<linux/fs.h>#include <linux/module.h>#include <linux/usb.h>
  Copyright (C) 2003 Fenghua Yu <fenghua.yu@intel.com> * - Change pt_regs_off() to make it less dependent on pt_regs structure. */
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  Copyright (C) 2003 Geert Uytterhoeven <geert@linux-m68k.org> * * Serial_console ascii image can be any size, * but should contain %s to display the
version
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  Copyright (C) 2003 Geert Uytterhoeven * * Loosely based on drivers/pci/pci-sysfs.c
  Copyright (C) 2003 Georges Menie * * This driver assumes an already configured controller (e.g. from config.c) * Keep the code clean of board specific
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  Copyright (C) 2003 Greg Kroah-Hartman (greg@kroah.com)
  Copyright (c) 2003 Greg Kroah-Hartman (greg@kroah.com) * * and from the Apple USB BCM5974 multitouch driver,
  Copyright (C) 2003 Guido Guenther <agx@sigxcpu.org> */
  Copyright (c) 2003 Hans-Frieder Vogt <hfvogt@arcor.de>, Based on SMBus 2.0 driver for AMD-8111 IO-Hub
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palinfo * code to create this file * Oct 23 2003 kaos@sg
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Copyright (C) 2003 Hewlett-Packard Co * David Mosberger-Tang <davidm@hpl.hp.com> * * 25-Sep-03 davidm Implement fsys_rt_sigprocmask().
* 18-Feb-03 louisk Implement fsys_gettimeofday(). * 28-Feb-
Copyright (C) 2003 Hewlett-Packard Co * David Mosberger-Tang <davidm@hpl.hp.com> */#include <linux/init.h>#include <linux/string.h>
Copyright (C) 2003 Hewlett-Packard Co * David Mosberger-Tang <davidm@hpl.hp.com> * * Loosely based on patch by Rusty Russell.
Copyright (C) 2003 Hewlett-Packard Co * David Mosberger-Tang <davidm@hpl.hp.com> * * There are a number of reasons for patching instructions.
Rather than duplicating code * all over the place, we
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code is a 1-to-1 copy.
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Copyright (C) 2003 IBM. */#ifndef _RPADLPAR_IO_H_#define _RPADLPAR_IO_H_
Copyright (C) 2003 IBM. */#include <linux/kobject.h>#include <linux/string.h>#include <linux/pci.h>#include <linux/pci_hotplug.h>#include
"rpphp.h"#include "rpadlpar.h"#include "../pci.h"
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<arun.sharma@intel.com> * * 12/07/98 S. Eranian added pt_regs & switch_pt
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Copyright (c) 2003 James Morris <jmorris@intercode.com.au>
Copyright (c) 2003 James Morris <jmorris@intercode.com.au> * * FIXME: deflate transforms will require up to a total of about 436k of kernel * memory
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Copyright (C) 2003 Jozsef Kadlecsik <kadlec@netfilter.org> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/netfilter_ipv4/ip_tables.h>#include <linux/slab.h>
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<linux/netfilter_ipv6/ip6_tables.h>#include <linux/slab.h
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Copyright (c) 2003 Kyle McMartin <kyle@debian.org> \*/#include <crypto/internal/hash.h>#include <linux/kernel.h>#include  
<linux/module.h>#include <linux/mm.h>#include <linux/init.h>#include <lin  
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Copyright (C) 2003 Ladislav Michl (ladis@linux-mips.org)  
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Copyright (C) 2003 Linus Torvalds \* \* Mon Mar 5, 2007: Davide Libenzi <davidel@xmailserver.org> \* Changed ->read() to return a siginfo structure  
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Copyright (C) 2003 MontaVista Software Inc. \* Author: Jun Sun, jsun@mvista.com or jsun@junsun.net \* \* 031022 - [jsun] add run-time configure and  
partition setup  
Copyright: (C) 2003 Monta Vista Software, Inc. \* \* Based on drivers/serial/8250.c by Russell King.  
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Borg.  
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Copyright: (C) 2003 Monta Vista Software, Inc. \* \* Note 1: This driver is made separate from the already too overloaded \* 8250.c because it needs  
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draft * and to make it work as a loadable module in
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Copyright (c) 2003 Open Source Development Lab * * The driver model core calls device_pm_add() when a device is registered. * This will initialize the
embedded device_pm_info object in the device
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Copyright (C) 2003 Paul Mundt */#ifndef __ASM_CPU_SH2_MMU_CONTEXT_H#define __ASM_CPU_SH2_MMU_CONTEXT_H
Copyright (C) 2003 Paul Mundt */#ifndef __ASM_SH_DREAMCAST_DMA_H#define __ASM_SH_DREAMCAST_DMA_H
Copyright (C) 2003 Paul Mundt */#ifndef __ASM_SH_FLAT_H#define __ASM_SH_FLAT_H
Copyright (C) 2003 Paul Mundt */#ifndef __ASM_SH_TLB_64_H#define __ASM_SH_TLB_64_H
Copyright (C) 2003 Paul Mundt */#ifndef DMA_REGISTER_H#define DMA_REGISTER_H
Copyright (C) 2003 Paul Mundt */#include <linux/sys.h>#include <linux/linkage.h>
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Copyright (C) 2003 Paul Mundt * * Per-interrupt selective. IRLM=0 (Fixed priority) is not * supported being useless without a cascaded interrupt *
controller.
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Copyright (C) 2003 Pavel Machek (pavel@ucw.cz) * * These routines maintain argument size conversion between 32bit and 64bit * ioctls.
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__ASM_MACH_IP28_CPU_FEATURE_OVERRIDES_H
Copyright (C) 2003 Ralf Baechle * * Handler for RM7000 extended interrupts. These are a non-standard * feature so we handle them separately from
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Copyright (C) 2003 Ralf Baechle */#ifndef __ASM_MACH_GENERIC_CPU_FEATURE_OVERRIDES_H#define
__ASM_MACH_GENERIC_CPU_FEATURE_OVERRIDES_H
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__ASM_MACH_NETLOGIC_CPU_FEATURE_OVERRIDES_H
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 <linux/string.h>#include <linux/spinlock.h>#include <linux/rcupdate  
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 <linux/kernel.h>#include <linux/slab.h>#include <linux/fs.h>#incl  
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 Copyright (C) 2003 Richard Curnow \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/mm.h>#include <linux/log2.h>#include  
 <asm/mmu\_context.h>#include <asm/processor.h>#include  
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various structure changes, and crypt  
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<linux/module.h>#include <linux/workqueue.h>#include <linux/blkd  
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Copyright (C) 2004-2005 Nokia Corporation \* Authors: Juha Yrjölä <juha.yrjola@nokia.com> \* Imre Deak <imre.deak@nokia.com> \* YUV support: Jussi Laako <jussi.laako@nokia.com> \*/  
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Copyright (c) 2004-2005 Richard Purdie \* \* Based on Sharp's 2.4 kernel patches/lubbock.c  
Copyright (c) 2004-2005 Richard Purdie \*/#ifndef \_MACH\_SHARPSL\_PM#define \_MACH\_SHARPSL\_PM  
Copyright (C) 2004-2005 Richard Purdie \* - omap\_ts.[hc], ads7846.h, ts\_osk.c  
Copyright (C) 2004-2005 Richard Purdie <rpurdie@rpsys.net> \* \* Sharp's bootloader doesn't pass any kind of machine ID \* so we have to figure out the machine for ourselves...  
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 Copyright (C) 2004-2005 Yoichi Yuasa <yuasa@linux-mips.org> \*/#include <linux/init.h>#include <linux/pci.h>  
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 \_\_IBM\_NEWEMAC\_DEBUG\_H#define \_\_IBM\_NEWEMAC\_DEBUG\_H  
 Copyright (C) 2004, 2006, 2007 Maciej W. Rozycki \* \* [31-AUG-98] triemer \* Changed IRQ to use Harald's dec internals interrupts.h \* removed  
 base\_addr code - moving address assignment to setup.c  
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 <linux/dma-mapping.h>#include <linux/memblock.h>#i  
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 <linux/module.h>#include <linux/ptrace.h>#include  
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 <linux/module.h>#include <linux/unistd.h>#include  
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 handles all the interrupt vector routines.; At  
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 Copyright (C) 2004-2006 Atmel Corporation \*/#ifndef \_\_MACB\_H#define \_\_MACB\_H  
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 <asm/assembler.h>#include <asm/asm-offsets.h>#include <asm/hwcap.h>#incl  
 Copyright (C) 2004-2006 Hyok S. Choi (hyok.choi@samsung.com) \*/#include <linux/linkage.h>#include <linux/init.h>#include  
 <asm/assembler.h>#include <asm/hwcap.h>#include <asm/pgtable-hwdef.h>#in

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 <linux/platform\_device.h>#include <linux/err.h>  
 Copyright (C) 2004 - 2006 Paul Mundt \*/#include <linux/pci.h>#include <linux/io.h>#include <linux/sh\_intc.h>#include "pci-sh4.h"  
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 Copyright (C) 2004 - 2006 rt2x00 SourceForge Project \* <http://rt2x00.serialmonkey.com> \* \* Module: eeprom\_93cx6 \* Abstract: EEPROM reader  
 routines for 93cx6 chipsets. \* Supported chipsets: 93c46  
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 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Delay routines using pre computed loops\_per\_jiffy value.  
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 <psokolov@synopsys.com>  
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 Insn Scheduling improvements to csum core routines. \*  
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## Interrupts

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Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Vineetg: Aug 2009 \* -"C" version of lowest level context switch asm  
macro called by scheduler \* gcc doesn't genera  
Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: aug 2009 \* -Implemented CONFIG\_STACKTRACE APIs,  
primarily save\_stack\_trace\_tsk( ) \* for displaying task'  
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entry.S into this file. \* -This is the more "natural" h  
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customer chips still have the issue  
Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Vineetg: August 2010: From Android kernel work  
Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Vineetg: Dec 2007 \* -Check if we are running on Simulator or on  
real hardware \* to skip certain things during b  
Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: Jan 2010 (Restarting of timer related syscalls)  
Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: June 2010 \* -\_\_clear\_user( ) called multiple times during  
elf load was byte loop \* converted to do as  
Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: June 2011 \* -"/proc/meminfo | grep PageTables" kept on  
increasing \* Recently added pgtable dtor was not g  
Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: March 2009 \* -Implemented task\_pt\_regs( )  
Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Vineetg: March 2009 (Supporting 2 levels of Interrupts) \* Stack  
switching code can no longer reliably rely on the fac  
Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Vineetg: May 16th, 2008 \* - Current macro is now implemented as  
"global register" r25  
Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: May 2011 \* -Folded PAGE\_PRESENT (used by VM) and  
PAGE\_VALID (used by MMU) into 1. \* They are semantical  
Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: May 2011: for Non-aliasing VIPT D-cache following can be  
NOPs \* -flush\_cache\_dup\_mm (fork) \* -likewise  
Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: May 2011 \* -Refactored get\_new\_mmu\_context( ) to only  
handle live-mm. \* retiring-mm handled in other hook  
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Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: May 2011 \* -We had half-optimised memset/memcpy, got  
better versions of those \* -Added memcmp, strchr, str  
Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Vineetg: Oct 2009 \* No need for ARC specific thread\_info allocator  
(kmalloc/free). This is \* anyways one page alloc  
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*/
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    Copyright (C) 2004-2007 John Williams <john.williams@petalogix.com> ** derived from arch/v850/kernel/ptrace.c
    Copyright (C) 2004, 2007 Maciej W. Rozycki
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    Copyright (C) 2004 - 2007 Paul Mundt */#include <linux/init.h>#include <linux/platform_device.h>#include <linux/mtd/mtd.h>#include
<linux/mtd/partitions.h>#include <linux/mtd/physmap.h>#include
    Copyright (C) 2004 - 2007 Paul Mundt */#include <linux/mm.h>#include <linux/init.h>#include <linux/dma-noncoherent.h>#include
<linux/module.h>#include <asm/cacheflush.h>#include <asm/addrspace
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<linux/interrupt.h>#include <linux/kernel_stat.h>#include <lin
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<linux/device.h>#include <linux/dmaengine.h>#include <linux/dma-map
    Copyright (c) 2004-2008 Ben Dooks ** S3C2410 LCD Framebuffer Driver
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\_ASM\_C6X\_DELAY\_H#define \_ASM\_C6X\_DELAY\_H  
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<linux/mm.h>#include <linux/swap.h>#include <linux/modul  
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<linux/module.h>#include <asm/checksum.h>#include <linux  
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<linux/module.h>#include <linux/syscalls.h>#include <lin  
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\_ASM\_C6X\_BITOPS\_H#define \_ASM\_C6X\_BITOPS\_H  
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<asm/io.h>#include <asm/div64.h>#include <asm/delay.h>
Copyright (C) 2004 Advanced Micro Devices, Inc. */#include <linux/spinlock.h>#include <linux/fb.h>#include <linux/delay.h>#include
<asm/io.h>#include <asm/div64.h>#include <asm/delay.h>#include
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<asm/ppc\_asm.h>#include <asm/vdso.h>#include <asm/asm-o

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Copyright (C) 2004 by Ralf Baechle (ralf@linux-mips.org) \* \* Based on arch/mips/ddb5xxx/ddb5477/pci\_ops.c

Copyright (C) 2004 by Ralf Baechle (ralf@linux-mips.org) \*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/interrupt.h>#include

<linux/pci.h>#include <linux/types.h>#include <as

Copyright (C) 2004 by Ralf Baechle (ralf@linux-mips.org) \* \* MIPS boards specific PCI support.

Copyright (C) 2004 by Ralf Baechle \* \* RTC routines for PC style attached Dallas chip with ARC epoch.

Copyright (C) 2004 by Ralf Baechle \* \* The cpustart method is a PMC-Sierra's function to start the secondary CPU. \* Stock PMON 2000 has the smpfork, semlock and semunlock methods instead.

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Copyright (C) 2004 by Thomas Rathbone, HP Labs

Copyright (C) 2004 Carl-Daniel Hailfinger (invalid MAC handling, insane \* IRQ rate fixes, bigendian fixes, cleanups, verification)

Copyright (C) 2004 Cavium Networks \*/#ifndef \_\_ASM\_MACH\_CAVIUM\_OCTEON\_CPU\_FEATURE\_OVERRIDES\_H#define

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 Copyright (C) 2004 David Gibson <dwg@au.ibm.com>, IBM \* Based on earlier code written by:  
 Copyright (C) 2004 David Gibson, IBM Corporation \*/#ifndef \_POWERPC\_PMC\_H#define \_POWERPC\_PMC\_H#endif \_\_KERNEL\_\_  
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 Copyright (c) 2004 Erik Rigtorp <erikki@linux.nu> <erik@rigtorp.com> \* \* This driver works with Elektor magazine USB Interface as published in \*  
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 <linux/unistd.h>#include <linux/interrupt.h>#include <linux

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 <stddef.h>#include <init.h>#include <elf\_user.h>#incl  
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 examining the MSFT drivers' debug output \* (insanely lef  
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 Copyright (C) 2004 Hidetoshi Seto <seto.hidetoshi@jp.fujitsu.com> \*/#include <linux/threads.h>  
 Copyright (C) 2004 Hollis Blanchard <hollisb@us.ibm.com>, IBM \*/  
 Copyright (C) 2004 Hollis Blanchard, IBM Corporation  
 Copyright (C) 2004 Hong-Gunn Chew <hglinux@gunnet.org> and \* Jean Delvare <jdelvare@suse.de> \* \* Ported to Linux 2.6 by Hong-Gunn Chew with  
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 Copyright (C) 2004 Hyok S. Choi \*/#ifndef \_ASMARM\_PGTABLE\_NOMMU\_H#define \_ASMARM\_PGTABLE\_NOMMU\_H  
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 parts of the 2.4 ASIC3 driver (4 bit \* support).  
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 Copyright (C) 2004 IBM Corporation, Rusty Russell. \* \* Creation is done via kthreadd, so that we get a clean environment \* even if we're invoked from  
 userspace (think modprobe, hotplug cpu, \* etc.  
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Andreas Gruenbacher <agruen@suse.de>. */
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<linux/security.h>#include <linux/posix_acl_xattr.h>#include "
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Copyright (C) 2004 Jan M. Hochstein * <hochstein@algo.informatik.tu-darmstadt.de> */#include <linux/device.h>#include <linux/kernel.h>#include
<linux/module.h>#include <linux/usb.h>#include <li
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Copyright (C) 2004 Jens Axboe <axboe@kernel.dk> * * Helper functions for setting/querying io priorities of processes. The * system calls closely
mimick getpriority/setpriority, see the man page fo
Copyright (c) 2004 Jocelyn Mayer
Copyright (C) 2004 Jochen Friedrich <jochen@scram.de> * * This code has basically these routines at the moment:
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Copyright (c) 2004 Joerg Albert <joerg.albert@gmx.de>
Copyright (C) 2004 John Steele Scott <toojays@toojays.net> * * TODO: Need a big cleanup here. Basically, we need to have different *
cpufreq_driver structures for the different type of HW in
Copyright (c) 2004 Jon Smirl <jonsmirl@gmail.com>
Copyright (c) 2004 Jouni Malinen <j@w1.fi> */#include <crypto/internal/hash.h>#include <asm/byteorder.h>#include <linux/init.h>#include
<linux/module.h>#include <linux/string.h>#include <linux/
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```



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Copyright (C) 2004 Maciej W. Rozycki \*/#ifndef \_\_ASM\_CPU\_TYPE\_H#define \_\_ASM\_CPU\_TYPE\_H  
Copyright (C) 2004 Maciej W. Rozycki \*/#ifndef \_\_ASM\_MACH\_ATH25\_CPU\_FEATURE\_OVERRIDES\_H#define  
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Copyright (C) 2004 Maciej W. Rozycki \*/#ifndef \_\_ASM\_MACH\_RC32434\_CPU\_FEATURE\_OVERRIDES\_H#define  
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Copyright (C) 2004 Maciej W. Rozycki \*/#include <linux/delay.h>#include <linux/kernel.h>#include <linux/sched.h>#include  
<linux/seq\_file.h>#include <asm/bootinfo.h>#include <asm/cpu.h>#include  
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<linux/fttrace.h>#include <linux/kprobes.h>
Copyright (C) 2004 Nadia Yvette Chambers */#include <linux/module.h>#include <linux/kallsyms.h>#include <linux/uaccess.h>#include
<linux/fttrace.h>#include <linux/sched/rt.h>#include <linux/sche
Copyright (C) 2004 Nadia Yvette Chambers */#include <linux/ring_buffer.h>#include <generated/utsrelease.h>#include <linux/stacktrace.h>#include
<linux/writeback.h>#include <linux/kallsyms.h>#in
Copyright (C) 2004 Nadia Yvette Chambers */#include <linux/ring_buffer.h>#include <linux/debugfs.h>#include <linux/uaccess.h>#include
<linux/fttrace.h>#include <linux/slab.h>#include <linux/fs.h
Copyright (C) 2004 Nathan Lynch IBM Corporation. ** This driver creates a proc file at /proc/ppc64/lparcfg which contains * keyword - value pairs that
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Copyright (C) 2004 Nokia Corporation * Author: Imre Deak <imre.deak@nokia.com> */#include <linux/module.h>#include <linux/device.h>#include
<linux/interrupt.h>#include <linux/spinlock.h>#includ
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Peter Ujfalusi <peter.ujfalusi@ti.com>
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<linux/platform_device.h>#include <linux/delay.h>#include <linu
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Copyright (C) 2004 Nokia Corporation by Tony Lindgren <tony@atomide.com> ** H2 specific changes and cleanup
Copyright (C) 2004 Nokia Corporation by Tony Lindgren <tony@atomide.com> */#include <linux/gpio.h>#include <linux/kernel.h>#include
<linux/init.h>#include <linux/platform_device.h>#include <linu
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```

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 <linux/ptrace.h>#include <linux/prefetch.h>#include <asm/sstep.h>#includ  
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<asm/page.h>#include <asm/ptrace.h>#include <a
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<linux/init.h>#include <linux/errno.h>#include <
  Copyright (C) 2004 Richard Curnow */#include <linux/rwsem.h>#include <linux/sched.h>#include <linux/mm.h>#include <linux/smp.h>#include
<linux/kernel.h>#include <linux/signal.h>#include <linu
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 \* FIXME: docs  
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 Copyright (C) 2005-2006 Intel Corporation \* Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com> \* \* WUIEs and MMC IEs...well, they are almost the same at the end. MMC \* IEs are Wireless USB IEs  
 Copyright (C) 2005-2006 Intel Corporation <linux-wimax@intel.com> \* Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com> \*/#include <linux/debugfs.h> #include <linux/wimax.h> #include "wimax-inter  
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 Copyright (C) 2005-2006 Intel Corporation <linux-wimax@intel.com> \* Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com> \* \* We assign a single generic netlink family ID to each device (to \* simp  
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 Copyright (C) 2005-2006 Jan Rychter <jan@rychter.com> \*/  
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Copyright (C) 2005-2006 Micronas USA Inc.static int go7007_usb_vendor_request(struct go7007 *go, u16 request, u16 value, u16 index, void
*transfer_buffer, int length, int in){ struct go7007_usb *u
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*/#include <linux/kernel.h>#include <linux/i
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 <linux/init.h>#include <linux/interrupt.h>#include <linux/io.h>  
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 obtained here:  
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 <linux/pm.h>#include <linux/mm.h>#include <asm/machvec.h>#i  
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<linux/spinlock.h>#include <linux/interrupt.h>
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<linux/device.h>#include <linux/atmel_pdc.h>#include <linux/gpio/cons
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<linux/errno.h>#include <linux/io.h>#include <linux/er
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<linux/errno.h>#include <linux/delay.h>#include <linux
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<linux/interrupt.h>#include <linux/timer.h>#include
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 Copyright (C) 2005-2017 Andes Technology Corporation#ifndef \_\_ASM\_VDSO\_DATAPAGE\_H#define \_\_ASM\_VDSO\_DATAPAGE\_H  
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 Copyright (C) 2005-2018 Andes Technology Corporation#include <asm/sfp-machine.h>#include <math-emu/soft-fp.h>#include <math-emu/double.h>int fcmpd(void \*ft, void \*fa, void \*fb, int cmpop){ FP\_DE  
 Copyright (C) 2005-2018 Andes Technology Corporation#include <asm/sfp-machine.h>#include <math-emu/soft-fp.h>#include <math-emu/single.h>int fcmps(void \*ft, void \*fa, void \*fb, int cmpop){ FP\_DE  
 Copyright (C) 2005-2018 Andes Technology Corporation#include <linux/uaccess.h>  
 Copyright (C) 2005-2019 Andes Technology Corporation#ifndef \_\_FP\_UDF\_IEX\_CRTL\_H#define \_\_FP\_UDF\_IEX\_CRTL\_H  
 Copyright (C) 2005-2019 Andes Technology Corporation#include <linux/uaccess.h>  
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Copyright (c) 2005 Andrea Bittau <a.bittau@cs.ucl.ac.uk> \* /#include <linux/types.h>#include "dccp.h"  
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Copyright (c) 2005 Andriy Skulysh \* \* Based on Sharp's Corgi Backlight Driver  
Copyright (C) 2005 Andriy Skulysh \* /#include <linux/init.h>#include <linux/interrupt.h>#include <linux/module.h>#include <linux/io.h>#include  
<mach-dreamcast/mach/dma.h>#include <asm/dma.h>#in  
Copyright (C) 2005 Andriy Skulysh \* /#include <linux/init.h>#include <linux/kernel.h>#include <asm/clock.h>#include <asm/freq.h>#include  
<asm/io.h>  
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Loveridge April 9, 2003 available at \* http://www.elo  
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Copyright (c) 2005 Arnaldo Carvalho de Melo <acme@conectiva.com.br> \* /#include "../dccp.h"#include "ccid3.h"  
Copyright (c) 2005 Arnaldo Carvalho de Melo <acme@conectiva.com.br> \* /#include <linux/ktime.h>#include <linux/list.h>#include <linux/slab.h>  
Copyright (c) 2005 Arnaldo Carvalho de Melo <acme@conectiva.com.br> \* /#include <net/sock.h>#include "tfr.h"  
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Copyright (c) 2005 Arnaldo Carvalho de Melo <acme@ghostprotocols.net> \*/  
Copyright (c) 2005 Arnaldo Carvalho de Melo <acme@ghostprotocols.net> \* /#include "dccp.h"#include <linux/kernel.h>#include  
<linux/slab.h>#include <linux/export.h>  
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 Copyright (C) 2005 by Ash Willis <ashwillis@programmer.net> \* \* TODO \* 4 channel playback for ALS300+ \* gameport \* mpu401 \* opl3  
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 Copyright (C) 2005 by David Brownell \*/#include <linux/module.h>#include <linux/netdevice.h>#include <linux/etherdevice.h>#include  
 <linux/ethtool.h>#include <linux/workqueue.h>#include <linux/s  
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 <linux/smp.h>#include <asm/irq\_regs.h>#include <asm/tim  
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Copyright (C) 2005 David Brownell */#include <linux/kernel.h>#include <linux/init.h>#include <linux/delay.h>#include <linux/module.h>#include
<linux/device.h>#include <linux/parport.h>
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Copyright (C) 2005 Dirk Behme */#include <linux/types.h>#include <linux/hwmon.h>#include <linux/err.h>#include <linux/sched.h>#include
<linux/delay.h>#include <linux/input.h>#include <linux/in
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polldev.h>#include <linux/input/sparse-keymap.h>#incl
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 <asm/unaligned.h>#include <linux/kernel.h>#include  
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 Copyright (C) 2005 IBM \* \* Entry and exit function for SPU-side of the context restore \* sequence. Sets up an initial stack frame, then branches to \*  
 'main'. On return, restores all 128 register  
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 <linux/interrupt.h>#include <linux/export.h>#in  
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 <asm/page.h>#include <asm/pgtable.h>#include <asm/mca\_asm.h>  
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<linux/opprofile.h>#include <linux/smp.h>#include <asm/cpu-info.h>
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<asm/addrspace.h>#include <asm/io.h>
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<linux/delay.h>#include <linux/init.h>#include <linux/spinlock.h>#include  
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<linux/lcd.h>#include <linux/module.h>#include <  
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 <linux/interrupt.h>#include <linux/percpu.h>#include <lin  
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<media/davinci/ccdc\_types.h>#include <media/davinci/vpfe\_types.h>  
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<linux/module.h>#include <linux/skbuff.h>#include <linu  
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<linux/capability.h>#include <linux/skbuff.h>#include <lin  
Copyright (c) 2006-2010 Patrick McHardy <kaber@trash.net> \*/#include <linux/module.h>#include <linux/moduleparam.h>#include  
<linux/types.h>#include <linux/kernel.h>#include <linux/in.h>#include  
Copyright (C) 2006-2010 Paul Mundt \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/io.h>#include <linux/clkdev.h>#include

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<asm/clock.h>#include <asm/freq.h>
Copyright (C) 2006-2010 PEAK System-Technik GmbH */#include <linux/kernel.h>#include <linux/module.h>#include <linux/interrupt.h>#include
<linux/netdevice.h>#include <linux/delay.h>#include <li
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 <linux/input.h>#include <linux/platform\_device.h>#inc  
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 <linux/slab.h>#include <sound/pcm.h>#include <sound/pcm\_params.h>  
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 PCI header is assumed to contain the followin  
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 <linux/i2c.h>#include <linux/i2c-algo-bit.h>#includ  
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 defined(CONFIG\_MAGIC\_SYSRQ)#define SUPPORT\_SYSRQ#endif  
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 <linux/slab.h>#include <linux/delay.h>#include <linux/serial\_core.h>#in  
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 <linux/device.h>#include <linux/platform\_device.h>#inclu  
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 Copyright (c) 2006 Herbert Xu <herbert@gondor.apana.org.au> \*/#ifndef \_CRYPTO\_ALGAPI\_H#define \_CRYPTO\_ALGAPI\_H  
 Copyright (c) 2006 Herbert Xu <herbert@gondor.apana.org.au> \*/#include <crypto/internal/skipcipher.h>#include <crypto/scatterwalk.h>#include  
 <linux/err.h>#include <linux/init.h>#include <linux/ker  
 Copyright (c) 2006 Herbert Xu <herbert@gondor.apana.org.au> \* \* The HMAC implementation is derived from USAGI.  
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 and by a test vector provided by Ken Buchanan at \*  
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 <linux/module.h>#include <linux/string.h>  
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 Copyright (C) 2006 James Smart, Emulex Corporation \*/#include <linux/time.h>#include <linux/jiffies.h>#include <linux/security.h>#include  
 <linux/delay.h>#include <linux/slab.h>#include <linux  
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 Copyright (C) 2006 Jamie Lenehan \*/#include <linux/init.h>#include <linux/irq.h>#include <mach/titan.h>#include <asm/io.h>  
 Copyright (C) 2006 Jamie Lenehan \*/#include <linux/platform\_device.h>#include <linux/init.h>#include <linux/serial.h>#include <linux/io.h>#include  
 <linux/serial\_sci.h>#include <linux/sh\_timer.  
 Copyright (C) 2006 Jamie Lenehan \*/#include <linux/platform\_device.h>#include <linux/init.h>#include <linux/serial.h>#include <linux/io.h>#include  
 <linux/sh\_timer.h>#include <linux/sh\_intc.h>  
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 <linux/gpio/consumer.h>#include <linux/gpio.h>#include <linux/kern  
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 Copyright (C) 2006 Komal Shah <komal\_shah802003@yahoo.com> \*/#include <linux/ahci\_platform.h>#include <linux/clk-provider.h>#include  
 <linux/clk.h>#include <linux/clkdev.h>#include <linux/dma-con  
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<linux/preempt.h>#include <linux/kdebug.h>#include <linux/slab.h
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<scsi/scsi_dh.h>#include <scsi/scsi_device.h>
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Copyright (C) 2006 Nokia Corporation * Author: Imre Deak <imre.deak@nokia.com> */#include <linux/device.h>#include <linux/delay.h>#include
<linux/slab.h>#include <linux/workqueue.h>#include <li
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Copyright (C) 2006 Nokia Corporation */#include <linux/gpio.h>#include <linux/platform_device.h>
Copyright (C) 2006 Nokia Corporation */#include <linux/gpio.h>#include <linux/platform_device.h>#include <linux/platform_data/gpio-
omap.h>#include <linux/mfd/tps65010.h>
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<linux/errno.h>#include <linux/usb.h>#include <linux/platfor
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<linux/serial.h>#include <linux/serial_sci.h>#incl
Copyright (C) 2006 Paul Mundt */#include <linux/init.h>#include <linux/slab.h>#include <linux/module.h>#include <linux/interrupt.h>#include
<linux/platform_device.h>#include <asm/push-switch.h>
Copyright (C) 2006 Paul Mundt */#include <linux/io.h>#include <linux/module.h>#include <linux/interrupt.h>#include
<linux/platform_device.h>#include <mach/highlander.h>#include <asm/push-switc
Copyright (C) 2006 Paul Mundt */#include <linux/linkage.h>#include <asm/page.h>
Copyright (C) 2006 Paul Mundt */#include <linux/platform_device.h>#include <linux/init.h>#include <linux/serial.h>#include <linux/io.h>#include
<linux/serial_sci.h>#include <linux/sh_dma.h>#i
Copyright (C) 2006 Paul Mundt */#include <linux/platform_device.h>#include <linux/init.h>#include <linux/serial.h>#include
<linux/serial_sci.h>#include <linux/uio_driver.h>#include <linux/sh_t
Copyright (C) 2006 Paul Mundt */#include <linux/platform_device.h>#include <linux/init.h>#include <linux/serial.h>#include
<linux/sh_timer.h>#include <linux/sh_intc.h>#include <linux/serial_sc
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Copyright (C) 2006 Ralf Baechle <ralf@linux-mips.org> \*/#include <linux/console.h>#include <linux/init.h>#include <linux/interrupt.h>#include <linux/mc146818rtc.h>#include <linux/param.h>#inclu  
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Copyright (C) 2006 Ralf Baechle (ralf@linux-mips.org) \*/#include <linux/kernel.h>#include <linux/sched.h>#include <linux/cpumask.h>#include <linux/interrupt.h>#include <linux/compiler.h>#includ  
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Copyright (C) 2006 Russ Anderson <rja@sgi.com> \*/#include <linux/types.h>#include <linux/init.h>#include <linux/sched.h>#include

```

<linux/interrupt.h>#include <linux/irq.h>#include <linux/kallsym
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Copyright (C) 2006 S1 Corp. * * Original driver for 2.6.10 pl2303 driver by * Greg Kroah-Hartman (greg@kroah.com) * Changes for 2.6.20 by Harald
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<linux/kernel.h>
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<linux/mtd/partitions.h>#include <linux/mtd/physmap.h>

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<linux/interrupt.h>#include <linux/time.h>#include <l
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access * the chip registers if a DME1737, A8000, or
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<linux/module.h>#include <linux/skbuff.h>#include <linux/netfilter/x_tabl
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<linux/module.h>#include <linux/skbuff.h>#include <net/tcp.h>#include <n
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<linux/mtd/mtd.h>#include <linux/mtd/map.h>#include <linux/of_p
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<linux/cpumfreq.h>#include <linux/clkdev.h>#include <
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<linux/io.h>#include <linux/smc91x.h>#include <linux/irq.h>#i
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<linux/spinlock.h>#include <linux/log2.h>#include <linux/io.h>
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Copyright (C) 2007 Brian Murphy <brian@murphy.dk> */#include <linux/errno.h>#include <linux/init.h>#include <linux/ioport.h>#include
<linux/platform_device.h>#include <linux/serial_8250.h>
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 Copyright (C) 2007 by Ralf Baechle \*/#ifndef \_\_ASM\_SGI\_SEEQ\_H#define \_\_ASM\_SGI\_SEEQ\_H  
 Copyright (C) 2007 by Ralf Baechle \*/#ifndef \_\_ASM\_SGI\_WD\_H#define \_\_ASM\_SGI\_WD\_H  
 Copyright (C) 2007 by Ralf Baechle \*/#ifndef \_\_ASM\_TOPOLOGY\_H#define \_\_ASM\_TOPOLOGY\_H  
 Copyright (C) 2007 by Ralf Baechle \*/#include <linux/clocksource.h>#include <linux/init.h>#include <linux/sched\_clock.h>  
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 implementation.  
 Copyright (C) 2007 by Thomas Bogendoerfer \*/#include <linux/eisa.h>#include <linux/init.h>#include <linux/ioport.h>#include  
 <linux/console.h>#include <linux/screen\_info.h>#include <linux/platfo  
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 Copyright (C) 2007 Cambridge Silicon Radio Ltd.  
 Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#ifndef \_\_WHCD\_H#define \_\_WHCD\_H  
 Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#ifndef \_\_WHCI\_WHCI\_HC\_H#define \_\_WHCI\_WHCI\_HC\_H  
 Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/delay.h>#include <linux/kernel.h>#include <linux/module.h>#include  
 <linux/pci.h>#include <linux/dma-mapping.h>#include <linux/s  
 Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>  
 Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/dma-mapping.h>  
 Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/dma-mapping.h>#include <linux/slab.h>#include  
 <linux/usb.h>  
 Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/export.h>#include "include/umc.h"  
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 Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/gfp.h>#include <linux/dma-mapping.h>  
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 <linux/usb.h>  
 Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/module.h>  
 Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/sysfs.h>#include <linux/workqueue.h>#include  
 <linux/module.h>#include <linux/pci.h>#include "include/u  
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Copyright (c) 2007 Dave Airlie <airlied@linux.ie> \* \* DRM core CRTC related functions  
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Copyright (C) 2007 (davem@davemloft.net) \*/#include <asm/page.h>  
Copyright (C) 2007 David Brownell \*/  
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Thomas Gleixner suggestions.  
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(schwidefsky@de.ibm.com) \* Derived from "include/asm-i386/current.  
Copyright (C) 2007 David S. Miller (davem@davemloft.net) \*/#include <asm/asi.h>  
Copyright (C) 2007 David S. Miller <davem@davemloft.net> \*/#include <linux/kernel.h>#include <linux/export.h>#include <linux/init.h>  
Copyright (C) 2007 David S. Miller (davem@davemloft.net) \*/#include <linux/kernel.h>#include <linux/interrupt.h>#include <linux/slab.h>#include  
<linux/irq.h>  
Copyright (C) 2007 David S. Miller (davem@davemloft.net) \*/#include <linux/kernel.h>#include <linux/pci.h>#include <linux/slab.h>#include  
<linux/init.h>#include <linux/msi.h>#include <linux/exp  
Copyright (C) 2007 David S. Miller <davem@davemloft.net> \*/#include <linux/sched.h>#include <linux/device.h>#include <linux/cpu.h>#include  
<linux/smp.h>#include <linux/percpu.h>#include <linux/  
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<linux/pfn.h>#include <linux/proc\_fs.h>#include <linux/strin  
Copyright (C) 2007 Eurek srl \* Author: Claudio Lanconelli <lanconelli.claudio@eptar.com> \* based on enc28j60.c written by David Anders for 2.4 kernel  
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Copyright (C) 2007 Finn Thain \* \* Converted to DMA API, converted to unified driver model, \* sync'd some routines with mace.c and fixed various  
bugs.  
Copyright (C) 2007 Florian Fainelli <florian@openwrt.org> \*/#include <linux/kernel.h>#include <linux/export.h>#include <linux/init.h>#include  
<linux/ctype.h>#include <linux/string.h>#include <l  
Copyright (c) 2007 Freescale Semiconductor \* \* Based on code by Vitaly Bordug <vbordug@ru.mvista.com> \* pq2\_restart fix by Wade Farnsworth  
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 <linux/unistd.h>#include <linux/interrupt.h>#include <linux  
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 <linux/err.h>#include <linux/slab.h>#include <linux/strin  
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 Copyright (C) 2007 Hewlett-Packard Development Company, L.P. \* Paul Moore <paul@paul-moore.com> \*/#include <linux/init.h>#include  
 <linux/types.h>#include <linux/slab.h>#include <linux/std  
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linux/arch/arm/mach-omap1/board-h2-mmc.c, which is:
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*/#include <linux/module.h>#include <linux/init.h>#include
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Copyright (C) 2007 Jens Axboe <jens.axboe@oracle.com> * * Scatterlist handling helpers.
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Copyright (C) 2007 Julien BLACHE <jb@jblache.org> \* \* This driver triggers SMIs which cause the firmware to change the \* backlight brightness. This is icky in many ways, but it's impractical to

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Copyright (C) 2007 kogiidena \*/#include <linux/io.h>#include <linux/init.h>#include <linux/interrupt.h>#include <linux/platform\_device.h>#include <mach-landisk/mach/iodata\_landisk.h>#include <

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Copyright (C) 2007 Lemote Inc. \* Author: Fuxin Zhang, zhangfx@lemote.com \*/#include <linux/interrupt.h>

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Copyright (C) 2007 Lemote, Inc. \* Author: Fuxin Zhang, zhangfx@lemote.com \*/#include <linux/pci.h>

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<linux/console.h>#include <linux/printk.h>#include <linux/init.h>
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<generated/machtypes.h>
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<linux/serial.h>#include <linux/serial_sci.h>#include <linux/sh_t
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<linux/skbuff.h>#include <linux/ip.h>#include <linux/gfp.h>#i

```

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 <linux/topology.h>#include <linux/node.h>#include <lin  
 Copyright (C) 2007 Paul Mundt \*/#include <linux/kernel.h>  
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<linux/module.h>#include <linux/mutex.h>#include <linux/rcupdate.
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*/
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<linux/kernel.h>#include <linux/ioport.h>#include
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<stdlib.h>#include <string.h>#include <stdarg.h>#include <sys/t
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stream video * frames on DaVinci SoCs (DM6446, DM
  Copyright (C) 2008-2009 Texas Instruments Inc */#ifndef _ISIF_REGS_H#define _ISIF_REGS_H
  Copyright (C) 2008-2009 Texas Instruments Inc */#ifndef _VPFE_TYPES_H#define _VPFE_TYPES_H
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<linux/vmalloc.h>#include <linux/init.h>#include <linux/kernel
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<linux/interrupt.h>#include <linux/smsc911x.h>#include <linux/i  
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 <linux/slab.h>#include <linux/cpu.h>#include <linu  
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 Copyright (C) 2008-2014 Mathieu Desnoyers \*/#include <linux/module.h>#include <linux/mutex.h>#include <linux/types.h>#include  
 <linux/jhash.h>#include <linux/list.h>#include <linux/rcupdate.h>#

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<linux/skbuff.h>#include <linux/netfilter/x\_tables.h>#inclu  
Copyright (c) 2008 Adrian-Ken Rueegsegger <ken@codelabs.ch> \*/#include <crypto/internal/hash.h>#include <linux/init.h>#include  
<linux/module.h>#include <linux/mm.h>#include <linux/types.h>#incl  
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<linux/cs5535.h>#include <asm/delay.h>  
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Copyright (C) 2008 Atmel Corporation */#ifndef AT_HDMAC_REGS_H#define AT_HDMAC_REGS_H
Copyright (C) 2008 Atmel Corporation */#include <linux/moduleparam.h>#include <linux/export.h>#include <linux/debugfs.h>#include
<linux/fs.h>#include <linux/seq_file.h>#include <linux/slab.h>#
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Copyright (c) 2008 by David Brownell */#include <linux/kernel.h>#include <linux/slab.h>#include <linux/input.h>#include <linux/input/sparse-
keymap.h>#include <linux/platform_device.h>#include <
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Copyright (C) 2008 Cambridge Silicon Radio Ltd. */#include <linux/kernel.h>#include <linux/export.h>
Copyright (C) 2008 Cambridge Silicon Radio Ltd. */#include <linux/kernel.h>#include <linux/random.h>#include <linux/slab.h>
Copyright (C) 2008 Cambridge Silicon Radio Ltd. */#include <linux/kernel.h>#include <linux/slab.h>#include <linux/random.h>#include
<linux/export.h>
Copyright (C) 2008 Cambridge Silicon Radio Ltd. */#include <linux/kernel.h>#include <linux/slab.h>#include "uwb.h"
Copyright (C) 2008 Cambridge Silicon Radio Ltd. */#include <linux/kthread.h>#include <linux/freezer.h>#include <linux/slab.h>#include
<linux/delay.h>#include "uwb-internal.h"
Copyright (C) 2008 Cambridge Silicon Radio Ltd. */#include <linux/slab.h>#include <linux/kernel.h>#include <linux/debugfs.h>#include
<linux/seq_file.h>#include <linux/export.h>
Copyright (C) 2008 Cambridge Silicon Radio Ltd. */#include "wusbhc.h"
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Copyright (C) 2008 Dmitri Vorobiev \*/#include <linux/cpu.h>#include <linux/init.h>#include <linux/sched.h>#include <linux/ioport.h>#include

<linux/irq.h>#include <linux/of\_fdt.h>#include <linux

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<linux/wm97xx.h>#include <linux/delay.h>#include <linux/spinlock.h>
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Copyright (c) 2008 Dmitry Eremin-Solenikov */#include <linux/kernel.h>#include <linux/init.h>#include <linux/io.h>#include
<linux/gpio/machine.h>#include <linux/platform_device.h>#include <linux/
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Copyright (C) 2008 Embedded Alley Solutions, Inc. */#include <linux/clk.h>#include <linux/delay.h>#include <linux/slab.h>#include
<linux/sched/task_stack.h>#include <linux/interrupt.h>#include
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<linux/kernel.h>#include <linux/leds.h>#include <linux/m
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drivers/media/video/omap/sensor_ov6650.c
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<linux/i2c.h>#include <linux/delay.h>#include <linux/log2.h>#incl
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Copyright (C) 2008 Hans Verkuil <hverkuil@xs4all.nl> ** These routines maintain argument size conversion between 32bit and 64bit * ioctls.
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Copyright (C) 2008 Harrison Metzger <harrisonmetz@gmail.com> * Based on usbled.c by Greg Kroah-Hartman (greg@kroah.com) */
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which is
Copyright (C) 2008 Hewlett-Packard Development Company, L.P. * Bjorn Helgaas <bjorn.helgaas@hp.com> */#include <linux/kernel.h>#include
<linux/acpi.h>#include <linux/pci.h>#include <linux/pnp.h>
Copyright (C) 2008 Hewlett-Packard Development Company, L.P. * David Altobelli <david.altobelli@hp.com> */#ifndef __HPILO_H#define __HPILO_H
Copyright (C) 2008 Hewlett-Packard Development Company, L.P. * David Altobelli <david.altobelli@hpe.com> */#include <linux/kernel.h>#include
<linux/types.h>#include <linux/module.h>#include <lin
Copyright (c) 2008 H Hartley Sweeten <hsweeten@visionengravers.com> ** Based on the pxa27x matrix keypad controller by Rodolfo Giometti.
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Copyright (C) 2008 IBM Corporation * Author: Mimi Zohar <zohar@us.ibm.com> ** File: integrity_audit.c * Audit calls for the integrity subsystem
Copyright (C) 2008 IBM Corporation * Author: Mimi Zohar <zohar@us.ibm.com> ** ima_policy.c * - initialize default measure policy rules
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Copyright (C) 2008 IBM Corporation * 'find_last_bit' is written by Rusty Russell <rusty@rustcorp.com.au> * (Inspired by David Howell's find_next_bit
implementation) ** Rewritten by Yury Norov <yu
Copyright (C) 2008 Igor Liplianin September, 9th 2008 Fixed locking on high symbol rates (>30000). Implement MPEG initialization
parameter. January, 17th 2009 Fill set_voltage with ac
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Copyright (C) 2008 Igor M. Liplianin (liplianin@me.by)*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/module.h>#include
<linux/string.h>#include <linux/slab.h>#include <linux/
Copyright (C) 2008 Igor M. Liplianin <liplianin@me.by> Removed stb6000 specific tuner code and revised some procedures.
Copyright (C) 2008 Igor M. Liplianin <liplianin@me.by> Removed stb6000 specific tuner code and revised some procedures. 2010-09-01
Josef Pavlik <josef@pavlik.it> Fixed diseqc_msg, diseqc_burst
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Copyright (C) 2008 Instituto Nokia de Tecnologia * Contact: Eduardo Valentin <eduardo.valentin@indt.org.br> * * Multichannel mode not supported.
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* interface for 64-bit kernels. * Authors: Adri
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Copyright (C) 2008, Intel Corp. * Author: Huang Ying <ying.huang@intel.com> * Vinodh Gopal <vinodh.gopal@intel.com> *
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these functions
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Copyright (C) 2008 Intel Corporation * Authors: Austin Zhang <austin_zhang@linux.intel.com> * Kent Liu <kent.liu@intel.com> */#include
<linux/init.h>#include <linux/module.h>#include <l
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Copyright (C) 2008 Intel Corporation <linux-wimax@intel.com> * Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com> * * This implements a simple
synchronous call to reset a WiMAX device.
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Linux Kernel rkill subsystem so that the * driver
Copyright (C) 2008 Intel Corp. Shaohua Li <shaohua.li@intel.com> * * See asm-generic/syscall.h for descriptions of what we must do here.
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Copyright (C) 2008 Ira W. Snyder <iws@ovro.caltech.edu> * * This driver is based on the ds1621 and ina209 drivers.
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 engineered by Jim Paris <jim@jtan.com> \* https://jim.sh/s  
 Copyright (C) 2008 Jiri Hladky <hladky\_dot\_jiri\_at\_gmail\_dot\_com> \* \* parisc implementation:  
 Copyright (C) 2008 Jiri Hladky <hladky\_dot\_jiri\_at\_gmail\_dot\_com> \* \* The \_\_delay function must \_NOT\_ be inlined as its execution time \*  
 depends wildly on alignment on many x86 processors. Th  
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 Copyright (C) 2008 Jochen Friedrich <jochen@scram.de>  
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 Copyright (c) 2008 Jonathan Cameron \*/#ifdef \_INDUSTRIAL\_IO\_H\_#define \_INDUSTRIAL\_IO\_H\_  
 Copyright (c) 2008 Jonathan Cameron \*/#include <linux/irq.h>#include <linux/module.h>#include <linux/atomic.h>  
 Copyright (c) 2008 Jonathan Cameron \*/#include <string.h>#include <stdlib.h>#include <stdio.h>#include <stdint.h>#include <dirent.h>#include  
 <errno.h>#include <ctype.h>#include "iio\_utils.h"  
 Copyright (c) 2008 Jonathan Cameron \* \* These definitions are meant for use only within the IIO core, not individual \* drivers.  
 Copyright (c) 2008 Jonathan Cameron \* \* This program is primarily intended as an example application. \* Reads the current buffer setup from sysfs and  
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Copyright (c) 2008 Magnus Damm \*/#ifndef \_VIDEobuf\_DMA\_CONTIG\_H#define \_VIDEobuf\_DMA\_CONTIG\_H

Copyright (C) 2008 Magnus Damm \*/#include <linux/clkdev.h>#include <linux/dma-mapping.h>#include <linux/init.h>#include <linux/platform\_device.h>#include <linux/interrupt.h>#include <linux/inpu

Copyright (C) 2008 Magnus Damm \*/#include <linux/init.h>#include <linux/irq.h>#include <linux/io.h>#include <mach/highlander.h>

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Copyright (c) 2008 Marc Pignat <marc.pignat@hevs.ch> \* \* The adcxx4s communicates with a host processor via an SPI/Microwire Bus \* interface. This driver supports the whole family of devices with n

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Copyright (C) 2008 Mark Nelson, IBM Corp. \*/#include <asm/page.h>#include <asm/processor.h>#include <asm/ppc\_asm.h>#include <asm/asm-offsets.h>#include <asm/export.h>#include <asm/feature-fixup

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Copyright (C) 2008 Martin Michlmayr <tbm@cyrius.com> \*/#include <linux/gpio.h>#include <linux/kernel.h>#include <linux/init.h>#include <linux/platform\_device.h>#include <linux/pci.h>#include <

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<linux/clk.h>#include <linux/etherdevice.h>#include <linux/clk.h>#include <linux/etherdevice.h>#include <linux/module.h>#include
Copyright (C) 2008 Maxime Bizon <mbizon@freebox.fr> * * Serial driver for BCM63xx integrated UART.
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Copyright (C) 2008 MaxLinear */static u16 MXL5005_RegisterInit(struct dvb_frontend *fe)
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actually ENABLE or DISABLE the thing * once af
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<mach/sdk7780.h>
Copyright (C) 2008 Nicholas Beck <nbeck@mpc-data.co.uk> */#include <linux/init.h>#include <linux/types.h>#include
<linux/platform_device.h>#include <linux/ata_platform.h>#include <asm/machvec.h>
Copyright (C) 2008 Nicholas Beck <nbeck@mpc-data.co.uk> */#include <linux/sh_intc.h>#include <asm/addrspace.h>
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Copyright (C) 2008 Nobuhiro Iwamatsu <iwamatsu.nobuhiro@renesas.com> */#include <asm/addrspace.h>
Copyright (C) 2008 Nobuhiro Iwamatsu <iwamatsu.nobuhiro@renesas.com> */#include <linux/init.h>#include <linux/platform_device.h>#include
<linux/interrupt.h>#include <linux/input.h>#include <linu
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<linux/string.h>#include <linux/delay.h>#include <linux/mmc/host.h>#
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Machek <pavel@ucw.cz>
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<linux/netdevice.h>#include <linux/etherdevice.h>#include
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<linux/l1c.h>#include <linux/slab.h>#include <linu
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<net/mrp.h>#include "vlan.h"
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as10x_bus_adapter_t;struct as102_dev_t;
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<linux/platform\_device.h>#include <linux/regulator/driver.h>#include <linux/sla  
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 <linux/spinlock.h>#include <linux/irqflags.h>#include <linux/uaccess.h>#  
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 <linux/trace\_clock.h>#include <linux/sched/clock.h>#include <linux  
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<linux/interrupt.h>#include <linux/init.h>#in
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<linux/interrupt.h>#include <linux/mc146818rtc.h>#include <linux/irq.h>
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<linux/smc91x.h>#include <linux/mtd/physmap.h>#include <linux/dela
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<linux/delay.h>#include <linux/input.h>#inclu
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 Copyright (c) 2009-2010 Analog Devices, Inc. \* Author: Michael Hennerich <michael.hennerich@analog.com> \* \* DEVID #Wipers  
 #Positions Resistor Options (kOhm) \* AD5258 1 64 1, 10, 50, 100 \* AD  
 Copyright (C) 2009, 2010 ARM Limited \* \* Author: Will Deacon <will.deacon@arm.com>  
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<asm/hw_breakpoint.h>
Copyright (C) 2009 - 2010 Paul Mundt */#include <linux/init.h>#include <linux/perf_event.h>#include <linux/sched/signal.h>#include
<linux/hw_breakpoint.h>#include <linux/percpu.h>#include <lin
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<linux/perf_event.h>#include <asm/processor.h>
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*/#include <linux/clk.h>#include <linux/delay.h>#include <
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 Copyright (C) 2009 Andy Walls <awalls@md.metrocast.net> \* \* However, the cx23885\_input\_{init,fini} functions contained herein are \* derived from  
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 Copyright (C) 2009 ARM Limited \* \_\_user\_\* functions adapted from include/asm/uaccess.h \* \* Implements emulation of the SWP/SWPB instructions  
 using load-exclusive and \* store-exclusive for proce  
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 "util/debug.h"#include "util/dso.h"#include <subcmd/page  
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 Copyright (C) 2009 Atsushi Nemoto \*/#ifndef TXX9DMAC\_H#define TXX9DMAC\_H  
 Copyright (C) 2009 Atsushi Nemoto \*/#include <linux/dma-mapping.h>#include <linux/init.h>#include <linux/interrupt.h>#include  
 <linux/io.h>#include <linux/module.h>#include <linux/platform\_devic  
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<linux/mod_devicetable.h>#include <linux/mmc/sdio_func.h>#include <linux/mmc/
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optimum numerator/denominator pairs for \* pll config  
Copyright (C) 2009 Eric Paris <Red Hat Inc> \* dnotify was largely rewritten to use the new fsnotify infrastructure \*/#include <linux/fs.h>#include  
<linux/module.h>#include <linux/sched.h>#include  
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<linux/platform_device.h>
Copyright (C) 2009 Gabor Juhos <juhosg@openwrt.org> */#include <linux/kernel.h>#include <linux/module.h>#include <linux/of.h>#include
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<linux/platform_device.h>#include <linux/spi/spi.h>
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<linux/pci.h>#include <linux/i2c.h>#include <linux/inter
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mapping.h>#include <linux/raid/pq.h>#include <linux/async
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Copyright (C) 2009 Lemote Inc. \* Author: Hu Hongbing <huhb@lemote.com> \* Wu Zhangjin <wuzhangjin@gmail.com> \*/#include <asm/sections.h>#include <asm/fpu.h>#include <asm/dsp.h>

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Copyright (c) 2009 Lemote Inc. \* Author: Wu Zhangjin, wuzhangjin@gmail.com \*/#include <linux/serial\_reg.h>#include <asm/setup.h>

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Copyright (C) 2009 Lemote Inc. \* Author: Wu Zhangjin <wuzhangjin@gmail.com> \*/#include <linux/types.h>#include <linux/pci.h>#include <linux/kernel.h>#include <linux/export.h>

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```

<asm/clock.h>
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<linux/sh_clk.h>#include <asm/clock.h>#include <cpu/sh77
Copyright (C) 2009 Magnus Damm */#include <linux/init.h>#include <linux/kernel.h>#include <linux/io.h>#include <linux/clk.h>#include
<linux/clkdev.h>#include <linux/sh_clk.h>#include <asm/cloc
Copyright (C) 2009 Magnus Damm */#include <linux/init.h>#include <linux/kernel.h>#include <linux/io.h>#include <linux/suspend.h>#include
<asm/suspend.h>#include <linux/uaccess.h>#include <asm/
Copyright (C) 2009 Magnus Damm */#include <linux/init.h>#include <linux/kernel.h>#include <linux/io.h>#include <linux/suspend.h>#include
<linux/cpuidle.h>#include <linux/export.h>#include <asm
Copyright (C) 2009 Magnus Damm */#include <linux/platform_device.h>#include <linux/init.h>#include <linux/serial.h>#include
<linux/serial_sci.h>#include <linux/sh_timer.h>#include <linux/sh_in
Copyright (C) 2009 Magnus Damm */#include <linux/sys.h>#include <linux/errno.h>#include <linux/linkage.h>#include <asm/asm-offsets.h>#include
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<linux/module.h>#include <linux/init.h>#include <linux/err.h>
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Copyright (C) 2009 Marvell International Ltd. */#include <linux/clk/mmp.h>#include <linux/module.h>#include <linux/kernel.h>#include
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<linux/mtd/physmap.h>#include <linux/regulator/fixd.h>#include <linux/re
Copyright (C) 2009 Paul Mundt */#include <linux/init.h>#include <linux/platform_device.h>#include <linux/sm501.h>#include <linux/sm501-
regs.h>#include <linux/fb.h>#include <linux/mtd/physmap.h>
Copyright (C) 2009 Paul Mundt */#include <linux/kernel.h>#include <linux/init.h>#include <linux/io.h>#include <linux/irq.h>#include
<linux/perf_event.h>#include <asm/processor.h>
Copyright (C) 2009 Paul Mundt */#include <linux/kernel.h>#include <linux/module.h>#include <asm/dwarf.h>
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<asm/unwinder.h>#include <asm/ptrace.h>
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<linux/serial_sci.h>#include <linux/io.h>#include <linux/mm.h>#inclu
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Copyright (C) 2009 Provigent Ltd. */#include <linux/acpi.h>#include <linux/clk-provider.h>#include <linux/clk.h>#include <linux/delay.h>#include
<linux/dmi.h>#include <linux/err.h>#include <li
Copyright (C) 2009 Provigent Ltd. */#include <linux/clk.h>#include <linux/delay.h>#include <linux/export.h>#include <linux/errno.h>#include
<linux/err.h>#include <linux/i2c.h>#include <linux/i
Copyright (C) 2009 Provigent Ltd. */#include <linux/delay.h>#include <linux/err.h>#include <linux/errno.h>#include <linux/export.h>#include
<linux/gpio/consumer.h>#include <linux/i2c.h>#includ
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Copyright (C) 2009 Red Hat, Inc. * Author: Michael S. Tsirkin <mst@redhat.com> ** Since the driver does not declare any device ids, you must allocate
* id and bind the device to the driver yourse
Copyright (C) 2009 Red Hat, Inc. * Author: Michael S. Tsirkin <mst@redhat.com> ** test virtio server in host kernel.
Copyright (C) 2009 Red Hat, Inc. * Author: Michael S. Tsirkin <mst@redhat.com> ** virtio-net server in host kernel.
Copyright (C) 2009 Red Hat, Inc., Ingo Molnar <mingo@redhat.com> ** Implements 3 trace clock variants, with differing scalability/precision *
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__SOUND_FSI_H
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Copyright (C) 2009 Steven Rostedt <rsostedt@redhat.com> \*/#include <linux/ring\_buffer.h>#include <linux/completion.h>#include <linux/kthread.h>#include <uapi/linux/sched/types.h>#include <linux/  
Copyright (C) 2009 Steven Rostedt <rsostedt@redhat.com> \*/#include <linux/stringify.h>#include <linux/kallsyms.h>#include <linux/seq\_file.h>#include <linux/uaccess.h>#include <linux/trace.h>#i  
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 Goede <hdegoede@redhat.com> and \* Thomas Kaiser <tho  
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 Copyright (C) 2009 Theodore Kilgore \* \* Sportscam DV15 support and control settings are  
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 <asm/cache.h>#include <asm/thread\_info.h>  
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 <linux/moduleparam.h>#include <linux/mount.h>#include <linux/namei.h>#  
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 Copyright (C) 2009 Uwe Kleine-Koenig <u.kleine-koenig@pengutronix.de> \* for Pengutronix \*/#include <linux/export.h>#include <linux/trace.h>  
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from MIPS.
Copyright (C) 2009 Wind River Systems, Inc. * Updated for supporting PPC405EX on Kilauea. * Tiejun Chen <tiejun.chen@windriver.com> */#include
<stddef.h>#include "types.h"#include "string.h"
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<linux/init.h>#include <linux/irqflags.h>#include <linux/notifier
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<linux/notifier.h>#include <linux/prefetch.h
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<linux/notifier.h>#include <linux/ptrace.h>
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<linux/slab.h>#include <linux/interrupt.h>#in
Copyright (C) 2009 Wind River Systems, * written by Ralf Baechle <ralf@linux-mips.org> */#include <linux/module.h>#include <linux/init.h>#include
<linux/slab.h>#include <linux/io.h>#include <
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can trigger an interrupt. See datasheet for more * d
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pci_register_driver(&ddb_pci_driver); if (stat < 0) ddb_exit_ddbridge(0, stat);
Copyright (c) 2010-2010 Analog Devices Inc. */
Copyright (c) 2010-2010 Analog Devices Inc. */#include <linux/types.h>#include <linux/mutex.h>#include <linux/device.h>#include
<linux/spi/spi.h>#include <linux/slab.h>#include <linux/sysfs.h>
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Blatter and Daniel Melander, the latter of which was  
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Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> \* \* Architecture specific procedures for creating, accessing and \* interpreting the device  
tree.  
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Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> \* et al. \*/ #ifndef \_\_ASM\_OPENRISC\_ELF\_H #define \_\_ASM\_OPENRISC\_ELF\_H  
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Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> \* \* Here we handle the break vectors not used by the system call \* mechanism, as well as  
some general stack/register dumping \* things.

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<crypto/authenc.h>#include <crypto/internal/des.h>  
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Mixer driver  
Copyright (C) 2010-2011 ST-Ericsson \* \* Implements u8500 semaphore handling for protocol 1, no interrupts.  
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<linux/interrupt.h>#include <linux/io.h>#include <
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to use the linux CEC framework: *
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Copyright (C) 2010-2012 Matthias Fuchs <matthias.fuchs@esd.eu>, esd gmbh */#include <linux/signal.h>#include <linux/slab.h>#include
<linux/module.h>#include <linux/netdevice.h>#include <linux/us
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jt8ev1.c by Vimarsh Zutshi * Based on smia-sensor.
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Copyright (C) 2010 - 2012 Samsung Electronics Co., Ltd.*/#define pr_fmt(fmt) "%s:%d " fmt, __func__, __LINE__

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Copyright (C) 2010-2012 Stephane Grosjean <s.grosjean@peak-system.com> \*\* CAN driver for PEAK-System PCAN-PC Card \* Derived from the PCAN  
project file driver/src/pcan\_pccard.c  
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Copyright (C) 2010-2012 Synopsys, Inc. (www.synopsys.com) \*\* vineetg: July 10th 2012 \* -Decoupled the driver from arch/arc \* +Using  
platform\_get\_resource() for irq/membase (thx to bfin\_uart.c  
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<linux/debugfs.h>#include <linux/random.h>#includ  
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view timeout and pretimeout are \* defined differe  
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Copyright (C) 2010 Alexey Charkov <alchark@gmail.com> * * Based on skeletonfb.c and pxaafb.c
Copyright (C) 2010 Alexey Charkov <alchark@gmail.com> * * Moved from arch/arm/mach-vt8500/include/mach/debug-macro.S * * Minor changes for
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Copyright (C) 2010 Andy Walls <awalls@md.metrocast.net> */
Copyright (C) 2010 Andy Walls <awalls@md.metrocast.net> * * CX23888 DIF support for the HVR1850
Copyright (C) 2010 Antti Palosaari <crope@iki.fi> */
Copyright (C) 2010 Antti Sepp
Copyright (c) 2010, Applied Micro Circuits Corporation * Author: Rupjyoti Sarmah <rsarmah@apm.com> */#include <linux/kernel.h>#include
<linux/init.h>#include <asm/pci-bridge.h>#include <asm/ppc4
Copyright (c) 2010, Applied Micro Circuits Corporation * Authors: Tirumala R Marri <tmarri@apm.com> * Feng Kan <fkan@apm.com> */
Copyright (C) 2010, Applied Micro Circuits Corporation * Victor Gallardo (vgallardo@apm.com) * * Based on arch/powerpc/platforms/44x/idle.c:
Copyright (C) 2010, ARM Ltd., Will Deacon <will.deacon@arm.com> * * Based on the previous xscale OProfile code.
Copyright (C) 2010 ARM Ltd., Will Deacon <will.deacon@arm.com> */#define pr_fmt(fmt) "hw perfevents: " fmt
Copyright (C) 2010 ARM Ltd., Will Deacon <will.deacon@arm.com> * * This code is based on the sparc64 perf event code, which is in turn based * on
the x86 code.
Copyright (C) 2010 ARM Ltd. * Written by Catalin Marinas <catalin.marinas@arm.com> */
Copyright (C) 2010 ARM Ltd. * Written by Catalin Marinas <catalin.marinas@arm.com> */#include <linux/bug.h>#include <linux/smp.h>#include
<asm/outercache.h>
Copyright (C) 2010, Arnaldo Carvalho de Melo <acme@redhat.com> */#include <sys/types.h>#include <sys/time.h>#include <time.h>#include
<dirent.h>#include <errno.h>#include <unistd.h>#include "b
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Copyright (c) 2010 Arnaud Patard <apatard@mandriva.com> */#ifndef _CS42L51_H#define _CS42L51_H
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Copyright (C) 2010 Benjamin Herrenschmidt <benh@kernel.crashing.org>
Copyright (C) 2010 Bernhard Loos <bernhardloos@googlemail.com> */#include <linux/module.h>#include <linux/kernel.h>#include
<linux/platform_device.h>#include <linux/pci.h>#include <linux/mfd/cor
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Copyright (c) 2010 Broadcom Corporation
Copyright (c) 2010 Broadcom Corporation */
Copyright (C) 2010 Broadcom Corporation. */
Copyright (c) 2010 Broadcom Corporation */#include <linux/kernel.h>#include <linux/delay.h>#include <linux/bitops.h>
Copyright (c) 2010 Broadcom Corporation */#include <linux/types.h>#include <linux/module.h>#include <linux/if_ether.h>#include
<linux/spinlock.h>#include <linux/skbuff.h>#include <linux/netdevi
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Copyright (C) 2010 Cavium Networks, Inc. \*/#include <linux/dma-direct.h>#include <linux/memblock.h>#include <linux/swiotlb.h>#include  
<linux/types.h>#include <linux/init.h>#include <linux/mm.h>  
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Copyright (c) 2010 Cisco Systems, Inc. \*/#ifndef \_\_TCM\_FC\_H\_\_#define \_\_TCM\_FC\_H\_\_  
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Copyright (C) 2010 CompuLab Ltd. \* \* 2010-13-07: Igor Grinberg <grinberg@compulab.co.il> \* initial version: pxa310 USB Host mode support  
Copyright (c) 2010 CompuLab Ltd \* Author: Denis Turishev <denis@compulab.co.il> \*/  
Copyright (C) 2010 Compulab Ltd. \* Author: Mike Rapoport <mike@compulab.co.il> \* \* Based on da903x  
Copyright (c) 2010, CompuLab, Ltd. \* Author: Mike Rapoport <mike@compulab.co.il> \* \* Based on NVIDIA PCIe driver  
Copyright (C) 2010 CompuLab Ltd. \* \* Igor Grinberg <grinberg@compulab.co.il>  
Copyright (C) 2010 CompuLab, Ltd. \* Konstantin Sinyuk <kostyas@compulab.co.il> \* \* Based on Marvell DB-MV88AP510-BP Development Board Setup  
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of Code 2010 \* program. Jeremy Fitzhardinge from Ci  
Copyright (c) 2010 Daniel Mack <daniel@caiaq.de>  
Copyright (C) 2010 Dan Williams <dcbw@redhat.com> \* \* Devices listed here usually provide a CDC ACM port on which normal modem \* AT commands  
and PPP can be used. But when that port is in-use by PP  
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Copyright (C) 2010 Devin Heitmueller <dheitmueller@kernellabs.com> \*/#include <linux/kernel.h>#include <linux/errno.h>#include  
<linux/ctype.h>#include <linux/delay.h>#include <linux/firmware.h>  
Copyright (C) 2010 Devin Heitmueller <dheitmueller@kernellabs.com> \*/#include <linux/kernel.h>#include <linux/errno.h>#include  
<linux/slab.h>#include <linux/mm.h>#include <linux/usb.h>  
Copyright (C) 2010 Devin Heitmueller <dheitmueller@kernellabs.com> \*/#include <linux/kernel.h>#include <linux/errno.h>#include  
<linux/slab.h>#include <linux/module.h>#include <linux/mm.h>#inclu  
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http://github.com/projectgus/kernel\_wm8505/blob/wm8505\_2.6.29/ \* drivers/video/wmt/r  
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<linux/debugfs.h>#include <linux/ieee80211.h>#include  
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 Copyright (C) 2010 Gra \* \* based on twl4030\_bci\_battery.c by TI  
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 Copyright (C) 2010 GUAN Xue-tao \*/#include <linux/kernel.h>#include <linux/errno.h>#include <linux/swap.h>#include <linux/init.h>#include  
 <linux/memblock.h>#include <linux/mman.h>#include <lin  
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 Copyright (C) 2010 Guennadi Liakhovetski <g.liakhovetski@gmx.de> \*/#ifndef CPU\_DMA\_REGISTER\_H#define CPU\_DMA\_REGISTER\_H  
 Copyright (C) 2010 Guennadi Liakhovetski <g.liakhovetski@gmx.de> \*/#ifndef SH\_DMA\_H#define SH\_DMA\_H  
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 Copyright (C) 2010, Guennadi Liakhovetski <g.liakhovetski@gmx.de> \*/#include <linux/clk.h>#include <linux/delay.h>#include <linux/err.h>#include  
 <linux/hdmi.h>#include <linux/irq.h>#include <li  
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 Copyright (c) 2010 Herbert Xu <herbert@gondor.apana.org.au> \* \* The following concept of the memory management is used: \*  
 Copyright (c) 2010 H Hartley Sweeten <hsweeten@visionengravers.com> \* \* This driver controls the pulse width modulated brightness control output, \*  
 BRIGHT, on the Cirrus EP9307, EP9312, and EP9315  
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 <linux/regulator/fixd.h>#include <linux/regulator/machine.h>#include <l  
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 \* notified via SCI, mainly the corrected errors.  
 Copyright (C) 2010, Intel Corp. \* Author: Huang Ying <ying.huang@intel.com> \* \* CPER is the format used to describe platform hardware error by \*

various tables, such as ERST, BERT and HEST etc.

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Copyright (C) 2010 John Crispin <john@phrozen.org> \*/#include <linux/io.h>#include <linux/export.h>#include <linux/init.h>#include <linux/kernel.h>#include <linux/types.h>#include <linux/clock.h>

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 Copyright (c) 2010 Katalix Systems Ltd \*/  
 Copyright (c) 2010 Kees Cook (kees.cook@canonical.com) \* \* This is a common code for verification whether CPU supports \* long mode and SSE or  
 not. It is not called directly instead this \* file i  
 Copyright (C) 2010 Kent Overstreet <kent.overstreet@gmail.com> \* \* Uses a block device as cache for other block devices; optimized for SSDs. \* All  
 allocation is done in buckets, which should match  
 Copyright (C) 2010 LaCie \* \* Author: Simon Guinot <sguinot@lacie.com>  
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 Copyright (C) 2010, Lars-Peter Clausen <lars@metafoo.de> \*/  
 Copyright (C) 2010, Lars-Peter Clausen <lars@metafoo.de> \* Driver for chargers which report their online status through a GPIO pin \*/  
 Copyright (C) 2010, Lars-Peter Clausen <lars@metafoo.de> \* JZ4740 platform PWM support \* \* Limitations:  
 Copyright (C) 2010, Lars-Peter Clausen <lars@metafoo.de> \* JZ4740 platform timer support \*/  
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 Copyright (C) 2010, Lars-Peter Clausen <lars@metafoo.de> \* JZ4740 SoC prom code \*/  
 Copyright (C) 2010, Lars-Peter Clausen <lars@metafoo.de> \* JZ7420/JZ4740 DMA definitions \*/  
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 Copyright (C) 2010, Laurent Pinchart <laurent.pinchart@ideasonboard.com>  
 Copyright (C) 2010, Laurent Pinchart <laurent.pinchart@ideasonboard.com> \* \* Based on the MT9M001 driver,  
 Copyright (c) 2010 LG Electronics \* Chan Jeong <chan.jeong@lge.com> \* \* lzo\_wrapper.c  
 Copyright (C) 2010 Linus Torvalds \*/  
 Copyright (C) 2010 Linus Walleij for ST-Ericsson \*/#include <linux/init.h>#include <linux/interrupt.h>#include <linux/irq.h>#include  
 <linux/io.h>#include <linux/clockchips.h>#include <linux/clo  
 Copyright (C) 2010 Linus Walleij \* \* MMC host class device management  
 Copyright (c) 2010 Linux-iSCSI.org  
 Copyright (C) 2010 Loongson Inc. \* Institute of Computing Technology \* Author: Xiang Gao, gaoxiang@ict.ac.cn \* Huacai Chen,  
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 Copyright (C) 2010 Marcelo Roberto Jimenez <mroberto@cpti.cetuc.puc-rio.br> \*/#ifndef \_\_ASM\_ARCH\_NANOENGINE\_H#define  
 \_\_ASM\_ARCH\_NANOENGINE\_H  
 Copyright (C) 2010 Marcelo Roberto Jimenez <mroberto@cpti.cetuc.puc-rio.br> \*/#include <linux/kernel.h>#include <linux/irq.h>#include  
 <linux/pci.h>  
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 Copyright (C) 2010 Marc Zyngier <maz@misterjones.org> \* \* based on fsg-setup.c:  
 Copyright (C) 2010 Marc Zyngier <maz@misterjones.org> \* \* based on ixdp425-pci.c:  
 Copyright (C) 2010 Marek Vasut <marek.vasut@gmail.com>  
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 Copyright (C) 2010 \* Marek Vasut <marek.vasut@gmail.com> \*/  
 Copyright (C) 2010 \* Marek Vasut <marek.vasut@gmail.com> \*/#ifndef \_\_INCLUDE\_MACH\_PALM27X\_\_#define \_\_INCLUDE\_MACH\_PALM27X\_\_  
 Copyright (C) 2010 \* Marek Vasut <marek.vasut@gmail.com> \* Pavel Revak <palo@bielyvlk.sk> \*/  
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 Copyright (C) 2010 Matthias Fuchs <matthias.fuchs@esd.eu>, esd gmbh  
 Copyright (C) 2010 Matthias Fuchs <matthias.fuchs@esd.eu>, esd gmbh \* \* Known issues:  
 Copyright (C) 2010 Mauro Carvalho Chehab \*/  
 Copyright (c) 2010 Mauro Carvalho Chehab <mchehab@kernel.org>  
 Copyright (c) 2010 - Mauro Carvalho Chehab \* - Ported the driver to use rc-core \* - IR raw event decoding is now done at rc-core \* - Code almost re-written \*/  
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 Copyright (C) 2010 Maurus Cuelenaere \*/  
 Copyright (C) 2010 - Maxim Levitsky \* driver for Ricoh memstick readers \*/  
 Copyright (C) 2010 Maxim Levitsky <maximlevitsky@gmail.com> \*/#include <linux/spinlock.h>  
 Copyright (C) 2010 Maxim Levitsky <maximlevitsky@gmail.com> \* \* Special thanks to:  
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 Copyright (C) 2010 Michael Hennerich, Analog Devices Inc. \*/  
 Copyright (C) 2010 Michael Krufky <mkrufky@linuxtv.org> \*/  
 Copyright (C) 2010 Michael Neuling IBM Corporation \* \* Driver for the pseries hardware RNG for POWER7+ and above  
 Copyright (c) 2010 Michael Poole <mdpoole@troilus.org>  
 Copyright (C) 2010 Michal Simek <monstr@monstr.eu>  
 Copyright (c) 2010, Microsoft Corporation.  
 Copyright (c) 2010, Microsoft Corporation. \* \* Authors:  
 Copyright (C) 2010 Miguel Gaio <miguel.gaio@efixo.com> \*/  
 Copyright (c) 2010 Mika Laitio <lamikr@pilppa.org> \* \* This driver will read and write the value of 4 counters to w1\_slave file in \* sys filesystem. \*  
 Inspired by the w1\_therm and w1\_ds2431 driver  
 Copyright (c) 2010 Mika Westerberg \* \* Based on s3c-ac97 ASoC driver by Jaswinder Singh.  
 Copyright (c) 2010 Mika Westerberg \* \* Based on snappercl15 machine driver by Ryan Mallon.  
 Copyright (C) 2010 Mike Christie \*/  
 Copyright (C) 2010 Mike Christie \*/#ifndef \_ISCSI\_BOOT\_SYSFS\_#define \_ISCSI\_BOOT\_SYSFS\_  
 Copyright (C) 2010 MIPS Technologies, Inc.  
 Copyright (C) 2010 MIPS Technologies, Inc. \* Author: Deng-Cheng Zhu \*/  
 Copyright (C) 2010 MIPS Technologies, Inc. \* Author: Deng-Cheng Zhu \* \* This code is based on the implementation for ARM, which is in turn \* based on the sparc64 perf event code and the x86 code.  
 Copyright (C) 2010 Nathan Fontenot  
 Copyright (C) 2010 NEC corporation \*/#include <linux/kernel.h>#include <linux/gfp.h>#include <linux/mm.h>#include <linux/mutex.h>#include "avc.h"#include "services.h"  
 Copyright (C) 2010 NetUP Inc.  
 Copyright (C) 2010 Nicholas A. Bellinger <nab@kernel.org>  
 Copyright (C) 2010 Nicholas A. Bellinger <nab@kernel.org> \* \* Authors: Bryant G. Ly <bryantly@linux.vnet.ibm.com> \* Authors: Michael Cyr <mikecyr@linux.vnet.ibm.com>  
 Copyright (c) 2010 Nikolai Kondrashov \*/  
 Copyright (C) 2010 NISHIMOTO Hiroki \*/#include <linux/io.h>#include <linux/kernel.h>#include <linux/module.h>#include <linux/pinctrl/pinconf-generic.h>#include <linux/regulator/driver.h>#include  
 Copyright (C) 2010 Nobuhiro Iwamatsu \*/#include <linux/kernel.h>#include <linux/types.h>#include <linux/init.h>#include <linux/delay.h>#include <linux/pci.h>#include <linux/sh\_intc.h>#include  
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 Copyright (C) 2010 Nokia Corporation. \* Author: Mika Westerberg \* \* This code is taken from arch/x86/kernel/crash\_dump\_64.c \* Created by:  
 Hariprasad Nellitheertha (hari@in.ibm.com)  
 Copyright (C) 2010 Nokia Corporation \* \* Based on drivers/media/video/v4l2\_dev.c code authored by \* Mauro Carvalho Chehab <mchehab@kernel.org> (version 2) \* Alan Cox, <alan@lxorguk.ukuu.org.uk> (v  
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Copyright (C) 2010 Nokia Corporation * * Contacts: Laurent Pinchart <laurent.pinchart@ideasonboard.com> * Sakari Ailus <sakari.ailus@iki.fi>
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Copyright (C) 2010 Nokia Corporation * * Original Driver Author: Imre Deak <imre.deak@nokia.com> * Based on panel-generic.c by Tomi Valkeinen
<tomi.valkeinen@nokia.com> * Adapted to new DSS2 frame
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Copyright (C) 2010 Nokia Corporation * Paul Walmsley * * This data/structures are to be used while defining OMAP on-chip module * data and their
integration with other OMAP modules and Linux.
Copyright (C) 2010 Nokia Corporation * Tony Lindgren <tony@atomide.com> * Added OMAP4 support - Santosh Shilimkar <santosh.shilimkar@ti.com>
*/#include <linux/kernel.h>#include <linux/init.h>
Copyright (C) 2010 Nokia Corporation * Tony Lindgren <tony@atomide.com> * Added OMAP4 support - Santosh Shilimkar <santosh.shilimkar@ti.com>
* * NOTE: This timer is not the same timer as the old O
Copyright (C) 2010 Nokia Corporation * Tony Lindgren <tony@atomide.com> * Santosh Shilimkar <santosh.shilimkar@ti.com> */#include
<linux/kernel.h>#include <linux/init.h>#include <linux/reboot.h>
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Copyright (C) 2010, Novell, Inc. * Author : K. Y. Srinivasan <ksrinivasan@novell.com> *
Copyright (C) 2010 Nuno Goncalves <nunogpg@gmail.com> * * This file was based on: /drivers/input/misc/cobalt_btms.c
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Copyright (c) 2010, Octasic semiconductor. */
Copyright (C) 2010 OKI SEMICONDUCTOR Co., LTD. */#include <linux/module.h>#include <linux/kernel.h>#include <linux/slab.h>#include
<linux/pci.h>#include <linux/gpio/driver.h>#include <linux/int
Copyright (C) 2010 OKI SEMICONDUCTOR Co., LTD. * * This code was derived from the Intel e1000e Linux driver.
Copyright (C) 2010 OMICRON electronics GmbH
Copyright (C) 2010 OMICRON electronics GmbH */
Copyright (C) 2010 OMICRON electronics GmbH */#define _GNU_SOURCE#define __SANE_USERSPACE_TYPES__ /* For PPC64, to get LL64 types
*/#include <errno.h>#include <fcntl.h>#include <inttypes.
Copyright (C) 2010 OMICRON electronics GmbH */#ifndef _LINUX_POSIX_CLOCK_H_#define _LINUX_POSIX_CLOCK_H_
Copyright (C) 2010 OMICRON electronics GmbH */#ifndef _PTP_PRIVATE_H_#define _PTP_PRIVATE_H_
Copyright (C) 2010 OMICRON electronics GmbH */#include <linux/capability.h>#include <linux/slab.h>
Copyright (C) 2010 OMICRON electronics GmbH */#include <linux/device.h>#include <linux/err.h>#include <linux/gpio.h>#include
<linux/init.h>#include <linux/interrupt.h>#include <linux/io.h>#inc
Copyright (C) 2010 OMICRON electronics GmbH */#include <linux/device.h>#include <linux/export.h>#include <linux/file.h>#include <linux/posix-
clock.h>#include <linux/slab.h>#include <linux/sysca
Copyright (C) 2010 OMICRON electronics GmbH */#include <linux/errqueue.h>#include <linux/phy.h>#include <linux/ptp_classify.h>#include
<linux/skbuff.h>#include <linux/export.h>
Copyright (C) 2010 OMICRON electronics GmbH */#include <linux/idr.h>#include <linux/device.h>#include <linux/err.h>#include
<linux/init.h>#include <linux/kernel.h>#include <linux/module.h>#inc
Copyright (C) 2010 OMICRON electronics GmbH */#include <linux/module.h>#include <linux/posix-clock.h>#include <linux/poll.h>#include
<linux/sched.h>#include <linux/slab.h>#include <linux/timeke
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 Digital UK Limited, whose \* registered office  
 Copyright (c) 2010 Patrick McHardy <kaber@trash.net> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include  
 <linux/gfp.h>#include <linux/skbuff.h>#include <linux/netfilter  
 Copyright (C) 2010, Paul Cercueil <paul@crapouillou.net> \* JZ4740 SoC RTC driver \*/  
 Copyright (C) 2010, Paul Cercueil <paul@crapouillou.net> \* JZ4740 Watchdog driver \*/  
 Copyright (C) 2010 Paul Mundt  
 Copyright (C) 2010 Paul Mundt \* \* Based on ohci-sh.c and ehci-atmel.c.  
 Copyright (C) 2010 Paul Mundt \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt  
 Copyright (C) 2010 Paul Mundt \*/#define pr\_fmt(fmt) "PCI: " fmt  
 Copyright (C) 2010 Paul Mundt \*/#include <linux/bug.h>#include <linux/init.h>#include <linux/kernel.h>#include <linux/ioport.h>#include  
 <cpu/pfc.h>  
 Copyright (C) 2010 Paul Mundt \*/#include <linux/init.h>#include <linux/interrupt.h>#include <linux/gpio/driver.h>#include <linux/irq.h>#include  
 <linux/kernel.h>#include <linux/spinlock.h>#include  
 Copyright (C) 2010 Paul Mundt \*/#include <linux/init.h>#include <linux/io.h>#include <linux/bcd.h>#include <mach/fpga.h>#include <linux/sizes.h>  
 Copyright (C) 2010 Paul Mundt \*/#include <linux/init.h>#include <linux/kernel.h>#include <cpu/shx3.h>  
 Copyright (C) 2010 Paul Mundt \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/clk.h>#include <linux/io.h>#include  
 <linux/clkdev.h>#include <asm/clock.h>#include <asm/freq.h>  
 Copyright (C) 2010 Paul Mundt \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/string.h>#include <mach/fpga.h>  
 Copyright (C) 2010 Paul Mundt \*/#include <linux/init.h>#include <linux/platform\_device.h>#include <linux/io.h>#include  
 <linux/regulator/fixd.h>#include <linux/regulator/machine.h>#include <li  
 Copyright (C) 2010 Paul Mundt \*/#include <linux/irq.h>#include <mach/fpga.h>#include <mach/irq.h>  
 Copyright (C) 2010 Paul Mundt \* \* Modelled after arch/powerpc/kernel/irq.c.  
 Copyright (c) 2010 Paul Thomas <pthomas8589@gmail.com> \* \* You need to have something like this in struct spi\_board\_info  
 Copyright (c) 2010 Pengutronix e.K. \* Author: Wolfram Sang <kernel@pengutronix.de> \*/  
 Copyright (c) 2010 Pengutronix e.K. \* Author: Wolfram Sang <w.sang@pengutronix.de> \*/  
 Copyright (C) 2010 Pengutronix, Marc Kleine-Budde <kernel@pengutronix.de> \*/#include "../hardware.h"#include "devices-common.h"  
 Copyright (C) 2010 Pengutronix \* Uwe Kleine-Koenig <u.kleine-koenig@pengutronix.de> \*/#include "devices-common.h"  
 Copyright (C) 2010 Pengutronix \* Uwe Kleine-Koenig <u.kleine-koenig@pengutronix.de> \*/#include "devices/devices-common.h"  
 Copyright (C) 2010 Pengutronix \* Uwe Kleine-Koenig <u.kleine-koenig@pengutronix.de> \*/#include "../hardware.h"#include "devices-common.h"  
 Copyright (C) 2010 Pengutronix \* Uwe Kleine-Koenig <u.kleine-koenig@pengutronix.de> \*/#include <linux/dma-mapping.h>  
 Copyright (C) 2010 Pengutronix \* Uwe Kleine-Koenig <u.kleine-koenig@pengutronix.de> \*/#include <linux/dma-mapping.h>#include <linux/sizes.h>  
 Copyright (C) 2010 Pengutronix \* Uwe Kleine-Koenig <u.kleine-koenig@pengutronix.de> \*/#include <linux/sizes.h>  
 Copyright (C) 2010 Pengutronix, Wolfram Sang <kernel@pengutronix.de> \*/  
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 Copyright (c) 2010 \* Phillip Lougher <phillip@squashfs.org.uk> \* \* xattr.h  
 Copyright (c) 2010 \* Phillip Lougher <phillip@squashfs.org.uk> \* \* xattr\_id.c  
 Copyright (c) 2010 Picochip Ltd., Jamie Iles \*/#ifndef \_\_PICOXCELL\_CRYPTOREGS\_H\_\_#define \_\_PICOXCELL\_CRYPTOREGS\_H\_\_  
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 Copyright (C) 2010 Rafael J. Wysocki, Novell Inc. \*/  
 Copyright (c) 2010 Rafael J. Wysocki <rjw@sisk.pl>, Novell Inc. \*/#define pr\_fmt(fmt) "PM: " fmt  
 Copyright (c) 2010 Rafael J. Wysocki <rjw@sisk.pl>, Novell Inc. \*/#include <linux/pm.h>#include <linux/pm\_runtime.h>#include <linux/export.h>  
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Copyright (c) 2010 Red Hat Inc. * Author : Dave Airlie <airlied@redhat.com> *
Copyright (c) 2010 Red Hat Inc. * Author : Dave Airlie <airlied@redhat.com> * * ATPX support for both Intel/ATI
Copyright(C) 2010 Red Hat. */#include <linux/kernel.h>#include <linux/module.h>#include <linux/init.h>#include <linux/mxm-wmi.h>#include
<linux/acpi.h>
Copyright (C) 2010 Red Hat, Inc. * Note: Most of this code is borrowed heavily from the original softlockup * detector, so thanks to Ingo for the initial
implementation. * Some chunks also taken
Copyright (C) 2010 Red Hat, Inc., Peter Zijlstra */
Copyright (C) 2010 Red Hat, Inc., Peter Zijlstra * Provides a framework for enqueueing and running callbacks from hardirq * context. The enqueueing
is NMI-safe.
Copyright (C) 2010 Red Hat Inc, Steven Rostedt <srstedt@redhat.com> *
Copyright (C) 2010 Renesas Electronics Corporation */
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Copyright (C) 2010 Renesas Solutions Corp. * Tony SIM <chinyeow.sim.xt@renesas.com> * * Using code from:
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Copyright (C) 2010 Roman Yeryomin <roman@advem.lv>
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True IDE mode. Currently only PIO mode has been * implement
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<linux/ioport.h>#include <linux/device.h>#include <linux/io.h>#includ
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<linux/init.h>#include <linux/kthread.h>#include <linux/ex
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Copyright (C) 2010 Texas Instruments Inc */#include <linux/kernel.h>#include <linux/init.h>#include <linux/module.h>#include
<linux/errno.h>#include <linux/fs.h>#include <linux/string.h>#inclu
Copyright (C) 2010 Texas Instruments Inc */#include <linux/module.h>#include <linux/mod_devicetable.h>#include <linux/kernel.h>#include
<linux/init.h>#include <linux/ctype.h>#include <linux/del
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<linux/module.h>#include <linux/errno.h>#include <linux/interrupt
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__ARCH_ARM_PLAT_OMAP_INCLUDE_PLAT_L3_2XXX_H#define __ARCH_ARM_PLAT_OMAP_INCLUDE_PLAT_L3_2XXX_H
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<linux/percpu.h>#include <linux/input.h>
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<asm/unaligned.h>#include <trace/events/scsi.h>
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buffer setup from sysfs and starts a short capture
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<linux/hrtimer.h>struct sirfsoc_uart_param {      const char *u
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<linux/skbuff.h>#include <linux/netdevice.h>#include <linux/rout  
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<linux/ethtool.h>#include <linux/mii.h>#include <linux/usb.h>#incl
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interrupts thus * allowing that driver's code to
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<linux/pinctrl/pinconf-generic.h>
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<linux/io.h>#include <linux/irqchip.h>#include <linux/irqchip/
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Copyright (C) 2011 LAPIS Semiconductor Co., Ltd. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/kernel.h>#include
<linux/module.h>#include <linux/pci.h>#include <linux/delay.h>#in
Copyright (C) 2011 LAPIS Semiconductor Co., Ltd. */#if defined(CONFIG_SERIAL_PCH_UART_CONSOLE) && defined(CONFIG_MAGIC_SYSRQ)#define
SUPPORT_SYSRQ#endif#include <linux/kernel.h>#include <linux/s
Copyright (C) 2011 LAPIS Semiconductor Co., Ltd. */#include <linux/gpio/driver.h>#include <linux/interrupt.h>#include <linux/irq.h>#include
<linux/kernel.h>#include <linux/module.h>#include <li
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<linux/of_address.h>#include <linux/of_platform.h>
Copyright (C) 2011 Magnus Damm */#include <linux/kernel.h>#include <linux/init.h>#include <linux/smp.h>#include <linux/spinlock.h>#include
<linux/io.h>#include <linux/delay.h>#include <linux/

```

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 <linux/delay.h>#include <linux/clk.h>#include <linux/s  
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 <linux/slab.h>#include <linux/dma-mapping.h>#include <  
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 <linux/bsg-lib.h>#include <linux/export.h>#i  
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 <asm/export.h>#include <asm/regdef.h>  
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 <linux/input.h>#include <linux/hid.h>#include <  
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<linux/platform\_device.h>#include <linux/slab.h>  
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<linux/export.h>#include <linux/slab.h>#include <linux/  
Copyright (C) 2011 Rafael J. Wysocki <rjw@sisk.pl>, Renesas Electronics Corp. \*/#include <linux/kernel.h>#include <linux/pm\_domain.h>#include  
<linux/pm\_qos.h>#include <linux/hrtimer.h>#include <  
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<linux/slab.h>#include "common.h"#include "pipe.h"
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<linux/init.h>#include <linux/err.h>#include <linux/slab.h>#include <
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```

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\_ASM\_C6X\_CHECKSUM\_H  
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\_ASM\_C6X\_DSCR\_H  
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\_ASM\_C6X\_UACCESS\_H  
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<linux/memblock.h>  
Copyright (C) 2011 Texas Instruments Incorporated \* Author: Mark Salter <msalter@redhat.com> \*/#include <linux/kernel.h>#include  
<linux/delay.h>#include <linux/errno.h>#include <linux/string.h>  
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<linux/ctype.h>#include <linux/etherdevice.h>#include <asm/setup  
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<linux/of\_address.h>#include <linux/io.h>  
Copyright (C) 2011 Texas Instruments Incorporated \* Author: Mark Salter <msalter@redhat.com> \*/#include <linux/of.h>#include  
<linux/of\_address.h>#include <linux/io.h>#include <asm/soc.h>#inclu  
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\* to support the OMAP2+ device tree boards with an  
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OMAP\_ARCH\_OMAP4\_SAR\_LAYOUT\_H#define OMAP\_ARCH\_OMAP4\_SAR\_LAYOUT\_H  
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OMAP\_ARCH\_WAKEUPGEN\_H  
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A9 with per-CPU \* Local timer and Watchdog, GIC, SCU, PL  
Copyright (C) 2011 Texas Instruments, Inc. \* Tero Kristo <t-kristo@ti.com> \* \* For historical purposes, the API used to configure the PRM \* interrupt  
handler refers to it as the "PRCM interrupt."  
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<linux/irqdomain.h>#include <linux/interrupt.h>#include  
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Copyright (c) 2011, Tom Herbert <therbert@google.com> \*/#include <linux/types.h>#include <linux/kernel.h>#include <linux/jiffies.h>#include  
<linux/dynamic\_queue\_limits.h>#include <linux/compiler  
Copyright (c) 2011, Tom Herbert <therbert@google.com> \* \* This header file contains the definitions for dynamic queue limits (dql). \* dql would be  
used in conjunction with a producer/consumer type  
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fully compatible with BMA150 and only differs in p  
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<linux/slab.h>#include <linux/interrupt.h>#incl  
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<linux/mm.h>#include <linux/memblock.h>#include  
Copyright (C) 2011 Wind River Systems, \* written by Ralf Baechle <ralf@linux-mips.org> \*/#include <linux/compiler.h>#include <linux/elf-  
randomize.h>#include <linux/errno.h>#include <linux/mm.h  
Copyright (C) 2011 Wistron Co.Ltd \* Joseph Lai <joseph\_lai@wistron.com> and trimmed down by \* Alan Cox <alan@linux.intel.com> in turn based on  
bma023.c. \* Device behaviour based on a misc driver po  
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Copyright (C) 2011 Xilinx \*/  
Copyright (C) 2011 Xilinx \*/#define UART\_CR\_OFFSET 0x00 /\* Control Register [8:0] \*/#define UART\_SR\_OFFSET 0x2C /\* Channel  
Status [11:0] \*/#define UART\_FIFO\_OFFSET0x30 /\* FIFO [15:0] or [7:0  
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Vinod Koul ,vinod.koul@intel.com> \*/#ifndef \_PLATFORM\_SST  
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<linux/spi/spi_bitbang.h>#include <linux/gpio.h>#inc
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<linux/of_platform.h>#include <linux/reboot.h>#include <lin
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"rmi\_driver.h"#include "rmi\_2d\_sensor.h"  
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<linux/mmc/mmc.h>#include <linux/mmc/slot-gpio.h>#includ  
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<linux/iopoll.h>#include <linux/irq.h>#include <linux/module.h>#i  
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__ARCH_WANT_SYS_PAUSE#define __ARCH_WANT_SYS_GETPRGRP#define __ARCH_
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<linux/sched.h>#include <linux/sched/debug.h>#includ
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<linux/devcoredump.h>
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<linux/interrupt.h>#include <linux/irq.h>#include <linux/ke
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<linux/iio/buffer.h>#include <linux/regmap.h>#in
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<linux/of_address.h>
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<asm/mach/arch.h>#include <asm/mach/map.h>
Copyright (C) 2012 Magnus Damm */#include <linux/kernel.h>#include <linux/init.h>#include <linux/smp.h>#include <linux/spinlock.h>#include
<linux/io.h>#include <linux/delay.h>#include <asm/sm
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  Copyright (C) 2012 Marvell International Ltd. */#include <linux/module.h>#include <linux/moduleparam.h>#include <sound/core.h>#include
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mapping.h>#include <linux/platform_device.h>#include "mmpf
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  Copyright (C) 2012 MIPS Technologies, Inc. */#include <linux/delay.h>#include <linux/fb.h>#include <linux/init.h>#include <linux/io.h>#include

```

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<linux/mm.h>#include <linux/proc_fs.h>
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<linux/kernel.h>#include <linux/of.h>#include <linux/m
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<linux/kernel.h>#include <linux/ioport.h>#include <cpu/pfc.h>
Copyright (C) 2012 Nobuhiro Iwamatsu <nobuhiro.iwamatsu.yj@renesas.com> */#include <linux/init.h>#include <linux/kernel.h>#include
<cpu/sh7734.h>
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Copyright (C) 2012 Paul Mundt * * Based on linux/arch/sh/boards/se/7343/irq.c
Copyright (C) 2012 Paul Mundt */#define DRV_NAME "SE7722-FPGA"#define pr_fmt(fmt) DRV_NAME ": " fmt
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Copyright (C) 2012 Peter Meerwald <pmeerw@pmeerw.net> * * Based on max517 by Roland Stigge <stigge@antcom.de>
Copyright (C) 2012 Peter Meerwald <pmeerw@pmeerw.net> * * driver for ADJD-S311-CR999 digital color sensor (10-bit channels for * red, green, blue,
clear); 7-bit I2C slave address 0x74
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Copyright (C) 2012 Phil Edworthy */#include <linux/init.h>#include <linux/kernel.h>#include <linux/io.h>#include <linux/clkdev.h>#include

```

```

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Copyright (C) 2012 Phil Edworthy */#include <linux/init.h>#include <linux/types.h>#include <linux/platform_device.h>#include
<linux/interrupt.h>#include <linux/input.h>#include <linux/smsc911x
Copyright (C) 2012 Phil Edworthy */#include <linux/platform_device.h>#include <linux/init.h>#include <linux/serial.h>#include
<linux/serial_sci.h>#include <linux/usb/r8a66597.h>#include <linux
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_LINUX_BALLOON_COMPACTIO_H#include <linux/pagemap.h>#include <linux/page-flags.h
Copyright (C) 2012, Red Hat, Inc. Rafael Aquini <aquini@redhat.com> */#include <linux/mm.h>#include <linux/slab.h>#include
<linux/export.h>#include <linux/balloon_compaction.h>
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__LINUX_OF_GRAPH_H
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_V4L2_FWNODE_H
Copyright (C) 2012 Renesas Electronics Corp. * Author: Guennadi Liakhovetski <g.liakhovetski@gmx.de> */#include <linux/acpi.h>#include
<linux/kernel.h>#include <linux/mm.h>#include <linux/module
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<linux/interrupt.h>#include <linux/input.h>#include <li
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Copyright (C) 2012 Renesas Electronics Europe Ltd */#include <linux/platform_device.h>#include <linux/init.h>#include <linux/serial.h>#include

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Copyright (C) 2012 Renesas Solutions Corp. * Kuninori Morimoto <kuninori.morimoto.gx@renesas.com> */#include <linux/delay.h>#include
<linux/err.h>#include <linux/irq.h>#include <linux/interrupt.
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Copyright (C) 2012 Robin Gareus <robin@gareus.org>
Copyright (C) 2012 ROHM CO.,LTD. */#include <linux/delay.h>#include <linux/firmware.h>#include <linux/i2c.h>#include <linux/input.h>#include
<linux/input/mt.h>#include <linux/interrupt.h>#incl
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Copyright (C) 2012 Russell King */#include <linux/device.h>#include <linux/dmaengine.h>#include <linux/module.h>#include <linux/spinlock.h>
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<linux/mpage.h>#include <linux/writeback.h>#include
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<linux/bio.h>#include <linux/blkdev.h>#include <
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<linux/buffer_head.h>#include <linux/backing-dev.
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<linux/buffer_head.h>#include <linux/mpage.h>#in
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<linux/mpage.h>#include <linux/backing-dev.h>#in
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<linux/pagemap.h>#include <linux/sched.h>#includ
Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */#include <linux/fs.h>#include <linux/f2fs_fs.h>#include
<linux/sched/signal.h>#include <linux/unicode.h>
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<linux/stat.h>#include <linux/buffer_head.h>#inc
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<linux/backing-dev.h>#include <linux/init.h>#incl
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<linux/fs.h>#include <linux/statfs.h>#include <
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*/#define START_NID(nid) (((nid) / NAT_ENTR
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*/#ifndef S5C73M3_H_#define S5C73M3_H_
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<linux/clk.h>#include <linux/io.h>#include <linux/clkdev.h>#include  
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<linux/slab.h>#include <linux/platform\_device.h>#in  
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<linux/err.h>#include <linux/delay.h>#include <  
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<linux/bio.h>#include <linux/blkdev.h>#include <linux/kmemle  
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<linux/memblock.h>#include <linux/of\_address.h>  
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<linux/smp.h>#include <linux/cpu.h>  
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 <stdbool.h>#include <string.h>#include <linux/bpf.h>  
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 <linux/miscdevice.h>#include <linux/atomic.h>#include <linux  
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```



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Copyright (c) 2013 Chanho Min <chanho.min@lge.com> */
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<linux/vmalloc.h>#include <linux/lz4.h>#include <crypto/in
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__builtin_ctz, and therefore they can't be remov
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Copyright (c) 2013 Coraid, Inc. See COPYING for GPL terms./* * aoedev.c * AoE device utility functions; maintains device list.
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Copyright (C) 2013 Daniel Tang <tangrs@tangrs.id.au> \*/#include <linux/init.h>#include <linux/of\_irq.h>#include <linux/of\_address.h>#include  
<linux/of\_platform.h>#include <linux/irqchip.h>#incl  
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futex2, N at a time.  
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<linux/slab.h>#include <linux/err.h>#include "thermal\_hwmon.h"  
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<linux/dmapool.h>#include <linux/dmaengine.h>#include <linux/init  
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Copyright (C) 2013 Imagination Technologies Ltd. */#include <linux/device.h>#include <linux/fs.h>#include <linux/err.h>#include
<linux/wait.h>#include <linux/sched.h>#include <linux/interrupt.h
Copyright (C) 2013 Imagination Technologies Ltd. */#include <linux/device.h>#include <linux/fs.h>#include <linux/err.h>#include
<linux/wait.h>#include <linux/sched.h>#include <linux/smp.h>
Copyright (C) 2013 Imagination Technologies Ltd. */#include <linux/errno.h>#include <linux/sched.h>#include <linux/sched/debug.h>#include
<linux/sched/task.h>#include <linux/sched/task_stack.h>
Copyright (C) 2013 Imagination Technologies Ltd. */#include <linux/kernel.h>#include <linux/device.h>#include <linux/fs.h>#include
<linux/slab.h>#include <linux/export.h>
Copyright (C) 2013 Imagination Technologies Ltd. */#include <linux/kernel.h>#include <linux/fs.h>#include <linux/syscalls.h>#include
<linux/moduleloader.h>#include <linux/atomic.h>#include <lin
Copyright (C) 2013 Imagination Technologies Ltd. * * Register definitions for Intel PIIX4 South Bridge Device.
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controller is located in the South Bridge *
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simple since there are no TLBs. It needs * to be relo
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<linux/ctype.h>#include <linux/errno.h>#includ
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<linux/kernel.h>#include <linux/kprobes.h>#include <linux/extable.h>  
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<linux/smp.h>#include <asm/cacheflush.h>#include <asm/smp_p
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<linux/kernel.h>#include <linux/module.h>#include <linux/slab.h>#incl
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<linux/ctype.h>#include <linux/errno.h>#include <linux/unistd.h>#inclu
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<linux/buffer_head.h>#include <linux/sched.h>#include <
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Copyright (C) 2013 Texas Instruments Incorporated - http://www.ti.com * Author: Sricharan R <r.sricharan@ti.com> */#include <linux/err.h>#include
<linux/io.h>#include <linux/irqchip.h>#include
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"nfs4session.h"#include "callback.h"#include "pnfs.h
Copyright (c) 2013 Trond Myklebust <Trond.Myklebust@netapp.com> */#undef TRACE_SYSTEM#define TRACE_SYSTEM nfs
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<linux/module.h>#include <linux/of.h>#include <linux/mutex.h>#include
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<linux/hw_random.h>#include <linux/slab.h>#include
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__LINUX_FS_NFS_NFS4_2XDR_H
Copyright (c) 2014 Anna Schumaker <Anna.Schumaker@Netapp.com> */#include <linux/fs.h>#include <linux/sunrpc/sched.h>#include
<linux/nfs.h>#include <linux/nfs3.h>#include <linux/nfs4.h>#include
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Copyright (C) 2014 ARM Ltd.*/#ifndef __ASM_PTDUMP_H#define __ASM_PTDUMP_H
Copyright (C) 2014 ARM Ltd.*/#include <asm/arch_timer.h>#include <asm/cache.h>#include <asm/cpu.h>#include <asm/cputype.h>#include
<asm/cpufeature.h>#include <asm/fpsimd.h>
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Copyright (c) 2014 Broadcom Corporation */#ifndef CONFIG_OFvoid brcmf_of_probe(struct device *dev, enum brcmf_bus_type bus_type,
struct brcmf_mp_device *settings);#elsestatic void brcmf_of_p
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Copyright (c) 2014 Broadcom Corporation */#ifndef BRCMFMAC_PCIE_H#define BRCMFMAC_PCIE_H
Copyright (c) 2014 Broadcom Corporation */#include <linux/init.h>#include <linux/of.h>#include <linux/of_irq.h>
Copyright (c) 2014 Broadcom Corporation */#include <linux/kernel.h>#include <linux/delay.h>#include <linux/list.h>#include
<linux/ssb/ssb_regs.h>#include <linux/bcma/bcma.h>#include <linux/bcma
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always store 16 bit OBC
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support simulating EEPROM types which take more
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```

```

<linux/sched.h>#include <linux/sunrpc/addr.h>
    Copyright (c) 2014 Christoph Hellwig. */#include "xfs.h"#include "xfs_shared.h"#include "xfs_format.h"#include "xfs_log_format.h"#include
"xfs_trans_resv.h"#include "xfs_mount.h"#include "xfs_
    Copyright (C) 2014 Christoph Hellwig * * SCSI queueing library. * Initial versions: Eric Youngdale (eric@andante.org). * Based upon
conversations with large numbers *
    Copyright (c) 2014 Christoph Hellwig. */#undef TRACE_SYSTEM#define TRACE_SYSTEM nfsd
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    Copyright (C) 2014 Davidlohr Bueso. */#include <linux/sched/signal.h>#include <linux/sched/task.h>#include <linux/mm.h>#include
<linux/vmcache.h>#include <asm/pgtable.h>
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    Copyright (c) 2014 eGauge Systems LLC * Contributed by David Mosberger-Tang <davidm@egauge.net> * * Platform-data structure for MAX3421
USB HCD driver.
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<linux/context_tracking.h> #include <linux/cpu_pm.h> #include
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<linux/sched.h> #include <linux/mm.h> #include <
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<asm/irqflags.h> #include <asm/mipsregs.h> #include <asm/regdef.h>
Copyright (C) 2014 Imagination Technologies Ltd. */ #include <linux/errno.h> #include <asm/asm.h> #include <asm/asm-offsets.h> #include
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<linux/io.h>#include <linux/interrupt.h>#include <linux/mod_de
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address 0x39 (TCS3414) or 0x29, 0x49, 0x59 (TCS34  
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<poll.h>#include <stdlib.h>#include <unistd.h>  
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Copyright (C) 2014 Varka Bhadram <varkab@cdac.in> \* Md.Jamal Mohiuddin <mjmohiuddin@cdac.in> \* P Sowjanya <sowjanya@cdac.in> \*/#include <linux/kernel.h>#include <linux/module.h>#i

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Copyright (C) 2014 Zi Shen Lim <zlim.lnx@gmail.com> \*/#ifndef \_\_ASM\_INS\_H#define \_\_ASM\_INS\_H#include <linux/build\_bug.h>#include <linux/types.h>

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```

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Copyright(c) 2015-17 Intel Corporation
Copyright(c) 2015-17 Intel Corporation.
Copyright(c) 2015-17 Intel Corporation.#include <sound/soc.h>
Copyright (C) 2015-17 Renesas Electronics Corporation */#include <linux/bitops.h>#include <linux/clk.h>#include <linux/delay.h>#include
<linux/io.h>#include <linux/kernel.h>#include <linux/modu
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Copyright (C) 2015-2016 Altera Corporation * by Alan Tull <atull@opensource.altera.com> */
Copyright (C) 2015-2016 Altera Corporation * by Alan Tull <atull@opensource.altera.com> */#include <linux/delay.h>#include <linux/fpga/altera-pr-ip-
core.h>#include <linux/fpga/fpga-mgr.h>#inclu
Copyright (C) 2015-2016 Altera Corporation * by Alan Tull <atull@opensource.altera.com> */#include <linux/fpga/altera-pr-ip-core.h>#include
<linux/module.h>#include <linux/of_device.h>
Copyright (C) 2015-2016 Altera Corporation */#include <linux/clk.h>#include <linux/device.h>#include <linux/delay.h>#include <linux/fpga/fpga-
mgr.h>#include <linux/io.h>#include <linux/module.h
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Copyright (C) 2015,2016 ARM Ltd. * Author: Andre Przywara <andre.przywara@arm.com> */
Copyright (C) 2015, 2016 ARM Ltd. */#ifndef __KVM_ARM_VGIC_H#define __KVM_ARM_VGIC_H
Copyright (C) 2015, 2016 ARM Ltd. */#ifndef __KVM_ARM_VGIC_MMIO_H#define __KVM_ARM_VGIC_MMIO_H__
Copyright (C) 2015, 2016 ARM Ltd. */#ifndef __KVM_ARM_VGIC_NEW_H#define __KVM_ARM_VGIC_NEW_H__
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Copyright (C) 2015-2016 Cogent Embedded, Inc. <source@cogentembedded.com> */
Copyright (C) 2015-2016 Cogent Embedded, Inc. <source@cogentembedded.com> * * Based on the SuperH Ethernet driver
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Copyright (c) 2015-2016 HGST, a Western Digital Company. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/atomic.h>#include
<linux/ctype.h>#include <linux/delay.h>#include <linux/err
Copyright (c) 2015-2016 HGST, a Western Digital Company. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/blkdev.h>#include
<linux/module.h>#include "nvmet.h"
Copyright (c) 2015-2016 HGST, a Western Digital Company. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/blkdev.h>#include
"nvmet.h"
Copyright (c) 2015-2016 HGST, a Western Digital Company. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/init.h>#include
<linux/miscdevice.h>#include <linux/module.h>#include <linux
Copyright (c) 2015-2016 HGST, a Western Digital Company. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/kernel.h>#include
<linux/module.h>#include <linux/slab.h>#include <linux/sta
Copyright (c) 2015-2016 HGST, a Western Digital Company. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/init.h>#include <linux/slab.h>#include <rdma/mr_poo
Copyright (c) 2015-2016 HGST, a Western Digital Company. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/random.h>#include <linux/rculist.h>#include <linux/
Copyright (c) 2015-2016 HGST, a Western Digital Company. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/rculist.h>
Copyright (c) 2015-2016 HGST, a Western Digital Company. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/scatterlist.h>#include
<linux/blk-mq.h>#include <linux/nvme.h>#include <linu
Copyright (c) 2015-2016 HGST, a Western Digital Company. */#ifndef _NVME_FABRICS_H#define _NVME_FABRICS_H 1
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```

```

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Copyright (c) 2015-2016 HiSilicon Technologies Co., Ltd. */#include <linux/bitops.h>#include <linux/clk.h>#include <linux/dma-mapping.h>#include
<linux/iopoll.h>#include <linux/module.h>#include
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Copyright (c) 2015-2016, Linaro Limited */#ifndef TEE_PRIVATE_H#define TEE_PRIVATE_H
Copyright (c) 2015-2016, Linaro Limited */#include <linux/device.h>#include <linux/dma-buf.h>#include <linux/fdtable.h>#include
<linux/idr.h>#include <linux/sched.h>#include <linux/slab.h>#include
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Copyright (c) 2015-2016 MediaTek Inc. * Author: Honghui Zhang <honghui.zhang@mediatek.com> * * Based on driver/iommu/mtk_iommu.c
Copyright (c) 2015-2016 MediaTek Inc. * Author: Houlong Wei <houlong.wei@mediatek.com> * Ming Hsiu Tsai <minghsiu.tsai@mediatek.com> */
Copyright (c) 2015-2016 MediaTek Inc. * Author: Yong Wu <yong.wu@mediatek.com> */#ifndef __DTS_IOMMU_PORT_MT8173_H#define
__DTS_IOMMU_PORT_MT8173_H
Copyright (c) 2015-2016 MediaTek Inc. * Author: Yong Wu <yong.wu@mediatek.com> */#ifndef MTK_IOMMU_SMI_H#define MTK_IOMMU_SMI_H
Copyright (c) 2015-2016 MediaTek Inc. * Author: Yong Wu <yong.wu@mediatek.com> */#include <linux/clk.h>#include <linux/component.h>#include
<linux/device.h>#include <linux/err.h>#include <linux
Copyright (c) 2015-2016 MediaTek Inc. * Author: Yong Wu <yong.wu@mediatek.com> */#include <linux/memblock.h>#include <linux/bug.h>#include
<linux/clk.h>#include <linux/component.h>#include <lin
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by Broadcom * sdhci-bcm2835.c by Stephen Warr
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Copyright (C) 2015-2016 Renesas Electronics Corporation * * Contact: Laurent Pinchart (laurent.pinchart@ideasonboard.com)
Copyright (C) 2015-2016 Russell King * * Network PHYs can appear on I2C buses when they are part of SFP module. * This driver exposes these PHYs to
the networking PHY code, allowing * our PHY driv
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<s.nawrocki@samsung.com>
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<k.opasiak@samsung.com> */
Copyright (C) 2015-2016 Samsung Electronics * Igor Kotrasinski <i.kotrasinsk@samsung.com> * Krzysztof Opasiak
<k.opasiak@samsung.com> * * Refactored from usbip_host_dri

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Copyright (C) 2015-2016, SUSE, IBM Corp. \*/#ifndef \_ASM\_POWERPC\_LIVEPATCH\_H#define \_ASM\_POWERPC\_LIVEPATCH\_H  
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Copyright (C) 2015 - 2016 ZTE Corporation. \*/#ifndef \_\_DT\_BINDINGS\_CLOCK\_ZX296718\_H#define \_\_DT\_BINDINGS\_CLOCK\_ZX296718\_H  
Copyright (C) 2015, 2017-2018 \* Author: Matt Ranostay <matt.ranostay@konsulko.com> \* \* TODO: interrupt mode, and signal strength reporting  
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Copyright (C) 2015-2017 Pengutronix, Uwe Kleine-K \*/  
Copyright (C) 2015-2017 Pengutronix, Uwe Kleine-K \*/#include <linux/kernel.h>#include <linux/device.h>#include <linux/module.h>#include  
<linux/slab.h>#include <linux/sysfs.h>  
Copyright (C) 2015-2017 Pengutronix, Uwe Kleine-K \*/#include <linux/kernel.h>#include <linux/kthread.h>#include <linux/siox.h>  
Copyright (c) 2015-2017 QLogic Corporation  
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Copyright (c) 2015,2017 Qualcomm Atheros, Inc.  
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Copyright (C) 2015-2017 Renesas Electronics Corporation * * This is based on the phy-rcar-gen2 driver:
Copyright (C) 2015-2017 Russell King. */#include <linux/interrupt.h>#include <linux/io.h>#include <linux/module.h>#include
<linux/platform_device.h>#include <linux/sched.h>#include <linux/slab.
Copyright (C) 2015-2017 Sebastian Reichel <sre@kernel.org> */
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Copyright (c) 2015-2017 Takashi Sakamoto <o-takashi@sakamocchi.jp> */
Copyright (c) 2015-2017 Takashi Sakamoto <o-takashi@sakamocchi.jp> */#include "motu.h"
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Copyright (C) 2015, 2018 * Author: Matt Ranostay <matt.ranostay@konsulko.com> * * TODO: enable pulse length controls via device tree properties
Copyright (C) 2015, 2018 * Author: Matt Ranostay <matt.ranostay@konsulko.com> * * TODO: gesture + proximity calib offsets
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Copyright (C) 2015-2018 Linaro Ltd. */#ifndef QC_MSM_CAMSS_CSID_H#define QC_MSM_CAMSS_CSID_H
Copyright (C) 2015-2018 Linaro Ltd. */#ifndef QC_MSM_CAMSS_H#define QC_MSM_CAMSS_H
Copyright (C) 2015-2018 Linaro Ltd. */#ifndef QC_MSM_CAMSS_ISPIF_H#define QC_MSM_CAMSS_ISPIF_H
Copyright (C) 2015-2018 Linaro Ltd. */#ifndef QC_MSM_CAMSS_VFE_H#define QC_MSM_CAMSS_VFE_H
Copyright (C) 2015-2018 Linaro Ltd. */#ifndef QC_MSM_CAMSS_VIDEO_H#define QC_MSM_CAMSS_VIDEO_H
Copyright (C) 2015-2018 Linaro Ltd. */#include <linux/clk.h>#include <linux/completion.h>#include <linux/interrupt.h>#include <linux/io.h>#include
<linux/iopoll.h>#include <linux/kernel.h>#inc
Copyright (C) 2015-2018 Linaro Ltd. */#include <linux/clk.h>#include <linux/completion.h>#include <linux/interrupt.h>#include <linux/io.h>#include
<linux/kernel.h>#include <linux/of.h>#include
Copyright (C) 2015-2018 Linaro Ltd. */#include <linux/clk.h>#include <linux/completion.h>#include <linux/interrupt.h>#include
<linux/iommu.h>#include <linux/mutex.h>#include <linux/of.h>#inclu
Copyright (C) 2015-2018 Linaro Ltd. */#include <linux/clk.h>#include <linux/media-bus-format.h>#include <linux/media.h>#include
<linux/module.h>#include <linux/platform_device.h>#include <linux
Copyright (C) 2015-2018 Linaro Ltd. */#include <linux/slab.h>#include <media/media-entity.h>#include <media/v4l2-dev.h>#include <media/v4l2-
device.h>#include <media/v4l2-ioctl.h>#include <media
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Copyright (c) 2015-2019, Linaro Limited */ #ifndef OPTEE_MSG_H #define OPTEE_MSG_H
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Garzik and Anton Vorontsov
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Copyright (C) 2015 Alban Bedel <albeu@free.fr> */
Copyright (C) 2015 Aleksa Sarai <cyphar@cyphar.com> */
Copyright (C) 2015 Alexander Sverdlin * * The driver uses polling to get the conversion status. According to EP93xx * datasheets, reading ADCResult
register starts the conversion, but user is also
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Copyright (c) 2015 Andrew Lutomirski * * Based on asm and ptrace code by many authors. The code here originated * in ptrace.c and signal.c.
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Copyright (c) 2015 Andrew Lutomirski * * On AMD CPUs, SYSRET can return with a valid SS descriptor with with * the hidden attributes set to an
unusable state. Make sure the kernel * doesn't let t
Copyright (c) 2015 Andrew Lutomirski * * These are little helpers that make it easier to switch bitness on * the fly.
Copyright (c) 2015 Andrew Lutomirski * * This makes sure that sa_restorer == NULL keeps working on 32-bit * configurations. Modern glibc doesn't use
it under any circumstances, * so it's easy to
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Copyright (C) 2015 Anshuman Khandual, IBM Corporation. */ #define GPR_1 1 #define GPR_2 2 #define GPR_3 3 #define GPR_4 4
Copyright (C) 2015 Anshuman Khandual, IBM Corporation. */ #define TAR_1 10 #define TAR_2 20 #define TAR_3 30 #define TAR_4 40 #define

```

```

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Copyright (C) 2015 Anshuman Khandual, IBM Corporation. */#include <inttypes.h>#include <unistd.h>#include <stdlib.h>#include <string.h>#include
<malloc.h>#include <errno.h>#include <time.h>#i
Copyright (C) 2015 Anshuman Khandual, IBM Corporation. */#include "ptrace.h"#include "ptrace-gpr.h"#include "reg.h"
Copyright (C) 2015 Anshuman Khandual, IBM Corporation. */#include "ptrace.h"#include "ptrace-gpr.h"#include "tm.h"
Copyright (C) 2015 Anshuman Khandual, IBM Corporation. */#include "ptrace.h"#include "ptrace-tar.h"
Copyright (C) 2015 Anshuman Khandual, IBM Corporation. */#include "ptrace.h"#include "ptrace-vsx.h"
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Copyright (C) 2015 Anshuman Khandual, IBM Corporation. */#include "ptrace.h"#include "tm.h"#include "ptrace-tar.h"
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<linux/interrupt.h>#include <linux/io.h>#include
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Copyright (C) 2015 ARM Limited */#include <linux/perf_event.h>#include <linux/uaccess.h>
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Copyright (C) 2015 ARM Ltd. * Author: Lorenzo Pieralisi <lorenzo.pieralisi@arm.com> */#include <linux/acpi.h>#include <linux/irq.h>#include
<linux/irqdomain.h>#include <linux/of.h>
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Copyright (C) 2015 - ARM Ltd * Author: Marc Zyngier <marc.zyngier@arm.com> */#include <linux/jump_label.h>
Copyright (C) 2015 ARM Ltd. * Author: Marc Zyngier <marc.zyngier@arm.com> */#include <linux/kvm_host.h>#include <kvm/arm_vgic.h>#include
<linux/uaccess.h>#include <asm/kvm_mmu.h>#include <asm/c
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 Copyright(c) 2015 Bryan O \*/#ifndef \_IMR\_H#define \_IMR\_H  
 Copyright(c) 2015 Bryan O \*\* IMR registers define an isolated region of memory that can \* be masked to prohibit certain system agents from accessing memory. \* When a device behind a masked port p  
 Copyright(c) 2015 Bryan O \*\* IMR self test. The purpose of this module is to run a set of tests on the \* IMR API to validate it's sanity. We check for overlapping, reserved \* addresses and setup/  
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 iomuxc-gpr.h>#include <linux/micrel\_phy.h>#include <lin  
 Copyright (C) 2015 Freescale Semiconductor, Inc. \*/#include <linux/irqchip.h>#include <linux/mfd/syscon.h>#include <linux/mfd/syscon/imx7-iomuxc-  
 gpr.h>#include <linux/of\_platform.h>#include <lin  
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 Copyright (C) 2015 FUJITSU LIMITED \* Author: Taku Izumi <izumi.taku@jp.fujitsu.com> \* \* This code introduces new boot option named  
 "efi\_fake\_mem" \* By specifying this parameter, you can add arbit

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<asm/cacheflush.h>#include <asm/fw/fw.h>#include <asm/sect  
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_CRYPTO_AKIPHER_INT_H#include <crypto/akcipher.h>#include <crypt
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Copyright (c) 2015, Intel Corporation * Authors: Tadeusz Struk <tadeusz.struk@intel.com> */#include <linux/errno.h>#include
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<linux/export.h>#include <linux/err.h>#include <linux/fips.h>#includ
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<linux/regmap.h>#include <linux/iio/iio.h>#include "inv_mpu_iio.h"
Copyright (c) 2015, Intel Corporation. */#include <linux/debugfs.h>          /* debugfs_create_u32()          */#include <linux/mm_types.h>
/* mm_struct, vma, etc... */#include <linux/pkeys.h>
Copyright (c) 2015, Intel Corporation. */#include <linux/kernel.h>#include <linux/module.h>#include <linux/init.h>#include <linux/acpi.h>#include
<linux/thermal.h>#include "int340x_thermal_zone
Copyright (C) 2015, Intel Corporation */#include <linux/kernel.h>#include <linux/slab.h>#include <linux/hrtimer.h>
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<linux/mm.h>#include <linux/slab.h>#include <li  
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<linux/tee\_drv.h>#include "tee\_private.h"  
Copyright (c) 2015, Linaro Limited \*/#include <linux/device.h>#include <linux/slab.h>#include <linux/uaccess.h>#include "optee\_private.h"  
Copyright (c) 2015, Linaro Limited \*/#include <linux/linkage.h>  
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<linux/irqchip/chained\_irq.h>#include <linux/in  
Copyright (C) 2015 Linaro Ltd. \*/#include <linux/platform\_device.h>#include <linux/init.h>#include <linux/cpumask.h>#include  
<linux/export.h>#include <linux/dma-direct.h>#include <linux/dma-map  
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Copyright (C) 2015 Linus Walleij \*/#include <linux/smp.h>#include <linux/io.h>#include <linux/of.h>#include <linux/of\_address.h>#include <linux/regmap.h>#include <linux/mfd/syscon.h>  
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<linux/ethtool.h>#include <linux/phy.h>#include <linux/microchipphy.h>  
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Arianna Avanzini <avanzini.arianna@gmail.com> */#include
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Copyright (c) 2015 Pengutronix, Philipp Zabel <p.zabel@pengutronix.de> * * TODO: add support for writing OTP register via API in boot ROM.
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Copyright (c) 2015 Pengutronix, Sascha Hauer <kernel@pengutronix.de> */#include <linux/clk.h>#include <linux/init.h>#include <linux/io.h>#include
<linux/iopoll.h>#include <linux/mfd/syscon.h>#i
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<stdbool.h>#include <string.h>#include <time.h>#include
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Copyright (C) 2015 Red Hat, Inc. * * This test allocates two virtual areas and bounces the physical * memory across the two virtual areas (from
area_src to area_dst) * using userfaultfd.
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Copyright (C) 2015 Renesas Electronics Corp. */#ifndef __DT_BINDINGS_CLOCK_R8A7795_CPG_MSSR_H__#define
__DT_BINDINGS_CLOCK_R8A7795_CPG_MSSR_H__

```

```

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provider.h>#include <linux/delay.h>#include <linux/clk.h>#include
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*/
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<linux/netdevice.h>#include <linux/of.h>#include <linux/of_mdio.h>
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<linux/io.h>#include <linux/of\_platform.h>#include <lin  
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<linux/gpio/driver.h>#include <linux/init.h>#include <linux/kern  
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<linux/string.h>#include <sound/core.h>#include <s  
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<linux/slab.h>#include <linux/err.h>#include <linux/init.h>#inc  
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 <linux/mm.h>#include <linux/filter.h>#include <linux/perf\_e  
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 direction.h>#include <linux/dma-mapping.h>#include <linu  
 Copyright (c) 2016-2017 Hisilicon Limited.#include <linux/crypto.h>#include <linux/dma-mapping.h>#include <linux/dmapool.h>#include  
 <linux/module.h>#include <linux/mutex.h>#include <linux/slab.h  
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 Copyright (c) 2016-2017 HiSilicon Technologies Co., Ltd. \*/#include <linux/kernel.h>#include <linux/mfd/syscon.h>#include <linux/module.h>#include  
 <linux/of\_device.h>#include <linux/platform\_dev  
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<linux/tty.h>#include <linux/tty\_driver.h>#include <linux/poll.h>  
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"qedf\_hsi.h"#include <linux/qed/qed\_if.h>  
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Copyright (c) 2016-2018 Cavium Inc. \*/#ifndef \_SCSI\_FW\_FUNCS\_H#define \_SCSI\_FW\_FUNCS\_H#include <linux/qed/common\_hsi.h>#include  
<linux/qed/storage\_common.h>#include <linux/qed/fcoe\_common.h>  
Copyright (c) 2016-2018 Cavium Inc. \*/#include "drv\_fcoe\_fw\_funcs.h"#include "drv\_scsi\_fw\_funcs.h"  
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Copyright (c) 2016-2018 Cavium Inc. \*/#include <linux/if\_ether.h>#include <linux/if\_vlan.h>#include "qedf.h"  
Copyright (c) 2016-2018 Cavium Inc. \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/module.h>#include <linux/pci.h>#include  
<linux/device.h>#include <linux/highmem.h>#include  
Copyright (c) 2016-2018 Cavium Inc. \*/#include <linux/spinlock.h>#include <linux/vmalloc.h>#include "qedf.h"#include <scsi/scsi\_tcq.h>  
Copyright (c) 2016-2018 Cavium Inc. \*/#include "qedf\_dbg.h"#include <linux/vmalloc.h>  
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<linux/iomap.h>#include <linux/backing-dev.h>#include <linux/uid.h>  
Copyright (c) 2016-2018 Christoph Hellwig. \*/#include <linux/module.h>#include <linux/compiler.h>#include <linux/fs.h>#include  
<linux/iomap.h>#include <linux/pagemap.h>#include <linux/uid.h>#i  
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Copyright (C) 2016-2018 Linaro Ltd. \*/#include <linux/clk.h>#include <linux/delay.h>#include <linux/interrupt.h>#include <linux/io.h>#include  
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<media/v4l2-async.h>#include <media/v4l2-event.h>#include  
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 <linux/of.h>#include <linux/platform\_device.h>#include <  
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 <linux/perf\_event.h>#include <linux/printk.h>#include <linux/  
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_DT_BINDINGS_AMLOGIC_MESON_GXBB_RESET_H#define _DT_BINDINGS_AMLOGIC_MESON_GXBB_RESET_H
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<linux/mmc/mmc.h>#include <linux/module.h>#include <linux/of.h>#inc
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<linux/virtio_config.h>#include <linux/module.h>#include "blk-
Copyright (c) 2016 Christoph Hellwig. */#include <linux/kobject.h>#include <linux/blkdev.h>#include <linux/blk-mq.h>#include <linux/blk-mq-
pci.h>#include <linux/pci.h>#include <linux/module.h>
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Copyright (C) 2016 Cogent Embedded Inc * Author: Andrey Gusakov <andrey.gusakov@cogentembedded.com> *
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__DT_BINDINGS_CLOCK_R8A7743_CPG_MSSR_H__
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__DT_BINDINGS_POWER_R8A7743_SYSC_H__
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__DT_BINDINGS_POWER_R8A7745_SYSC_H__
Copyright (C) 2016 Cogent Embedded Inc. */#ifndef __DT_BINDINGS_POWER_R8A7792_SYSC_H__#define
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<errno.h>#include <string.h>#include <assert.h>#i
Copyright (c) 2016 Facebook */#define __GNU_SOURCE#include <sched.h>#include <errno.h>#include <stdio.h>#include <sys/types.h>#include
<asm/unistd.h>#include <fcntl.h>#include <unistd.h>#incl
Copyright (c) 2016 Facebook */#define __GNU_SOURCE#include <sched.h>#include <stdio.h>#include <sys/types.h>#include <asm/unistd.h>#include
<unistd.h>#include <assert.h>#include <sys/wait.h>#

```



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Copyright (c) 2016 Facebook */#define _GNU_SOURCE#include <stdio.h>#include <unistd.h>#include <errno.h>#include <string.h>#include
<assert.h>#include <sched.h>#include <stdlib.h>#include <t
Copyright (c) 2016 Facebook */#ifndef __BPF_LRU_LIST_H_#define __BPF_LRU_LIST_H_
Copyright (c) 2016 Facebook */#ifndef __PERCPU_FREELIST_H_#define __PERCPU_FREELIST_H_#include <linux/spinlock.h>#include
<linux/percpu.h>
Copyright (c) 2016 Facebook */#ifndef _SAMPLES_BPF_XDP_TX_IPTNL_COMMON_H_#define _SAMPLES_BPF_XDP_TX_IPTNL_COMMON_H
Copyright (c) 2016 Facebook */#ifndef _TEST_IPTNL_COMMON_H_#define _TEST_IPTNL_COMMON_H
Copyright (c) 2016 Facebook */#include <linux/bpf.h>#include <linux/btf.h>#include <linux/jhash.h>#include <linux/filter.h>#include
<linux/rculist_nulls.h>#include <linux/random.h>#include <ua
Copyright (c) 2016 Facebook */#include <linux/bpf.h>#include <linux/if_link.h>#include <assert.h>#include <errno.h>#include <signal.h>#include
<stdio.h>#include <stdlib.h>#include <string.h>
Copyright (c) 2016 Facebook */#include <linux/bpf.h>#include <linux/jhash.h>#include <linux/filter.h>#include <linux/stacktrace.h>#include
<linux/perf_event.h>#include <linux/elf.h>#include <l
Copyright (c) 2016 Facebook */#include <linux/cpumask.h>#include <linux/spinlock.h>#include <linux/percpu.h>
Copyright (c) 2016 Facebook */#include <linux/kernel.h>#include <linux/types.h>#include <linux/slab.h>#include <linux/bpf.h>#include
<linux/bpf_perf_event.h>#include <linux/filter.h>#include <
Copyright (c) 2016 Facebook */#include <linux/unistd.h>#include <linux/bpf.h>
Copyright (c) 2016 Facebook */#include "percpu_freelist.h"
Copyright (c) 2016 Facebook */#include <stdio.h>#include <unistd.h>#include <stdlib.h>#include <signal.h>#include <linux/bpf.h>#include
<string.h>#include <linux/perf_event.h>#include <errno.
Copyright (c) 2016 Facebook */#include <stdio.h>#include <unistd.h>#include <stdlib.h>#include <stdbool.h>#include <string.h>#include
<fcntl.h>#include <poll.h>#include <sys/ioctl.h>#include
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Copyright (C) 2016 Felix Fietkau <nbd@nbd.name> */#include <linux/of.h>#include <linux/of_net.h>#include <linux/mtd/mtd.h>#include
<linux/mtd/partitions.h>#include <linux/etherdevice.h>#include
Copyright (C) 2016 Felix Fietkau <nbd@nbd.name> */#include <linux/of.h>#include "mt76.h"
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Copyright (C) 2016 Florian Fainelli <f.fainelli@gmail.com> * * FPGA Manager Driver for the on-board Altera Cyclone II FPGA found on * TS-7300, heavily
based on load_fpga.c in their vendor tree.
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<linux/fs.h>#include <linux/hwmon.h>#include <linux/h
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Copyright (C) 2016 Glider bvba \*/#ifndef \_\_DT\_BINDINGS\_POWER\_R8A7794\_SYSC\_H\_\_ #define \_\_DT\_BINDINGS\_POWER\_R8A7794\_SYSC\_H\_\_  
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Copyright (c) 2016 HGST, a Western Digital Company. \*/#ifndef \_\_RDMA\_RW\_H\_\_ #define \_\_RDMA\_RW\_H\_\_  
Copyright (c) 2016 HGST, a Western Digital Company. \*/#include <linux/moduleparam.h> #include <linux/slab.h> #include <linux/pci-p2pdma.h> #include <rdma/mr\_pool.h> #include <rdma/rw.h>  
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Copyright (C) 2016 IBM Corp. \*/#include <linux/bitops.h> #include <linux/init.h> #include <linux/io.h> #include <linux/kernel.h> #include <linux/mfd/syscon.h> #include <linux/mutex.h> #include <linux/>  
Copyright (C) 2016 IBM Corp. \*/#include <linux/bitops.h> #include <linux/init.h> #include <linux/io.h> #include <linux/kernel.h> #include <linux/mutex.h> #include <linux/of.h> #include <linux/platfo  
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<linux/kernel.h>#include <linux/signal.h>#include <linux
Copyright (C) 2016, Imagination Technologies Ltd. */#include <linux/compiler.h>#include <linux/errno.h>#include <linux/signal.h>#include
<linux/sched/signal.h>#include <linux/uaccess.h>
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```

```

_CRYPTO_SCOMP_INT_H#include <linux/crypto.h>
    Copyright (c) 2016, Intel Corporation * Author: Giovanni Cabiddu <giovanni.cabiddu@intel.com> */#include <linux/errno.h>#include
<linux/kernel.h>#include <linux/module.h>#include <linux/seq_file
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    Copyright (C) 2016, Intel Corporation * Author: Rafael J. Wysocki <rafael.j.wysocki@intel.com> */#include "sched.h"
    Copyright (c) 2016 Intel Corporation * Author: Ross Zwisler <ross.zwisler@linux.intel.com> * Author: Matthew Wilcox <matthew.r.wilcox@intel.com>
*/#include <linux/radix-tree.h>#include <linux/sla
    Copyright (c) 2016 Intel Corporation * Author: Ross Zwisler <ross.zwisler@linux.intel.com> */#include <pthread.h>#include "test.h"
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_CRYPTO_ECDH_
    Copyright (c) 2016, Intel Corporation * Authors: Salvatore Benedetto <salvatore.benedetto@intel.com> */#ifndef _CRYPTO_KPP_INT_H#define
_CRYPTO_KPP_INT_H#include <crypto/kpp.h>#include <crypto/a
    Copyright (c) 2016, Intel Corporation * Authors: Salvatore Benedetto <salvatore.benedetto@intel.com> */#include <linux/errno.h>#include
<linux/kernel.h>#include <linux/module.h>#include <linux/s
    Copyright (c) 2016, Intel Corporation * Authors: Salvatore Benedetto <salvatore.benedetto@intel.com> */#include <linux/kernel.h>#include
<linux/export.h>#include <linux/err.h>#include <linux/str
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_CRYPTO_ACOMP_H#define _CRYPTO_ACOMP_H#inclu
    Copyright (c) 2016, Intel Corporation * Authors: Weigang Li <weigang.li@intel.com> * Giovanni Cabiddu <giovanni.cabiddu@intel.com> */#ifndef
_CRYPTO_ACOMP_INT_H#define _CRYPTO_ACOMP_INT_
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<linux/platform_device.h>#include <linux/spi/pxa2xx_spi.h>
    Copyright (c) 2016, Intel Corporation. */#include <linux/device.h>#include <linux/mod_devicetable.h>#include <linux/i2c.h>#include
<linux/module.h>#include <linux/acpi.h>#include <linux/regmap.
    Copyright (c) 2016, Intel Corporation. */#include "test/nfit_test.h"#include <linux/mm.h>#include "../././drivers/dax/dax-private.h"
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<linux/netdevice.h>#include <linux/export.h>#include <asm/unaligne
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mq.h>#include <linux/elevator.h>#include <linux/bio.
Copyright (C) 2016 Jens Axboe */#include <linux/kernel.h>#include <linux/module.h>#include <linux/blk-mq.h>
Copyright (C) 2016 Jens Axboe */#include <linux/kernel.h>#include <linux/rculist.h>#include <linux/blk-mq.h>
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<errno.h>#include <signal.h>#include <stdio.h>#incl
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<linux/of.h>#include <linux/of_address.h>#include
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Google/HTC \* and an early endavour into mobil  
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<linux/of\_address.h>#include <linux/of\_device.h>#include <linux/platfo  
Copyright (C) 2016 Linaro Ltd \*/#include <linux/module.h>#include <linux/ulpi/driver.h>#include <linux/ulpi/regs.h>#include <linux/clock.h>#include  
<linux/regulator/consumer.h>#include <linux/of\_  
Copyright (C) 2016 Linaro Ltd \*/#include <linux/module.h>#include <linux/ulpi/driver.h>#include <linux/ulpi/regs.h>#include  
<linux/phy/phy.h>#include <linux/pinctrl/consumer.h>#include <linux/p  
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must use device\_property\_read\_u8\_array().  
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<linux/i2c.h>#include <linux/io.h>#include <linux/kernel  
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<linux/kernel.h>#include <linux/errno.h>  
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Copyright (c) 2016 MediaTek Inc.\* Author: Andrew-CT Chen <andrew-ct.chen@mediatek.com>\*/#include <linux/clock.h>#include  
<linux/debugfs.h>#include <linux/firmware.h>#include <linux/interrupt.h>#i  
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 Copyright (c) 2016 Mentor Graphics Inc. \*/#include <linux/delay.h>#include <linux/irq.h>#include <linux/module.h>#include  
 <linux/platform\_device.h>#include <linux/slab.h>#include <linux/spinloc  
 Copyright (c) 2016 Mentor Graphics Inc. \*/#include <linux/module.h>#include "imx-media.h"  
 Copyright (c) 2016 Mentor Graphics Inc. \*/#include <linux/of\_platform.h>#include <media/v4l2-ctrls.h>#include <media/v4l2-device.h>#include  
 <media/v4l2-fwnode.h>#include <media/v4l2-subdev.h>#i  
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 <linux/kernel.h>#include <linux/module.h>#inc  
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Copyright (c) 2016 Quentin Schulz <quentin.schulz@free-electrons.com> \* \* The Allwinner SoCs all have an ADC that can also act as a touchscreen \* controller and a thermal sensor. \* The thermal sen  
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Copyright (C) 2016 Red Hat, Inc. All Rights Reserved  
Copyright (C) 2016 Red Hat, Inc. \* Author: Michael S. Tsirkin <mst@redhat.com> \* \* Command line processing and common functions for ring benchmarking.  
Copyright (C) 2016 Red Hat, Inc. \* Author: Michael S. Tsirkin <mst@redhat.com> \* \* Common macros and functions for ring benchmarking.  
Copyright (C) 2016 Red Hat, Inc. \* Author: Michael S. Tsirkin <mst@redhat.com> \*/#include <linux/bits.h>#include <linux/compiler.h>#include <asm/byteorder.h>  
Copyright (C) 2016 Red Hat, Inc. \* Author: Michael S. Tsirkin <mst@redhat.com> \* \* Partial implementation of virtio 0.9. event index is used for signalling, \* unconditionally. Design roughly follo

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Copyright (C) 2016 Renesas Electronics Corp. \*/#ifndef \_\_DT\_BINDINGS\_CLOCK\_R8A7796\_CPG\_MSSR\_H\_\_#define \_\_DT\_BINDINGS\_CLOCK\_R8A7796\_CPG\_MSSR\_H\_\_

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Copyright (C) 2016 Sang Engineering \*/#include <linux/delay.h>#include <linux/err.h>#include <linux/interrupt.h>#include <linux/io.h>#include <linux/module.h>#include <linux/of\_device.h>#include

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Copyright (C) 2016, Semihalf * Author: Tomasz Nowicki <tn@semihalf.com> */
Copyright (C) 2016, Semihalf * Author: Tomasz Nowicki <tn@semihalf.com> * * This file implements early detection/parsing of I/O mapping *
reported to OS through firmware via I/O Remapping Table (I
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Copyright (C) 2016 Simon Guo, IBM Corporation. */#include <ppc-asm.h>#include "reg.h"
Copyright (c) 2016 Slawomir Stepien * Based on: Peter Rosin's code from mcp4531.c * * Datasheet:
http://ww1.microchip.com/downloads/en/DeviceDoc/22060b.pdf
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Copyright (C) 2016 Song Liu <songliubraving@fb.com> */#include <linux/kernel.h>#include <linux/wait.h>#include <linux/blkdev.h>#include
<linux/slab.h>#include <linux/raid/md_p.h>#include <linux
Copyright (C) 2016, Sony Mobile Communications Inc. * * This implementation is based on zbud written by Seth Jennings.
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<linux/interrupt.h>#include <linux/io.h>#include <linux/
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Copyright (C) 2016 Tomasz Chilinski <tomasz.chilinski@chilan.com> */
Copyright (c) 2016 Tom Haynes <loghыр@primarydata.com> */#ifndef _NFSD_FLEXFILELAYOUTXDR_H#define _NFSD_FLEXFILELAYOUTXDR_H 1
Copyright (c) 2016 Tom Haynes <loghыр@primarydata.com> */#include <linux/sunrpc/svc.h>#include <linux/nfs4.h>
Copyright (c) 2016 Tom Haynes <loghыр@primarydata.com> * * The following implements a super-simple flex-file server * where the NFSv4.1 mds is
also the ds. And the storage is * the same. I.e., wri

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Copyright (C) 2016 William Breathitt Gray \*/#include <linux/bitops.h>#include <linux/device.h>#include <linux/errno.h>#include <linux/gpio/driver.h>#include <linux/iio/iio.h>#include <linux/iio  
Copyright (C) 2016 William Breathitt Gray \*/#include <linux/device.h>#include <linux/dmi.h>#include <linux/errno.h>#include <linux/io.h>#include <linux/ioport.h>#include <linux/isa.h>#include  
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Copyright (c) 2016 Yusuke Fujimaki <usk.fujimaki@gmail.com> \* \* This module based on hid-ortek by  
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 <linux/if\_ether.h>#include <linux/if\_packet.h>#include <linux/ip.  
 Copyright (c) 2017-2018 Covalent IO, Inc. http://covalent.io#include <stdio.h>#include <stdlib.h>#include <sys/socket.h>#include  
 <sys/ioctl.h>#include <sys/select.h>#include <netinet/in.h>#incl  
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 \_\_EROFS\_INTERNAL\_H#define \_\_EROFS\_INTERNAL\_H  
 Copyright (C) 2017-2018 HUAWEI, Inc. \* http://www.huawei.com/ \* Created by Gao Xiang <gaoxiang25@huawei.com> \*/#ifndef  
 \_\_EROFS\_XATTR\_H#define \_\_EROFS\_XATTR\_H  
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 Copyright (C) 2017-2018 HUAWEI, Inc. \* http://www.huawei.com/ \* Created by Gao Xiang <gaoxiang25@huawei.com> \*/#include  
 "internal.h"#include <linux/prefetch.h>  
 Copyright (C) 2017-2018 HUAWEI, Inc. \* http://www.huawei.com/ \* Created by Gao Xiang <gaoxiang25@huawei.com> \*/#include  
 <linux/module.h>#include <linux/buffer\_head.h>#include <linux/  
 Copyright (C) 2017-2018 HUAWEI, Inc. \* http://www.huawei.com/ \* Created by Gao Xiang <gaoxiang25@huawei.com> \*/#include  
 <linux/security.h>#include "xattr.h"  
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 Copyright (C) 2017-2018 Jason A. Donenfeld <Jason@zx2c4.com>. All Rights Reserved  
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 \*/  
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 Copyright (c) 2017-2018 Western Digital Corporation or its \* affiliates. \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/uio.h>#include  
 <linux/falloc.h>#include <linux/file.h>#incl  
 Copyright (c) 2017-2019 Borislav Petkov, SUSE Labs. \*/#include <linux/mm.h>#include <linux/gfp.h>#include <linux/ras.h>#include  
 <linux/kernel.h>#include <linux/workqueue.h>  
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Copyright (c) 2017 Akinobu Mita <akinobu.mita@gmail.com> \* \* Datasheet: <https://datasheets.maximintegrated.com/en/ds/MAX1117-MAX1119.pdf>  
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Copyright (c) 2017 Andreas F \*/#ifndef DT\_BINDINGS\_POWER\_OWL\_S700\_POWERGATE\_H#define DT\_BINDINGS\_POWER\_OWL\_S700\_POWERGATE\_H  
Copyright (c) 2017 Andreas Klinger <ak@it-klinger.de> \* \* For details about the device see:  
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Copyright (C) 2017 ARM Ltd. \* Author: Marc Zyngier <marc.zyngier@arm.com> \*/  
Copyright (C) 2017 Arm Ltd.#define pr\_fmt(fmt) "sdei: " fmt  
Copyright (C) 2017 ARM Ltd. \*/#ifndef \_\_ASM\_DAIFFLAGS\_H#define \_\_ASM\_DAIFFLAGS\_H  
Copyright (C) 2017 ARM Ltd. \*/#ifndef \_\_ASM\_PAGE\_DEF\_H#define \_\_ASM\_PAGE\_DEF\_H  
Copyright (C) 2017 Arm Ltd.#ifndef \_\_ASM\_SDEI\_H#define \_\_ASM\_SDEI\_H  
Copyright (C) 2017 Arm Ltd.#ifndef \_\_ASM\_VMAP\_STACK\_H#define \_\_ASM\_VMAP\_STACK\_H  
Copyright (C) 2017 Arm Ltd.#ifndef \_\_LINUX\_ARM\_SDEI\_H#define \_\_LINUX\_ARM\_SDEI\_H  
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Copyright (C) 2017 Cavium, Inc. */#include <linux/bpf.h>#include <linux/netlink.h>#include <linux/rtnetlink.h>#include <assert.h>#include
<errno.h>#include <signal.h>#include <stdio.h>#include
Copyright (c) 2017 Chao Yu <chao@kernel.org> */#include <linux/compiler.h>#include <linux/proc_fs.h>#include <linux/f2fs_fs.h>#include
<linux/seq_file.h>#include <linux/unicode.h>
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Copyright (C) 2017 Cogent Embedded Inc. */#ifndef __DT_BINDINGS_POWER_R8A77970_SYSC_H__#define
__DT_BINDINGS_POWER_R8A77970_SYSC_H__
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<errno.h>#include <signal.h>#include <stdio.h>#include <st
Copyright (C) 2017 Cumulus Networks
Copyright (C) 2017 David Lechner <david@lechnology.com>
Copyright (C) 2017 David Lechner <david@lechnology.com> * * Based on: drivers/clk/keystone/gate.c
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Copyright (c) 2017 Dell Inc. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt
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Copyright (C) 2017 Dennis Zhou <denniszhou@gmail.com> * * The percpu allocator handles both static and dynamic areas. Percpu * areas
are allocated in chunks which are divided into units. There
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Copyright (C) 2017, EETS GmbH, Felix Brack <fb@ltec.ch> \* \* This driver implements a lcd device for the ORISE OTM3225A display \* controller. The control interface to the display is SPI and the disp  
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Copyright (C) 2017 Endless Mobile, Inc. \* Authors: David Yang <yangxiaohua@everest-semi.com>, \* Daniel Drake <drake@endlessm.com> \*  
Copyright (c) 2017, EPAM Systems \*/#include <linux/device.h>#include <linux/dma-buf.h>#include <linux/genalloc.h>#include <linux/slab.h>#include <linux/tee\_drv.h>#include "optee\_private.h"#inc  
Copyright (C) 2017 Eric Anholt \*/  
Copyright (C) 2017 Eric Nelson \*/#include <linux/module.h>#include <linux/i2c.h>#include <linux/slab.h>#include <linux/rtc.h>#include <linux/init.h>#include <linux/err.h>#include <linux/errno.h>  
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Copyright (c) 2017 Eva Rachel Retuya <eraretuya@gmail.com> \* \* 7-bit I2C slave address: 0x1D (ALT ADDRESS pin tied to VDDIO) or \* 0x53 (ALT ADDRESS pin grounded)  
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Copyright (C) 2017 Facebook// Author: Roman Gushchin <guro@fb.com>  
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Copyright (C) 2017 Facebook Inc.  
Copyright (c) 2017 Facebook \*/#include <linux/bpf.h>#include <linux/slab.h>#include <linux/vmalloc.h>#include <linux/etherdevice.h>#include <linux/filter.h>#include <linux/sched/signal.h>#incl  
Copyright (c) 2017 Facebook \*/#include <linux/slab.h>#include <linux/bpf.h>  
Copyright (c) 2017 Facebook \*/#include <stddef.h>#include <linux/bpf.h>#include <linux/pkt\_cls.h>#include "bpf\_helpers.h"  
Copyright (c) 2017 Facebook#include <stddef.h>#include <stdbool.h>#include <string.h>#include <linux/pkt\_cls.h>#include <linux/bpf.h>#include <linux/in.h>#include <linux/if\_ether.h>#include <linux/if\_packet.h>#include <linux/ip.h>#include <linux/ipv6.h>#i  
Copyright (c) 2017 Facebook \*/#include <stddef.h>#include <string.h>#include <linux/bpf.h>#include <linux/pkt\_cls.h>#include "bpf\_helpers.h"  
Copyright (c) 2017 Facebook \*/#include <stdio.h>#include <unistd.h>#include <fcntl.h>#include <stdlib.h>#include <signal.h>#include <linux/bpf.h>#include <string.h>#include <linux/perf\_event.h>  
Copyright (c) 2017 Facebook \*/#include <sys/socket.h>#include <arpa/inet.h>#include <stdint.h>#include <assert.h>#include <errno.h>#include <stdlib.h>#include <stdio.h>  
Copyright (c) 2017 Facebook \*/#include "test\_progs.h"#include "cgroup\_helpers.h"#include "bpf\_rlimit.h"#include <argp.h>#include <string.h>  
Copyright (c) 2017 Facebook \*/#include <uapi/linux/bpf.h>#include "bpf\_helpers.h"  
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Copyright (C) 2017 Free Electrons, \* Quentin Schulz <quentin.schulz@free-electrons.com> \* \* The Sama5d2 SoC has two audio PLLs (PMC and PAD) that shares the same parent \* (FRAC). FRAC can  
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Copyright (C) 2017 Glider bvba */#ifndef _DT_BINDINGS_CLOCK_R8A77995_CPG_MSSR_H__#define
__DT_BINDINGS_CLOCK_R8A77995_CPG_MSSR_H__
Copyright (C) 2017 Glider bvba */#ifndef __DT_BINDINGS_POWER_R8A77995_SYSC_H__#define __DT_BINDINGS_POWER_R8A77995_SYSC_H__
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Copyright (c) 2017 Google, Inc. * * Based on it87_wdt.c by Oliver Schuster * gpio-it8761e.c by Denis Turischev * gpio-stmpe.c by
Rabin Vincent
Copyright (C) 2017 Google, Inc. * Matthew Garrett <mjg59@google.com> * Thiebaud Weksteen <tweek@google.com> */#include
<linux/efi.h>#include <linux/tpm_eventlog.h>#include <asm/efi.h>
Copyright (C) 2017 Google, Inc. * Thiebaud Weksteen <tweek@google.com> */
Copyright (c) 2017 Google Inc * * VOUT_MODE is not supported by the device. The driver fakes VOUT linear16 * mode with exponent value -8 as direct
mode with m=256/b=0/R=0.
Copyright (c) 2017 Hans de Goede <hdegoede@redhat.com> */
Copyright (C) 2017 Hans de Goede <hdegoede@redhat.com> */
Copyright (C) 2017 Hans de Goede <hdegoede@redhat.com> * * Based on various non upstream patches to support the CHT Whiskey Cove PMIC:
Copyright (C) 2017 Hans de Goede <hdegoede@redhat.com> * * Loosely based on android x86 kernel code which is: *
Copyright (C) 2017 Hans de Goede <hdegoede@redhat.com> * * Note the quirks in this file are shared with fbdev/efib and as such * must not depend
on other drm code.
Copyright (C) 2017 Hans de Goede <hdegoede@redhat.com> * * Some Intel Cherry Trail based device which ship with Windows 10, have * this weird
INT33FE ACPI device with a CRS table with 4 I2cSerialBu
Copyright (C) 2017 Hans P. Moller <hmoller@uc.cl> */
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Copyright (C) 2017 Hauke Mehrstens <hauke@hauke-m.de> */
Copyright (C) 2017 Hauke Mehrstens <hauke@hauke-m.de> * Based on EP93xx wdt driver */
Copyright (C) 2017 Hauke Mehrstens <hauke@hauke-m.de> */#ifndef _DT_BINDINGS_MIPS_LANTIQ_RCU_GPHY_H#define
__DT_BINDINGS_MIPS_LANTIQ_RCU_GPHY_H
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Copyright (C) 2017 Hisilicon Limited * Author: Anurup M <anurup.m@huawei.com> * Shaokun Zhang <zhangshaokun@hisilicon.com> * * This
code is based on the uncore PMUs like arm-cci and arm-cc
Copyright (C) 2017 Hisilicon Limited * Author: Shaokun Zhang <zhangshaokun@hisilicon.com> * Anurup M <anurup.m@huawei.com> * * This
code is based on the uncore PMUs like arm-cci and arm-cc
Copyright (c) 2017 HiSilicon Technologies Co., Ltd. */
Copyright (c) <2017> HiSilicon Technologies Co., Ltd.// http://www.hisilicon.com
Copyright (c) 2017 HiSilicon Technologies Co., Ltd. * * Simple HiSilicon phase clock implementation.
Copyright (C) 2017 Horms Solutions Ltd., Simon Horman
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```

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Copyright (c) 2017, Impinj, Inc. \* \* i.MX7 System Reset Controller (SRC) driver  
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<sys/mman.h>#include <sys/stat.h>#include <fcntl.h>#i  
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<samu.onkalo@intel.com> \* Jouni Högander <j  
Copyright (C) 2017 Intel Corporation/\* Functions to access TPS68470 power management chip. \*/  
Copyright (C) 2017 Intel Corporation \*/#ifndef \_LINUX\_FPGA\_MGR\_H#define \_LINUX\_FPGA\_MGR\_H  
Copyright (C) 2017, Intel Corporation \*/#include <linux/clk-provider.h>#include <linux/io.h>#include <linux/slab.h>#include "stratix10-clk.h"#include  
"clk.h"

```

Copyright (c) 2017, Intel Corporation. */#include <linux/device.h>#include <linux/hid-sensor-hub.h>#include <linux/iio/buffer.h>#include
<linux/iio/iio.h>#include <linux/iio/triggered_buffer.h>
Copyright (C) 2017 Intel Corporation */#include <linux/fpga/fpga-bridge.h>#include <linux/fpga/fpga-mgr.h>#include <linux/fpga/fpga-
region.h>#include <linux/idr.h>#include <linux/kernel.h>#incl
Copyright (C) 2017 Intel Corporation */#include <linux/fpga/fpga-bridge.h>#include <linux/idr.h>#include <linux/kernel.h>#include
<linux/module.h>#include <linux/of_platform.h>#include <linux/s
Copyright (C) 2017, Intel Corporation */#include <linux/slab.h>#include <linux/clk-provider.h>#include <linux/io.h>
Copyright (C) 2017, Intel Corporation */#include <linux/slab.h>#include <linux/clk-provider.h>#include <linux/of_device.h>#include
<linux/of_address.h>#include <linux/platform_device.h>
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Copyright (c) 2017 Jesper Dangaard Brouer, Red Hat Inc. * * Example howto extract XDP RX-queue info
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Copyright (c) 2017 Jesper Dangaard Brouer, Red Hat Inc. */#include <linux/bpf.h>#include <linux/filter.h>#include <linux/types.h>#include
<linux/mm.h>#include <linux/netdevice.h>#include <linux
Copyright(c) 2017 Jesper Dangaard Brouer, Red Hat, Inc. */#include <uapi/linux/if_ether.h>#include <uapi/linux/if_packet.h>#include
<uapi/linux/if_vlan.h>#include <uapi/linux/ip.h>#include <uapi
Copyright(c) 2017 Jesper Dangaard Brouer, Red Hat, Inc. */static const char * __doc__ = "XDP monitor tool, based on tracepoints\n"
Copyright(c) 2017 Jesper Dangaard Brouer, Red Hat, Inc. */static const char * __doc__ = " XDP redirect with a CPU-map type
\"BPF_MAP_TYPE_CPUMAP\"";
Copyright (c) 2017 Jesper Dangaard Brouer, Red Hat Inc. */static const char * __doc__ = " XDP RX-queue info extract example\n\n" "Monitor how
many packets per sec (pps) are received\n" "per NIC RX
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Copyright (C) 2017 Josh Poimboeuf <jpoimboe@redhat.com> */#ifndef _ORC_LOOKUP_H#define _ORC_LOOKUP_H
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Copyright (C) 2017 Linaro Ltd. */#ifndef __VENUS_HFI_CMDS_H__ #define __VENUS_HFI_CMDS_H__
Copyright (C) 2017 Linaro Ltd. */#ifndef __VENUS_HFI_HELPER_H__ #define __VENUS_HFI_HELPER_H__
Copyright (C) 2017 Linaro Ltd. */#ifndef __VENUS_HFI_MSGS_H__ #define __VENUS_HFI_MSGS_H__
Copyright (C) 2017 Linaro Ltd. */#ifndef __VENUS_HFI_VENUS_H__ #define __VENUS_HFI_VENUS_H__
Copyright (C) 2017 Linaro Ltd. */#ifndef __VENUS_HFI_VENUS_IO_H__ #define __VENUS_HFI_VENUS_IO_H__
Copyright (C) 2017 Linaro Ltd. */#ifndef __VENUS_VDEC_H__ #define __VENUS_VDEC_H__
Copyright (C) 2017 Linaro Ltd. */#ifndef __VENUS_VENC_H__ #define __VENUS_VENC_H__
Copyright (C) 2017 Linaro Ltd. */#include <linux/clock.h> #include <linux/init.h> #include <linux/ioctl.h> #include <linux/list.h> #include
<linux/module.h> #include <linux/of_device.h> #include <linux/
Copyright (C) 2017 Linaro Ltd. */#include <linux/clock.h> #include <linux/iopoll.h> #include <linux/list.h> #include <linux/mutex.h> #include
<linux/pm_runtime.h> #include <linux/slab.h> #include <linux/
Copyright (C) 2017 Linaro Ltd. */#include <linux/clock.h> #include <linux/module.h> #include <linux/mod_devicetable.h> #include
<linux/platform_device.h> #include <linux/pm_runtime.h> #include <linux/
Copyright (C) 2017 Linaro Ltd. */#include <linux/errno.h> #include <linux/hash.h>
Copyright (c) 2017, Linaro Ltd. */#include <linux/firmware.h> #include <linux/module.h> #include <linux/notifier.h> #include <linux/slab.h> #include
<linux/interrupt.h> #include <linux/io.h> #include
Copyright (C) 2017 Linaro Ltd. */#include <linux/hash.h> #include <linux/list.h> #include <linux/slab.h> #include <media/videobuf2-v4l2.h>
Copyright (C) 2017 Linaro Ltd. */#include <linux/kernel.h> #include <linux/module.h> #include <linux/debugfs.h> #include <linux/device.h> #include
<linux/platform_device.h> #include <linux/qtr.h> #include
Copyright (C) 2017 Linaro Ltd. */#include <linux/kernel.h> #include <linux/module.h> #include <linux/device.h> #include <linux/qtr.h> #include
<linux/net.h> #include <linux/completion.h> #include <linux/
Copyright (C) 2017 Linaro Ltd. */#include <linux/slab.h> #include <linux/mutex.h> #include <linux/list.h> #include <linux/completion.h> #include
<linux/platform_device.h> #include <linux/video2.h>
Copyright (C) 2017 Linaro Ltd. */#include <linux/slab.h> #include <linux/uaccess.h> #include <linux/module.h> #include <linux/kernel.h> #include
<linux/errno.h> #include <linux/string.h> #include <linux/
Copyright (C) 2017 Linaro Ltd. */#include <linux/types.h> #include <media/v4l2-ctrls.h>
Copyright (C) 2017 Linaro, Ltd., Rob Herring <robh@kernel.org> * * Based on hci_ldisc.c: *
Copyright (C) 2017 Linus Walleij */#include <linux/kernel.h> #include <linux/module.h> #include <linux/platform_device.h> #include
<linux/gpio/consumer.h> #include <linux/of.h> #include <linux/of_de
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MCU with some firmware in its internal 8KB flash.  
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Copyright (c) 2017 MediaTek Inc. \* Author: Yong Wu <yong.wu@mediatek.com> \*/#ifndef \_\_DTS\_IOMMU\_PORT\_MT2712\_H#define  
\_\_DTS\_IOMMU\_PORT\_MT2712\_H

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<media/v4l2-mc.h>#include <media/v4l2-subdev.h>#include
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Copyright (c) 2017, Microsemi Corporation
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Copyright (c) 2017 Microsemi Corporation */#include <asm/machine.h>#include <asm/prom.h>
Copyright (c) 2017 Microsemi Corporation */#include <linux/bitops.h>#include <linux/irq.h>#include <linux/of_address.h>#include
<linux/of_irq.h>#include <linux/irqchip.h>#include <linux/irqchip
Copyright (c) 2017 Microsemi Corporation */#include <linux/delay.h>#include <linux/io.h>#include <linux/notifier.h>#include
<linux/mfd/syscon.h>#include <linux/of_address.h>#include <linux/of_d
Copyright (c) 2017 Microsemi Corporation */#include <linux/etherdevice.h>#include <linux/ethtool.h>#include <linux/if_bridge.h>#include
<linux/if_ether.h>#include <linux/if_vlan.h>#include <lin
Copyright (c) 2017 Microsemi Corporation */#include <linux/interrupt.h>#include <linux/module.h>#include <linux/of_net.h>#include
<linux/netdevice.h>#include <linux/of_mdio.h>#include <linux/of
Copyright (c) 2017 Microsemi Corporation */#include <linux/io.h>#include <linux/kernel.h>#include <linux/platform_device.h>
Copyright (c) 2017 Microsemi Corporation */#include "ocelot.h"#include <soc/mscc/ocelot_hsio.h>
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XArray.
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Copyright (c) 2017, Microsoft Corporation. */#include <linux/module.h>#include <linux/vmalloc.h>#include <linux/hyperv.h>#include
<net/sock.h>#include <net/af_vsock.h>
Copyright (C) 2017 MIPS Tech, LLC */#ifndef __ASM_SERIAL_H#define __ASM_SERIAL_H
Copyright (C) 2017 Mobica Limited * * This driver is inspired by the 4.6.2 version of net/can/usb/usb_8dev.c
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<fw@strlen.de>.  
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Copyright (C) 2017 Paolo Valente <paolo.valente@linaro.org> \* \* BFQ is a proportional-share I/O scheduler, with some extra \* low-latency capabilities.  
BFQ also supports full hierarchical \* schedul  
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Copyright (C) 2017 Pengutronix, Juergen Borleis <jbe@pengutronix.de> \*/#include <linux/dsa/lan9303.h>#include <linux/etherdevice.h>#include  
<linux/list.h>#include <linux/slab.h>  
Copyright (C) 2017 Pengutronix, Juergen Borleis <kernel@pengutronix.de> \*/#include <linux/kernel.h>#include <linux/module.h>#include  
<linux/gpio/consumer.h>#include <linux/regmap.h>#include <lin  
Copyright (C) 2017 Pengutronix, Juergen Borleis <kernel@pengutronix.de> \*/#include <linux/kernel.h>#include <linux/module.h>#include  
<linux/i2c.h>#include <linux/of.h>  
Copyright (C) 2017 Pengutronix, Juergen Borleis <kernel@pengutronix.de> \* \* Partially based on a patch from  
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Copyright (C) 2017 Pengutronix, Philipp Zabel <kernel@pengutronix.de> \* \* Based on Allwinner SoCs Reset Controller driver  
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<linux/interrupt.h>#include <linux/module.h>#include <linux/of\_grap  
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that the above \* copyright notice and this  
Copyright (C) 2017 Peter Meerwald-Stadler <pmeerw@pmeerw.net> \* \* 7-bit I2C chip address: 0x57 \* TODO: proximity power saving feature  
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Copyright (c) 2017-present, Facebook, Inc. \*/#include <linux/crypto.h>#include <linux/init.h>#include <linux/interrupt.h>#include  
<linux/mm.h>#include <linux/module.h>#include <linux/net.h>#in  
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<linux/compiler.h>#include <subcmd/parse-options.h>#include "debug
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<linux/module.h>#include <uapi/linux/kvm_para.h>#inclu
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Copyright (c) 2017 Ruslan Bilovol <ruslan.bilovol@gmail.com> * * This file holds USB constants and structures defined * by the USB DEVICE CLASS
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<linux/iopoll.h>#include <linux/mdio.h>#include <linux/mfd/syscon.h>#i
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<linux/mfd/mt6323/registers.h>#include <linux/mfd/mt6397/core.h>#include <linux/m
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<linux/mod_devicetable.h>#include <linux/init.h>#include <linux/device.h>#incl

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and can use the SBI to setup \* events.  
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"nl80211.h"#include "rdev-ops.h"  
Copyright (C) 2018-2019 Intel Corporation \*/#include <linux/export.h>#include <linux/bitops.h>#include <linux/etherdevice.h>#include  
<linux/slab.h>#include <linux/ieee80211.h>#include <net/cfg8  
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<signal.h>#include <inttypes.h>  
Copyright (C) 2018-2019 Jens Axboe \*/#include <stdio.h>#include <fcntl.h>#include <string.h>#include <stdlib.h>#include <unistd.h>#include  
<assert.h>#include <errno.h>#include <inttypes.h>#i  
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Copyright (c) 2018 Amlogic, inc. * Author: Qiufang Dai <qiufang.dai@amlogic.com> */#include <linux/clk-provider.h>#include
<linux/platform_device.h>#include <linux/reset-controller.h>#include <
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Copyright (C) 2018 Andes Technology Corporation <zong@andestech.com> */
Copyright (C) 2018 Andreas Brauchli <andreas.brauchli@sensirion.com> * * I2C slave address: 0x58
Copyright (c) 2018 Andrew Lutomirski */
Copyright (C) 2018 Anup Patel <anup@brainfault.org> */#include <linux/kernel.h>#include <linux/console.h>#include <linux/init.h>#include
<linux/serial_core.h>#include <asm/sbi.h>
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Copyright (C) 2018 ARM Limited */#ifndef __ASM_VDSO_GETTIMEOFDAY_H#define __ASM_VDSO_GETTIMEOFDAY_H
Copyright (C) 2018 ARM Limited */#ifndef __COMPAT_BARRIER_H#define __COMPAT_BARRIER_H
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Copyright (C) 2018 - Arm Ltd
Copyright (C) 2018 ARM Ltd. */
Copyright (C) 2018 ARM Ltd, All Rights Reserved
Copyright (C) 2018 ARM Ltd. */#include <linux/device.h>#include <linux/types.h>
Copyright (C) 2018 ARM Ltd. * Sudeep Holla <sudeep.holla@arm.com> */
Copyright (c) 2018, Arm Ltd. * Written by: Quentin Perret, Arm Ltd. */
Copyright (C) 2018, ARM * * This file implements parsing of the Processor Properties Topology Table * which is optionally used to describe the processor

```

and cache topology. \* Due to the relative p  
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Copyright (C) 2018 \* Author: Jeff LaBundy <jeff@labundy.com> \* \* These devices require firmware exported from a PC-based configuration tool \* made  
available by the vendor. Firmware files may be pu  
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Copyright (C) 2018 BayLibre, SAS \* Author: Maxime Jourdan <mjourdan@baylibre.com> \* \* The Elementary Stream Parser is a HW bitstream parser. \*  
It reads bitstream buffers and feeds them to the VIFI  
Copyright (C) 2018 BayLibre, SAS \* Author: Maxime Jourdan <mjourdan@baylibre.com> \* \* VDEC\_1 is a video decoding block that allows decoding of \*  
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Copyright (C) 2018 BayLibre, SAS \* Author: Neil Armstrong <narmstrong@baylibre.com> \*/  
Copyright (C) 2018 BayLibre, SAS \*/#ifndef \_\_SOC\_MESON\_CANVAS\_H#define \_\_SOC\_MESON\_CANVAS\_H  
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Copyright (c) 2018 Broadcom. All Rights Reserved  
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Copyright (c) 2018 Broadcom Inc  
Copyright (C) 2018 Broadcom \*/#include <linux/delay.h>#include <linux/device.h>#include <linux/io.h>#include <linux/module.h>#include  
<linux/of.h>#include <linux/platform\_device.h>#include <li  
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Copyright (C) 2018 Cavium Inc. \* Author: Ganapatrao Kulkarni <gkulkarni@cavium.com> \*/  
Copyright (c) 2018 Cavium, Inc. \*/#include <linux/bcd.h>#include <linux/delay.h>#include <linux/i2c.h>#include <linux/module.h>#include  
<linux/mutex.h>#include <linux/nvmem-provider.h>#include  
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Copyright (c) 2018 Chris Coffey <cmc@babblebit.net> \* Based on: Slawomir Stepien's code from mcp4131.c \* \* Datasheet:  
http://ww1.microchip.com/downloads/en/devicedoc/11195c.pdf  
Copyright (c) 2018 Christian Hewitt  
Copyright (C) 2018 Christophe Leroy CS Systemes d \* \* Inspired from glibc implementation  
Copyright (C) 2018 Christoph Hellwig \*/  
Copyright (C) 2018 Christoph Hellwig \*/#define pr\_fmt(fmt) "plic: " fmt#include <linux/interrupt.h>#include <linux/io.h>#include <linux/irq.h>#include  
<linux/irqchip.h>#include <linux/irqdomain  
Copyright (C) 2018 Christoph Hellwig. \* \* DMA operations that map physical memory directly without using an IOMMU.  
Copyright (c) 2018 Christoph Hellwig. \*/#include <linux/module.h>#include <linux/compiler.h>#include <linux/fs.h>#include <linux/iomap.h>#include  
<linux/pagemap.h>#include <linux/pagevec.h>  
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Copyright (C) 2018 Cogent Embedded, Inc. \* \* Based on r8a7795-cpg-mssr.c  
Copyright (C) 2018 Cogent Embedded, Inc. \*/#ifndef \_\_DT\_BINDINGS\_CLOCK\_R8A77980\_CPG\_MSSR\_H\_\_#define  
\_\_DT\_BINDINGS\_CLOCK\_R8A77980\_CPG\_MSSR\_H\_\_  
Copyright (C) 2018 Cogent Embedded, Inc. \*/#ifndef \_\_DT\_BINDINGS\_POWER\_R8A77980\_SYSC\_H\_\_#define  
\_\_DT\_BINDINGS\_POWER\_R8A77980\_SYSC\_H\_\_  
Copyright (C) 2018 Cogent Embedded, Inc. \* \* This file is based on the drivers/pinctrl/sh-pfc/pfc-r8a7795.c  
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Copyright (c) 2018, Covalent IO, Inc. http://covalent.io  
Copyright (c) 2018 Covalent IO, Inc. http://covalent.io#define SOCKMAP#define TEST\_MAP\_TYPE BPF\_MAP\_TYPE SOCKMAP  
Copyright (c) 2018 Covalent IO, Inc. http://covalent.io \*/#include <uapi/linux/btf.h>#include <linux/kernel.h>#include <linux/types.h>#include  
<linux/slab.h>#include <linux/bpf.h>#include <linu  
Copyright (c) 2018 Covalent IO, Inc. http://covalent.io#undef SOCKMAP#define TEST\_MAP\_TYPE BPF\_MAP\_TYPE SOCKHASH  
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Copyright (C) 2018 David Lechner <david@lechnology.com> \*/  
Copyright (C) 2018 David Lechner <david@lechnology.com> \* \* Based on arch/arm/mach-davinci/clock.c  
Copyright (C) 2018 Davidlohr Bueso. \* \* Benchmark the various operations allowed for epoll\_ctl(2). \* The idea is to concurrently stress a single epoll  
instance  
Copyright (C) 2018 Davidlohr Bueso. \* \* This program benchmarks concurrent epoll\_wait(2) monitoring multiple \* file descriptors under one or two load  
balancing models. The first, \* and default, is  
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Copyright (C) 2018 Digital Devices GmbH  
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Copyright (c) 2018 Dmitry V. Levin <ldv@altlinux.org> \* All rights reserved  
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Copyright (C) 2018 embedded brains GmbH \*/#include "libfdt\_env.h"  
Copyright (C) 2018 EPAM Systems Inc. \* \* Author: Oleksandr Andrushchenko <oleksandr\_andrushchenko@epam.com>  
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Copyright (C) 2018 Facebook
Copyright (C) 2018 Facebook// Author: Yonghong Song <yhs@fb.com>
Copyright (c) 2018 Facebook * * BPF program to automatically reflect TOS option from received syn packet
Copyright (c) 2018 Facebook#ifndef _UAPI__LINUX_BT_H__#define _UAPI__LINUX_BT_H__
Copyright (c) 2018 Facebook, Inc. * Author: Johannes Weiner <hannes@cmpxchg.org> * * Polling support by Suren Baghasaryan
<surenb@google.com>
Copyright (c) 2018 Facebook#include <linux/bpf.h>#include "bpf_helpers.h"
Copyright (c) 2018 Facebook */#include <linux/bpf.h>#include <linux/err.h>#include <linux/sock_diag.h>#include <net/sock_reuseport.h>
Copyright (c) 2018 Facebook#include <stddef.h>#include <linux/bpf.h>#include <linux/types.h>#include "bpf_helpers.h"
Copyright (c) 2018 Facebook#include <uapi/linux/bpf.h>#include "bpf_helpers.h"
Copyright (C) 2018 Felix Fietkau <nbd@nbd.name> */#include "mt76.h"
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Copyright (C) 2018 Gateworks Corporation */
Copyright (C) 2018 Gateworks Corporation */#include <linux/delay.h>#include <linux/hdmi.h>#include <linux/i2c.h>#include <linux/init.h>#include
<linux/interrupt.h>#include <linux/kernel.h>#inc
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Copyright (C) 2018 Google, Inc. * * Author: schuffelen@google.com
Copyright (C) 2018 Google, Inc. */#ifndef __APEX_H__#define __APEX_H__
Copyright (C) 2018 Google, Inc.#ifndef __GASKET_CONSTANTS_H__#define __GASKET_CONSTANTS_H__
Copyright (C) 2018 Google, Inc. */#ifndef __GASKET_CORE_H__#define __GASKET_CORE_H__
Copyright (C) 2018 Google, Inc. */#ifndef __GASKET_H__#define __GASKET_H__
Copyright (C) 2018 Google, Inc. */#ifndef __GASKET_INTERRUPT_H__#define __GASKET_INTERRUPT_H__
Copyright (C) 2018 Google, Inc.#ifndef __GASKET_IOCTL_H__#define __GASKET_IOCTL_H__
Copyright (C) 2018 Google, Inc.#include "gasket.h"#include "gasket_ioctl.h"#include "gasket_constants.h"#include "gasket_core.h"#include
"gasket_interrupt.h"#include "gasket_page_table.h"#inclu
Copyright (C) 2018 Google, Inc.#include "gasket_sysfs.h"
Copyright (c) 2018 Google, Inc. * * When CPU, memory and IO are contended, tasks experience delays that * reduce throughput and introduce latencies
into the workload. Memory * and IO contention, i
Copyright (C) 2018 Google LLC */
Copyright (C) 2018, Google LLC.
Copyright (C) 2018, Google LLC. */
Copyright (C) 2018, Google LLC. * * Adaptions for s390x:
Copyright (C) 2018, Google LLC. * * Header file that describes API to the sparsebit library. * This library provides a memory efficient means of storing *
the settings of bits indexed via a uint64
Copyright (C) 2018, Google LLC. * * IA32_TSC_ADJUST test
Copyright (C) 2018, Google LLC. */#ifndef SELFTEST_KVM_UTIL_H#define SELFTEST_KVM_UTIL_H
Copyright (C) 2018, Google LLC. * * This is a regression test for the bug fixed by the following commit:
Copyright (C) 2018, Google LLC. * * Verifies expected behavior of x86 KVM_CAP_SYNC_REGS functionality, * including requesting an invalid register set,
updates to/from values * in kvm_run.s.regs wh
Copyright (c) 2018 Guenter Roeck */
Copyright (c) 2018 Guillaume Dou * * Implementation inspired by existing x86 platform drivers, in special * asus/eepc/fujitsu-laptop, thanks to their
authors.
Copyright (C) 2018 Hangzhou C-SKY Microsystems co.,ltd.
Copyright (C) 2018 Hangzhou C-SKY Microsystems co.,ltd.#include <linux/kernel.h>#include <linux/module.h>#include <linux/init.h>#include
<linux/delay.h>
Copyright (c) 2018 Hanno Zulla <kontakt@hanno.de> */
Copyright (C) 2018 Hans de Goede <hdegoede@redhat.com> * * Based on various non upstream patches for ISP support:
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```



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Copyright (C) 2018 Himanshu Jha <himanshujha199640@gmail.com> */#include <linux/acpi.h>#include <linux/module.h>#include
<linux/of.h>#include <linux/regmap.h>#include <linux/spi/spi.h>
Copyright (c) 2018 Hisilicon Limited. */
Copyright (c) 2018 HiSilicon Technologies Co., Ltd. */
Copyright (C) 2018 HUAWEI, Inc. * http://www.huawei.com/ * Created by Gao Xiang <gaoxiang25@huawei.com> */#ifndef
__EROFS_FS_ZDATA_H#define __EROFS_FS_ZDATA_H
Copyright (C) 2018 HUAWEI, Inc. * http://www.huawei.com/ * Created by Gao Xiang <gaoxiang25@huawei.com> */#ifndef
__EROFS_FS_ZPVEC_H#define __EROFS_FS_ZPVEC_H
Copyright (C) 2018 HUAWEI, Inc. * http://www.huawei.com/ * Created by Gao Xiang <gaoxiang25@huawei.com> */#include
"internal.h"#include <linux/pagevec.h>
Copyright (C) 2018 HUAWEI, Inc. * http://www.huawei.com/ * Created by Gao Xiang <gaoxiang25@huawei.com> */#include "zdata.h"#include
"compress.h"#include <linux/prefetch.h>
Copyright (c) 2018 IBM Corp.
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Copyright (C) 2018 IBM Corporation */
Copyright (C) 2018 IBM Corporation. */#include <limits.h>#include <linux/kernel.h>#include <sys/mman.h>#include <sys/types.h>#include
<sys/stat.h>#include <sys/time.h>#include <sys/resource.h>
Copyright (C) 2018 IBM Corporation */#include <linux/efi.h>#include <linux/module.h>#include <linux/ima.h>
Copyright (C) 2018 IBM Corporation. */#include "ptrace.h"#include "child.h"
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Copyright (C) 2018 Intel Corporation * * Author: Lu Baolu <baolu.lu@linux.intel.com>
Copyright (C) 2018 Intel Corporation * Author: Rafael J. Wysocki <rafael.j.wysocki@intel.com> * * The idea of this governor is based on the observation
that on many systems * timer events are two
Copyright (C) 2018 Intel Corporation * Author: Rafael J. Wysocki <rafael.j.wysocki@intel.com> * * This driver is based on Section 9.18 of the ACPI 6.2

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specification revision.

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Copyright (C) 2018 Intel Corporation * * Contact Information:
Copyright (C) 2018, Intel Corporation * Copied from reset-sunxi.c */
Copyright (C) 2018 Intel Corporation */#ifndef __NET_WIRELESS_NL80211_H#define __NET_WIRELESS_NL80211_H
Copyright (C) 2018 Intel Corporation */#include <linux/completion.h>#include <linux/fpga/fpga-mgr.h>#include <linux/firmware/intel/stratix10-svc-
client.h>#include <linux/module.h>#include <linux
Copyright (C) 2018 Intel Corporation */#include <linux/slab.h>#include <linux/kernel.h>#include <linux/if_arp.h>#include
<linux/netdevice.h>#include <linux/rtnetlink.h>#include <net/mac80211.h>
Copyright (C) 2018 Intel Corporation */#include <linux/types.h>#include <net/cfg80211.h>#include "ieee80211_i.h"#include "sta_info.h"#include
"driver-ops.h"
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header files.
Copyright (C) 2018 Intel Corporation * * Specification for this interface is available at: *
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Copyright (C) 2018 IVI Foundation, Inc. * * This file holds USB constants defined by the USB Device Class * and USB488 Subclass Definitions for Test
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Copyright (C) 2018 Jacopo Mondi <jacopo */
Copyright (C) 2018 Jacopo Mondi <jacopo */
Copyright (C) 2018 Jacopo Mondi <jacopo * * Based on Renesas R-Car M3-W System Controller
Copyright (C) 2018 Jacopo Mondi <jacopo */#ifndef __DT_BINDINGS_CLOCK_R8A77965_CPG_MSSR_H__define
__DT_BINDINGS_CLOCK_R8A77965_CPG_MSSR_H__
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Copyright (C) 2018 Jacopo Mondi <jacopo@jmondi.org> * * Based on mt9v032 driver
Copyright (c) 2018 James.Bottomley@HansenPartnership.com * * CFB is a stream cipher mode which is layered on to a block * encryption scheme. It
works very much like a one time pad where * the pad
Copyright (C) 2018 Jan Breuer <jan.breuer@jaybee.cz> * * Improve driver for i2cdetect from i2c-tools to detect i2c devices on the bus.
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Copyright (c) 2018 Jesper Dangaard Brouer, Red Hat Inc.
Copyright (c) 2018 Jesper Dangaard Brouer, Red Hat Inc. * * Example howto transfer info from XDP to SKB, e.g. skb->mark
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Copyright(c) 2018 Jesper Dangaard Brouer. * * XDP/TC VLAN manipulation example
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Copyright (C) 2018 Johan Hovold <johan@kernel.org> */
Copyright (C) 2018 Johannes Thumshirn <jth@kernel.org> */#include <linux/io.h>#include <linux/kernel.h>#include <linux/mcb.h>#include
<linux/module.h>#include <linux/watchdog.h>
Copyright (c) 2018 Johannes Thumshirn, SUSE Linux GmbH */
Copyright (c) 2018 Johannes Thumshirn, SUSE Linux GmbH * * This is entirely based on drivers/nvme/host/trace.h
Copyright (C) 2018 John Crispin <john@phrozen.org> */
Copyright (c) 2018 Jonathan Marek <jonathan@marek.ca> * * Based on msm_pwm_vibrator.c from downstream Android sources:
Copyright (C) 2018 Josef Bacik */#include <linux/kernel.h>#include <linux/blk_types.h>#include <linux/backing-dev.h>#include
```

```

<linux/module.h>#include <linux/timer.h>#include <linux/memcontrol.h
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Copyright (c) 2018 Julian Sax <jsbc@gmx.de> *
Copyright (C) 2018 Kent Gustavsson <kent@minoris.se> */#include <linux/clk.h>#include <linux/delay.h>#include <linux/err.h>#include
<linux/iio/iio.h>#include <linux/module.h>#include <linux/reg
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Copyright (C) 2018 Kontron Electronics GmbH ** Transition to SPI MEM interface:
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Copyright (C) 2018 Laurent Pinchart <laurent.pinchart@ideasonboard.com> ** Based on work from Jyri Sarha <jsarha@ti.com>
Copyright (C) 2018 Laurent Pinchart <laurent.pinchart@ideasonboard.com> */#ifndef __RCAR_DU_OF_H__#define __RCAR_DU_OF_H__
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Copyright (C) 2018 Linaro Ltd. ** Author: Stanimir Varbanov <stanimir.varbanov@linaro.org>
Copyright (C) 2018 Linaro Ltd ** Based on OV5640 Sensor Driver
Copyright (c) 2018 Linaro Ltd. */#ifndef DT_BINDINGS_POWER_OWL_S900_POWERGATE_H#define
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Copyright(c) 2018 Linus Torvalds. All rights reserved
Copyright (C) 2018 Linus Walleij <linus.walleij@linaro.org> * Parts of this file were based on the MCDE driver by Marcus Lorentzon
Copyright (C) 2018 Linus Walleij ** This module binds to the "arm,vexpress-muxfpga" device on the * Versatile Express configuration bus and sets up
which CLCD instance * gets muxed out on the DVI
Copyright (C) 2018 Linus Wallej <linus.walleij@linaro.org> * Includes portions of code from the firmware uploader by:
Copyright (C) 2018 Lorenzo Bianconi <lorenzo.bianconi83@gmail.com>
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Copyright (C) 2018 Lorenzo Bianconi <lorenzo.bianconi83@gmail.com> */#include <linux/kernel.h>#include <linux/firmware.h>
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<linux/module.h>
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Copyright (c) 2018 Manivannan Sadhasivam ** RDA8810PL has two independent timers: OSTIMER (56 bit) and HWTIMER (64 bit). * Each timer

```

```

provides optional interrupt support. In this driver, OSTIMER i
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<linux/iio/sysfs.h>#include <linux/init.h>#include <linux/module.h>
Copyright (C) 2018 Marek Behun <marek.behun@nic.cz> */
Copyright (C) 2018 Marek Vasut <marex@denx.de> */#include <linux/delay.h>#include <linux/ethtool.h>#include <linux/kernel.h>#include
<linux/mii.h>#include <linux/module.h>#include <linux/phy.h>
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Copyright (c) 2018 MediaTek Inc. * Author: Yong Wu <yong.wu@mediatek.com> */#ifndef __DTS_IOMMU_PORT_MT8183_H#define
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Copyright (C) 2018 MediaTek Inc. * * Author: Zhiyong Tao <zhiyong.tao@mediatek.com>
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<linux/of.h>#include <linux/of_device.h>#inclu
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```

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sheets/AD5272_5274.pdf
  Copyright (c) 2018 Politecnico di Torino#define MAP_TYPE BPF_MAP_TYPE_QUEUE
  Copyright (c) 2018 Politecnico di Torino#define MAP_TYPE BPF_MAP_TYPE_STACK
  Copyright (c) 2018 Politecnico di Torino */#include <linux/bpf.h>#include <linux/list.h>#include <linux/slab.h>#include <linux/capability.h>#include
"percpu_freelist.h"
  Copyright (c) 2018 Politecnico di Torino#include <stddef.h>#include <string.h>#include <linux/bpf.h>#include <linux/if_ether.h>#include
<linux/ip.h>#include <linux/pkt_cls.h>#include "bpf_helpe
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  Copyright (C) 2018, Red Hat, Inc. (code style cleanup and fuzzing driver) * * This library provides functions to support a memory efficient bit array, *
with an index size of 2^64. A sparsebit arr
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  Copyright (C) 2018 Red Hat, Inc. */#ifndef __SS_SIDTAB_H__define __SS_SIDTAB_H__
  Copyright (C) 2018, Red Hat, Inc. */#include "kvm_util.h"
  Copyright (C) 2018, Red Hat, Inc. */#include "kvm_util.h"#include "../kvm_util_internal.h"
  Copyright (C) 2018 Red Hat, Inc. */#include <linux/errno.h>#include <linux/kernel.h>#include <linux/slab.h>#include <linux/sched.h>#include
<linux/spinlock.h>#include <asm/barrier.h>#include "
  Copyright (C) 2018, Red Hat, Inc. * * Tests for Enlightened VMCS, including nested guest state.
  Copyright (C) 2018, Red Hat, Inc. * * Tests for SMM.
  Copyright (C) 2018, Red Hat, Inc. * * Tests for vCPU state save/restore, including nested guest state.
  Copyright (c) 2018 Red Hat, Inc. * * This is a test "dust" device, which fails reads on specified * sectors, emulating the behavior of a hard disk drive
sending * a "Read Medium Error" sense.
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__DT_BINDINGS_CLOCK_R8A774A1_CPG_MSSR_H__

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Copyright (c) 2018 Rodrigo Rivas Costa <rodrigorivascosta@gmail.com> \* \* Supports both the wired and wireless interfaces.  
Copyright (C) 2018 ROHM Semiconductors  
Copyright (C) 2018 ROHM Semiconductors// bd70528-regulator.c ROHM BD70528MWV regulator driver  
Copyright (C) 2018 ROHM Semiconductors// bd71837-regulator.c ROHM BD71837MWV/BD71847MWV regulator driver  
Copyright (C) 2018 ROHM Semiconductors// gpio-bd70528.c ROHM BD70528MWV gpio driver  
Copyright (C) 2018 ROHM Semiconductors// ROHM BD70528MWV watchdog driver  
Copyright (C) 2018 Russell King, Deep Blue Solutions Ltd. \* \* Partly derived from CP110 comphy driver by Antoine Tenart \*  
<antoine.tenart@bootlin.com>  
Copyright (c) 2018 Samsung Electronics Co., Ltd.// Author: Marek Szyprowski <m.szyprowski@samsung.com>// Common Clock Framework support for  
Exynos5 power-domain dependent clocks  
Copyright (C) 2018 Samsung Electronics Co., Ltd \* \* Authors:  
Copyright (c) 2018 Samsung Electronics Co., Ltd. \* <http://www.samsung.com> \* \* Based on buddha.c: \*  
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Copyright (C) 2018 Song Qiang <songqiang1304521@gmail.com> \* \* Datasheet available at \*  
<<https://www.st.com/resource/en/datasheet/v15310x.pdf>>  
Copyright (C) 2018 Song Qiang <songqiang1304521@gmail.com> \* \* i2c slave address: 0x20 + SA1 << 1 + SA0.  
Copyright (C) 2018 Song Qiang <songqiang1304521@gmail.com> \* \* User Manual available at \* <<https://www.pnicorp.com/download/rm3100-user-manual/>>

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Copyright (C) 2018 Stanislaw Gruszka <stf\_xl@wp.pl> \*/  
Copyright (C) 2018 Stanislaw Gruszka <stf\_xl@wp.pl> \*/#ifndef \_MT76X0\_PHY\_H\_#define \_MT76X0\_PHY\_H\_  
Copyright (C) 2018 Stefan Agner <stefan@agner.ch>  
Copyright (C) 2018 Stefan Wahren <stefan.wahren@i2se.com> \*/#include <linux/device.h>#include <linux/err.h>#include <linux/hwmon.h>#include  
<linux/module.h>#include <linux/platform\_device.h>#in  
Copyright (C) 2018 STMicroelectronics \* Author(s): Amelie Delaunay <amelie.delaunay@st.com>. \*/#include <linux/bitfield.h>#include  
<linux/clk.h>#include <linux/delay.h>#include <linux/io.h>#inc  
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Copyright (C) 2018 Texas Instruments Incorporated - http://www.ti.com \* Author: Peter Ujfalusi <peter.ujfalusi@ti.com> \*/  
Copyright (C) 2018 Texas Instruments Incorporated - http://www.ti.com/ \* \* Based on cpsw-phy-sel.c driver created by Mugunthan V N  
<mugunthanvnm@ti.com>  
Copyright (C) 2018 Texas Instruments Incorporated - http://www.ti.com/ \*/#ifndef \_DT\_BINDINGS\_PINCTRL\_TI\_K3\_H#define  
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Copyright (C) 2018 Vasily Khoruzhick <anarsoul@gmail.com> \*/  
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Copyright (c) 2018 Vincent Pelletier \*/  
Copyright (c) 2018 Vitaly Chikunov <vt@altlinux.org>  
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Copyright (c) 2018 VMware, Inc., Palo Alto, CA., USA  
Copyright (C) 2018 Western Digital Corporation  
Copyright (C) 2018 Western Digital Corporation \*/  
Copyright (C) 2018 Western Digital Corporation \*/#ifndef SCSI\_BSG\_UFS\_H#define SCSI\_BSG\_UFS\_H  
Copyright (C) 2018 Western Digital Corporation \*/#ifndef UFS\_BSG\_H#define UFS\_BSG\_H  
Copyright (C) 2018 Western Digital Corporation \*/#include "ufs\_bsg.h"  
Copyright (C) 2018 William Breathitt Gray



Copyright (C) 2018 William Breathitt Gray \*/#ifndef \_COUNTER\_ENUM\_H#define \_COUNTER\_ENUM\_H\_  
Copyright (C) 2018 William Breathitt Gray \*/#ifndef \_COUNTER\_H#define \_COUNTER\_H\_  
Copyright (C) 2018 William Breathitt Gray \*/#include <linux/counter.h>#include <linux/device.h>#include <linux/err.h>#include  
<linux/export.h>#include <linux/fs.h>#include <linux/gfp.h>#include  
Copyright (c) 2018 William Whistler <wtbw@wtbw.co.uk> \*/  
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Copyright (C) 2018 Xilinx, Inc. \*  
Copyright (C) 2018 Xue Liu <liuxuenetmail@gmail.com> \*/#ifndef \_MCR20A\_H#define \_MCR20A\_H  
Copyright (C) 2018 Xue Liu <liuxuenetmail@gmail.com> \*/#include <linux/kernel.h>#include <linux/module.h>#include  
<linux/gpio/consumer.h>#include <linux/spi/spi.h>#include <linux/workqueue.h>#i  
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Copyright (C) 2019 Arm Ltd. \* \* Based on msm\_gem\_freedreno.c:  
Copyright (C) 2019 Arm Ltd. \*/#ifndef \_UAPI\_LINUX\_VIRTIO\_IOMMU\_H#define \_UAPI\_LINUX\_VIRTIO\_IOMMU\_H  
Copyright (C) 2019 Arrikto, Inc. All Rights Reserved  
Copyright (c) 2019 Artur Rojek <contact@artur-rojek.eu> \* \* based on drivers/mfd/jz4740-adc.c  
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Copyright (c) 2019 Baylibre SAS. \* Author: Neil Armstrong <narmstrong@baylibre.com> \*/#include <linux/clock-provider.h>#include  
<linux/platform\_device.h>#include <linux/reset-controller.h>#include  
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Copyright (C) 2019 Christian Hewitt <christianshewitt@gmail.com>  
Copyright (c) 2019 Christian Mauderer <oss@c-mauderer.de>  
Copyright (C) 2019 Christoph Hellwig \*/#ifndef LINUX\_IO\_URING\_H#define LINUX\_IO\_URING\_H  
Copyright (c) 2019 Christoph Hellwig. \*/#include "xf.h"  
Copyright (c) 2019 Cloudflare  
Copyright (C) 2019 Collabora Ltd. \*/  
Copyright (c) 2019 David Ahern <dsahern@gmail.com>. All rights reserved  
Copyright (C) 2019 Dell Inc \* by Narendra K <Narendra.K@dell.com> \* \* System firmware advertises the address of the RC12 Table via \* an EFI

Configuration Table entry. This code retrieves the RC12

```
Copyright (C) 2019, Dell Inc * * The PCIe Link Bandwidth Notification provides a way to notify the * operating system when the link width or data rate
changes. This * capability is required for a
Copyright (C) 2019 Dialog Semiconductor
Copyright (C) 2019 Dialog Semiconductor * * Author: Eric Jeong <eric.jeong.opensource@diasemi.com>
Copyright (c) 2019 Facebook
Copyright (c) 2019 Facebook
Copyright (c) 2019 Facebook */
Copyright (C) 2019 Facebook
Copyright (c) 2019 Facebook#define STACK_MAX_LEN 100
Copyright (c) 2019 Facebook#define STACK_MAX_LEN 180
Copyright (c) 2019 Facebook#define STACK_MAX_LEN 50
Copyright (c) 2019 Facebook#define STACK_MAX_LEN 600/* clang will not unroll the loop 600 times. * Instead it will unroll it to the amount it deemed *
appropriate, but the loop will still execute
Copyright (c) 2019 Facebook#define STACK_MAX_LEN 600#define NO_UNROLL/* clang will not unroll at all. * Total program size is around 2k insns */
Copyright (c) 2019 Facebook#ifndef __ASM_GOTO_WORKAROUND_H#define __ASM_GOTO_WORKAROUND_H
Copyright (c) 2019 Facebook#ifndef _BPF_SK_STORAGE_H#define _BPF_SK_STORAGE_H
Copyright (c) 2019 Facebook */#ifndef __LIBBPF_HASHMAP_H#define __LIBBPF_HASHMAP_H
Copyright (c) 2019 Facebook Inc *
Copyright (c) 2019 Facebook#include <features.h>
Copyright (c) 2019 Facebook#include <linux/bpf.h>#include "bpf_helpers.h"#define ATTR __always_inline#include "test_jhash.h"
Copyright (c) 2019 Facebook#include <linux/bpf.h>#include "bpf_helpers.h"#define ATTR __attribute__((noinline))#include "test_jhash.h"
Copyright (c) 2019 Facebook#include <linux/bpf.h>#include <linux/version.h>#include "bpf_helpers.h"
Copyright (c) 2019 Facebook#include <linux/compiler.h>#include <linux/err.h>
Copyright (c) 2019 Facebook#include <linux/rculist.h>#include <linux/list.h>#include <linux/hash.h>#include <linux/types.h>#include
<linux/spinlock.h>#include <linux/bpf.h>#include <net/bpf_sk_
Copyright (c) 2019 Facebook#include <linux/sched.h>#include <linux/ptrace.h>#include <stdint.h>#include <stddef.h>#include <stdbool.h>#include
<linux/bpf.h>#include "bpf_helpers.h"
Copyright (c) 2019 Facebook */#include <stdbool.h>
Copyright (c) 2019 Facebook#include <stddef.h>#include <string.h>#include <linux/bpf.h>#include <linux/if_ether.h>#include
<linux/if_packet.h>#include <linux/ip.h>#include <linux/ipv6.h>#inclu
Copyright (c) 2019 Facebook */#include <stdint.h>#include <stdlib.h>#include <stdio.h>#include <errno.h>#include <linux/err.h>#include
"hashmap.h"
Copyright (c) 2019 Facebook */#include <stdio.h>#include <errno.h>#include <linux/err.h>#include "hashmap.h"
Copyright (c) 2019 Facebook#include <test_progs.h>static int libbpf_debug_print(enum libbpf_print_level level,
va_list args){ if (level != LIBBPF_DEBUG) { vprintf(fo
Copyright (C) 2019 Facebook * * One challenge of controlling IO resources is the lack of trivially * observable cost metric. This is distinguished from CPU
and memory where * wallclock time and t
Copyright (c) 2019 Facebook */* ----- START-EXPECTED-OUTPUT ----- */enum e1 {A = 0, B = 1,
Copyright (c) 2019 Facebook */* ----- START-EXPECTED-OUTPUT ----- */struct packed_trailing_space { int a; short b;} __attribute__((packed));
Copyright (c) 2019 Facebook */* ----- START-EXPECTED-OUTPUT ----- */struct padded_implicitly { int a; long int b; char c;
Copyright (c) 2019 Facebook */* ----- START-EXPECTED-OUTPUT ----- */struct s1 {};
Copyright (c) 2019 Facebook */* ----- START-EXPECTED-OUTPUT ----- */struct S { int S; int U;
Copyright (c) 2019 Facebook */* ----- START-EXPECTED-OUTPUT ----- */typedef int arr_t[2];
Copyright (C) 2019 Genesys Logic, Inc. * * Authors: Ben Chuang <ben.chuang@genesyslogic.com.tw>
Copyright (C) 2019 Glider bvba */
Copyright (c) 2019 Google Inc. */
Copyright (C) 2019 Google, Inc.
Copyright (c) 2019, Google Inc. * Author - Rajat Jain * * This code instantiates platform devices for intel_pmc_core driver, only * on supported platforms
that may not have the ACPI devices in the
Copyright (C) 2019 Google, Inc. * * Authors:
Copyright (C) 2019 Google, Inc. *Tomasz Figa <tfiga@chromium.org> */
Copyright (C) 2019, Google LLC.
Copyright (c) 2019 Google LLC * * Based on aesni-intel_glue.c by:
Copyright (C) 2019 Google LLC <ebiggers@google.com>
Copyright (C) 2019, Google LLC. * * Test for KVM_CAP_MAX_VCPUS and KVM_CAP_MAX_VCPU_ID.
Copyright (C) 2019, Google LLC. * * This test verifies the integrity of calling the ioctl KVM_SET_NESTED_STATE.
Copyright (c) 2019 Google LLC * * Updated RFC4106 AES-GCM testing. * Authors: Aidan O'Mahony (aidan.o.mahony@intel.com) * Adrian
Hoban <adrian.hoban@intel.com> * Gabriele
Copyright (C) 2019 GRATE-DRIVER project */
Copyright (c) 2019 Greg Kroah-Hartman <gregkh@linuxfoundation.org>
```

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Copyright (C) 2019 Hangzhou C-SKY Microsystems co.,ltd.
Copyright (C) 2019 Hangzhou C-SKY Microsystems co.,ltd. * Mapping of DWARF debug register numbers into register names. */
Copyright (C) 2019 Hangzhou C-SKY Microsystems co.,ltd.// Mapping of DWARF debug register numbers into register names.
Copyright (C) 2019 Helge Deller <deller@gmx.de> */
Copyright (C) 2019 Helge Deller <deller@gmx.de> * * Based on arch/arm64/kernel/jump_label.c
Copyright (c) 2019 Hisilicon Limited.
Copyright (c) 2019 HiSilicon Limited.#ifndef HISI_ACC_QM_H#define HISI_ACC_QM_H
Copyright (c) 2019 HiSilicon Limited.#ifndef HISI_ACC_SGL_H#define HISI_ACC_SGL_H
Copyright (c) 2019 HiSilicon Limited.#ifndef HISI_ZIP_H#define HISI_ZIP_H
Copyright (c) 2019 HiSilicon Limited.#include <asm/page.h>#include <linux/bitmap.h>#include <linux/debugfs.h>#include <linux/dma-
mapping.h>#include <linux/io.h>#include <linux/irqreturn.h>#incl
Copyright (c) 2019 HiSilicon Limited.#include <crypto/internal/acompress.h>#include <linux/bitfield.h>#include <linux/dma-mapping.h>#include
<linux/scatterlist.h>#include "zip.h"
Copyright (c) 2019 HiSilicon Limited.#include <linux/acpi.h>#include <linux/aer.h>#include <linux/bitops.h>#include <linux/debugfs.h>#include
<linux/init.h>#include <linux/io.h>#include <linux/
Copyright (c) 2019 HiSilicon Limited.#include <linux/dma-mapping.h>#include <linux/module.h>#include "./sgl.h"
Copyright (C) 2019 HUAWEI, Inc. * http://www.huawei.com/ * Created by Gao Xiang <gaoxiang25@huawei.com> */#ifndef
__EROFS_FS_COMPRESS_H#define __EROFS_FS_COMPRESS_H
Copyright (C) 2019 HUAWEI, Inc. * http://www.huawei.com/ * Created by Gao Xiang <gaoxiang25@huawei.com> */#include
"compress.h"#include <linux/module.h>#include <linux/lz4.h>
Copyright (C) 2019 IBM Corp.
Copyright (C) 2019 IBM Corp.#include <linux/bitops.h>#include <linux/init.h>#include <linux/io.h>#include <linux/kernel.h>#include
<linux/mfd/syscon.h>#include <linux/mutex.h>#include <linux/of
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Copyright (C) 2019 Intel Corporation. All rights reserved
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Copyright (C) 2019 Intel Corporation * * Author:
Copyright (c) 2019, Intel Corporation. * Author: Andy Shevchenko <andriy.shevchenko@linux.intel.com> */
Copyright(c) 2019 Intel Corporation. * * Author: Björn Töpel <bjorn.topel@intel.com>
Copyright (C) 2019 Intel Corporation * * Author: Lu Baolu <baolu.lu@linux.intel.com>
Copyright (C) 2019 Intel Corporation * Author: Luis Carlos Cobo <luisca@cozybit.com> */
Copyright (C) 2019 Intel Corporation * Author: Luis Carlos Cobo <luisca@cozybit.com> */#include <linux/gfp.h>#include <linux/kernel.h>#include
<linux/random.h>#include <linux/rculist.h>
Copyright (C) 2019, Intel Corporation * Author: Mika Westerberg <mika.westerberg@linux.intel.com> */
Copyright (C) 2019 Intel Corporation. * * Authors:
Copyright (C) 2019 Intel Corporation. * * Author: Zhang Rui <rui.zhang@intel.com>
Copyright (C) 2019 Intel Corporation * * Contact Information:
Copyright (c) 2019, Intel Corporation. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt
Copyright (c) 2019, Intel Corporation. * * Heterogeneous Memory Attributes Table (HMAT) representation
Copyright (C) 2019 Intel Corporation */#include <linux/export.h>#include <net/mac80211.h>#include "ieee80211_i.h"#include "driver-ops.h"
Copyright (C) 2019 Intel Corporation * * Permission to use, copy, modify, and/or distribute this software for any * purpose with or without fee is hereby
granted, provided that the above * copyrig
Copyright (C) 2019 Intel Corporation * * Portions of this file are derived from the ipw3945 project, as well * as portions of the ieee80211 subsystem

```

header files.

```
Copyright(c) 2019 Intel Corporation * * This module allows virtio devices to be used over a vhost-user socket.
Copyright (c) 2019 Isovalent, Inc.
Copyright (C) 2019 Isovalent, Inc. */
Copyright (C) 2019 Jacek Anaszewski <jacek.anaszewski@gmail.com> */
Copyright (C) 2019 Jeff Kletsky * * Author: Jeff Kletsky <git-commits@allycomm.com>
Copyright (c) 2019 Jeff Layton * * I/O and data path helper functionality.
Copyright (c) 2019, Jeffrey Hugo */
Copyright (C) 2019 Jens Axboe
Copyright (C) 2019 Jeroen Hofstee <jhofstee@victtronenergy.com>
Copyright (C) 2019, Jiaxun Yang <jiaxun.yang@flygoat.com> * Loongson-1 platform IRQ support */
Copyright (C) 2019 Kai-Heng Feng <kai.heng.feng@canonical.com> */
Copyright (c) 2019 Kevin Darbyshire-Bryant <ldir@darbyshire-bryant.me.uk> */
Copyright (c) 2019 Kuninori Morimoto <kuninori.morimoto.gx@renesas.com>
Copyright (C) 2019 Laurent Pinchart <laurent.pinchart@ideasonboard.com> */
Copyright (c) 2019, Linaro Limited */
Copyright (c) 2019, Linaro Limited */#include "nvmmem.h"
Copyright (c) 2019 Linaro Ltd. */
Copyright (c) 2019 Linaro Ltd
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Copyright (C) 2019 Linaro Ltd <ard.biesheuvel@linaro.org> */
Copyright (C) 2019 Linaro, Ltd. <ard.biesheuvel@linaro.org> */
Copyright (c) 2019 Linaro, Ltd. <ard.biesheuvel@linaro.org> * * Heavily based on:
Copyright (c) 2019, Linaro Ltd. * Author: Georgi Djakov <georgi.djakov@linaro.org> */
Copyright (C) 2019 Linaro Ltd * Author: Georgi Djakov <georgi.djakov@linaro.org> */
Copyright (c) 2019 Linaro Ltd. * Author: Manivannan Sadhasivam <manivannan.sadhasivam@linaro.org> */
Copyright (c) 2019, Linaro Ltd */#include <dt-bindings/power/qcom-aoss-qmp.h>#include <linux/clk-provider.h>#include <linux/interrupt.h>#include
<linux/io.h>#include <linux/mailbox_client.h>#in
Copyright (C) 2019 Linus Walleij
Copyright (C) 2019 Linus Walleij <linus.walleij@linaro.org>
Copyright (C) 2019 Linus Walleij <linus.walleij@linaro.org> * * Based on arch/arm/mach-ixp4xx/common.c
Copyright (c) 2019 Linux Foundation
Copyright (C) 2019 Linux Foundation */
Copyright (C) 2019 Linux Foundation <gregkh@linuxfoundation.org> * * debugfs is for people to use instead of /proc or /sys. * See
./Documentation/core-api/kernel-api.rst for more details.
Copyright (C) 2019 Lorenzo Bianconi <lorenzo.bianconi83@gmail.com> */
Copyright (C) 2019 Lorenzo Bianconi <lorenzo@kernel.org> */
Copyright (C) 2019 Luca Weiss <luca@z3ntu.xyz> * * Based on PWM vibrator driver:
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<linux/err.h>#include <linux/gpio.h>#include <linux/io.
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<linux/io.h>#include <linux/scatterlist.h>#include "c
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  Copyright (c) 2019, Vladimir Oltean <olteanv@gmail.com> */#include <linux/if_vlan.h>#include <linux/dsa/sja1105.h>#include
<linux/dsa/8021q.h>#include <linux/packing.h>#include "dsa_priv.h"
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 <linux/skbuff.h>#include <net/ipv6.h>#include <linux/netfilter  
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<linux/io.h>#include <linux/irqchip.h>#include <linux/i
Copyright (C) Deepak Saxena <dsaxena@plexity.net> */#include <linux/interrupt.h>#include <linux/io.h>#include <linux/clockchips.h>#include
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<linux/types.h>#include <linux/socket.h>#include <
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<linux/socket.h>#include <linux/in.h>#include <linux/kernel.h>#include  
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<linux/ftrace.h>  
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<linux/hardirq.h>#include <linux/preempt.h>#include  
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 1#define VDMA\_END 2  
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<linux/in.h>#include <linux/kernel.h>#include <linux/ti

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<linux/smp.h>#include <linux/interrupt.h>#include <linux/kernel\_st

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<linux/in.h>#include <linux/kernel.h>#include <linux/tim
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len = length;
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putc(*c, stderr); putc('\\n', stderr); }
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<linux/cpu.h>#include <linux/sched.h>#include <asm/fpu/types.h>#includ
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Copyright IBM Corp. 2015 * * Author: Vasant Hegde <hegdevasant@linux.vnet.ibm.com> * Author: Anshuman Khandual
<khandual@linux.vnet.ibm.com>
Copyright IBM Corp. 2015 */#ifndef __S390_NUMA_MODE_H#define __S390_NUMA_MODE_H
Copyright IBM Corp. 2015 */#ifndef S390_TOPTREE_H#define S390_TOPTREE_H
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```

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Copyright IBM Corp. 2016, 2018 * * Author(s): Ursula Braun <ubraun@linux.vnet.ibm.com>
Copyright IBM Corp. 2016, 2018 * * Author(s): Ursula Braun <ubraun@linux.vnet.ibm.com> * based on prototype from Frank Blaschka
Copyright IBM Corp. 2016,2019 * * Author: Harald Freudenberger <freude@de.ibm.com>
Copyright IBM Corp. 2016 * Author(s): Harald Freudenberger <freude@de.ibm.com> * * Currently there are two tracepoint events defined here. * An
s390_zcrypt_req request event occurs as soon as the
Copyright IBM Corp. 2016 * Author(s): Harald Freudenberger <freude@de.ibm.com> */#ifndef AP_DEBUG_H#define AP_DEBUG_H
Copyright IBM Corp. 2016 * Author(s): Holger Dengler (hd@linux.vnet.ibm.com) * Harald Freudenberger <freude@de.ibm.com> */#ifndef
ZCRYPT_DEBUG_H#define ZCRYPT_DEBUG_H
Copyright IBM Corp. 2016 * Author(s): Janosch Frank <frankja@linux.vnet.ibm.com> */#include <linux/errno.h>#include <linux/pagemap.h>#include
<linux/vmalloc.h>#include <linux/syscalls.h>#include
Copyright IBM Corp. 2016 * * Author(s): Klaus Wacker <Klaus.Wacker@de.ibm.com> * Ursula Braun <ubraun@linux.vnet.ibm.com>
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Copyright IBM Corp. 2016 * Author(s): Martin Schwidefsky <schwidefsky@de.ibm.com> * * Adjunct processor bus, card related code.
Copyright IBM Corp. 2016 * Author(s): Martin Schwidefsky <schwidefsky@de.ibm.com> * * Adjunct processor bus, queue related code.
Copyright IBM Corp. 2016 * Author(s): Martin Schwidefsky <schwidefsky@de.ibm.com> */#ifndef _ASM_S390_XOR_H#define _ASM_S390_XOR_H
Copyright IBM Corp. 2016 * Author(s): Martin Schwidefsky <schwidefsky@de.ibm.com> */#include <asm/vdso.h>#include <asm/asm-
offsets.h>#include <asm/dwarf.h>
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Copyright IBM Corp. 2016 * * Author(s): Ursula Braun <Ursula.Braun@linux.vnet.ibm.com>
Copyright IBM Corp. 2016 */#define KMSG_COMPONENT "sclp_cmd"#define pr_fmt(fmt) KMSG_COMPONENT ": " fmt
Copyright IBM Corp. 2016 */#include <linux/kernel.h>#include <asm/processor.h>#include <asm/facility.h>#include <asm/lowcore.h>#include
<asm/sclp.h>#include "boot.h"
Copyright IBM Corp. 2017
Copyright IBM Corp. 2017 */
Copyright IBM Corp. 2017 *
Copyright IBM Corp. 2017, 2018 * Author(s): Harald Freudenberger * * The s390_arch_random_generate() function may be called from random.c * in
interrupt context. So this implementation does the be
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Copyright IBM Corp. 2017,2019 * Author(s): Harald Freudenberger */
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*/
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S390_CPUMSF_KERNEL_H#define S390_CPUMSF_KERNEL_H
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Copyright IBM Corp. 2018 * * Author(s): Philipp Rudo <prudo@linux.vnet.ibm.com>
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<pmorel@linux.ibm.com>
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Copyright IBM Corp. 2018 * Auxtrace support for s390 CPU-Measurement Sampling Facility * * Author(s): Thomas Richter <tmricht@linux.ibm.com>
Copyright IBM Corp. 2018 */#define KMSG_COMPONENT "ism"#define pr_fmt(fmt) KMSG_COMPONENT ": " fmt
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Copyright IBM Corp. 2019");
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Copyright IBM Corp. 2019 * * Author(s):
Copyright IBM Corp. 2019 * Author(s): Harald Freudenberger <freude@linux.ibm.com> * Ingo Franzki <ifranzki@linux.ibm.com> * * Collection of

```



```

CCA misc functions used by zcrypt and pkey
Copyright IBM Corp. 2019 * Author(s): Hendrik Brueckner <brueckner@linux.ibm.com> */#define KMSG_COMPONENT "cpum_cf_common"#define
pr_fmt(fmt) KMSG_COMPONENT ": " fmt
Copyright IBM Corp. 2019 * Author(s): Hendrik Brueckner <brueckner@linux.ibm.com> */#ifndef _ASM_S390_CPU_MCF_H#define
_ASM_S390_CPU_MCF_H
Copyright IBM Corp. 2019 * Author(s): Hendrik Brueckner <brueckner@linux.ibm.com> * Thomas Richer <tmricht@linux.ibm.com> */#define
KMSG_COMPONENT "cpum_cf_diag"#define pr_fmt(fmt) KMSG_C
Copyright IBM Corp. 2019 Author(s): Hendrik Brueckner <brueckner@linux.ibm.com> * Thomas Richter <tmricht@linux.ibm.com> */#ifndef
S390_CPUMCF_KERNEL_H#define S390_CPUMCF_KERNEL_H
Copyright IBM Corp. 2019 * Author(s): Hendrik Brueckner <brueckner@linux.vnet.ibm.com> */#ifndef _ASM_S390_CPU_MF_INSN_H#define
_ASM_S390_CPU_MF_INSN_H
Copyright IBM Corp. 2019 * Author(s): Joerg Schmidbauer (jschmidb@de.ibm.com) */#include <crypto/internal/hash.h>#include
<linux/init.h>#include <linux/module.h>#include <linux/cpumask.h>#include <linux/cpumask.h>#include <linux/cpumask.h>#include
Copyright IBM Corp. 2019 * Author(s): Joerg Schmidbauer (jschmidb@de.ibm.com) */#include <crypto/internal/hash.h>#include <linux/init.h>#include
<linux/module.h>#include <linux/cpumask.h>#include <linux/cpumask.h>#include <linux/cpumask.h>#include <linux/cpumask.h>#include
Copyright IBM Corp. 2019 */#include <asm/mem_detect.h>#include <asm/pgtable.h>#include <asm/cpacf.h>#include <asm/timex.h>#include
<asm/scfp.h>#include "compressed/decompressor.h"#include "boo
Copyright IBM Corp 2019/* * The DPS310 is a barometric pressure and temperature sensor. * Currently only reading a single temperature is supported
by * this driver.
Copyright IBM Corporation 1999, 2009 * * Author(s): Martin Peschke <mpeschke@de.ibm.com> * Martin Schwiddefsky
<schwiddefsky@de.ibm.com>
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Copyright IBM Corporation, 2007 * Author Balbir Singh <balbir@linux.vnet.ibm.com> *
Copyright IBM Corporation, 2007 * * Author : Cedric Le Goater <clg@fr.ibm.com>
Copyright IBM Corporation, 2007 * Author: Mike Anderson <andmike@linux.vnet.ibm.com> */#ifndef DM_UEVENT_H#define DM_UEVENT_H
Copyright IBM Corporation, 2007 * Author: Mike Anderson <andmike@linux.vnet.ibm.com> */#include <linux/list.h>#include
<linux/slab.h>#include <linux/kobject.h>#include <linux/dm-ioct.h>#include
Copyright IBM Corporation, 2007 * Authors:
Copyright IBM Corporation, 2007 * Authors: Anthony Liguori <aliguori@us.ibm.com> */
Copyright IBM Corporation, 2007 * Author: Srivatsa Vaddagiri <vatsa@linux.vnet.ibm.com> * * Scaled math optimizations by Thomas Gleixner
Copyright IBM Corporation, 2007 * * Based on Bamboo code by Josh Boyer <jwboyer@linux.vnet.ibm.com>
Copyright IBM Corporation 2007 */#ifndef JITDUMP_H#define JITDUMP_H
Copyright IBM Corporation 2007 */#include <sys/types.h>#include <sys/stat.h> /* for mkdir() */#include <stdio.h>#include <errno.h>#include
<string.h>#include <stdlib.h>#include <stdint.h>#include
Copyright IBM Corporation, 2007 * Josh Boyer <jwboyer@linux.vnet.ibm.com> * * Based on ebony wrapper:
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Copyright IBM Corporation, 2008 * * Author: Dipankar Sarma <dipankar@in.ibm.com> * Paul E. McKenney <paulmck@linux.ibm.com> Hierarchical
algorithm
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McKenney <paulmck@linux.ibm.com> Hierarchical versio
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Copyright IBM Corporation, 2012
Copyright IBM Corporation, 2012 */#include <linux/types.h>

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Copyright IBM Corporation, 2018 * Author(s): Nayna Jain <nayna@linux.ibm.com> */
Copyright IBM Corporation, 2018 * Authors Suraj Jitindar Singh <sjitindarsingh@gmail.com> * Paul Mackerras <paulus@ozlabs.org> * * Description:
KVM functions specific to running nested KVM-HV g
Copyright IBM Corporation, 2019 * * Author: Paul E. McKenney <paulmck@linux.ibm.com>
copyright); i copy[i] = (char)(get_u16(&sensor0->copyright[i]) >> 8); copy[i] = '\0';
COPYRIGHT | IEC958_AES0_CON_EMPHASIS_5015; ctrl->ch_status[1] = IEC958_AES1_CON_DIGDIGCONV_ID; ctrl->ch_status[2]
= 0x00; ctrl->ch_status[3] = IEC958_AES3_CON_FS_44100 | IEC958_AES3
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COPYRIGHT | IEC958_AES0_CON_EMPHASIS_NONE; cs[1] = IEC958_AES1_CON_GENERAL; cs[2] = IEC958_AES2_CON_SOURCE_UNSPEC |
IEC958_AES2_CON_CHANNEL_UNSPEC; cs[3] = IEC958_AES3_CON_CLOCK_1000PPM | IEC958_AES
COPYRIGHT | IEC958_AES0_CON_EMPHASIS; ucontrol->value.iec958.status[1] =
IEC958_AES1_CON_ORIGINAL | IEC958_AES1_CON_CATEGORY; ucontrol->value.iec958.status[3] = IEC958_AES3_C
COPYRIGHT | IEC958_AES0_CON_EMPHASIS; ucontrol->value.iec958.status[1] = IEC958_AES1_CON_ORIGINAL |
IEC958_AES1_CON_CATEGORY; ucontrol->value.iec958.status[3] = IEC958_AES3_CON
COPYRIGHT | IEC958_AES0_CON_EMPHASIS; value->value.iec958.status[1] = IEC958_AES1_CON_CATEGORY |
IEC958_AES1_CON_ORIGINAL; return 0;}
COPYRIGHT (IEC958_AES1_CON_ADC_COPYRIGHT_ID#define IEC958_AES1_CON_ADC_COPYRIGHT_OTHER
(IEC958_AES1_CON_ADC_COPYRIGHT_ID[0x60]#define IEC958_AES1_CON_SOLIDMEM_MASK0x0f#define IEC958_AES1_CON_SOLID
COPYRIGHT) \ | ((IEC958_AES1_CON_MIXER \ | IEC958_AES1_CON_ORIGINAL) << 8) \ | (0x10 <<
16) \ | ((IEC958_AES3_CON_FS_48000) << 24))
copyright IN
copyright ;-) */#include <linux/etherdevice.h>
copyright info and historical * changelogs. */#include <linux/capability.h>#include <linux/clocksource.h>#include <linux/workqueue.h>#include
<linux/hrtimer.h>#include <linux/jiffies.h>#includ
copyright information. * -----
copyright information. * -----
copyright information. */
copyright information
copyright information */
Copyright Information:
copyright information. * 03-16-15 02.00.10 Updated for MPI v2.6. * Added MPI2_SATA_PT_REQ_PT_FLAGS_FPDMA. * -----
-----
copyright information. * 03-16-15 02.00.17 Updated for MPI v2.6. * Added MPI26_SCSIIO_IOFLAGS_ESCAPE_PASSTHROUGH. *
Added MPI2_SEP_REQ_SLOTSTATUS_DEV_OFF a
copyright information. * 03-16-15 02.00.26 Updated for MPI v2.6. * Added MPI2_EVENT_ACTIVE_CABLE_EXCEPTION and *
MPI26_EVENT_DATA_ACTIVE_CABLE_EXCEPT. * Added
copyright information. * 08-25-16 02.00.14 Added new values for the Flags field of Toolbox Clean * Tool Request Message. * 07-22-18
02.00.15 Added defines for new TOOLBOX_PC
copyright information. * Added MPI2_BIOSPAGE1_OPTIONS_ADVANCED_CONFIG. * Added AdapterOrderAux fields to BIOS
Page 3. * 03-16-15 02.00.31 Updated for MPI v
copyright information. * * a header file containing define's for the USB device supported by the * various drivers.
copyright information. * * a header file containing prototypes and types for internal use of the dvb-usb-lib
copyright information and source see the end of this file.static u8 mac_reader[] = { 0x06, 0x00, 0x00, 0xea, 0x04, 0x00, 0xea, 0x03, 0x00, 0x00,
0xea, 0x02, 0x00, 0x00, 0xea, 0x01, 0x00, 0x00
copyright* information as follows:
copyright information. * Bumped MPI2_HEADER_VERSION_UNIT. * 03-16-15 02.00.37 Bumped MPI2_HEADER_VERSION_UNIT. *
Added Scratchpad registers to *
copyright information */#define FC_LOG_PREFIX "flexcop_usb"#include "flexcop-usb.h"#include "flexcop-common.h"
copyright information. Do not change the version information unless an improvement has been made.Merely removing my name, as Compex has done in
the past, does not count as an improvement.

```

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copyright information */#ifndef __FLEXCOP_COMMON_H__ #define __FLEXCOP_COMMON_H__
copyright information */#ifndef __FLEXCOP_H__ #define __FLEXCOP_H__
copyright information */#ifndef __FLEXCOP_REG_H__ #define __FLEXCOP_REG_H__
copyright information */#ifndef __FLEXCOP_USB_H_INCLUDED__ #define __FLEXCOP_USB_H_INCLUDED__
copyright information */#include "flexcop.h"
copyright information */#include <media/tuner.h> #include "flexcop.h" #include "mt312.h" #include "stv0299.h" #include "s5h1420.h" #include
"itd1000.h" #include "cx24113.h" #include "cx24123.h" #inc
copyright information of those functions. *
copyright information. pr_info("NetWinder Floating Point Emulator V0.97 (" NWFPE_BITS " precision)\n");
copyright information. * * the headerfile, all dvb-usb-drivers have to include.
copyright information. * * This file contains functions for (de-)initializing an I2C adapter.
copyright information. * * This file contains functions for downloading the firmware to Cypress FX 1 and 2 based devices.
copyright information. * * This file contains functions for downloading the firmware to Cypress FX 1 * and 2 based devices.
copyright information. * * This file contains functions for initializing and handling the * linux-dvb API.
copyright information. * * This file contains functions for initializing the input-device and for handling remote-control-queries.
copyright information *//* This file is automatically generated, do not edit things here. */#ifndef __FLEXCOP_IBI_VALUE_INCLUDED__ #define
__FLEXCOP_IBI_VALUE_INCLUDED__
copyright information. * * This file keeps functions for initializing and handling the * BULK and ISOC USB data transfers in a generic way. * Can be used
for DVB-only and also, that's the plan, fo
copyright information. * * This file keeps functions for initializing and handling the * USB and URB stuff.
Copyright: Intel Corp 2005 * Jonathan Cameron <jjc23@cam.ac.uk> 2009 */
Copyright Intel Corp. 2018 #include <linux/init.h> #include <linux/module.h> #include <linux/moduleparam.h> #include <linux/nd.h> #include
"pmem.h" #include "pfn.h" #include "nd.h" #include "nd-core.h
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Copyright Intel Corporation (C) 2014-2016. All Rights Reserved
Copyright Intel Corporation (C) 2017. All Rights Reserved
Copyright Intel Corporation (C) 2017. * * Based on the i2c-axxia.c driver.
Copyright © International Business Machines Corp., 2006
Copyright © International Business Machines Corp., 2006-2008 * * DESCRIPTION * This test excercises the futex syscall op codes needed for
requeuing * priority inheritance aware POSIX co
Copyright © International Business Machines Corp., 2006-2008 * * DESCRIPTION * This test exercises the futex_wait_requeue_pi() signal handling
both * before and after the requeue. The f
Copyright © International Business Machines Corp., 2009
Copyright © International Business Machines Corp., 2009 * * DESCRIPTION * 1. Block a thread using FUTEX_WAIT * 2. Attempt to use
FUTEX_CMP_REQUEUE_PI on the futex from 1. * 3. The
Copyright © International Business Machines Corp., 2009 * * DESCRIPTION * Block on a futex and wait for timeout.
Copyright © International Business Machines Corp., 2009 * * DESCRIPTION * GCC atomic builtin wrappers * http://gcc.gnu.org/onlinedocs/gcc-
4.1.0/gcc/Atomic-Builtins.html
Copyright © International Business Machines Corp., 2009 * * DESCRIPTION * Glibc independent futex library for testing kernel functionality.
Copyright © International Business Machines Corp., 2009 * * DESCRIPTION * Test if FUTEX_WAIT op returns -EWOULDBLOCK if the futex value
differs * from the expected one.
copyright I put the copyright notice from the * frle-0.22 device driver. * Some code is based on the nicstar driver by M. Welsh. * *
Author: Rui Prior (rprior@inescn.pt) * P
copyright is: *
Copyright is a null terminated ASCII string containing the JR3 * copyright notice. */
copyright); ixgbev_f_wq = create_singlethread_workqueue(ixgbev_driver_name); if (!ixgbev_f_wq) { pr_err("%s: Failed to create
workqueue\n", ixgbev_driver_name); return -ENOMEM; }
Copyright: J */
Copyright: Jamal Hadi Salim (2002-13) */
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copyright Jamal Hadi Salim (2015) */
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Copyright: jfclere@sinix.net */
copyright Joerg Dorchain (dorchain@mpi-sb.mpg.de) *
Copyright Joern Engel <joern@lazybastard.org> # Inspired by Linus Torvalds # Original idea maybe from Keith Owens # s390 port and big speedup
by Arnd Bergmann <arnd@bergmann-dalldorf.de> # Mips port b
Copyright Jonathan Naylor G4KLX (g4klx@g4klx.demon.co.uk)
Copyright Jonathan Naylor G4KLX (g4klx@g4klx.demon.co.uk) */ #include <linux/errno.h> #include <linux/types.h> #include <linux/socket.h> #include
<linux/in.h> #include <linux/kernel.h> #include <linu
Copyright Jonathan Naylor G4KLX (g4klx@g4klx.demon.co.uk) */ #include <linux/module.h> #include <linux/proc_fs.h> #include
<linux/kernel.h> #include <linux/interrupt.h> #include <linux/fs.h> #include

```

```

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Copyright KOSAKI Motohiro <kosaki.motohiro@jp.fujitsu.com> * * DESCRIPTION * Internally, Futex has two handling mode, anon and file. The
private file * mapping is special. At first it be
Copyright KOSAKI Motohiro <kosaki.motohiro@jp.fujitsu.com> * * DESCRIPTION * Wait on uninitialized heap. It shold be zero and FUTEX_WAIT
should * return immediately. This test is intent
copyright label on that
Copyright Larry Finger <Larry.Finger@lwfinger.net> * * A huge thanks goes to Sara V. Nari who forgives me when I'm * sitting in front of my laptop at
evening, week-end, night...
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Copyright Linas Vepstas <linas@austin.ibm.com> 2007 * * All rights reserved
Copyright Linas Vepstas <linas@linas.org> 2004, 2005 * * All rights reserved
Copyright line includes x86 and tile ones. */
Copyright Linus Torvalds and others. *
copyright Linus Torvalds and others. * Heavily rewritten. */
Copyright Management Information (UDF 2.50 3.3.4.5.1.2)struct DVDCopyrightImpUse { __le16 headerChecksum; uint8_t
CGMSInfo; uint8_t dataType; uint8_t protectionSystemInfo[4];} __packed;
Copyright: Marvell International Ltd. */
Copyright: Marvell Semiconductors Inc. */
Copyright Matthew Wilcox for Intel Corp, 2010
Copyright Matthew Wilcox for Intel Corp, 2010 * * This file contains definitions taken from the * USB Mass Storage Class Specification Overview
Copyright Matt Mackall <mpm@selenic.com>, 2003, 2004, 2005 *
copyright MaxLinear
Copyright message. * 0.0.17 * Add iec958 file in proc file system to show status of SPDIF in. * 0.0.18 * Implement support for Line-in capture on
SB Live 24bit. * * This code was initia
Copyright message. * 0.0.17 * Implement Mic and Line in Capture. * 0.0.18 * Add support for mute control on SB Live 24bit (cards w/ SPI DAC) *
* This code was initially based on code fr
Copyright message. * 0.0.19 * Added I2C and SPI registers. Filled in interrupt enable. * 0.0.20 * Added GPIO info for SB Live 24bit. * 0.0.21 *
Implement support for Line-in capture o
Copyright message. * 0.0.19 * Finally fix support for SB Live 24 bit. SB0410 and SB0413. * The output codec needs resetting, otherwise all output is
muted. * 0.0.20 * Merge "pci_disabl
copyright message * below) provided to Red Hat by VIA Networking Technologies, Inc. for * addition to the Linux kernel. * * The code has been merged
into one source file, cleaned up to follow * L
copyright message in any source redistribution in whole or part. *
copyright messages: *
Copyright Micro Memory Inc 2001-2002. All rights reserved
Copyright Micro Memory INC 2001. All rights reserved
copyright) * Modifications: 2006 (c) MontaVista Software, Inc. * * Modified by: Mark A. Greer <mgreer@mvista.com>
COPYRIGHT);MODULE_LICENSE("GPL");
COPYRIGHT);MODULE_LICENSE("GPL");MODULE_ALIAS_MISCDEV(TUN_MINOR);
COPYRIGHT);MODULE_LICENSE("GPL");MODULE_FIRMWARE("ipw2200-ibss.fw");#ifdef CONFIG_IPW2200_MONITORMODULE_FIRMWARE("ipw2200-
sniffer.fw");#endifMODULE_FIRMWARE("ipw2200-bss.fw");
COPYRIGHT);MODULE_LICENSE("GPL
v2");MODULE_VERSION(DRV_VERSION);MODULE_FIRMWARE(FIRMWARE_D101M);MODULE_FIRMWARE(FIRMWARE_D101S);MODULE_FIRMWARE(FIRMWAR
E_D102E);
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Copyright: Monta Vista Software, Inc. */
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Copyright: MontaVista Software Inc. * * Code common to all PXA machines.
Copyright: MontaVista Software Inc. * * Code specific to PXA21x/25x/26x variants.
Copyright: MontaVista Software Inc. * * Code specific to PXA27x aka Bulverde.
Copyright: MontaVista Software, Inc. */#ifndef __ASM_ARCH_DMA_H#define __ASM_ARCH_DMA_H
Copyright: MontaVista Software Inc. */#ifndef __ASM_MACH_IRQS_H#define __ASM_MACH_IRQS_H
Copyright: MontaVista Software Inc. */#include <linux/bitops.h>#include <linux/init.h>#include <linux/module.h>#include <linux/interrupt.h>#include
<linux/syscore_ops.h>#include <linux/io.h>#i

```

```

Copyright: MontaVista Software Inc. */#include <linux/clkdev.h>#include <linux/gpio.h>#include <linux/gpio/gpio-reg.h>#include
<linux/gpio/machine.h>#include <linux/module.h>#include <linux/ker
Copyright: MontaVista Software Inc. */#include <linux/gpio/consumer.h>#include <linux/module.h>#include <linux/init.h>#include
<linux/interrupt.h>#include <linux/kernel.h>#include <linux/errno.
Copyright: MontaVista Software Inc. */#include <linux/gpio.h>#include <linux/gpio/gpio-reg.h>#include <linux/gpio/machine.h>#include
<linux/init.h>#include <linux/platform_device.h>#include <li
Copyright: MontaVista Software Inc. */#include <linux/gpio.h>#include <linux/init.h>#include <linux/platform_device.h>#include
<linux/syscore_ops.h>#include <linux/interrupt.h>#include <linux/s
Copyright: MontaVista Software Inc. */#include <linux/module.h>#include <linux/clk.h>#include <linux/err.h>#include <linux/gpio/driver.h>#include
<linux/gpio-pxa.h>#include <linux/init.h>#incl
Copyright: MontaVista Software Inc. * * Originally based upon linux/drivers/pcmcia/sa1100_neponset.c
Copyright: MontaVista Software, Inc. * * Splitting done by: Marek Vasut <marek.vasut@gmail.com> * If something doesn't work and it worked before
splitting, e-mail me, * dont bother Nicolas please ;-
Copyright Motorola 1984 (
copyright;      } mpeg_format_info; } format_info;
COPYRIGHT);    new      new |= ((ucontrol->value.iec958.status[3] & IEC958_AES3_CON_FS) << 24);      if ((new &
IEC958_AES0_CON_EMPHASIS) == IEC958_AES0_CON_EMPHASIS_5015)      val |= 1<<3;
copyright notes. */
copyright notes. */#include <api/fs/fs.h>#include <errno.h>#include <inttypes.h>#include <poll.h>#include "cpumap.h"#include
"util/mmap.h"#include "thread_map.h"#include "target.h"#include "
copyright not found\n");#endif /* Validate checksum */ for (i = s = 0; i < EEPROM_CHECKSUM; i++)      s += e[i]; s &= 0xFF; if (s !=
e[EEPROM_CHECKSUM]) {      printk(KERN_ERR DEV_LABEL "(itf %d): EEPR
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Copyright Novell Inc. 2010 * * Authors: Alexander Graf <agraf@suse.de>
Copyright NXP *
Copyright NXP 2016 *
copyright of original i386 version is: *
copyright of skeleton.c was as follows: * * skeleton.c Written 1993 by Donald Becker.
copyright * of their respective owners. */
copyright of the      respective owners.*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/module.h>#include <linux/string.h>#include
<linux/slab.h>#include <linux/delay.h>#inc
copyright of who own it
Copyright: Phytex Messtechnik GmbH * e-Mail: armlinux@phytec.de * * based on Intel Mainstone Board
Copyright Pierre Ossman\n");
copyright preserved *      below. *      Steve Whitehouse: Some bug fixes, cleaning up some code to make it *
compatible with my routing layer. *
copyright ;- ) * * PS wx handler mostly stolen from hostap, copyright who
Copyright RDA Microelectronics Company Limited
Copyright Realtek corp. * (available in drivers/staging/rtl8187se directory of Linux 3.14) * - other GPL, unpublished (until now), Linux driver code,
Copyright Red Hat 2003
Copyright Red Hat, 2009
Copyright Red Hat Inc 2010.
Copyright Red Hat Inc 2010. * Author : Dave Airlie <airlied@redhat.com> */
Copyright Red Hat, Inc. 2011 * * Authors:
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Copyright Red Hat, Inc. 2014 * * Authors:
Copyright Red Hat, Inc. 2019 * * Author(s): Cornelia Huck <cohuck@redhat.com>
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Cornelia Huck <cohuck@redhat.com>
Copyright RedHat, Inc. * * libata documentation is available via 'make {ps|pdf}docs', * as Documentation/driver-api/libata.rst
Copyright RedHat, Inc. * * This driver probably works with non-Apple versions of the * Broadcom chipset...
COPYRIGHT;      regmap_write(ak4104->regmap, AK4104_REG_CHN_STATUS(0), val);
copyright      ReturnCode - One of the ERR_CAPTURE_...
COPYRIGHT:      return dvd_read_copyright(cdi, s, cgc);
COPYRIGHT); }      return pci_register_driver(}
copyright);return pci_register_driver(}module_init(atl2_init_module);
COPYRIGHT:      return "SET_COPYRIGHT"; case CX2341X_ENC_SET_EVENT_NOTIFICATION:
COPYRIGHT:      return "SET_COPYRIGHT"; case CX2341X_ENC_SET_EVENT_NOTIFICATION:
Copyright Roman Zippel, 1997. All rights reserved
Copyright Rusty Russell IBM Corporation 2007.#ifndef __KERNEL__#include <stdint.h>#endif#include <linux/types.h>#include <linux/virtio_types.h>

```

```

Copyright Samuel Mendoza-Jonas, IBM Corporation 2018.
Copyright Samuel Mendoza-Jonas, IBM Corporation 2018. */
Copyright Sarah Sharp for Intel Corp, 2010 */
COPYRIGHT))          sbits      if (val & AC_DIG1_LEVEL)          sbits |= (IEC958_AES1_CON_ORIGINAL << 8);          sbits |= val &
(0x7f << 8);          }          return sbits;
copyright Sebastian Carlier
Copyright © Sebastian Cla *          Jan Engelhardt, 2007-2010 * *          based on ipt_ROUTE.c from Cédric de Launois *          <delaunois@info.ucl.be>
copyright; see LICENSE.FlashPoint for details.
copyright: %s\n", copy);}
COPYRIGHT);          snd_emu10k1x_ptr_write(chip, SPCS1, 0,          chip->spdif_bits[1] =          SPCS_CLKACCY_1000PPM |
SPCS_SAMPLERATE_48 |          SPCS_CHANNELNUM_LEFT | SPCS_SOURCENUM_UNSPEC |
COPYRIGHT);          snd_emu10k1x_ptr_write(chip, SPCS2, 0,          chip->spdif_bits[2] =          SPCS_CLKACCY_1000PPM |
SPCS_SAMPLERATE_48 |          SPCS_CHANNELNUM_LEFT | SPCS_SOURCENUM_UNSPEC |
COPYRIGHT)          snd_iprintf(buffer, " Non-Copyright");          if (digi1 & AC_DIG1_NONAUDIO)          snd_iprintf(buffer, " Non-Audio");          if (digi1 &
AC_DIG1_PROFESSIONAL)          snd_iprintf(buffer, " Pro");          if (digi1 & A
COPYRIGHT) {          snd_iprintf(buffer, "permitted\n");          } else {          snd_iprintf(buffer, "protected\n");          }
snd_iprintf(buffer, "Emphasis: ");          if ((status[0] & IEC958_AES0_CON_EMPHASIS) != IEC958_AE
Copyright          : %s\n", (status          snd_iprintf(buffer, "Emphasis          : %s\n", emphasis[(status & SPCS_EMPHASISMASK) >> 3]);
snd_iprintf(buffer, "Mode          : %i\n", (status
COPYRIGHT | SPCS_NOTAUDIODATA;          snd_emu10k1_ptr_write(emu, SPCS0 + i, 0, bits);          }
Copyright (ST) 2011 Vipin Kumar (vipin.kumar@st.com) */
Copyright (ST) 2012 Rajeev Kumar (rajeevkumar.linux@gmail.com)
Copyright (ST) 2012 Rajeev Kumar (rajeevkumar.linux@gmail.com) */
Copyright (ST) 2012 Rajeev Kumar (rajeevkumar.linux@gmail.com) */
Copyright (ST) 2012 Vipin Kumar (vipin.kumar@st.com) */
copyright) */static inline int in_8(const volatile unsigned char{int ret;
Copyright status#define AC97_ALC650_PRE          0x0038 /* Preemphasis status */#define AC97_ALC650_PRE_SHIFT          3#define
AC97_ALC650_MODE          0x00C0 /* Preemphasis status */#define
Copyright status#define AC97_SC_PRE          0x0008 /* Preemphasis status */#define AC97_SC_CC_MASK          0x07f0 /* Category Code mask
*/#define AC97_SC_CC_SHIFT          4#define AC97_SC_L          0x0800 /* Generation Level
copyright status * of the DEC code. */
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permitted provided that the following conditions *
Copyright Stephan Mueller <smueller@chronox.de>, 2015 - 2019 * * Design
Copyright Stephan Mueller <smueller@chronox.de>, 2015 * * Redistribution and use in source and binary forms, with or without * modification, are
permitted provided that the following conditions *
Copyright ST-Ericsson 2012. * * Author: Arun Murthy <arun.murthy@stericsson.com>
Copyright STMicroelectronics, 2007. */
copyright string __le32 int_fw_offset; /* internal firmware image offset */ __le32 int_fw_len; /* internal firmware image length */ __le32
ext_fw_offset; /* external firmware image offset */ __le32
copyright(struct cdrom_device_info          struct packet_command *cgc){          int ret;          u_char buf[8];          const struct cdrom_device_ops
*cdo = cdi->ops;
copyright(struct comedi_device{          struct jr3_block __iomem *block = dev->mmio;          struct jr3_sensor __iomem *sensor0 = &block[0].sensor;
Copyright;          struct regid          appIdent;          struct timestampRecordingDateAndTime;          struct regid          impIdent;          uint8_t
impUse[64];          __le32          predecessorVolDescSeqLocation;          __le16          flags;          uint8_t          reserve
Copyright SUSE Linux Products GmbH 2009 * * Authors: Alexander Graf <agraf@suse.de>
Copyright SUSE Linux Products GmbH 2010
Copyright SUSE Linux Products GmbH 2010 * * Authors: Alexander Graf <agraf@suse.de>
COPYRIGHT;          switch (bits          case 0x10: diga->status[0] |= IEC958_AES0_CON_EMPHASIS_NONE;          break;          case 0x00: diga-
>status[0] |= IEC958_AES0_CON_EMPHASIS_5015;          break;          }          if (!(bits & 0x80))          diga-
Copyright SysKonnect 1998,1999. * * The information in this file is provided "AS IS" without warranty.
copyright text*/
copyright text          2001jul05          change message format to improve OVERRUN case
copyright text          2001jul05          change message format to improve OVERRUN case
copyright text          2001jul05          change message format to improve OVERRUN case*/
copyright = 'The kernel development communityauthor = 'The kernel development community'
Copyright Theodore Ts */
Copyright Theodore Ts * rights reserved. * * Redistribution and use in source and binary forms, with or without * modification, are permitted provided
that the following conditions * are met: *
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COPYRIGHT *          This file is distributed under the terms of the GNU General Public

```

```

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Copyright: Toby Churchill Ltd * Derived from pxa2xx_mainstone.c, by Nico Pitre * * Various modification by Marek Vasut <marek.vasut@gmail.com>
Copyright Tomi Manninen OH2BNS (oh2bns@sral.fi) */#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include
<linux/in.h>#include <linux/kernel.h>#include <linux/timer.h>
Copyright Tomi Manninen OH2BNS (oh2bns@sral.fi) */#include <linux/types.h>#include <linux/slab.h>#include <linux/socket.h>#include
<linux/timer.h>#include <net/ax25.h>#include <linux/skbuff.h>
Copyright Torsten Duwe <duwe@informatik.uni-erlangen.de> 1993
copyright = True
copyright {__u8 type;
COPYRIGHT; ucontrol->value.iec958.status IEC958_AES1_CON_ORIGINAL; return 0;}
COPYRIGHT; ucontrol->value.iec958.status IEC958_AES1_CON_ORIGINAL; ucontrol->value.iec958.status[3] =
IEC958_AES3_CON_FS; return 0;}
Copyright unasserted) * AN = 0 (Audio data) * P = 0 (Consumer) */ def_bits = SPCS_CLKACCY_1000PPM |
SPCS_SAMPLERATE_48 | SPCS_CHANNELNUM_LEFT | SPCS
Copyright unasserted) * AN = 0 (Audio data) * P = 0 (Consumer) */ emu->spdif_bits[0] = emu->spdif_bits[1] =
emu->spdif_bits[2] = SPCS_CLKACCY_1000PPM
Copyright unasserted) * AN = 0 (Audio data) * P = 0 (Consumer) */ snd_emu10k1x_ptr_write(chip, SPCS0, 0,
chip->spdif_bits[0] = SPCS
Copyright */ val = (cfg->iec60958_cfg->status
COPYRIGHT)) val if (sbits & (IEC958_AES1_CON_ORIGINAL << 8)) val |= AC_DIG1_LEVEL; val |= sbits &
(IEC958_AES1_CON_CATEGORY << 8); } return val;
COPYRIGHT)) val val |= ((new >> 8) & 0xff) << 4; // category + original switch ((new >> 24) & 0xff) { case
IEC958_AES3_CON_FS_44100: val |= 0<<12; break; case IEC958_AES3_CON_FS_48000: val
Copyright: Vovan888 at gmail com
copyright was as follows: * * skeleton.c Written 1993 by Donald Becker.
copyright was changed, and the change * is retroactive to all "true" BSD software (ie everything * from UCB as opposed to other peoples code that just
carried
Copyright who own it
Copyright who own it */#include "ieee80211.h"
Copyright who own it */#include "rtlib.h"
Copyright: Wolfson Microelectronics. PLC. */
copyright * * WPA code stolen from the ipw2200 driver.
Copyright Yaegashi Takeshi, 2001 * Adrian McMenamin, 2008 - 2009 */
(C) ordering is still observed by the pmu driver. */ if (!rb->aux_overwrite) { aux_tail = READ_ONCE(rb->user_page->aux_tail);
handle->wakeup = rb->aux_wakeup + rb->aux_watermark; if (aux_head
(C) or pretend (B,A) that the write * was successfully written on the peer. */ __RQ_NET_OK,
(C) Peter T. Breuer (ptb@ot.uc3m.es) 2003 * * bitmap_create - sets up the bitmap structure * bitmap_destroy - destroys the bitmap structure
(C) programmable clock divider controlled by <Select * (D) constant div-by-3 clock divider * (E) programmable clock divider bypass controlled by
<Switch> * (F) constant div-by-3 clock mux co
(C) protects the consumer from speculatively loading the data before * the producer pointer actually has been read. If we do not have this * barrier,
some architectures could load old data as specul
(C) pxor GHASH_MUL %xmm8, %xmm13, %xmm9, %xmm10, %xmm11, %xmm5, %xmm6 # final GHASH computation movdqa
SHUF_MASK(%rip), %xmm10 PSHUFB_XMM %xmm10, %xmm8
(C) Roman Zippel (assigned DJD, 1999) *
(C) Rusty Russell, IBM 2002 * * Generalized futexes, futex requeueing, misc fixes by Ingo Molnar
(C) * 'R * * Write * '' = Other Information * '' = Must not be written (D), value ignored when written (R,A,F) * 'W' = Can be written
(C) SBE Inc., used by permission. */
(C) SGI 2006, Christoph Lameter * Cleaned up and restructured to ease the addition of alternative * implementations of SLAB allocators.
(C) SK" ;#define LINT_USE(x)#else#define LINT_USE(x) (x)=(x)#endif#define MS2BCLK(x) ((x)*12500L)
(C) SK" ;#endif
(C) SK" ;#endif
(C) State timer broadcast controlstatic void lapic_timer_state_broadcast(struct acpi_processor *pr, struct acpi_processor_cx *cx,
int broadcast){ int state = cx - pr->power.st
(C) ST-Ericsson SA 2013 */
(C) ST-Ericsson SA 2013 */#include <drm/drm_simple_kms_helper.h>
(C) ST-Ericsson SA 2013 */#include <linux/clk.h>#include <linux/delay.h>#include <linux/dma-buf.h>
(C) Steve Whitehouse 1999 derived from the * ipv4 netfilter header file which is * (C)1998 Rusty Russell -- This code is GPL. */
(C) * STORE * smp_wmb() (B) smp_mb() (D) * STORE ->data_head STORE ->data_tail * }

```

```

(C) * STORE * smp_wmb() (B) smp_mb() (D) * STORE ->data_head STORE ->data_tail * }
(C) * STORE * smp_wmb() (B) smp_mb() (D) * STORE ->producer STORE ->consumer * }
(C) Texas Instruments. */
(C) Texas Instruments, Inc.
(C) Texas Instruments, Inc. http://www.ti.com/
(C) The elitist m68k Users(TM) * * based on the existing parport_amiga and lp_mfc
(c) there is clear notice in each modified Data File or * in the Software as well as in the documentation associated with the Data * File(s) or Software that
the data or software has been modified.
(C) using AArch64 CRC instructions *
(C) using ARM CRC, NEON and Crypto Extensions instructions *
(C) UV_Meta_Plane * (D) UV_UBWC_Plane * * Y_Meta_Plane consists of meta information to decode compressed * tile data in
Y_UBWC_Plane. * Y_UBWC_Plane consists of Y data in compressed macro-ti
(C) UV_Meta_Top_Field_Plane * (D) UV_UBWC_Top_Field_Plane * (E) Y_Meta_Bottom_Field_Plane * (F) Y_UBWC_Bottom_Field_Plane
* (G) UV_Meta_Bottom_Field_Plane * (H) UV_UBWC_Bottom_Field_Plane
(C) Waldorf Electronics, Germany * Written by Andreas Busse * * NOTE: most of the structure definitions here are endian dependent. * If you want to
use this driver on big endian machines, the data
(C) Xilinx Inc. * * This driver has originally been pushed by Xilinx using a Zynq-branding. This * still shows in the naming of this file, the kconfig symbols
and some symbols * in the code.
(C) YOKOTA Hiroshi <yokota (at) netlab. is. tsukuba. ac. jp>
Linux Kernel 5.6.19 fedora:kernel-debuginfo-common-aarch64/5.6.19-300.fc32/aarch64
Copyright(c) 2012 - 2015 Intel Corporation. All rights reserved
Copyright (C) 2005 Zymeta Corporation - Michael Downey (downey@zymeta.com) * * This driver has been put together with the support of Innosys, Inc.
* and Keyspan, Inc the manufacturers of the Keyspa
Copyright (C) 2005-7 DiBcom (http://www.dibcom.fr/) */
Copyright 2019 Google LLC * * This file is a rework and part of the code is ported from * drivers/mfd/cros_ec_dev.c that was originally written by * Bill
Richardson.
copyright Linus Torvalds and others. * Heavily rewritten. */
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(c) Copyright 2003-2007 Wim Van Sebroeck <wim@iguana.be>. * * Based on source code of the following authors:
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Copyright (C) 2008 Thomas Champagne (lafeuil@gmail.com) */
Copyright (c) 2019 Shuah Khan <shuah@kernel.org> *
Copyright © 2006 MontaVista Software Inc. * Author: Vitaly Wool <vwool@ru.mvista.com> * * Revised to handle newer style flash binding by:
Copyright message. * 0.0.19 * Added I2C and SPI registers. Filled in interrupt enable. * 0.0.20 * Added GPIO info for SB Live 24bit. * 0.0.21 *
Implement support for Line-in capture o
Copyright 2013 Peter Meerwald <pmeerw@pmeerw.net> * * IIO driver for the TSL4531x family * TSL45311/TSL45313: 7-bit I2C slave address 0x39 *
TSL45315/TSL45317: 7-bit I2C slave address 0x29
Copyright (C) 2017 Intel Corporation */#ifndef _LINUX_FPGA_MGR_H#define _LINUX_FPGA_MGR_H
Copyright (C) 2014 Google, Inc */
Copyright (c) 2013 by Jan Kl */
Copyright Tomi Manninen OH2BNS (oh2bns@sral.fi) */#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include
<linux/in.h>#include <linux/kernel.h>#include <linux/timer.h>
Copyright © 1998, 1999, 2000, 2001 Free Software Foundation, Inc.
Copyright (C) 2016 Intel Corp. */
Copyright (c) 1998-1999 by Frank van de Pol <fvdpol@coil.demon.nl> * * MAJOR CHANGES * Nov. 13, 1999 Takashi Iwai <iwai@ww.uni-
erlangen.de> * - Queues are allocated dynamically via ioctl. *
Copyright 2005 Andrea Merello <andrea.merello@gmail.com>, et al. */
Copyright 2008 Advanced Micro Devices, Inc.
Copyright (C) ARM Limited, 2017. * * Based on ARM CCI-PMU, ARMv8 PMU-v3 drivers.
Copyright (c) 2012 Mellanox Technologies. All rights reserved
Copyright (C) 2008 Igor M. Liplianin <liplianin@me.by> Removed stb6000 specific tuner code and revised some procedures. 2010-09-01
Josef Pavlik <josef@pavlik.it> Fixed diseqc_msg, diseqc_burst
Copyright (c) 2007 Michel Ludwig <michel.ludwig@gmail.com>// - DVB-T support
Copyright (C) 2012,2013 - ARM Ltd * Author: Marc Zyngier <marc.zyngier@arm.com> * * Derived from arch/arm/kvm/reset.c

```



Copyright (C) 2007-2020 B.A.T.M.A.N. contributors: \* \* Marek Lindner, Simon Wunderlich  
 Copyright (c) 2006 Herbert Xu <herbert@gondor.apana.org.au> \*/#include <crypto/internal/skcipher.h>#include <crypto/scatterwalk.h>#include  
 <linux/err.h>#include <linux/init.h>#include <linux/ker  
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 Copyright (c) 2004 Rudolf Marek <r.marek@assembler.cz> \* \* Partly imported from i2c-vid.h of the lm\_sensors project  
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 <nbd@nbd.name>  
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 (sfrench@us.ibm.com) \*/  
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 Copyright (C) 2017 Analog Devices Inc. \* \* Based on: i2c-mux-pca954x.c  
 Copyright (c) 2007, 2011 Jean Delvare <jdelvare@suse.de> \*/  
 Copyright (C) 2013 Davidlohr Bueso <davidlohr.bueso@hp.com> \* \* Based on the shift-and-subtract algorithm for computing integer \* square root  
 from Guy L. Steele.  
 Copyright 2008 Red Hat Inc.  
 Copyright (C) 2009 Motorola, Inc. \* \* Simple driver for National Semiconductor LM3530 Backlight driver chip  
 Copyright (C) 2010 IBM Corporation \* \* Author: John Stultz <john.stultz@linaro.org>  
 Copyright (C) 2017-2018 Ryan Mounce <ryan@mounce.com.au> \* \* The CAKE Principles:  
 Copyright (C) 2018 Topic Embedded Products \* Author: Mike Looijmans <mike.looijmans@topic.nl> \*/  
 Copyright (c) 2012-2016 Qualcomm Atheros, Inc.  
 Copyright (C) 2016 Jens Axboe \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/blk-mq.h>  
 Copyright 2018 NXP \* Dong Aisheng <aisheng.dong@nxp.com> \*/  
 Copyright (c) 2016 Advanced Micro Devices, Inc. \* All rights reserved  
 Copyright (c) 2009-2010 Samsung Electronics Co., Ltd. \* Pawel Osciak, <pawel@osciak.com> \* Marek Szymanski, <m.szymanski@samsung.com>  
 \*/#include <linux/clock.h>#include <linux/delay.h>#include <  
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Copyright (c) 2012 Samsung Electronics Co., Ltd. \* http://www.samsung.com/ \*/#include <linux/fs.h>#include <linux/f2fs\_fs.h>#include  
<linux/buffer\_head.h>#include <linux/mpage.h>#in  
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<linux/ethtool.h>#include <linux/workqueue.h>#include <linux/s  
Copyright (c) 2017 Pablo M. Bermudeo Garay <pablombg@gmail.com> \* \* This code is based on net/netfilter/nft\_fib\_inet.c, written by \* Florian Westphal  
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<sowjanya@cdac.in> \*/#include <linux/kernel.h>#include <linux/module.h>#in  
Copyright (C) Frederic Rible F1OAT (frible@teaser.fr) \*/#include <linux/capability.h>#include <linux/module.h>#include <linux/errno.h>#include  
<linux/types.h>#include <linux/socket.h>#include <  
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Copyright (c) 2016-2018 Cavium Inc. \*/#include <linux/spinlock.h>#include <linux/vmalloc.h>#include "qedf.h"#include <scsi/scsi\_tcq.h>  
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\_LINUX\_BALLOON\_COMPACTION\_H#include <linux/pagemap.h>#include <linux/page-flags.h  
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Copyright 2019 Collabora Ltd.#include <linux/bitfield.h>#include <linux/delay.h>#include <linux/dma-mapping.h>#include  
<linux/interrupt.h>#include <linux/io.h>#include  
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Copyright (C) 2015 ARM Limited \*/#include <linux/perf\_event.h>#include <linux/uaccess.h>  
Copyright 2013 Intel Corporation \* \* The Synopsys DesignWare 8250 has an extra feature whereby it detects if the \* LCR is written whilst busy. If it is,  
then a busy detect interrupt is \* raised,  
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<linux/io.h>#include <linux/of.h>#include <linux/pla  
Copyright (C) 1995-1997 Olaf Kirch <okir@monad.swb.de> \*/#ifndef NFSD\_EXPORT\_H#define NFSD\_EXPORT\_H  
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<linux/skbuff.h>#include <linux/ipv6.h>#include <linux/types.h>#
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<psokolov@synopsys.com>
Copyright (C) 1999 Arun Sharma <arun.sharma@intel.com>
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Copyright (C) 2016 Cavium Inc. */#include <linux/device.h>#include <linux/dma-mapping.h>#include <linux/interrupt.h>#include
<linux/mmc/mmc.h>#include <linux/module.h>#include <linux/of.h>#include
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(C) * in woken_wake_function() such that either we see the wait condition * being true or the store to wq_entry->flags in woken_wake_function()
* follows ours in the coherence order. */ smp_s
Copyright (C) 2009 Novell Inc. */
Copyright (c) 2015, Linaro Limited */#include <linux/arm-smccc.h>#include <linux/device.h>#include <linux/errno.h>#include <linux/errno.h>#include
<linux/mm.h>#include <linux/slab.h>#include <li
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Copyright (c) 2008, Darius Augulis <augulis.darius@gmail.com> */
Copyright (c) 2019 Facebook#ifndef _BPF_SK_STORAGE_H#define _BPF_SK_STORAGE_H
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Copyright (C) 2004 Thomas Gleixner (tglx@linutronix.de) * * Reed Solomon code lifted from reed solomon library written by Phil Karn
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<linux/sched.h>#include <linux/sched/debug.h>#include
Copyright (C) 1991, 1992, 1993, 1994 Linus Torvalds * * Swap reorganised 29.12.95, * Asynchronous swapping added 30.12.95. Stephen Tweedie *
Removed race in async swapping. 14.4.1996. Bruno H
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Copyright (c) 2015, Intel Corporation * Authors: Tadeusz Struk <tadeusz.struk@intel.com> */#include <linux/errno.h>#include
<linux/kernel.h>#include <linux/module.h>#include <linux/seq_file.h>#
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<linux/delay.h>#include "uwb-internal.h"
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Copyright (C) 2007-2008 BalaBit IT Ltd. * Author: Krisztian Kovacs */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include
<linux/module.h>#include <linux/skbuff.h>#include <linux/netfilter/x_table
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<linux/uaccess.h>#include <linux/crc16.h>#include <linux/  
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Copyright (C) 2017 Eric Nelson \*/#include <linux/module.h>#include <linux/i2c.h>#include <linux/slab.h>#include <linux/rtc.h>#include  
<linux/init.h>#include <linux/err.h>#include <linux/errno.  
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copyright information. \* \* This file contains functions for downloading the firmware to Cypress FX 1 \* and 2 based devices.  
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Copyright (c) 2012 Samsung Electronics Co., Ltd. \* http://www.samsung.com/ \*/#include <linux/fs.h>#include <linux/bio.h>#include  
<linux/mpage.h>#include <linux/writeback.h>#include  
Copyright (C) 2001 Silicon Graphics, Inc. (Trust Technology Group)  
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Copyright (C) 2004 Amaury Demol for DiBcom \* \* Acknowledgements  
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Copyright (C) 2007-2008 Advanced Micro Devices, Inc. \* Author: Joerg Roedel <joerg.roedel@amd.com> \*/  
Copyright 2017 \* Dong Aisheng <aisheng.dong@nxp.com> \*/  
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Copyright (C) 2017-2018 HUawei, Inc. \* http://www.huawei.com/ \* Created by Gao Xiang <gaoxiang25@huawei.com> \*/#ifndef  
\_\_EROFS\_XATTR\_H#define \_\_EROFS\_XATTR\_H  
Copyright (C) 2013,2017 Johan Hovold <johan@kernel.org> \* \* See Documentation/usb/usb-serial.rst for more information on using this \* driver  
Copyright (c) 2016, ROCKCHIP CORPORATION. All rights reserved  
Copyright (C) 2012 Russell King \*/#include <linux/device.h>#include <linux/dmaengine.h>#include <linux/module.h>#include <linux/spinlock.h>  
Copyright (C) 1992 Rick Sladkey \* \* nfs inode and superblock handling functions  
Copyright (c) International Business Machines Corp., 2006 \* \* Authors: Artem Bityutskiy (Битюцкий Артём), Thomas Gleixner  
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Copyright (C) 2018-2019, Bridge Systems BV  
Copyright (C) 2010 Hans de Goede <hdegoede@redhat.com> \* \* Based on the usbvideo v4l1 konicawc driver which is: \*  
Copyright 2016 NXP Semiconductor \* \* Derived from mpc85xx\_edac.c \* Author: Dave Jiang <djiang@mvista.com>  
Copyright(c) 2003-2011 Intel Corporation"  
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Copyright 2005 MontaVista Software, Inc. \* Matt Porter <mporter@kernel.crashing.org> \*/  
(c) Copyright 2004, Red Hat Inc. <alan@lxorguk.ukuu.org.uk> \* Additional fixes and clean up: Francois Romieu \* \* This source has not been verified for  
use in safety critical systems.  
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Copyright 2013 Linaro Ltd \*/  
Copyright 2018 NXP \* Author: Dong Aisheng <aisheng.dong@nxp.com> \* \* Implementation of the SCU IPC functions using MUs (client side).  
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Copyright (c) 2013 Andrew Duggan <aduggan@synaptics.com>  
Copyright (C) 2011-2015, Yann Collet.  
Copyright (C) 2007 - 2010 ID7 Ltd. \* \* Forward port and refactoring to modern qla2xxx and target/configfs  
Copyright (C) 2011 Mike Christie \*/#ifndef \_BLK\_BSG\_#define \_BLK\_BSG\_  
Copyright (C) 2015 - ARM Ltd \* Author: Marc Zyngier <marc.zyngier@arm.com> \*/  
Copyright (c) 2013 Broadcom Corporation \*/  
Copyright 2013 Qtechnology A/S \*/  
Copyright (C) 1993 Linus Torvalds \* \* Delay routines, using a pre-computed "loops\_per\_jiffy" value.  
Copyright (C) 2014 Red Hat Inc, author: Steven Rostedt <srostedt@redhat.com> \* \* tracefs is the file system that is used by the tracing infrastructure.  
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Copyright (c) 2002, 2003, 2004, 2005, 2006, 2007, 2008 \* Phillip Lougher <phillip@squashfs.org.uk> \* \* squashfs\_fs\_sb.h  
Copyright (C) 2000-2004 Neil Brown \* \* RAID-10 support for md.  
Copyright (C) 2017 Arm Ltd.#define pr\_fmt(fmt) "sdei: " fmt  
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http://qanu.de/specs/terratec\_cinergyT2.pdf  
Copyright (C) 2008 Marvell Semiconductor  
Copyright(c) 2017 Intel Corporation.  
Copyright (c) 2015-2016 QLogic Corporation  
Copyright (c) 2012-2019 Linux Foundation \* \* Core driver model functions and structures that should not be \* shared outside of the drivers/base/  
directory.  
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permitted provided that the following conditions  
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14-Jan-1998  
Copyright (c) 2014, Applied Micro Circuits Corporation \* Author: Loc Ho <lho@apm.com> \* Tuan Phan <tphan@apm.com> \* Suman Tripathi  
<stripathi@apm.com> \* \* The APM X-Gene PHY consi  
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Copyright (C) 2000 Takashi Iwai <tiwai@suse.de> \* \* Proc interface for Emu8k/Emu10k1 WaveTable synth  
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Copyright (C) 2008 <srinivasa.deevi at conexant dot com> Based on cx88 driver  
Copyright 2008 Pierre Ossman \* \* Based on code by Siano Mobile Silicon, Inc.,  
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Copyright (C) 2005 Pete Zaitcev (zaitcev@redhat.com) \*/  
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Copyright 2019 NXP#include <linux/fs/enetc\_mdio.h>#include <linux/of\_mdio.h>#include "enetc\_pf.h"  
Copyright (C) 2019 Christoph Hellwig \*/#ifndef LINUX\_IO\_URING\_H#define LINUX\_IO\_URING\_H  
Copyright (C) 2018 Song Qiang <songqiang1304521@gmail.com> \* \* User Manual available at \* <https://www.pnicorp.com/download/rm3100-user-  
manual/>  
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Copyright (C) 2015, 2018 \* Author: Matt Ranostay <matt.ranostay@konsulko.com> \* \* Datasheets:  
Copyright (C) 2012 Bill Pemberton (wfp5p@virginia.edu) \* \* These devices all have only 1 bulk in and 1 bulk out that is shared \* for all serial ports.  
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 Copyright (c) 2016, Intel Corporation \* Authors: Salvatore Benedetto <salvatore.benedetto@intel.com> \*/#ifndef \_CRYPTO\_KPP\_INT\_H#define  
 \_CRYPTO\_KPP\_INT\_H#include <crypto/kpp.h>#include <crypto/a  
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 Copyright (c) 2007 Tejun Heo <teheo@suse.de> \*/  
 Copyright (c) 2013 Alexey Degtyarev <alexey@renatasystems.org>  
 Copyright 2017 Tempo Semiconductor, Inc.// Author: Steven Eckhoff <steven.eckhoff.opensource@gmail.com>  
 Copyright (C) 2014 Linaro Ltd. \* Author: Ashwin Chaugule <ashwin.chaugule@linaro.org> \* \* PCC (Platform Communication Channel) is defined in  
 the ACPI 5.0+ \* specification. It is a mailbox like m  
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 Copyright (c) 2011, Sagrad Inc. \* Author: Solomon Peachy <speachy@sagrad.com> \* \* Based on cw1200\_sdio.c  
 Copyright (C) 2008 by David Brownell  
 Copyright (C) 2018 Paolo Pisati <p.pisati@gmail.com> \*/  
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 <ghansper@apana.org.au>, \* Hannu Savolainen \*  
 Copyright (c) 2007 Novell Inc. \*/#include <linux/kobject.h>#include <linux/module.h>#include <linux/init.h>#include <linux/device.h>  
 Copyright (C) 2002 David S. Miller (davem@redhat.com) \*  
 Copyright (C) 2010 Freescale Semiconductor, Inc. \* \* Header file for the FEC platform data  
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 Gleixner <tglx@linutronix.de> \* \* Contains all JEDEC related definitions  
 Copyright 2001, Rik van Riel <riel@conectiva.com.br>  
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 copyright { CX231xx\_COPYRIGHT\_OFF, CX231xx\_COPYRIGHT\_ON,};  
 Copyright (c) 2000-2004 by David Brownell \* \* Derived from the ohci-pci driver  
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 Copyright IBM Corporation, 2007 \* Author Balbir Singh <balbir@linux.vnet.ibm.com> \*  
 Copyright (C) 2000 Don Dugger <n0ano@valinux.com>  
 Copyright (C) 2017 Willy Tarreau <w@1wt.eu> \* Partially based on Atsushi Nemoto's ledtrig-heartbeat.c. \*/  
 Copyright (C) 2008-2012 Jean-Francois Moine <http://moinejf.free.fr>  
 Copyright 2015 Allworx Corp. \* \* Datasheets:  
 (C) Copyright David Gibson <dwg@au1.ibm.com>, IBM Corporation. 2005. \*/#define YY\_NO\_INPUT 1  
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 Copyright (c) 2016-2018 Cavium Inc. \*/#ifndef \_\_QEDF\_HSI\_#define \_\_QEDF\_HSI\_  
 Copyright (c) 2016, Collabora Ltd. \* \* Authors: Sjoerd Simons <sjoerd.simons@collabora.com>, \* Romain Perier <romain.perier@collabora.com>  
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 Copyright (C) 2017 Rafa \* \* Permission to use, copy, modify, and/or distribute this software for any \* purpose with or without fee is hereby granted,  
 provided that the above \* copyright notice and  
 Copyright (c) 2016-2018 Cavium Inc. \*/#ifndef \_\_QEDF\_DBG\_H\_#define \_\_QEDF\_DBG\_H\_  
 Copyright (C) 1991, 1992, 1993, 1994 Linus Torvalds \* \* Swap reorganised 29.12.95, Stephen Tweedie. \* kswapd added: 7.1.96 sct \* Removed  
 kswapdctl limits, and swap out as many pages as needed  
 Copyright (C) 2005 Mike Isely <isely@pobox.com> \*/  
 Copyright (C) 2005-2006 Intel Corporation \* Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com> \* \* FIXME: docs \* FIXME: there are issues here  
 on how BEACON and SCAN on USB RCI deal \* wi  
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 Copyright (C) 2015 Linaro Ltd <ard.biesheuvel@linaro.org> \*/  
 (C) 2001 Johannes Erdfelt <jerdfelt@valinux.com>  
 Copyright 2012 Analog Devices Inc. \* Author: Lars-Peter Clausen <lars@metafoo.de> \*/  
 Copyright IBM Corporation, 2007 \* Author: Mike Anderson <andmike@linux.vnet.ibm.com> \*/#include <linux/list.h>#include  
 <linux/slab.h>#include <linux/kobject.h>#include <linux/dm-ioct.h>#include  
 (c) Copyright 2004 Hewlett-Packard Development Company, L.P. \* Bjorn Helgaas <bjorn.helgaas@hp.com> \*/  
 Copyright 2011 Xillybus Ltd, http://xillybus.com \* \* Driver for the Xillybus FPGA/host framework.  
 Copyright 2017 \* Author: Dong Aisheng <aisheng.dong@nxp.com> \* \* File containing client-side RPC functions for the MISC service. These \* function  
 are ported to clients that communicate to the SC.  
 Copyright (C) 2009 Theodore Kilgore <kilgota@auburn.edu> \* \* Based closely upon the file gspca/pac\_common.h  
 Copyright (C) 2014-2019 Xilinx, Inc. \* \* Davorin Mista <davorin.mista@aggios.com> \* Jolly Shah <jollys@xilinx.com> \* Rajan Vaja  
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 Copyright (c) 2008 Patrick McHardy <kaber@trash.net> \*/#include <linux/mutex.h>#include <linux/skbuff.h>#include <linux/etherdevice.h>#include  
 <linux/llc.h>#include <linux/slab.h>#include <linu  
 Copyright (c) 2014 Scott Feldman <sfeldma@gmail.com> \*/  
 Copyright (C) 1998 \* Daniel Pirkel <daniel.pirkel@email.cz> \* Charles University, Faculty of Mathematics and Physics \* \* ext2 - inode (block) bitmap  
 caching inspired  
 Copyright (c) 2018 Quantenna Communications  
 Copyright (C) 2003 Deep Blue Solutions Ltd, All Rights Reserved  
 Copyright (C) 2013 Hauke Mehrkens <hauke@hauke-m.de> \* \* Distribute under GPL.  
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 (c) Copyright 1996 Alan Cox <alan@lxorguk.ukuu.org.uk>, \* All Rights Reserved  
 Copyright(C) 2006, Greg Kroah-Hartman <greg@kroah.com> \* \* Userspace IO driver.  
 Copyright (c) 2016 PMC-Sierra, Inc. \* \* Questions/Comments/Bugfixes to storagedev@microchip.com  
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 Copyright (c) 2017-19 David Ahern <dsa@cumulusnetworks.com> \*/  
 Copyright (C) 2010 Johan Hovold <jhovold@gmail.com> \* \* This driver allows a USB IrDA device to be used as a "dumb" serial device. \* This can be  
 useful if you do not have access to a full IrDA stack  
 (C) 2000 Red Hat. GPL\*/  
 Copyright (c) 2014 Mahesh Bandewar <maheshb@google.com> \*/#ifndef \_\_IPVLAN\_H#define \_\_IPVLAN\_H  
 Copyright (c) 2001-2012 Anton Altaparmakov  
 copyright); return pci\_register\_driver(&module\_init(atl2\_init\_module));  
 Copyright (C) 2013 Intel Corporation \* \* Some parts of this driver are derived from the original dw\_dmac.

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 Copyright (C) 1999 Takashi Iwai <tiwai@suse.de> \* \* Changes \* 19990227 Steve Ratcliffe Made separate file and merged in latest \*  
 midi emulation.  
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 (C) Copyright 2014 - 2015, Xilinx, Inc. \*/  
 Copyright (c) 2007-2017 Nicira, Inc.  
 Copyright (C) 2016-2017 Dmitry Osipenko <digetx@gmail.com> #ifndef \_UAPI\_TEGRA\_VDE\_H\_ #define \_UAPI\_TEGRA\_VDE\_H\_  
 (C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Search routines for btrees  
 Copyright (C) 2007 Cambridge Silicon Radio Ltd.  
 Copyright (C) 2015 Tai Chi Minh Ralph Eastwood  
 Copyright (c) 2007 Wind River Systems, Inc. \* \* Mark Zhan <rongkai.zhan@windriver.com>  
 Copyright © 2005-2009 Analog Devices Inc. \*/  
 Copyright (C) 2004, 2005 Hans Verkuil <hverkuil@xs4all.nl> \* \* Note: the saa7126 is identical to the saa7127, and the saa7128 is \* identical to the  
 saa7129, except that the saa7126 and saa7128 have  
 Copyright (c) 2016 Trond Myklebust  
 Copyright (C) 2012 ARM Ltd. \*/ #ifndef \_\_ASM\_SPINLOCK\_H #define \_\_ASM\_SPINLOCK\_H  
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 Copyright (C) 1992 Lawrence Foard \* Version 2 and 3 extensions to driver:  
 Copyright (c) 2012, Jeff Layton <jlayton@redhat.com> \*/  
 Copyright © 2000-2010 David Woodhouse <dwmw2@infradead.org> \* Steven J. Hill <sjhill@realitydiluted.com> \* Thomas  
 Gleixner <tglx@linutronix.de> \* \* Contains all ONFI related definitions  
 Copyright (C) 2009 DiBcom (http://www.dibcom.fr/) \*/  
 Copyright (c) 2016 Tom Herbert <tom@herbertland.com> \*/  
 Copyright (C) 2015-2018 Linaro Ltd. \*/ #ifndef QC\_MSM\_CAMSS\_H #define QC\_MSM\_CAMSS\_H  
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 Copyright (c) 2002 Jean-Francois Dive <jef@linuxbe.org>  
 Copyright (C) 1997-2002 Inside Out Networks, Inc. \* \* Feb-16-2001 DMI Added I2C structure definitions \* May-29-2002 gkh Ported to  
 Linux  
 (C) Copyright 2012 by Wolfram Sang, Pengutronix e.K.  
 Copyright (C) 2002 2003 by MontaVista Software. \* \* 2004-06-01 Fix CLOCK\_REALTIME clock/timer TIMER\_ABSTIME bug.  
 Copyright (c) 2005-2008 Stefano Brivio <stefano.brivio@polimi.it>  
 Copyright (C) 2001,2002 Richard Russon <ldm@flatcap.org>  
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 Copyright (C) 2012 ARM Ltd. \*/ #ifndef \_\_ASM\_COMPAT\_H #define \_\_ASM\_COMPAT\_H  
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 (C) Copyright 1999 Randy Dunlap.  
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 Copyright (c) 2017 Ruslan Bilovol <ruslan.bilovol@gmail.com> \* \* This file holds USB constants and structures defined \* by the USB DEVICE CLASS  
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 Copyright (C) Stefan Hajnoczi <stefanha@redhat.com>, 2015 \*/  
 Copyright (C) 2005-2008 Nippon Telegraph and Telephone Corporation. \* \* Written by Koji Sato.  
 Copyright 2006, Michael Buesch \*/ #ifndef LINUX\_SSB\_EXTIFCORE\_H\_ #define LINUX\_SSB\_EXTIFCORE\_H\_  
 Copyright (C) 2005 Hans Verkuil <hverkuil@xs4all.nl> \* - Cleanup \*/  
 Copyright (C) 2019 Integrated Device Technology, Inc., a Renesas Company. \*/ #include <linux/firmware.h> #include <linux/i2c.h> #include  
 <linux/module.h> #include <linux/ptp\_clock\_kernel.h> #include  
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 Spinlocks: Mohamed Abbas

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Copyright (C) 2004 IBM Corporation \* Authors:  
Copyright (C) 2004 IBM Corporation, Rusty Russell. \* \* Creation is done via kthreadd, so that we get a clean environment \* even if we're invoked from  
userspace (think modprobe, hotplug cpu, \* etc.  
Copyright (C) 2002 \* Kuba Ober (kuba@mareimbrium.org) \* \* See Documentation/usb/usb-serial.rst for more information on using this \* driver  
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Copyright (c) 1998 - 2002 Frodo Looijaard <frodol@dds.nl>, \* Kyösti Mälkki <kmalkki@cc.hut.fi>, \* Mark  
Studebaker <mdsxyz123@yahoo.com>, \* and Bob Dougherty <bobd@stanford.  
Copyright (C) 2008 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/random.h>#include <linux/slab.h>  
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Copyright (C) 2004-2007 Jean Delvare <jdelvare@suse.de> \* Ported to Linux 2.6 by Gabriele Gorla <gorlik@yahoo.com> \* and Jean Delvare  
\*/  
Copyright (C) 2003-2004 Giuliano Pochini <pochini@shiny.it> \*/  
Copyright (C) 2008 Steven Toth <stoth@linuxtv.org> \*/static int mxl5005s\_reset(struct dvb\_frontend \*fe)  
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copyright information. \* \* This file contains functions for downloading the firmware to Cypress FX 1 and 2 based devices.  
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Copyright (c) 2015-2016 HGST, a Western Digital Company. \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/init.h>#include  
<linux/miscdevice.h>#include <linux/module.h>#include <linux

```

Copyright (c) 2009 Pavel Machek <pavel@ucw.cz> */
(C) * Notes: To enter USB suspend mode * Command Mode */struct usb_suspend_parm {      u32 action; /* 1: sleep, 0:resume */
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Copyright (c) 2011 Florian Westphal <fw@strlen.de> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/skbuff.h>#include <linux/netdevice.h>#include <linux/rout
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Copyright (C) 2015 Russell King */#ifndef MDIO_I2C_H#define MDIO_I2C_H
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(C) 2007 SoftwareBitMaker
Copyright (C) 2008-2009 Frederic Weisbecker <fweisbec@gmail.com> * * Based on the code from trace_branch.c which is
Copyright (C) 2009 Jean-Francois Moine http://moinejf.free.fr * * Based on a prototype written by Mark Ferrell <majortrips@gmail.com> * USB protocol
reverse engineered by Jim Paris <jim@jtan.com>
Copyright (C) 2010 Juan Jes * * Inspired by the original lirc_it87 and lirc_ite8709 drivers, on top of the * skeleton provided by the nuvoton-cir driver.
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Copyright (c) 2012 Jeff Layton <jlayton@redhat.com>* All rights reserved
Copyright (C) 1999 Paul
Copyright (C) 2006 Herbert Poetzl * adapted from Remy Card's ext2/ioctl.c */
COPYRIGHT;      ucontrol->value.iec958.status      IEC958_AES1_CON_ORIGINAL; return 0; }
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Copyright (C) 2015 Richard Pospesel
Copyright (c) 2018 Mellanox Technologies.
Copyright (C) 1999 Juergen Peitz
Copyright (C) 2004 Chris Kennedy <c@groovy.org> * * VBI additions & cleanup:
Copyright (c) 2010 Adam Bennett) */
Copyright (c) 2002 Vladimir Dergachev * * This 2.2.0 version is a rewrite / cleanup of the 2.1.1 driver, including * porting to the 2.6 kernel interfaces,
along with other modification * to bet
Copyright (C) 2017 - 2018 Sandvik Mining and Construction Oy * * Description: * This driver is developed for Axi CAN IP and for Zynq CANPS Controller.
*/
*/
Copyright (c) 2008,2009 NEC Software Tohoku, Ltd. * Written by Takashi Sato <t-sato@yk.jp.nec.com> *      Akira Fujita <a-fujita@rs.jp.nec.com>
*/
Copyright 1999 Linus Torvalds
Copyright (C) 2002 Russell King. */#include <linux/module.h>#include <linux/serio.h>#include <linux/errno.h>#include <linux/interrupt.h>#include
<linux/ioport.h>#include <linux/device.h>#inclu
Copyright (C) 2006, 2008, 2010 Jean Delvare <jdelvare@suse.de> * * Supports the following chips: *
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(C) 2001-2002 Bj * * Developed with the assistance of:
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Copyright (c) 2009 Davide Ferri <d.ferri@zero11.it>
Copyright 2016 Peter Meerwald-Stadler <pmeerw@pmeerw.net> * * IIO driver for VEML6070 (7-bit I2C slave addresses 0x38 and 0x39)
Copyright (c) 2010 Cisco Systems, Inc *
Copyright (C) 2014 Dan Streetman * * This is a common frontend for the zbud and zsmalloc memory * storage pool implementations. Typically, this is
used to * store compressed memory.
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Copyright (C) 2006 Hans Verkuil <hverkuil@xs4all.nl> */
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(C) Copyright 2002 Red Hat Inc <alan@redhat.com>
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Copyright (C) 2013 Marvell */#include <linux/device.h>#include <linux/err.h>#include <linux/io.h>#include <linux/kernel.h>#include
<linux/of.h>#include <linux/module.h>#include <linux/delay.h>
Copyright (c) 2016, Linaro Limited */
Copyright (C) 2016 Google, Inc * * This driver uses the cros-ec interface to communicate with the Chrome OS * EC about sensors data. Data access is
presented through iio sysfs.
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```

```

copyright date, improve comments      2006feb06 modify for FX1 chip
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Copyright (c) 2012 Samsung Electronics Co., Ltd. *      http://www.samsung.com/ */#include <linux/fs.h>#include <linux/f2fs_fs.h>#include
<linux/sched/signal.h>#include <linux/unicode.h>
Copyright (C) 1995-2003 Russell King
Copyright (C) 2010-2011 Nicholas A. Bellinger <nab@kernel.org> * * Additional file for the target driver support.
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Copyright 2009 Intel Corporation. * Huang Ying <ying.huang@intel.com> */
Copyright (C) 2001 Russell King. * * A note about mapbase / membase
Copyright (C) 2017 Linaro Ltd. */#include <linux/kernel.h>#include <linux/module.h>#include <linux/device.h>#include <linux/qtr.h>#include
<linux/net.h>#include <linux/completion.h>#include <
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Copyright 2011 Google, Inc. */
Copyright (C) 2005-2006, Thomas Gleixner * * This file contains the IRQ-resend code
Copyright (c) 2007-2008 Tungsten Graphics, Inc., Cedar Park, TX., USA
Copyright (c) 2016-2018 Cavium Inc. */#include "drv_fcoe_fw_funcs.h"#include "drv_scsi_fw_funcs.h"
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Copyright (c) 2016-2018, NXP Semiconductors
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Copyright 2011 Xillybus Ltd, http://xillybus.com * * Driver for the Xillybus FPGA/host framework using PCI Express.
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Copyright 2015 - 2016 Intel Deutschland GmbH
Copyright(C) Timesys Corporation 2016 * * Based on Microchip 251x CAN Controller (mcp251x) Linux kernel driver
Copyright (C) 2007-2016 Mauro Carvalho Chehab// - Port to work with the in-kernel driver// - Cleanups, fixes, alsa-controls, etc.
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Copyright (C) 2017-2019 Broadcom. All Rights Reserved
Copyright (c) by Jaroslav Kysela <perex@perex.cz>, * Hannu Savolainen 1993-1996 * * The OPL-3 mode is switched on by writing 0x01,
to the offset 5 * of the right side.
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Copyright (C) 2015, Samsung Electronics Co., Ltd. * * Author: Marek Szyprowski <m.szyprowski@samsung.com>
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<tuukka.tikkanen@elektrobit.com>.
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Copyright (C) 2005 Frank Arnold (frank@scirocco-5v-turbo.de)
Copyright (c) 2008 Marvell Semiconductor */

```

```
(C) 2001 Martin Josefsson <gandalf@wlug.westbo.se> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/skbuff.h>#include <linux/netfilter.h>#include <net/netfil
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Copyright (c) 2016,2017 Facebook */#include <linux/bpf.h>#include <linux/btf.h>#include <linux/err.h>#include <linux/slab.h>#include
<linux/mm.h>#include <linux/filter.h>#include <linux/perf_e
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Copyright (c) 2009 Daniel Mack <daniel@caiaq.de> * * Based on code written by * Rodolfo Giometti <giometti@linux.it> * Eurotech S.p.A.
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Copyright (C) 1991, 1992 Linus Torvalds * * Added devfs support. * Jan-11-1998, C. Scott Ananian <cananian@alumni.princeton.edu> * Shared
/dev/zero mmaping support, Feb 2000, Kanoj Sarcar <
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 Copyright (C) 2015 Linaro Ltd. <ard.biesheuvel@linaro.org> \* \* Based on altivec.uc:  
 Copyright (C) 2016 Peter Hong (Peter\_Hong@fintek.com.tw) \* \* The F81532/F81534 had 1 control endpoint for setting, 1 endpoint bulk-out \* for all serial port TX and 1 endpoint bulk-in for all serial  
 Copyright (c) 2010 Simon Wood <simon@mungewell.org> \*/  
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 Copyright (C) 2000-2004 Netfilter Core Team <coreteam@netfilter.org> \*/#include <linux/module.h>#include  
 <linux/netfilter\_ipv4/ip\_tables.h>#include <linux/netdevice.h>#include <linux/skbuff.h>#i  
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 Copyright (C) 2007 Cambridge Silicon Radio Ltd. \* \* UMC (UWB Multi-interface Controller) capabilities (e.g., radio \* controller, host controller) are presented as devices on the "umc" \* bus.  
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 \* (Reported by Charles Bertsch, <CBertsch@microtest.c  
 (c) Copyright 1995 Alan Cox <alan@lxorguk.ukuu.org.uk> \* \* Software only watchdog driver. Unlike its big brother the WDT501P \* driver this won't always recover a failed machine.  
 Copyright 2018 Intel Corporation \*/  
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 <linux/interrupt.h>#include <linux/nmi.h>#include <linux/  
 Copyright (c) 2011-2014 PLUMgrid, http://plumgrid.com \*/#ifndef \_LINUX\_BPF\_VERIFIER\_H#define \_LINUX\_BPF\_VERIFIER\_H 1  
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 Copyright (C) 2005-2007 AMD (http://www.amd.com) \* Author: Thomas Dahmann \*/  
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 <linux/dsa/8021q.h>#include <linux/packing.h>#include "dsa\_priv.h"  
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Copyright (C) 2003 Bernardo Innocenti <bernie@develer.com> \* \* Based on former do\_div() implementation from asm-parisc/div64.h:  
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Copyright (C) 2016 - ARM Ltd. \* Author: Andre Przywara <andre.przywara@arm.com> \* \* Based on pinctrl-sun7i-a20.c, which is:  
(C) 2002 Oliver Neukum \* \* Original author: The Zapman <zapman@interlan.net> \* Inspired by, and much credit goes to Michael Rothwell \*  
<rothwell@interlan.net> for the test equipment, h  
Copyright (C) 2006-2008 Nokia Corporation. \* \* Author: Adrian Hunter  
Copyright (c) 2014 Christoph Hellwig. \*/#undef TRACE\_SYSTEM#define TRACE\_SYSTEM nfsd  
Copyright (C) 2000 Stephane Eranian <eranian@hpl.hp.com>  
Copyright 2006 Johannes Berg <johannes@sipsolutions.net> \*/  
Copyright (c) 2009-2010 NSN GmbH \* \* Simplifies access to complex multiplexed I2C bus topologies, by presenting \* each multiplexed bus segment as  
an additional I2C adapter. \* Supports multi-level  
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Copyright (c) 2000 Markus Demleitner <msdemlei@cl.uni-heidelberg.de>\*/  
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Copyright (c) 2005 - James Bottomley <James.Bottomley@steeleye.com> \* \* This class is designed to allow raid attributes to be visualised and \*  
manipulated in a form independent of the underlying ra  
Copyright (c) 2019 Mellanox Technologies. All rights reserved  
Copyright (C) 1992 Rick Sladkey \* \* nfs superblock handling functions  
Copyright (C) 2008 Dave S.r.l. <www.dave.eu> \*/#ifndef \_DNET\_H#define \_DNET\_H  
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Copyright (c) 2016, The Linux Foundation. All rights reserved  
Copyright (c) 2019, Intel Corporation. \* \* Heterogeneous Memory Attributes Table (HMAT) representation  
Copyright 2016 Freescale Semiconductor, Inc. \* \* There is no Shared Descriptor for PKC so that the Job Descriptor must carry \* all the desired key  
parameters, input and output pointers.  
Copyright(c) 2002-2010 Exar Corp.  
Copyright (C) 2005 Patrick Mochel \* \* This klist interface provides a couple of structures that wrap around \* struct list\_head to provide explicit list "head"  
(struct klist) and list \* "node" (str  
Copyright (C) 2014 Sergey Kozlov <serjk@netup.ru>  
Copyright 2017 NXP \* \* Based on WM8523 ALSA SoC Audio driver written by Mark Brown  
Copyright (C) 2016 Sensirion AG, Switzerland \* Author: David Frey <david.frey@sensirion.com> \* Author: Pascal Sachs <pascal.sachs@sensirion.com>  
\*/  
Copyright 2012 Nouveau community  
Copyright (c) 2001 Frederic Lepied <flepie@mandrakesoft.com>  
Copyright (C) 2018 Intel Corporation \*/#include <linux/slab.h>#include <linux/kernel.h>#include <linux/if\_arp.h>#include  
<linux/netdevice.h>#include <linux/rtnetlink.h>#include <net/mac80211.h>  
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chips.com> \*/  
Copyright (C) 2011 Mike Christie \*/#include <linux/slab.h>#include <linux/blk-mq.h>#include <linux/delay.h>#include <linux/scatterlist.h>#include  
<linux/bsg-lib.h>#include <linux/export.h>#i  
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purpose with or without fee is hereby granted, prov  
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Copyright (C) Hans-Joachim Hetscher DD8NE (dd8ne@bnv-bamberg.de) \* \* Most of this code is based on the SDL diagrams published in the 7th ARRL \*  
Computer Networking Conference papers. The diagrams h  
Copyright 2007 Jon Loeliger, Freescale Semiconductor, Inc. \*/  
Copyright (c) 2017-present, Facebook, Inc. \*/#include <linux/crypto.h>#include <linux/init.h>#include <linux/interrupt.h>#include  
<linux/mm.h>#include <linux/module.h>#include <linux/net.h>#in  
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<peterz@infradead.org>  
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Copyright (c) 2018 Socionext Inc.  
Copyright (C) 2005-2006 Thomas Gleixner \* \* This file contains driver APIs to the irq subsystem.  
Copyright (C) 2011 Andy Walls <awalls@md.metrocast.net> \*/  
Copyright 1999 Paul Mackerras. \* \* This driver provides the encapsulation and framing for sending \* and receiving PPP frames over async serial lines. It  
relies on \* the generic PPP layer to give  
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<linux/err.h>#include <linux/idr.h>#include <linux/  
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Copyright (c) 2007-2008 Claudio Nieder <private@claudio.ch> \* \* Based on Touchright driver (drivers/input/touchscreen/touchright.c)  
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Carvalho Chehab <mchehab@kernel.org>  
Copyright (C) 2012 Texas Instruments  
Copyright (C) 2014 Angelo Compagnucci <angelo.compagnucci@gmail.com> \* \* Driver for Texas Instruments' ADC128S052, ADC122S021 and  
ADC124S021 ADC chip. \* Datasheets can be found here:  
Copyright (C) 2010 Google, Inc. \*/  
Copyright (C) 2004-2005 Marcel Holtmann <marcel@holtmann.org> \*/  
Copyright (C) 2003-2004 Venkatesh Pallipadi <venkatesh.pallipadi@intel.com>.  
Copyright (C) 2009 \* Theodore Ts'o (tytso@mit.edu) \* \* Track which blocks in the filesystem are metadata blocks that \* should never be used as data  
blocks by files or directories.  
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Copyright (C) 2011 Rafael J. Wysocki <rjw@sisk.pl>, Renesas Electronics Corp. \*/#define pr\_fmt(fmt) "PM: " fmt  
Copyright (C) 2009 \* Albin Tonnerre, Free Electrons <albin.tonnerre@free-electrons.com> \* \* Original code:  
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Copyright (C) 2006 Andrey Volkov <avolkov@varma-el.com> \* Varma Electronics Oy \*  
Copyright (c) 2008 Jonathan Cameron \* \* Based on elements of hwmon and input subsystems.  
Copyright (C) 2012 Bertrand Achard (nvram access fixes) \*/  
Copyright (C) 2004-2013 Synopsys, Inc. (www.synopsys.com) \* \* Driver for the ARC EMAC 10100 (hardware revision 5)  
Copyright (C) 2017 Linaro Ltd. \*/#include <linux/errno.h>#include <linux/hash.h>  
Copyright (C) 2018 Linaro Ltd. \* \* Author: Stanimir Varbanov <stanimir.varbanov@linaro.org>  
Copyright (C) 2010 Renesas Solutions Corp. \* Tony SIM <chinyeow.sim.xt@renesas.com> \* \* Using code from:  
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Copyright 2002-2005, Devicescape Software, Inc.  
Copyright (C) 2009, Kay Sievers <kay.sievers@vrfy.org> \* \* During bootup, before any driver core device is registered, \* devtmpfs, a tmpfs-based  
filesystem is created. Every driver-core \* device w  
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Copyright (C) 2000 Wolfgang Grandegger (wolfgang@ces.ch) \* \* This program is largely derived from the Belkin USB Serial Adapter Driver \* (see  
belkin\_sa.[ch]). All of the information about the devic  
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copyright information. \* 03-16-15 02.00.17 Updated for MPI v2.6. \* Added MPI26\_SCSIIO\_IOFLAGS\_ESCAPE\_PASSTHROUGH. \*  
Added MPI2\_SEP\_REQ\_SLOTSTATUS\_DEV\_OFF a  
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Copyright (C) 1999-2005 - Karim Yaghmour (karim@opersys.com) \* \* Moved to kernel/relay.c by Paul Mundt, 2006. \* November 2006 - CPU hotplug  
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Copyright Tomi Manninen OH2BNS (oh2bns@sral.fi) \*/#include <linux/types.h>#include <linux/slab.h>#include <linux/socket.h>#include  
<linux/timer.h>#include <net/ax25.h>#include <linux/skbuff.h>  
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Copyright (C) 2017 Stefan Roese <sr@denx.de> \* \* Based on drivers/dma/xilinx/zynqmp\_dma.c, which is:  
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Copyright (C) 2016 ARM Limited \* \* Author: Will Deacon <will.deacon@arm.com>  
Copyright 2017-2018 NXP \* \* Header file containing the public API for the System Controller (SC) \* Power Management (PM) function. This includes  
functions for power state \* control, clock control,  
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Copyright (c) 2008 Patrick McHardy <kaber@trash.net> \*/#include <linux/kernel.h>#include <linux/timer.h>#include <linux/skbuff.h>#include  
<linux/netdevice.h>#include <linux/etherdevice.h>#inclu  
Copyright (C) 1994-2002 Russell King  
Copyright (C) 2009 Ilya Yanok, Emcraft Systems Ltd, <yanok@emcraft.com> \*/#include <linux/io.h>#include <linux/module.h>#include  
<linux/moduleparam.h>#include <linux/kernel.h>#include <linux/typ  
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(C) 2000 In-System Design, Inc. (support@in-system.com) \* \* The ISD200 ASIC does not natively support ATA devices. The chip \* does implement an  
interface, the ATA Command Block (ATACB) which provi  
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\_\_AD\_SIGMA\_DELTA\_H\_\_  
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 Copyright 2011 Wolfson Microelectronics plc  
 Copyright (C) 2003 Tresys Technology, LLC \* \* Updated: Yuichi Nakamura <ynakam@hitachisoft.jp> \* Tuned number of hash slots for avtab to  
 reduce memory usage  
 Copyright (C) 2001-2002 Convergence Integrated Media GmbH Holger Waechtler <holger@convergence.de>  
 Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_CPU\_TYPE\_H#define \_\_ASM\_CPU\_TYPE\_H  
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 Copyright (c) 2006-2008 Daniel Mack, Karsten Wiese\*/  
 Copyright (c) 2014 Anna Schumaker <Anna.Schumaker@Netapp.com> \*/#include <linux/fs.h>#include <linux/sunrpc/addr.h>#include  
 <linux/sunrpc/sched.h>#include <linux/nfs.h>#include <linux/nfs3.h>#i  
 Copyright (C) 2015 - 2017 Intel Deutschland GmbH  
 Copyright(c) 2019 Intel Corporation. \* \* Author: Björn Töpel <bjorn.topel@intel.com>  
 Copyright (c) 2014 Christoph Hellwig. \*/#include <linux/blkdev.h>#include <linux/kmod.h>#include <linux/file.h>#include <linux/jhash.h>#include  
 <linux/sched.h>#include <linux/sunrpc/addr.h>  
 Copyright (C) Alex Aizman, 2004 - 2005 \*/#include <linux/module.h>#include <linux/mutex.h>#include <linux/slab.h>#include <linux/bsg-  
 lib.h>#include <linux/idr.h>#include <net/tcp.h>#include <s  
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 Copyright (c) by Uros Bizjak <uros@kss-loka.si> \* \* Midi synth routines for OPL2/OPL3/OPL4 FM  
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 problems to v4l MailingList: linux-media@vger.kernel.o  
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 Copyright (C) 2008 Ira W. Snyder <iws@ovro.caltech.edu> \* \* This driver is based on the ds1621 and ina209 drivers.  
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 code\*\*\*\*\*  
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Copyright (c) 2007 Samuel Ortiz <samuel@sortiz.org> \* which is in turn partly based on the wireless netlink code:  
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Copyright (C) 2013 Avionic Design GmbH  
Copyright (C) 2015-2019 Corentin Labbe <clabbe.montjoie@gmail.com> \* \* Core file which registers crypto algorithms supported by the CryptoEngine.  
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Copyright (c) 2016 Facebook \*/#include "percpu\_freelist.h"  
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Copyright (c) 2017, Intel Corporation. \*/#include <linux/device.h>#include <linux/hid-sensor-hub.h>#include <linux/iio/buffer.h>#include <linux/iio/iio.h>#include <linux/iio/triggered\_buffer.h>  
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Copyright (C) 2018 Marcus Folkesson <marcus.folkesson@gmail.com> \*/#include <linux/bitops.h>#include <linux/iio/iio.h>#include <linux/iio/sysfs.h>#include <linux/init.h>#include <linux/module.h>  
Copyright (C) 2013, Angelo Compagnucci \* Author: Angelo Compagnucci <angelo.compagnucci@gmail.com> \* \* Datasheet: <http://ww1.microchip.com/downloads/en/devicedoc/22088b.pdf> \* <http://ww1>  
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Copyright (c) 2015-2016 HiSilicon Technologies Co., Ltd. \*/#include <linux/bitops.h>#include <linux/clk.h>#include <linux/dma-mapping.h>#include <linux/iopoll.h>#include <linux/module.h>#includ  
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Copyright (C) 2005 Antonino Daplas <adaplas@pol.net> \* \* Based from the VESA(TM) Coordinated Video Timing Generator by \* Graham Lovridge April 9, 2003 available at \* <http://www.elo>  
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 Copyright (c) by Jaroslav Kysela <perex@perex.cz> \* Universal interface for Audio Codec '97 \* \* For more details look to AC '97 component  
 specification revision 2.2 \* by Intel Corporation (http  
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 copyright information. \* 03-16-15 02.00.26 Updated for MPI v2.6. \* Added MPI2\_EVENT\_ACTIVE\_CABLE\_EXCEPTION and \*  
 MPI26\_EVENT\_DATA\_ACTIVE\_CABLE\_EXCEPT. \* Added  
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 (C) 2007 Kees Lemmens (adapted for kernel 2.6.20)  
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 Copyright (C) 2004 Red Hat, Inc., James Morris <jmorris@redhat.com> \*/#include <linux/types.h>#include <linux/kernel.h>#include  
 <linux/netlink.h>#include <linux/rtnetlink.h>#include <linux/if.h>  
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 (C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Export hfs data via xattr  
 (C) Copyright 2001 Linus Torvalds \* \* Atomic wait-for-completion handler data structures. \* See kernel/sched/completion.c for details.  
 Copyright (C) 2012,2013 - ARM Ltd \* Author: Marc Zyngier <marc.zyngier@arm.com> \* \* Based on arch/arm/kvm/emulate.c  
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 Copyright (c) 2017, EPAM Systems \*/#include <linux/device.h>#include <linux/dma-buf.h>#include <linux/genalloc.h>#include <linux/slab.h>#include  
 <linux/tee\_drv.h>#include "optee\_private.h"#inc  
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 <linux/device.h>#include <linux/slab.h>#include <l  
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 Copyright (C) 2012 Texas Instruments Inc.  
 Copyright (C) by Todd E. Johnson (mtouchusb.c) \* \* Driver is based on touchkitusb.c \* - ITM parts are from itmtouch.c \* - 3M parts are from  
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 Copyright (C) 2005-2006 Intel Corporation \* Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com> \* \* Code for uploading the PHY parameters to the  
 PHY through the UWB \* Radio Control interface.  
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 <linux/moduleparam.h>#include <sound/core.h>#include  
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 Copyright (C) Andreas Gruenbacher, 2001  
 Copyright (C) 2004 - 2005 Alex Aizman \* maintained by open-iscsi@googlegroups.com \*/#include <linux/types.h>#include <linux/kfifo.h>#include  
 <linux/delay.h>#include <linux/log2.h>#include <linu  
 Copyright (C) 2006 Dominik Kühlen \* Based on lbnp21 driver \* \* the project's page is at https://linuxtv.org  
 Copyright (c) by Jaroslav Kysela <perex@perex.cz> \* Routines for control of MPU-401 in UART mode \* \* Modified for the Aureal Vortex based  
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(C) Nadia Yvette Chambers, April 2004 \*/#include <linux/list.h>#include <linux/init.h>#include <linux/mm.h>#include <linux/seq\_file.h>#include  
<linux/sysctl.h>#include <linux/highmem.h>#include  
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Copyright (c) 2016-2018 Cavium Inc. \*/#ifndef \_FCOE\_FW\_FUNCS\_H#define \_FCOE\_FW\_FUNCS\_H#include "drv\_scsi\_fw\_funcs.h"#include  
"qedf\_hsi.h"#include <linux/qed/qed\_if.h>  
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<linux/xattr.h>#include <linux/mount.h>#include  
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Copyright 2015 Marcus Cooper <codekipper@gmail.com> \* \* Based on the Allwinner SDK driver, released under the GPL.  
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<linux/skbuff.h>#include <linux/slab.h>#include <linux/i

Copyright (c) 2012 Steffen Trumtrar <s.trumtrar@pengutronix.de>, Pengutronix \*/#include <linux/errno.h>#include <linux/export.h>#include  
<linux/of.h>#include <video/display\_timing.h>#include <vi  
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<linux/export.h>#include <linux/balloon\_compaction.h>  
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<linux/random.h>#include <linux/rculist.h>#include <linux/  
Copyright (C) 2010 Intel Corporation <tony.luck@intel.com> \* \* This code is the generic layer to export data records from platform \* level persistent  
storage via a file system.  
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<linux/capability.h>#include <linux/skbuff.h>#include <lin  
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Copyright (C) 2005 MontaVista Software Inc. <source@mvista.com> \* \* 2\_by\_8 routines added by Simon Munton  
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Copyright 2006-7 Jonathan Corbet <corbet@lwn.net> \*/#include <linux/clk.h>#include <linux/init.h>#include <linux/module.h>#include  
<linux/slab.h>#include <linux/i2c.h>#include <linux/delay.h>#  
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<linux/debugfs.h>#include <linux/ieee80211.h>#include  
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copyright: * Copyright(c) 2013, Google Inc. */#include <linux/delay.h>#include <linux/err.h>#include <linux/gpio/consumer.h>#include
<linux/i2c.h>#include <linux/interrupt.h>#include <linux/ke
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<linux/smp.h>#include <linux/cpu.h>
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Copyright 2009 Albert Herranz <albert_herranz@yahoo.es> * * Based on drivers/ssb/pcmcia.c
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```

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activated and de-activated by the line \* disci  
Copyright 1999-2002 Paul Mackerras. \* \* The generic PPP layer handles the PPP network interfaces, the \* /dev/ppp device, packet and VJ compression,  
and multilink. \* It talks to PPP `channels' via  
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<thomas.lendacky@amd.com>  
Copyright Jonathan Naylor G4KLX (g4klx@g4klx.demon.co.uk) \*/#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include  
<linux/in.h>#include <linux/kernel.h>#include <linu  
Copyright (C) 2014 Olli Salonen <olli.salonen@iki.fi> \* \* Heavily based on CIMax2(R) SP2 driver in conjunction with NetUp Dual \* DVB-S2 CI card  
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Copyright (C) 2006, 2007 Chris Pascoe (c.pascoe@itee.uq.edu.au)  
Copyright (c) 2013 Peter Meerwald <pmeerw@pmeerw.net> \* \* Color light sensor with 16-bit channels for red, green, blue, clear); \* 7-bit I2C slave  
address 0x39 (TCS34721, TCS34723) or 0x29 (TCS34725  
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copyright bit#define CM\_DAC2SPDO 0x00200000 /\* enable wave+fm\_midi -> SPDIF/OUT \*/#define CM\_INVIDWEN 0x00100000  
/\* internal vendor ID write enable, model 039? \*/#define CM\_SETRETRY 0x00100000 /  
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 Copyright (c) 2006 Willem Duinker \* v4l2 interface modeled after the V4L2 driver \* for SN9C10x PC Camera Controllers \* \* Register defines taken from  
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 Copyright (C) 2007 Alan Stern  
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 Copyright (c) 2017 United Western Technologies, Corporation \* \* Joshua Clayton <stillcompiling@gmail.com>  
 (C) UV\_Meta\_Plane \* (D) UV\_UBWC\_Plane \* \* Y\_Meta\_Plane consists of meta information to decode compressed \* tile data in  
 Y\_UBWC\_Plane. \* Y\_UBWC\_Plane consists of Y data in compressed macro-ti  
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 <linux/mm.h>#include <linux/pm.h>#include <linux/  
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 Copyright (C) 2018 Marvell \* \* Gregory Clement <gregory.clement@bootlin.com>  
 Copyright (C) 2018 Vasily Khoruzhick <anarsoul@gmail.com> \*/  
 Copyright (C) 2003 Linus Torvalds \* \* Mon Mar 5, 2007: Davide Libenzi <davidel@xmailserver.org> \* Changed ->read() to return a siginfo structure  
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sundance.c.  
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Copyright 2007 Pierre Ossman \* \* SDIO function driver model  
Copyright (c) 2018 Google, Inc. \* \* When CPU, memory and IO are contended, tasks experience delays that \* reduce throughput and introduce latencies  
into the workload. Memory \* and IO contention, i  
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Copyright (c) 2006 Herbert Xu <herbert@gondor.apana.org.au> \* \* Added AEAD support to cryptd. \* Authors: Tadeusz Struk  
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<linux/module.h>#include <linux/buffer\_head.h>#include <linux/  
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Copyright (C) 2017 Linus Walleij \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/platform\_device.h>#include  
<linux/gpio/consumer.h>#include <linux/of.h>#include <linux/of\_de  
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Copyright (c) 2014-2015 Thomas Graf <tgraf@suug.ch>  
Copyright (c) 2019 HiSilicon Limited.#include <linux/dma-mapping.h>#include <linux/module.h>#include <linux/slab.h>#include "qm.h"  
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Copyright 2018-2019 NXP \* \* There is no Shared Descriptor for PKC so that the Job Descriptor must carry \* all the desired key parameters, input and  
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http://www.microchip.com/downloads/en/DeviceDoc/22147a.pdf  
Copyright (C) 2008 Google, Inc. \* Author: Mike Lockwood <lockwood@android.com> \*/  
copyright(struct cdrom\_device\_info struct packet\_command \*cgc){ int ret; u\_char buf[8]; const struct cdrom\_device\_ops  
\*cdo = cdi->ops;  
Copyright (C) 2013 Intel Corporation. All rights reserved  
Copyright (C) 2007 Simon Arlott  
COPYRIGHT)) bits if ((diga->status[0] & IEC958\_AES0\_CON\_EMPHASIS) == IEC958\_AES0\_CON\_EMPHASIS\_NONE)  
bits |= 0x40; if (!(diga->status[1] & IEC958\_AES1\_CON\_ORIGINAL)) bits |= 0x80; if ((d  
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Copyright Darryl Miles G7LED (dlm@g7led.demon.co.uk) \*/#include <linux/module.h>#include <linux/moduleparam.h>#include  
<linux/capability.h>#include <linux/errno.h>#include <linux/types.h>#inclu  
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Copyright (c) 2016 BayLibre, SAS. \* Author: Kevin Hilman <khilman@baylibre.com> \*/#include <linux/kernel.h>#include <linux/module.h>#include  
<linux/init.h>#include <linux/delay.h>#include <linu  
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 Copyright (c) 2006, 2006 Michael Buesch <m@bues.ch>  
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 (C) 2006 by Patrick McHardy <kaber@trash.net>  
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 Copyright (C) 2018 Marvell \* \* Author: Thomas Petazzoni <thomas.petazzoni@bootlin.com>  
 Copyright (C) 2019 Linux Foundation <gregkh@linuxfoundation.org> \* \* debugfs is for people to use instead of /proc or /sys. \* See  
 ./Documentation/core-api/kernel-api.rst for more details.  
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 and by a test vector provided by Ken Buchanan at \*  
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 Copyright (c) 2007, 2008, 2009, 2010 Juerg Haefliger <juergh@gmail.com> \* \* This driver is an I2C/ISA hybrid, meaning that it uses the I2C bus to  
 access \* the chip registers if a DME1737, A8000, or  
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 <linux/ssb/ssb\_regs.h>#include <linux/bcma/bcma.h>#include <linux/bcma  
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 Copyright (C) 2006 S1 Corp. \* \* Original driver for 2.6.10 pl2303 driver by \* Greg Kroah-Hartman (greg@kroah.com) \* Changes for 2.6.20 by Harald  
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 Copyright 2016-2019 NXP \*/#include <linux/init.h>#include <linux/module.h>#include <linux/platform\_device.h>#include  
 <linux/etherdevice.h>#include <linux/of\_net.h>#include <linux/interrupt.h>#  
 Copyright 2014 Sutajio Ko-Usagi PTE LTD \* \* Author: Sean Cross <xobs@kosagi.com>  
 Copyright (c) 2011-2014,2016-2017 Qualcomm Atheros, Inc. \*/  
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 Copyright (C) 2018 Linaro Ltd.#ifndef \_\_VENUS\_HFI\_PARSER\_H\_\_#define \_\_VENUS\_HFI\_PARSER\_H\_\_  
 Copyright (c) 2013 Florian Fainelli <florian@openwrt.org> \*/  
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 Copyright (c) 2008 Michael Krufky <mkrufky@linuxtv.org> \*/  
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Copyright (C) 2017 Pengutronix, Philipp Zabel <kernel@pengutronix.de> \* \* Based on Allwinner SoCs Reset Controller driver  
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Copyright (c) 2016, Zodiac Inflight Innovations  
Copyright (C) 2010-2011 Samsung Electronics Co., Ltd. \*/  
Copyright (c) 2015 Intel Corporation \*/#ifndef \_\_IIO\_CONFIGFS#define \_\_IIO\_CONFIGFS  
Copyright (C) 2007 David Brownell (simplification, cleanup) \*/  
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Copyright(c) 2019 - 2020 Intel Corporation \* All rights reserved  
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Copyright (C) 2003 Michiel Rook <michiel@grendelproject.nl>  
Copyright (C) 2015 Aleksa Sarai <cyphar@cyphar.com> \*/  
Copyright (C) 2010 Matthias Fuchs <matthias.fuchs@esd.eu>, esd gmbh \* \* Known issues:  
Copyright (c) 2007-2009 Oliver Neukum \* \* Some code taken from cdc-acm.c  
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Wang Lei (wang840925@gmail.com) \* David Howells  
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(C) 1994-2001 Russell King  
Copyright (C) 2002-2003 Fabrizio Gennari <fabrizio.gennari@philips.com>  
Copyright (C) 2004 Michel Xhaard mxhaard@magic.fr \* \* V4L2 by Jean-Francois Moine <http://moinejf.free.fr>  
Copyright (C) 2008 Hans Verkuil <hverkuil@xs4all.nl> \* \* These routines maintain argument size conversion between 32bit and 64bit \* ioctl.  
Copyright (C) Fuzhou Rockchip Electronics Co.Ltd \* Author: Jacob Chen <jacob-chen@iotwrt.com> \*/  
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Copyright (C) 2002-2011 Hugh Dickins.  
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Copyright(c) 2002-2010 Exar Corp. \*\*\*\*\*/#ifndef  
VXGE\_REG\_H#define VXGE\_REG\_H  
Copyright 2011, Broadcom Corporation  
Copyright(c) 1999 - 2018 Intel Corporation.  
Copyright (C) 1999-2002 Vojtech Pavlik \*/#ifndef \_SERIO\_H#define \_SERIO\_H  
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Copyright(c) 2007 - 2008 Intel Corporation. All rights reserved  
Copyright (c) 2005 David Shaohua Li <shaohua.li@intel.com>  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_FB\_H#define \_\_ASM\_FB\_H\_  
(C) 2004 by Bart De Schuymer <bdschuym@pandora.be> \*  
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(C) 2009 Alexander Clouter <alex@digriz.org.uk> \*/  
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Copyright (C) 2008 Christoph Hellwig.  
Copyright (C) 2010 OMICRON electronics GmbH \*/#include <linux/device.h>#include <linux/export.h>#include <linux/file.h>#include <linux/posix-  
clock.h>#include <linux/slab.h>#include <linux/sysca  
Copyright (c) 2004 Utilitek Systems, Inc. \* \* derived in part from lm78.c:  
Copyright (c) 2019, Cadence Design Systems \* Author: Tom Joseph <tjoseph@cadence.com> \*/#include <linux/kernel.h>#include  
<linux/of\_address.h>#include <linux/of\_pci.h>#include <linux/platform\_de  
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 Copyright (C) 2000-2004 Netfilter Core Team <coreteam <at> netfilter.org>  
 Copyright (C) 2016-2019 Corentin LABBE <clabbe.montjoie@gmail.com> \* \* This file add support for AES cipher with 128,192,256 bits keysize in \* CBC and ECB mode.  
 Copyright IBM Corporation, 2017 \* \* Authors: Paul E. McKenney <paulmck@linux.net.ibm.com>  
 Copyright (C) 2017 Arm Ltd.#ifndef \_\_ASM\_SDEI\_H#define \_\_ASM\_SDEI\_H  
 Copyright(c) 2011 Intel Corporation. All rights reserved  
 Copyright (c) 2002, Dr Brian Gladman <brg@gladman.me.uk>, Worcester, UK.  
 Copyright (c) 2017 Amlogic, inc. \* Author: Qiufang Dai <qiufang.dai@amlogic.com> \*/  
 Copyright (C) 2004-2013 Synopsys, Inc. (www.synopsys.com) \* \* MDIO implementation for ARC EMAC  
 Copyright (C) 2005-2009 Rodolfo Giometti <giometti@linux.it> \*/  
 Copyright (c) 2018 Christoph Hellwig. \*/#include <linux/module.h>#include <linux/compiler.h>#include <linux/fs.h>#include <linux/iomap.h>#include <linux/pagemap.h>#include <linux/pagevec.h>  
 Copyright (C) 2018 Linaro Ltd <ard.biesheuvel@linaro.org> \*/  
 Copyright (c) 2010 Paul Thomas <pthomas8589@gmail.com> \* \* You need to have something like this in struct spi\_board\_info  
 Copyright (C) 1998 \* Daniel Pirkel <daniel.pirkel@email.cz> \* Charles University, Faculty of Mathematics and Physics \* \* UFS2 write support Evgeniy Dushistov <dushistov@mail.ru>, 2007  
 Copyright (C) 2001, 2002 Andreas Dilger <adilger@clusterfs.com> \* \* This could probably be made into a module, because it is not often in use.  
 Copyright 31 August 2008 James Bottomley  
 Copyright (C) 2004 - 2005 Dmitry Yusupov  
 Copyright (C) 2018 HUAWEI, Inc. \* http://www.huawei.com/ \* Created by Gao Xiang <gaoxiang25@huawei.com> \*/#include  
 "internal.h"#include <linux/pagevec.h>  
 Copyright (C) 2005 Greg Kroah-Hartman <gregkh@suse.de> \* \* Based on fs/debugfs/inode.c which had the following copyright notice:  
 COPYRIGHT | IEC958\_AES0\_CON\_EMPHASIS\_NONE; cs[1] = IEC958\_AES1\_CON\_GENERAL; cs[2] = IEC958\_AES2\_CON\_SOURCE\_UNSPEC | IEC958\_AES2\_CON\_CHANNEL\_UNSPEC; cs[3] = IEC958\_AES3\_CON\_CLOCK\_1000PPM | fs;  
 Copyright (C) 2010 OMICRON electronics GmbH \*/#include <linux/errqueue.h>#include <linux/phy.h>#include <linux/ptp\_classify.h>#include <linux/skbuff.h>#include <linux/export.h>  
 Copyright (c) 2011 Jiri Pirkko <jpirko@redhat.com> \*/#ifndef LINUX\_IF\_TEAM\_H#define LINUX\_IF\_TEAM\_H  
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 Copyright(c) 2018 Intel Corporation.  
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 (C) 2007 Hans J. Koch <hjk@hansjkoch.de>  
 (C) 2002 - 2004 Dominik Brodowski <linux@brodo.de> \*/  
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 Copyright (C) 1999 Martin Mares <mj@ucw.cz> \* \* Arbitrary resource management.  
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 Copyright (C) 2001-2004 by David Brownell \*/  
 Copyright (C) 2004-2006 Red Hat, Inc. All rights reserved  
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 Copyright (C) 2003-2008 Takahiro Hirofuchi  
 Copyright (c) 1998-1999 by Frank van de Pol <fvdpol@coil.demon.nl> \*/#ifndef \_\_SND\_SEQ\_CLIENTMGR\_H#define \_\_SND\_SEQ\_CLIENTMGR\_H  
 Copyright (c) 2018 Rodrigo Rivas Costa <rodrigorivascosta@gmail.com> \* \* Supports both the wired and wireless interfaces.

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 Copyright (c) 2012 Broadcom Corporation \*/#include <linux/netdevice.h>  
 Copyright (C) 2004 Li Shaohua <shaohua.li@intel.com> \*/  
 Copyright (C) 2008 Steven Noonan <steven@uplinklabs.net> \*  
 Copyright (c) 2006 Sam Leffler, Errno Consulting  
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 Copyright 2002 MontaVista Software Inc.  
 Copyright (C) 2008 Matthias Schwarzott <zzam@gentoo.org>  
 Copyright (c) 2013 Jiri Slaby \* \* Data parsing taken from elousb driver by Vojtech Pavlik.  
 Copyright (c) 2015 Sabrina Dubroca <sd@queasysnail.net>  
 Copyright (c) 2017 Linaro Ltd. \*/  
 Copyright (C) 2013 Huawei Ltd. \* Author: Jiang Liu <liuj97@gmail.com> \* \* Based on arch/arm/kernel/jump\_label.c  
 Copyright (C) 2004-5 Patrick Boettcher (patrick.boettcher@desy.de) \* \* see Documentation/media/dvb-drivers/dvb-usb.rst for more information  
 Copyright (c) 2009 Nicholas A. Bellinger <nab@linux-iscsi.org> \*/  
 Copyright (c) 2011 Samsung Electronics Co., Ltd. \*/  
 Copyright(c) 2004 Intel Corporation. All rights reserved  
 Copyright (C) 2017-2018 ASIX  
 (C) Copyright 2005 Christoph Lameter, Silicon Graphics, Inc. \* \* NUMA policy allows the user to give hints in which node(s) memory should \* be  
 allocated.  
 Copyright (c) 2017 NXP.  
 Copyright (c) 2017, 2018 Oracle. All rights reserved  
 Copyright (C) 2004 Andrew de Quincey (tuning thread cleanup) \*/  
 Copyright (C) 2004 IBM Corporation. All rights reserved  
 Copyright (C) 2012 ARM Ltd. \* \* Note: this file should not be included by non-asm/.h files  
 (C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handler routines for unicode strings  
 Copyright (c) 2015, NVIDIA Corporation. \*/  
 Copyright: (C) Torsten Schenk \*/#ifndef USB6FIRE\_COMM\_H#define USB6FIRE\_COMM\_H  
 Copyright (C) 2007 IBM \* \* Author: Darrick J. Wong <darrick.wong@oracle.com>  
 Copyright (c) 2010-2018 Nikolai Kondrashov  
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 Copyright 2004 Andi Kleen, SuSE Labs.  
 Copyright (c) 2014, Applied Micro Circuits Corporation \* Author: Tanmay Inamdar <tinamdar@apm.com> \* Duc Dang <dhdang@apm.com>  
 \*/#include <linux/cpu.h>#include <linux/interrupt.h>#include <li  
 Copyright (C) 2012-2020 B.A.T.M.A.N. contributors: \* \* Martin Hundebøll, Jeppe Ledet-Pedersen  
 Copyright (C) 2018 Felix Fietkau <nbd@nbd.name> \*/#include "mt76.h"  
 (C) Jens Axboe <jens.axboe@oracle.com> 2008 \*/  
 Copyright(c) 2007 - 2015 Intel Corporation. All rights reserved  
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 Copyright (C) 2010,2012 - NVIDIA, Inc. \*  
 Copyright (C) 2008, 2009 Intel Corporation \* Authors: Andi Kleen, Fengguang Wu \* \* High level machine check handler. Handles pages reported by the  
 \* hardware as being corrupted usually due to a mu  
 Copyright 2002 Phil Karn, KA9Q \* \* Description: \*  
 Copyright 2000,2009-2015 Hewlett-Packard Development Company, L.P.  
 COPYRIGHT | IEC958\_AES0\_CON\_EMPHASIS; ucontrol->value.iec958.status[1] =

```

IEC958_AES1_CON_ORIGINAL | IEC958_AES1_CON_CATEGORY; ucontrol->value.iec958.status[3] = IEC958_AES3_C
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Copyright (c) 2016 Advanced Micro Devices, Inc.
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Copyright 2008-2011 Freescale Semiconductor, Inc. *
Copyright (C) 2007 James E.J. Bottomley * <James.Bottomley@HansenPartnership.com> */#include <linux/scatterlist.h>#include
<linux/blkdev.h>#include <linux/slab.h>#include <linux/export.h>
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copyright information. * Bumped MPI2_HEADER_VERSION_UNIT. * 03-16-15 02.00.37 Bumped MPI2_HEADER_VERSION_UNIT. *
Added Scratchpad registers to *
(C) Copyright 2007 Novell Inc. */
Copyright (C) 2012 ARM Ltd. */#ifndef __ASM_HW_BREAKPOINT_H#define __ASM_HW_BREAKPOINT_H
Copyright (c) 2003-2004, Jouni Malinen <j@w1.fi>
Copyright (c) 2003-2006, Cluster File Systems, Inc, info@clusterfs.com * Written by Alex Tomas <alex@clusterfs.com> * * Architecture independence:
Copyright (c) 2010 Google, Inc
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Copyright (C) 2015 Rockchip Electronics Co., Ltd. */#ifndef _ANALOGIX_DP_H_#define _ANALOGIX_DP_H_
Copyright (c) 2006 Anssi Hannula <anssi.hannula@gmail.com> */
Copyright (c) 2014 Benjamin Tissoires <benjamin.tissoires@redhat.com> * * ChangeLog:
Copyright (c) 2014, Applied Micro Circuits Corporation * Author: Rameshwar Prasad Sahu <rsahu@apm.com> * Loc Ho <lho@apm.com> */
(C) Copyright 2001-2006 * Alex Zeffert, Cambridge Broadband Ltd, ajz@cambridgebroadband.com * Re-worked by Ben Greear
<greearb@candelatech.com> * ---
Copyright (C) 2014 Samsung Electronics * Author: Chanwoo Choi <cw00.choi@samsung.com> */
Copyright (C) 2004 Florian Schirmer (jolt@tuxbox.org)
Copyright (c) 2012-2014 NVIDIA CORPORATION. All rights reserved
Copyright (C) 2019 Linaro Ltd. * Author: Manivannan Sadhasivam <manivannan.sadhasivam@linaro.org> * * TODO: Triggered buffer support
Copyright (C) 2000 Marc Boucher <marc@mbsi.ca>
Copyright (C) 2017-18 Texas Instruments Incorporated - http://www.ti.com/
Copyright (c) 2004 TDF */
Copyright (C) 1991, 1992 Linus Torvalds * * proc base directory handling functions
Copyright (C) 2009 Arjan van de Ven <arjan@linux.intel.com> */
Copyright (C) 2002 Peter Hettkamp <peter.hettkamp@htp-tel.de>
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Copyright (C) 2005 Antonino Daplas <adaplas@pol.net>
Copyright (c) 2017-2018, Ericsson AB * All rights reserved
Copyright (C) 1999-2000 Takashi Iwai
Copyright (c) 2014 Christoph Hellwig. */#include "xfs.h"#include "xfs_shared.h"#include "xfs_format.h"#include "xfs_log_format.h"#include
"xfs_trans_resv.h"#include "xfs_mount.h"#include "xfs_
Copyright (C) 2005-9 DiBcom, SA et al */#include "dib0700.h"
Copyright (c) 2011-2014 PLUMgrid, http://plumgrid.com */#ifndef _LINUX_BPF_H#define _LINUX_BPF_H 1
Copyright (C) 2010 Felix Fietkau <nbd@openwrt.org> */
Copyright (C) 2013, Intel Corporation */#include <linux/bug.h>#include <linux/kernel.h>#include <linux/math64.h>#include <linux/export.h>#include
<linux/ctype.h>#include <linux/errno.h>#include

```

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Copyright (C) 2012 Google, Inc \* \* NOTE: This file is auto-generated from ChromeOS EC Open Source code from \*  
[https://chromium.googlesource.com/chromiumos/platform/ec/+master/include/ec\\_commands.h](https://chromium.googlesource.com/chromiumos/platform/ec/+master/include/ec_commands.h)  
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Copyright (C) 2006, 2007, 2008 Kevin Lloyd <kloyd@sierrawireless.com>,  
Copyright 2019 NXP \* \* Header file for the DSP IPC implementation  
Copyright(C) 2015 Intel Ltd  
Copyright (C) 2015 Intel Corp., Peter Zijlstra <peterz@infradead.org> \* \* Since RB-trees have non-atomic modifications they're not immediately suited \*  
for RCU/lockless queries. Even though we made  
Copyright (C) 1995-2005 Mark Adler \* For conditions of distribution and use, see copyright notice in zlib.h \* \* Based on zlib 1.2.3 but modified for the  
Linux Kernel by \* Richard Purdie <richard@o  
Copyright (C) 2008 MIMOMax Wireless Ltd. \*/  
Copyright (C) 2007 Tehuti Networks Ltd. All rights reserved  
Copyright (C) 1997 David S. Miller (davem@caip.rutgers.edu)  
Copyright (C) David Howells \* \* NFS mount handling.  
Copyright 2016 Broadcom \*/  
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Copyright (C) 2009 Crane Cai <crane.cai@amd.com> \*/  
Copyright (c) 2004 The Regents of the University of Michigan.  
Copyright (c) 2015 Pengutronix, Philipp Zabel <p.zabel@pengutronix.de> \* \* Based on the barebox ocotp driver,  
Copyright (c) 2005, 2006 Cisco Systems. All rights reserved  
Copyright (c) 2004 - 2013 Broadcom Corporation  
Copyright 2012 Lothar Felten <l-felten@ti.com>  
Copyright 2018-2019 NXP \* \* Based on caamalg.c crypto API driver.  
Copyright (C) Icenowy Zheng <icenowy@aosc.io> \* \* Based on sun4i\_layer.h, which is:  
Copyright (c) 2015 QLogic Corporation \*/  
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Copyright (C) 2008 Harrison Metzger <harrisonmetz@gmail.com> \* Based on usbled.c by Greg Kroah-Hartman (greg@kroah.com) \*/  
Copyright (C) 2013 Emilio L \* \* Adjustable factor-based clock implementation  
Copyright (c) 2019-2020 Red Hat GmbH \* \* Author: Stefano Brivio <sbrivio@redhat.com>  
(C) Copyright 2015 Intel Corp.  
Copyright (C) 2014 Beniamino Galvani <b.galvani@gmail.com> \*/  
Copyright (C) 2011 Hans de Goede <hdegoede@redhat.com> \* \* Based on the usbvideo vcam driver, which is: \*  
Copyright (c) 2010 - 2011 Ping Cheng, Wacom. <pingc@wacom.com>  
Copyright (C) 2011 \* Laurent Pinchart (laurent.pinchart@ideasonboard.com) \*/  
Copyright (C) 2009 PetaLogix. All rights reserved  
Copyright (C) 2002 David S. Miller  
(C) 2001-2003 Bart De Schuymer <bdschuym@pandora.be> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt  
Copyright (C) 2011, SUSE, Jan Kara <jack@suse.cz> \* \* The goal of this code is: Given different types of event, measure proportion \* of each type of  
event over time. The proportions are measured wi  
Copyright 1993 assigned to the United States Government as represented  
Copyright 2011 Marvell. <jyli@marvell.com>\*/  
Copyright (C) 2003-2008, LINBIT Information Technologies GmbH.  
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Copyright (C) 2008 Eurotech S.p.A. <info@eurotech.it>

Copyright (C) 2012 ARM Limited \* Author: Will Deacon <will.deacon@arm.com> \*/  
Copyright 2019 Collabora Ltd#ifndef \_\_PANFROST\_PERFCNT\_H\_\_#define \_\_PANFROST\_PERFCNT\_H\_\_  
(C) 2012 by Pablo Neira Ayuso <pablo@netfilter.org> \*/  
Copyright (C) 2000 Deep Blue Solutions Ltd.  
Copyright (C) 2008 Red Hat, Inc., James Morris <jmorris <at> redhat.com> \*/#include <linux/module.h>#include  
<linux/netfilter\_ipv6/ip6\_tables.h>#include <linux/slab.h>  
Copyright 2013 \* \* Based on \* Raspberry Pi PCM I2S ALSA Driver  
Copyright (C) 2003-2004 Marcel Holtmann <marcel@holtmann.org>  
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Copyright (c) 2005-2008 Michael Buesch <m@bues.ch>  
Copyright (C) 2010-2013 Felix Fietkau <nbd@openwrt.org> \*/#include <linux/netdevice.h>#include <linux/types.h>#include <linux/skbuff.h>#include  
<linux/debugfs.h>#include <linux/random.h>#include  
Copyright 2011 - 2012 Cavium, Inc. \* \* Based on code taken from recortmcount.c which is: \*  
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Copyright (C) 1991, 1992 Linus Torvalds \* \* Modified to make sys\_syslog() more flexible: added commands to \* return the last 4k of kernel messages,  
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Copyright(c) 2009 Intel Corporation \*/#include <linux/kernel.h>#include <linux/interrupt.h>#include <linux/module.h>#include <linux/dma-  
mapping.h>#include <linux/raid/pq.h>#include <linux/async  
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Copyright 2013 Philipp Zabel, Pengutronix \*/#include <linux/atomic.h>#include <linux/device.h>#include <linux/err.h>#include  
<linux/export.h>#include <linux/kernel.h>#include <linux/kref.h>#in  
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<linux/socket.h>#include <linux/in.h>#include <linux/kernel.h>#include <  
Copyright (c) 2015-2016 HGST, a Western Digital Company. \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/scatterlist.h>#include  
<linux/blk-mq.h>#include <linux/nvme.h>#include <linu  
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 <linux/pci.h>#include <linux/dma-mapping.h>#include <linux/s  
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 <linux/slab.h>#include <linux/serial\_core.h>#include <linux/serial\_reg.h  
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 driver began life based on an old version of th  
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 <linux/of\_device.h>#include <linux/of\_address.h>#inc  
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 <linux/pm\_qos.h>#include <linux/hrtimer.h>#include <  
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 Copyright (C) 2010 ST-Ericsson SA \*/#include <linux/module.h>#include <linux/moduleparam.h>#include <linux/init.h>#include  
 <linux/ioport.h>#include <linux/device.h>#include <linux/io.h>#includ  
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"percpu\_freelist.h"  
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 <linux/module.h>#include <linux/skbuff.h>#include <linux/ip.h>  
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 Copyright (C) 2004 Thomas Gleixner (tglx@linutronix.de) \* \* RS code lifted from reed solomon library written by Phil Karn  
 Copyright (C) 2008 Russell King. \* \* Helper for the clk API to assist looking up a struct clk.  
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 368 standard.  
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[http://www.cesko.host.sk/IgorPlugUSB/IgorPlug-USB%20\(AVR\)\\_eng.htm](http://www.cesko.host.sk/IgorPlugUSB/IgorPlug-USB%20(AVR)_eng.htm)  
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 Andreas Gruenbacher <agruen@suse.de>. \*/  
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 Copyright (C) 2015 Microchip Technology \*/#include <linux/version.h>#include <linux/module.h>#include <linux/netdevice.h>#include  
 <linux/etherdevice.h>#include <linux/ethtool.h>#include <linux/  
 Copyright (C) 2002 \* Tapio Laxström (tapio.laxstrom@iptime.fi) \* \* emi26.c,v 1.13 2002/03/08 13:10:26 tapio Exp  
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(C) * STORE * smp_wmb() (B) smp_mb() (D) * STORE ->data_head STORE ->data_tail * }
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Copyright (C) 2008-2014 Christoph Lameter */#include <linux/fs.h>#include <linux/mm.h>#include <linux/err.h>#include <linux/module.h>#include
<linux/slab.h>#include <linux/cpu.h>#include <linu
(C) Copyright 1999 Georg Acher, acher@in.tum.de
Copyright (C) 2016-2019 Xilinx * * Adjustable divider clock implementation
Copyright 2004-2005 Red Hat, Inc. * Jeff Garzik <jgarzik@pobox.com>
Copyright (c) 2014, Applied Micro Circuits Corporation * Author: Loc Ho <lho@apm.com> * Tuan Phan <tphan@apm.com> * Suman Tripathi
<stripathi@apm.com> * * NOTE: PM support is not
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Copyright(c) 2007 Yuri Tikhonov <yur@emcraft.com>
Copyright(c) 2009-2013 Realtek Corporation. * * Contact Information:
(C) 2006-2012 Patrick McHardy <kaber@trash.net> * * TODO: - NAT to a unique tuple, not to TCP source port * (needs netfilter tuple
reservation)
Copyright 2011 Analog Devices Inc. */
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Copyright (C) 2005-2007 Derek Smithies <derek@indranet.co.nz> * Sponsored by Indranet Technologies Ltd * * Based on sample.c:
Copyright (c) 2013 Ondrej Zary * * Based on Syntekdriver (stk11xx) by Nicolas VIVIEN:
Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */#ifndef _LINUX_F2FS_FS_H#define _LINUX_F2FS_FS_H

```

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Copyright (C) 2010 OMICRON electronics GmbH \*/#include <linux/idr.h>#include <linux/device.h>#include <linux/err.h>#include  
<linux/init.h>#include <linux/kernel.h>#include <linux/module.h>#include  
Copyright (c) 2016-2018 Christoph Hellwig. \*/#include <linux/module.h>#include <linux/compiler.h>#include <linux/fs.h>#include  
<linux/iomap.h>#include <linux/backing-dev.h>#include <linux/uid.h>  
Copyright 2005 Andrea Merello <andrea.merello@gmail.com>, et al. \* \* Magic delays, register offsets, and phy value tables below are \* taken from the  
original r8187 driver sources. Thanks to Realte  
Copyright (c) 2001 Greg Kroah-Hartman (greg@kroah.com) \*  
(C) 2006-2012 \*/#include <linux/kernel.h>#include <linux/netfilter.h>#include <linux/netfilter\_ipv4.h>#include <linux/ip.h>#include  
<linux/skbuff.h>#include <linux/gfp.h>#include <linux/export  
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Copyright (c) 2018 Pengutronix, Philipp Zabel \* \* based on vim2m  
Copyright 2002-2004, Instant802 Networks, Inc.  
COPYRIGHT) snd\_iprintf(buffer, " Non-Copyright"); if (digi1 & AC\_DIG1\_NONAUDIO) snd\_iprintf(buffer, " Non-Audio"); if (digi1 &  
AC\_DIG1\_PROFESSIONAL) snd\_iprintf(buffer, " Pro"); if (digi1 & A  
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Copyright (c) 2014, Inforce Computing. All rights reserved  
Copyright (c) 2004-2005 Stephane VOLTZ <svoltz@numericable.fr> \* \* USB Acecad "Acecad Flair" tablet support  
(C) Copyright 2014, 2015 Linaro Ltd. \* Author: Ashwin Chaugule <ashwin.chaugule@linaro.org> \* \* CPPC describes a few methods for controlling CPU  
performance using \* information from a per CPU tabl  
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Copyright (C) 2015-2019 Texas Instruments Incorporated - <http://www.ti.com/> \* Author: Tomi Valkeinen <tomi.valkeinen@ti.com> \* \* Based on  
pwm\_bl.c  
Copyright (C) 2009-2012 Konstantin Dimitrov <kosio.dimitrov@gmail.com>  
Copyright (C) 1998 - 2014 Douglas Gilbert \* \* Version: 3.5.36 (20140603) \* This version is for 2.6 and 3 series kernels.  
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Copyright (C) 2012 FUJITSU SEMICONDUCTOR LIMITED \*/  
Copyright (C) 2006 ARM Ltd. \* written by Russell King, Deep Blue Solutions Ltd. \*/#include <linux/kernel.h>#include <linux/module.h>#include  
<linux/i2c.h>#include <linux/i2c-algo-bit.h>#includ  
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(C) Copyright 1999 Thomas Sailer, sailer@ife.ee.ethz.ch  
Copyright (c) 2019, Vladimir Oltean <olteanv@gmail.com> \* \* This module is not a complete tagger implementation. It only provides \* primitives for  
taggers that rely on 802.1Q VLAN tags to use. The  
Copyright (C) 2007-2020 B.A.T.M.A.N. contributors: \* \* Marek Lindner  
Copyright (2004, 2005) Ingo Molnar \* \* This file contains the spinlock/rwlock implementations for the \* SMP and the DEBUG\_SPINLOCK cases. (UP-  
nondebug inlines them)  
Copyright (C) 2003-2009 Jean Delvare <jdelvare@suse.de> \* \* The ADM1025 is a sensor chip made by Analog Devices. It reports up to 6 \* voltages  
(including its own power source) and up to two temper  
Copyright (C) 2003-2010 \* Julian Anastasov \* \* Authors: \* Ben North <ben@redfrontdoor.org>  
Copyright (C) 2016 BayLibre SAS. \* Author: Neil Armstrong <narmstrong@baylibre.com> \* Synchronised with arm\_mhu.c from :  
Copyright (C) 2018-2019 Intel Corporation  
Copyright (c) 2012 Taobao. \* Written by Tao Ma <boyu.mt@taobao.com> \*/

Copyright (c) 2018 Facebook \*/#include <linux/bpf.h>#include <linux/err.h>#include <linux/sock\_diag.h>#include <net/sock\_reuseport.h>  
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Copyright Samuel Mendoza-Jonas, IBM Corporation 2018.  
copyright (C) 1996 okir@monad.swb.de.\n");  
Copyright (c) 2016 HGST, a Western Digital Company. \*/#include <linux/moduleparam.h>#include <linux/slab.h>#include <linux/pci-  
p2pdma.h>#include <rdma/mr\_pool.h>#include <rdma/rw.h>  
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(C) Copyright 2008 Intel Corporation \* Author: Arjan van de Ven <arjan@linux.intel.com> \*/#define pr\_fmt(fmt) "rodata\_test: " fmt  
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Copyright (c) 2000 RP Internet (www.rpi.net.au). \* \* Author: James Morris <jmorris@intercode.com.au>  
Copyright (c) 2003 K.M. Liu <kmliu@sis.com>  
Copyright (c) 2015-2016 HGST, a Western Digital Company. \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/atomic.h>#include  
<linux/ctype.h>#include <linux/delay.h>#include <linux/err  
Copyright (C) 2007 Hans J. Koch <hjk@hansjkoch.de> \*/  
Copyright (C) 2002 Simon Evans <spse@secret.org.uk> \* \* The code for making gspca work with a webcam with 2 isoc endpoints was \* taken from the  
benq gspca subdriver which is: \*  
Copyright (c) 2015 Rockchip Electronics Co. Ltd. \* Author: Xing Zheng <zhengxing@rock-chips.com> \* Jeffy Chen <jeffy.chen@rock-chips.com>  
\*/  
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Copyright (c) 2004, 2005 James Bottomley <James.Bottomley@SteelEye.com> \*/#include <linux/ctype.h>#include <linux/init.h>#include  
<linux/module.h>#include <linux/workqueue.h>#include <linux/blkd  
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Copyright (c) 2013 Dialog Semiconductor \* \* Author: Adam Thomson <Adam.Thomson.Opensource@diasemi.com> \* Based on DA9055 ALSA SoC codec  
driver.  
Copyright (C) 2011 Sony Ericsson Mobile Communications Inc. \* \* Author: Courtney Cavin <courtney.cavin@sonyericsson.com> \* Prepared for up-  
stream by: Oskar Andero <oskar.andero@sonyericsson.com>  
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Copyright 2019 Collabora Ltd.#include <linux/devfreq.h>#include <linux/devfreq\_cooling.h>#include <linux/platform\_device.h>#include <linux/pm\_opp.h>#include <linux/clk.h>#include <linux/regulator

Copyright (c) 2006-2012 Patrick McHardy <kaber@trash.net> \* \* Based on the 'brute force' H.323 connection tracking module by \* Jozsef Kadlec <kadlec@netfilter.org>

Copyright 2017 David Lechner <david@lechnology.com> \* \* Some code copied from mipi-dbi.c

Copyright (C) 2001 Networks Associates Technology, Inc <ssmalley@nai.com>

Copyright (C) 2010 Tejun Heo <tj@kernel.org> \* \* Chunks are mapped into vmalloc areas and populated page by page. \* This is the default chunk allocator.

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VXGE\_TRAFFIC\_H#define VXGE\_TRAFFIC\_H

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Copyright 1995 Linus Torvalds \*/#include <linux/mm.h>#include <linux/fs.h>#include <linux/list.h>#include <linux/highmem.h>#include <linux/compiler.h>#include <linux/uaccess.h>#include <linux/

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Copyright 2018 Analog Devices Inc. \*/#include <linux/bitfield.h>#include <linux/clk.h>#include <linux/delay.h>#include <linux/device.h>#include <linux/err.h>#include <linux/interrupt.h>#include

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Copyright (c) 2010 - Mauro Carvalho Chehab// - Ported the driver to use rc-core// - IR raw event decoding is now done at rc-core// - Code almost re-written  
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Copyright (C) 1998, 1999 Frodo Looijaard <frodol@dds.nl> and \* Kyosti Malkki <kmalkki@cc.hut.fi>  
Copyright (C) 2015 Jemma Denson <jdenson@gmail.com> April 2015 Refactored & simplified driver Updated to work with delivery system  
supplied by DVBv5 Add frequency, fec & pilot to get\_f  
Copyright (C) 1996-2012 Markus F.X.J. Oberhumer <markus@oberhumer.com> \* \* The full LZO package can be found at:  
Copyright (c) 2016 Jiri Pirko <jiri@mellanox.com> \*/#ifndef \_NET\_DEVLINK\_H\_#define \_NET\_DEVLINK\_H\_  
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copyright Jamal Hadi Salim (2002-4) \*/  
Copyright (C) 2018 BayLibre, SAS \* Author: Maxime Jourdan <mjourdan@baylibre.com> \* \* The Elementary Stream Parser is a HW bitstream parser. \*  
It reads bitstream buffers and feeds them to the VIFI  
COPYRIGHT 0x01#define DVD\_STRUCT\_DISCKEY 0x02#define DVD\_STRUCT\_BCA 0x03#define DVD\_STRUCT\_MANUFACT 0x04  
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Copyright (c) 2005 Henk Vergonet <Henk.Vergonet@gmail.com> \*/  
COPYRIGHT); } return pci\_register\_driver()  
Copyright 2012-15 Advanced Micro Devices, Inc.cls  
Copyright 2019 NXP \*/#ifndef \_FSL\_DPMAC\_CMD\_H#define \_FSL\_DPMAC\_CMD\_H  
Copyright (C) 2003-2004, Paul Clements, SteelEye Technology, Inc. \*/#ifndef BITMAP\_H#define BITMAP\_H 1  
(C) 2000 Andrea Arcangeli <andrea@suse.de> SuSE \* Queue request tables / lock, selectable elevator, Jens Axboe <axboe@suse.de> \* kernel-doc documentation started by NeilBrown <neilb@cse.unsw.edu.au>  
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Copyright (C) 2013 Tom Gundersen <teg@jklm.no> \* \* This code registers /sys/firmware/efi{/efivars} when EFI is supported, \* allowing the efivarfs to be mounted or the efivars module to be loaded.  
Copyright 2018 Hannes Reinecke, SUSE Linux GmbH <hare@suse.com> \* \* Based on the original DAC960 driver, which has  
Copyright (C) 2011 Novell Inc.  
Copyright unasserted) \* AN = 0 (Audio data) \* P = 0 (Consumer) \*/ snd\_emu10k1x\_ptr\_write(chip, SPCS0, 0,  
chip->spdif\_bits[0] = SPCS  
Copyright (c) 2011, Tom Herbert <therbert@google.com> \* \* This header file contains the definitions for dynamic queue limits (dql). \* dql would be used in conjunction with a producer/consumer type  
Copyright (c) 2011 Mauro Carvalho Chehab \* \* This driver came from a driver originally written by:  
Copyright (C) 2005-2007 Atmel Corporation  
Copyright (c) 2011 Jonathan Cameron \*/#include <linux/err.h>#include <linux/export.h>#include <linux/slab.h>#include <linux/mutex.h>#include <linux/of.h>  
Copyright 2005 Wolfson Microelectronics PLC. \* Author: Liam Girdwood <lrg@slimlogic.co.uk> \* \* Generic AC97 support.  
Copyright unasserted) \* AN = 0 (Audio data) \* P = 0 (Consumer) \*/ emu->spdif\_bits[0] = emu->spdif\_bits[1] =  
emu->spdif\_bits[2] = SPCS\_CLKACCY\_1000PPM  
Copyright (c) by Jaroslav Kysela <perex@perex.cz> \* Universal interface for Audio Codec '97 \* \* For more details look to AC '97 component specification revision 2.1 \* by Intel Corporation (http  
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Copyright (c) 2002 James Morris <jmorris@intercode.com.au> \*/#include <linux/crypto.h>#include "internal.h"  
Copyright (c) 2008-2009 Frederic Weisbecker <fweisbec@gmail.com> \* Mostly borrowed from function tracer which

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 DSCP field within the IP Header.  
 Copyright (C) 2015, Intel Corporation \* Author: Jiang Liu <jiang.liu@linux.intel.com> \*/#ifndef \_LINUX\_RESOURCE\_EXT\_H#define  
 \_LINUX\_RESOURCE\_EXT\_H#include <linux/types.h>#include <linux/list.h>  
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 Copyright 2012 Steffen Trumtrar <s.trumtrar@pengutronix.de> \* \* generic videomode description  
 Copyright (C) 2009 Provigent Ltd. \*/#include <linux/delay.h>#include <linux/err.h>#include <linux/errno.h>#include <linux/export.h>#include  
 <linux/gpio/consumer.h>#include <linux/i2c.h>#includ  
 Copyright (C) 2014 Sony Mobile Communications AB  
 Copyright (C) 2015 Martin Willi \* \* Based on public domain code by Andrew Moon and Daniel J. Bernstein.  
 Copyright(c) 2010 Larry Finger. All rights reserved  
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 Copyright (C) Frederic Rible F1OAT (fribl@teaser.fr)  
 Copyright (C) 2006 James Smart, Emulex Corporation \*/#include <linux/time.h>#include <linux/jiffies.h>#include <linux/security.h>#include  
 <linux/delay.h>#include <linux/slab.h>#include <linux  
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 Copyright (c) 2011 Stefan Schmidt <stefan@datenfreihafen.org>



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 Copyright (C) 2018 Masahiro Yamada <yamada.masahiro@socionext.com>  
 Copyright 1999-2002 Paul Mackerras.  
 Copyright (C) 2008 <srinivasa.deevi at conexant dot com> Based on em28xx driver Based on cx23885 driver Based on cx88 driver  
 Copyright (C) International Business Machines Corp., 2000-2002 \*/#ifndef \_H\_JFS\_DTREE#define \_H\_JFS\_DTREE  
 Copyright (c) 2006-2014 Broadcom Corporation  
 Copyright 2013 Analog Devices Inc. \* Author: Lars-Peter Clauen <lars@metafoo.de> \*/  
 Copyright (C) 2004 Motoyuki Ito (motoyuki@soft.fujitsu.com)  
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 Copyright (C) 2010 R.M. Thomas \* <rmthomas--a.t--sciolus.org> \* \* TODO: \*  
 Copyright (C) 1991-1998 Linus Torvalds \* Re-organised Feb 1998 Russell King \* \* We now have independent partition support from the \* block  
 drivers, which allows all the partition code to \* b  
 Copyright (C) Intel 2017 \*/  
 Copyright (C) 2007 Ben Skeggs.  
 Copyright (c) 2015, Applied Micro Circuits Corporation \* Author: Feng Kan <fkan@apm.com> \* Loc Ho <lho@apm.com> \*/  
 Copyright Matt Mackall <mpm@selenic.com>, 2003, 2004, 2005 \*  
 Copyright (C) 2008-2009 Eric Piel \*/#include <linux/platform\_device.h>#include <linux/input.h>#include <linux/regulator/consumer.h>#include  
 <linux/miscdevice.h>  
 Copyright 2011-2014, ARM Ltd. \* \* This module allows virtio devices to be used over a virtual, memory mapped \* platform device.  
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 Studebaker <mdsxyz123@yahoo.com> \* Ported to Linux 2.6 by Aurelien Jarno  
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 Copyright (C) 2012 Samsung Electronics \* MyungJoo Ham <myungjoo.ham@samsung.com> \* \* Modified for calling to IIO to get adc by  
 <anish.singh@samsung.com>  
 Copyright (C) 2013 WindRiver \* \* Author:  
 Copyright (c) 2012 Paolo Valente. \*/  
 Copyright (C) 2018 BayLibre, SAS \* Author: Maxime Jourdan <mjourdan@baylibre.com> \* \* VDEC\_1 is a video decoding block that allows decoding of \*  
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 Copyright (C) 2006-2014 Michael Krufky <mkrufky@linuxtv.org> \* based on nxt2002 by Taylor Jacob <rtjacob@earthlink.net> \* and nxt2004 by  
 Jean-Francois Thibert <jeanfrancois@sagetv.com>\*/  
 Copyright 2017 Google, Inc \* \* This driver uses the memory mapper cros-ec interface to communicate \* with the Chrome OS EC about accelerometer  
 data or older commands. \* Accelerometer access is pre  
 COPYRIGHT | IEC958\_AES0\_CON\_EMPHASIS; ucontrol->value.iec958.status[1] = IEC958\_AES1\_CON\_ORIGINAL |  
 IEC958\_AES1\_CON\_CATEGORY; ucontrol->value.iec958.status[3] = IEC958\_AES3\_CON  
 Copyright (C) 2008 Nokia Corporation \*Contact: Jarkko Nikula <jarkko.nikula@bitmer.com> \* Peter Ujfalusi <peter.ujfalusi@ti.com> \* \*  
 Freescale SSI ALSA SoC Digital Audio Interface (DAI) driver  
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 Copyright (c) 1999-2002 Vojtech Pavlik \*/#ifndef \_GAMEPORT\_H#define \_GAMEPORT\_H  
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Copyright (C) 2018 Intel Corporation \* Author: Rafael J. Wysocki <rafael.j.wysocki@intel.com> \* \* This driver is based on Section 9.18 of the ACPI 6.2 specification revision.  
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Copyright (c) 2007 Michel Ludwig (michel.ludwig@gmail.com)// - frontend interface  
Copyright (c) 2016-2018 Cavium Inc. \*/#include "drv\_scsi\_fw\_funcs.h"  
Copyright (C) 2017 Marvell \* \* Antoine Tenart <antoine.tenart@free-electrons.com>  
Copyright 2015-2017 Advanced Micro Devices, Inc.  
Copyright © 2001-2007 Red Hat, Inc. \* Created by David Woodhouse <dwmw2@infradead.org> \*/  
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Copyright (C) 2015 Google, Inc. \* \* Author: Mikulas Patocka <mpatocka@redhat.com>  
Copyright (C) 2016 Song Liu <songliubraving@fb.com> \*/#include <linux/kernel.h>#include <linux/wait.h>#include <linux/blkdev.h>#include <linux/slab.h>#include <linux/raid/md\_p.h>#include <linux  
Copyright (C) 2017 Cadence Design Systems - http://www.cadence.com \* \* Authors: Rafal Ozieblo <rafalo@cadence.com> \* Bartosz Folta <bfolta@cadence.com>  
Copyright (C) 1996 Paul H. Hargrove  
Copyright © 2017 Keith Packard <keithp@keithp.com> \*/#include <linux/file.h>#include <linux/uaccess.h>  
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Copyright (C) 2006 Oliver Endriss \* \* the project's page is at https://linuxtv.org  
Copyright (C) 2017 Intel Corporation \*/#include <linux/fpga/fpga-bridge.h>#include <linux/idr.h>#include <linux/kernel.h>#include <linux/module.h>#include <linux/of\_platform.h>#include <linux/s  
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Copyright (C) 2001 ARM Limited, by David A Rusling  
Copyright (C) 2010 Devin Heitmueller <dheitmueller@kernellabs.com> \*/#ifndef \_AS102\_USB\_DRV\_H\_#define \_AS102\_USB\_DRV\_H\_  
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Copyright (c) 2004 Evgeniy Polyakov <zbr@ioremap.net> \*/  
Copyright (C) 2010 Devin Heitmueller <dheitmueller@kernellabs.com> \*/#include <linux/kernel.h>#include <linux/errno.h>#include <linux/ctype.h>#include <linux/delay.h>#include <linux/firmware.h>  
Copyright 2006-11 One Laptop Per Child Association, Inc.  
Copyright (c) 2018 Amlogic, inc. \* Author: Qiufang Dai <qiufang.dai@amlogic.com> \*/#include <linux/clk-provider.h>#include <linux/platform\_device.h>#include <linux/reset-controller.h>#include <  
Copyright (C) 2018 Linus Walleij \* \* This module binds to the "arm,vexpress-muxfpga" device on the \* Versatile Express configuration bus and sets up which CLCD instance \* gets muxed out on the DVI  
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Copyright (C) 2011 Freescale Semiconductor, Inc. \*/  
Copyright (C) 2013 Red Hat, Inc. \* Mark Salter <msalter@redhat.com> \*/#include <linux/efi.h>#include <asm/efi.h>  
Copyright (c) 2008 Adrian-Ken Rueegsegger <ken@codelabs.ch> \*/#include <crypto/internal/hash.h>#include <linux/init.h>#include <linux/module.h>#include <linux/mm.h>#include <linux/types.h>#incl  
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Copyright (C) 2006-2007 Michael Wu <flamingice@sourmilk.net> \*/  
Copyright (C) 2004 Anil S Keshavamurthy <anil.s.keshavamurthy@intel.com> \* - Added processor hotplug support  
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Copyright 2015 Martin Peres  
Copyright (C) 2017 Google, Inc. \* Matthew Garrett <mjg59@google.com> \* Thiebaud Weksteen <tweek@google.com> \*/#include  
<linux/efi.h>#include <linux/tpm\_eventlog.h>#include <asm/efi.h>  
Copyright (c) 2012 Samsung Electronics Co., Ltd. \* http://www.samsung.com/ \* \* Portions of this code from linux/fs/ext3/hash.c  
Copyright (C) 2012 Philipp Zabel, Pengutronix \*/  
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Copyright (C) 2013 Nandita Dukkipati <nanditad@google.com> \*/  
Copyright (C) 2004 David Brownell  
(C) 2001-2002 Andras Kis-Szabo <kisza@sch.bme.hu> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include  
<linux/skbuff.h>#include <linux/ip.h>#include <linux/ipv6.h>#inc  
Copyright (C) 2008 Sujith Thomas <sujith.thomas@intel.com> \*/  
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Copyright (c) 2014 Jeff Layton <jlayton@primarydata.com> \*/#ifndef \_LOCKD\_PROCFS\_H#define \_LOCKD\_PROCFS\_H  
Copyright (c) 2015 Linaro Limited. \* \* Author: Leo Yan <leo.yan@linaro.org>  
Copyright (C) 2008 Sergey Lapin \* Based on ds1307 driver by James Chapman and David Brownell \*/  
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Copyright (C) 1991, 1992 Linus Torvalds \* \* ext4 fs regular file handling primitives  
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Copyright (C) 2005 Hans Verkuil <hverkuil@xs4all.nl>  
Copyright (c) 2003 Hans-Frieder Vogt <hfvoigt@arcor.de>, Based on SMBus 2.0 driver for AMD-8111 IO-Hub  
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```

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Copyright (C) 2013-2014 Stephane Grosjean <s.grosjean@peak-system.com> */#include <linux/netdevice.h>#include <linux/usb.h>#include
<linux/module.h>
Copyright (c) 2019 Facebook#include <linux/hash.h>#include <linux/bpf.h>#include <linux/filter.h>#include <linux/fttrace.h>#include
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Copyright (C) 1999-2002 Ralph Metzler * & Marcus Metzler for convergence integrated media GmbH * * 26feb2004 Support for FS Activy
Card (Grundig tuner) by * Michae
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Copyright (c) 2017 Christoph Hellwig. */
Copyright (c) 2013,2016 Qualcomm Atheros, Inc.
Copyright (C) 2002 by Ron Minnich <rminnich@lanl.gov> */#ifndef FS_9P_V9FS_VFS_H#define FS_9P_V9FS_VFS_H
Copyright (c) 2011-2012 Qualcomm Atheros, Inc.
Copyright © 2001-2007 Red Hat, Inc. All Rights Reserved
Copyright (C) 2002 Ralph Metzler <rjkm@metzlerbros.de> * * ULE Decapsulation code:
Copyright (C) 2002 Dominik Brodowski <devel@brodo.de> * * TBD: * 1. Support more than one IRQ resource entry per link device (index). *
2. Implement start/stop mechanism and use ACPI B
Copyright (C) 2003 Kartikey Mahendra Bhatt <kartik_me@hotmail.com>. */
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Copyright (C) 2005-2006 Intel Corporation * Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com> * * Common code for firmware upload used by
the USB and PCI version; * i1480_fw_upload() takes a d
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Copyright (c) 2013 Eric Leblond <eric@regit.org> * * Development of this code funded by Astaro AG (http://www.astaro.com/)
Copyright (C) 2013 Guangliang Zhao, <lucienchao@gmail.com> */
Copyright (C) 2018 Intel Corporation */#include <linux/types.h>#include <net/cfg80211.h>#include "ieee80211_i.h"#include "sta_info.h"#include
"driver-ops.h"

```

```

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Copyright (C) 2005 Thomas Hergenbahn <thomas.hergenbahn@suse.de>
Copyright (c) 2013 Michael Stapelberg <michael@stapelberg.de> */#include <linux/kernel.h>#include <linux/string.h>#include
<linux/ctype.h>#include <linux/errno.h>#include <linux/unistd.h>#inclu
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hereby granted, provided that the above * co
Copyright (C) 2010 Juan Jes */
Copyright (C) 2002, Linus Torvalds. * * Contains all the functions related to writing back and waiting * upon dirty inodes against superblocks, and writing
back dirty * pages against inodes. ie:
Copyright (C) 2012 ARM Ltd. */#ifndef __ASM_CACHEFLUSH_H#define __ASM_CACHEFLUSH_H
Copyright (C) 2001-2006,2008,2013 Greg Kroah-Hartman <greg@kroah.com>
Copyright (c) 2004 Torrey Hoffman <thoffman@arnor.net>
Copyright (C) 2008 - 2009 Novell Inc. */
copyright info and historical * changelogs. */#include <linux/capability.h>#include <linux/clocksource.h>#include <linux/workqueue.h>#include
<linux/hrtimer.h>#include <linux/jiffies.h>#includ
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(C) 2002 Alan Stern <stern@rowland.org> * * Initial work:
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Copyright: (C) Torsten Schenk */#ifndef USB6FIRE_CHIP_H#define USB6FIRE_CHIP_H
Copyright (C) 2004 Aurelien Alleaume <slts@free.fr> */
(C) 2005 Chuck Lever <cel@netapp.com> * * IPv6 support contributed by Gilles Quillard, Bull Open Source, 2005. * <gilles.quillard@bull.net>
Copyright 1999 Paul Mackerras. * * Also touched by the grubby hands of Paul Fulghum paulkf@microgate.com
Copyright (C) 2012 Samsung Electronics// Jonghwa Lee <jonghwa3.lee@samsung.com>
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Copyright (C) 2008 <srinivasa.deevi at conexant dot com>
Copyright (C) 2004 Amaury Demol for DiBcom * * see Documentation/media/dvb-drivers/dvb-usb.rst for more information
(C) 2009 Daniel Mack <daniel@caiaq.de> ). *
Copyright (C) 2008 Takashi Iwai */
Copyright (C) 2010 - Maxim Levitsky * driver for Ricoh memstick readers */
Copyright (C) 2015 Russell King */#include <linux/ethtool.h>#include <linux/export.h>#include <linux/gpio/consumer.h>#include
<linux/netdevice.h>#include <linux/of.h>#include <linux/of_mdio.h>
Copyright (C) 2008 Pierrick Hascoet <pierrick.hascoet@abilis.com> */#define MAX_FW_PKT_SIZE 64
Copyright (C) 2008, 2009 Provigent Ltd. * * Author: Baruch Siach <baruch@tkos.co.il>
Copyright (c) 2011 Anssi Hannula <anssi.hannula@iki.fi> */
Copyright (c) 2011 Jonathan Cameron */#ifndef _IIO_INKERN_CONSUMER_H#define _IIO_INKERN_CONSUMER_H
Copyright (C) 2005-2006 Michael Buesch <m@bues.ch>
Copyright (c) 2004 Kailang Yang <kailang@realtek.com.tw> * PeiSen Hou <pshou@realtek.com.tw> * Takashi Iwai
<tiwai@suse.de> * Jonathan Woith
Copyright asserted flag -- do not modify#define SPCS_NOTAUDIODATA 0x00000002 /* 0 = Digital audio, 1 = not audio */#define
SPCS_PROFESSIONAL 0x00000001 /* 0 = Consumer (IEC-958), 1 = pro (AES3-1992)
Copyright (C) 2006-2009 Patrick McHardy <kaber@trash.net> *
Copyright (c) 2004 Intel Corporation. All rights reserved
Copyright(c) 2006 - 2008 Jay Cliburn <jcliburn@gmail.com>
Copyright (C) 2001-2004 Jes Sorensen, Wild Open Source Inc.
Copyright (c) 2010 Rafa
Copyright (C) 1998, 1999, 2000, 2001 Free Software Foundation, Inc.
Copyright (c) 2008-2009 Jonathan Cameron <jic23@kernel.org> * * The i2c interface is very similar, so shouldn't be a problem once * I have a suitable
wire made up.
Copyright (C) 2008-2009 Nokia Corporation */
Copyright (c) 2006,2007 Daniel Mack*/
Copyright (C) 2000 Takashi Iwai <tiwai@suse.de> * * Routines for control of EMU WaveTable chip
Copyright (C) 2009-2012 Jason Baron <jbaron@redhat.com>
Copyright (C) 2016 * Guennadi Liakhovetski (guennadi.liakhovetski@intel.com) */
Copyright (C) Purism SPC 2019 */
Copyright (c) 2008 Marc Pignat <marc.pignat@hevs.ch> * * The adcxx4s communicates with a host processor via an SPI/Microwire Bus * interface. This

```

```

driver supports the whole family of devices with n
(C) 1999-2001 Michal Ludvig <michal@logix.cz> */
Copyright (C) 2015 Samsung Electronics Co., Ltd. * Author: Jacek Anaszewski <j.anaszewski@samsung.com> */
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Copyright (C) 2015 Martin Sperl */
Copyright (C) 2017-2018 Intel Corporation, Inc. * * Authors:
Copyright (C) IBM Corporation 2016 */
Copyright 2014 MundoReader S.L.
Copyright (C) 1998, 1999 Frodo Looijaard <frodol@dds.nl> and * Philip Edelbrock <phil@netroedge.com>
Copyright (c) 2015 NVIDIA Corporation. */
(C) COPYRIGHT 2016 ARM Limited. All rights reserved
Copyright (C) 2010 ARM Ltd., Will Deacon <will.deacon@arm.com> * * This code is based on the sparc64 perf event code, which is in turn based * on
the x86 code.
Copyright (C) 2003 Christoph Hellwig. */
Copyright (C) 2015-2016 Samsung Electronics * Igor Kotrasinski <i.kotrasinski@samsung.com> * Krzysztof Opasiak
<k.opasiak@samsung.com> */
Copyright (C) 2005 Mike Isely <isely@pobox.com> */#ifndef __PVRUSB2_DEBUG_H#define __PVRUSB2_DEBUG_H
Copyright (C) 2017 Marvell * * Hanna Hawa <hannah@marvell.com> * Thomas Petazzoni <thomas.petazzoni@free-electrons.com>
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Copyright (c) 2005 Intel Corporation. All rights reserved
Copyright (c) 2004, 2005 Infinicon Corporation. All rights reserved
Copyright (c) 2013 Broadcom Corporation */#if !defined(BRCMF_TRACEPOINT_H_) || defined	TRACE_HEADER_MULTI_READ#define
BRCMF_TRACEPOINT_H_
Copyright 2009 Johannes Berg <johannes@sipsolutions.net>
Copyright 2011-2012 Hauke Mehrtens <hauke@hauke-m.de>
Copyright 2009-2011 Christian Lamparter <chunkeey@googlemail.com> */
Copyright (C) 2017 Free Electrons * Maxime Ripard <maxime.ripard@free-electrons.com> */
copyright information *//* This file is automatically generated, do not edit things here. */#ifndef __FLEXCOP_IBI_VALUE_INCLUDED__#define
__FLEXCOP_IBI_VALUE_INCLUDED__
Copyright (c) 2016, Fuzhou Rockchip Electronics Co., Ltd. * Author: Lin Huang <hl@rock-chips.com> */
Copyright (C) 2011 Freescale Semiconductor, Inc.
Copyright (c) 2008 MtekVision Co., Ltd. * Kwangwoo Lee <kwlee@mtekvision.com> * * Using code from:
Copyright (C) 2009 Vivek Goyal <vgoyal@redhat.com> * Nauman Rafique <nauman@google.com> * * For policy-specific per-blkcg data:
(C) 2003 Ardis Technologies <roman@ardistech.com> * * Handle individual btree records
Copyright (c) 2012, NVIDIA CORPORATION. All rights reserved
Copyright(c) 1999 - 2015 Intel Corporation.\n");
Copyright (C) 2012 Google, Inc. * Will Drewry <wad@chromium.org> * * This defines a simple but solid secure-computing facility.
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Copyright (C) 2013-2014 Christoph Hellwig */#include <linux/kernel.h>#include <linux/module.h>#include <linux/backing-dev.h>#include
<linux/bio.h>#include <linux/blkdev.h>#include <linux/kmemle
Copyright (C) 2002-2007 Kristian H */
Copyright (C) 2019-2020 Arm Ltd.
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Copyright 2010 Matt Turner.
Copyright (C) 2016 Nicolai Stange <nicstange@gmail.com> */
Copyright (c) 2003-2006, 2013, Ericsson AB
Copyright (C) 2008 Steven Rostedt <srostedt@redhat.com> */#include <linux/trace_events.h>#include <linux/ring_buffer.h>#include
<linux/trace_clock.h>#include <linux/sched/clock.h>#include <linux
Copyright (c) 2019 Google Inc. */
Copyright © CC Computer Consultants GmbH, 2007 - 2008 */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/skbuff.h>#include <net/ipv6.h>#include <linux/netfilter

```

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Copyright (C) 2008 Devin Heitmueller <dheitmueller@linuxtv.org>  
Copyright (c) 2003 Winfried Ritsch (IEM) \* code based on hdsp.c Paul Davis \* Marcus Andersson \* Thomas  
Charbonnel \* Modified 200  
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copyright information. \* \* This file keeps functions for initializing and handling the \* BULK and ISOC USB data transfers in a generic way. \* Can be used  
for DVB-only and also, that's the plan, fo  
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Copyright (c) 2009 Rafael J. Wysocki <rjw@sisk.pl>, Novell Inc.  
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Copyright (c) 1998 - 2001 Frodo Looijaard <frodol@dds.nl>, \* Philip Edelbrock <phil@netroedge.com>, \* and  
Mark Studebaker <mdsxyz123@yahoo.com>  
Copyright (c) 2006 Willem Duinker \* v4l2 interface modeled after the V4L2 driver \* for SN9C10x PC Camera Controllers \* \* Some defines taken from the  
mt9m111 sensor driver  
Copyright (C) 2009-2010 Texas Instruments Incorporated. \* Nishanth Menon \* Romit Dasgupta \* Kevin Hilman \*/  
Copyright (C) 2018-19 Texas Instruments Incorporated - http://www.ti.com/  
Copyright (C) 2005 m5603x Linux Driver Project <m5602@x3ng.com.br> \* \* Portions of code to USB interface and ALi driver software,  
Copyright (C) 2002-2003 Christoph Hellwig \* \* mid to lowlevel SCSI driver interface \* Initial versions: Drew Eckhardt \* Subsequent revisions: Eric  
Youngdale  
Copyright (C) 2010 Dan Williams <dcbw@redhat.com> \* \* Devices listed here usually provide a CDC ACM port on which normal modem \* AT commands  
and PPP can be used. But when that port is in-use by PP  
Copyright (c) 2011 Jarod Wilson <jarod@redhat.com> \* based on the hid-topseed driver, which is in turn, based on hid-cherry... \*/  
Copyright (c) 2017-2018 Hans de Goede <hdegoede@redhat.com> \*/  
Copyright (c) 2014-2015, Intel Corporation. \*/#include <linux/highmem.h>#include <linux/debugfs.h>#include <linux/blkdev.h>#include  
<linux/module.h>#include <linux/device.h>#include <linux/mutex  
Copyright 2018 Cisco Systems, Inc. and/or its affiliates. All rights reserved  
Copyright (C) 2019 HUAWEI, Inc. \* http://www.huawei.com/ \* Created by Gao Xiang <gaoxiang25@huawei.com> \*/#ifndef  
\_\_EROFS\_FS\_COMPRESS\_H#define \_\_EROFS\_FS\_COMPRESS\_H  
Copyright 2012 Google, Inc. \* \* Allocation in bcache is done in terms of buckets: \*  
Copyright (C) 2017 Landen Chao <landen.chao@mediatek.com> \* Sean Wang <sean.wang@mediatek.com> \*/  
(C) 2005 Harald Welte <laforge@netfilter.org> \* \* This software is distributed under GNU GPL v2, 1991  
Copyright (C) 2008 Pierrick Hascoet <pierrick.hascoet@abilis.com>  
Copyright (C) 2014-2017 Pengutronix, \* Marc Kleine-Budde <kernel@pengutronix.de> \*/  
Copyright (c) 2012-2016 Synaptics Incorporated \*/#include <linux/input.h>#include <linux/input/mt.h>#include <linux/rmi.h>#include  
"rmi\_driver.h"#include "rmi\_2d\_sensor.h"  
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Copyright 2007 Red Hat, Inc.  
Copyright (C) 2006 Bob Copeland (me@bobcopeland.com) \* based on osf.c \*/  
Copyright (C) 2008 Antonio Ospite <ospite@studenti.unina.it>  
Copyright (c) 2018 Crane Merchandising Systems. All rights reserved  
Copyright (c) 2008-2013 Broadcom Corporation  
Copyright (C) 2004-2008 Ingo Molnar <mingo@redhat.com> \* \* Originally ported from the -rt patch by:  
Copyright (C) 2005 Michael Hanselmann (linux-kernel@hansmi.ch)  
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Copyright Pierre Ossman\n");  
Copyright (C) Nokia Corporation \* \* Author: Peter Ujfalusi <peter.ujfalusi@ti.com>  
Copyright 2015 Advanced Micro Devices, Inc.#include "dm\_services.h"#include "dc.h"#include "dc\_link\_dp.h"#include "dm\_helpers.h"#include  
"opp.h"#include "dsc.h"#include "resource.h"  
Copyright (C) 2004-2007 Hans Verkuil <hverkuil@xs4all.nl>  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_CACHE\_H#define \_\_ASM\_CACHE\_H  
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Copyright (c) 2009,2018 Daniel Mack <daniel@zonque.org>  
Copyright (c) 2013, Kenneth MacKay \* All rights reserved  
Copyright (c) 2013 Coraid, Inc. See COPYING for GPL terms.#include <linux/blk-mq.h>  
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Copyright (c) 2010, Intel Corporation. \*/  
Copyright (C) 2006 Atsushi Nemoto <anemo@mba.ocn.ne.jp> \* \* Based on Richard Purdie's ledtrig-timer.c and some arch's \* CONFIG\_HEARTBEAT  
code.

Copyright (C) 2004-5 Patrick Boettcher (patrick.boettcher@posteo.de) \* \* Thanks to Tynhan who kindly provided hardware and information.  
Copyright (C) 2018-2019 Corentin LABBE <clabbe@baylibre.com> \*/#include <crypto/aes.h>#include <crypto/engine.h>#include  
<crypto/skcipher.h>#include <linux/debugfs.h>#include <linux/crypto.h>#i  
Copyright (c) 2011 Jonathan Cameron \*/  
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Copyright © 2013 Tom St Denis <tstdenis@elliptictech.com> \* Based on crypto/xcbc.c:  
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header files.  
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Copyright (c) 2007 Mellanox Technologies. All rights reserved  
Copyright (c) 2016 Christoph Hellwig. \*/#include <linux/kobject.h>#include <linux/blkdev.h>#include <linux/blk-mq.h>#include <linux/blk-mq-  
pci.h>#include <linux/pci.h>#include <linux/module.h>  
Copyright 2007 Rusty Russell <rusty@rustcorp.com.au> IBM Corporation \*/#define DEBUG#include <linux/netdevice.h>#include  
<linux/etherdevice.h>#include <linux/ethtool.h>#include <linux/module.h  
Copyright (c) 2009,2018 Daniel Mack <daniel@zonque.org> \* \* See EETI's software guide for the protocol specification: \*  
<http://home.eeti.com.tw/documentation.html> \*  
Copyright 2007 Andrea Merello <andrea.merello@gmail.com> \* \* Code from the BSD driver and the rtl8181 project have been \* very useful to  
understand certain things  
Copyright (C) 2004-2005 Richard Purdie \*  
Copyright 2004-2006 Jean-Baptiste Note <jbnote@gmail.com>, et al. \*/  
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<tiwai@suse.de> \*/  
Copyright 2017 Valve Corporation  
Copyright (C) 2007 Andrew Sharp <andy.sharp@lsi.com> \* \* Real time clock driver for the Dallas 1511 chip, which also \* contains a watchdog timer.  
There is a tiny amount of code that \* platform co  
Copyright (C) 2010-2020 B.A.T.M.A.N. contributors: \* \* Sven Eckelmann  
Copyright (C) 2016 Western Digital  
Copyright (C) 2009-2010 Douglas Landgraf <douglslan@redhat.com> \*/  
Copyright (C) 2018 Bootlin \* \* Based on the vim2m driver, that is: \*  
Copyright (C) Tomi Manninen OH2BNS (oh2bns@sral.fi)  
Copyright (C) 2009 Rafael J. Wysocki <rjw@sisk.pl>, Novell Inc. \* \* This file contains power management functions related to interrupts.  
Copyright (c) 1997, 1998 Erik Andersen <andersee@debian.org>  
Copyright (C) 2004-2006 Red Hat, Inc., Ingo Molnar <mingo@redhat.com>  
Copyright (C) 2008 Atmel Corporation \*/#include <linux/moduleparam.h>#include <linux/export.h>#include <linux/debugfs.h>#include  
<linux/fs.h>#include <linux/seq\_file.h>#include <linux/slab.h>#  
Copyright (C) 2009-2012 Siemens AG \* \* Written by:  
Copyright 2014 Freescale Semiconductor, Inc. \*/  
Copyright (c) 2013 Anssi Hannula <anssi.hannula@iki.fi> \* \* Authors:  
Copyright (C) 2007-2008 BalaBit IT Ltd. \* Author: Krisztian Kovacs \*/  
copyright); ixgbev\_f\_wq = create\_singlethread\_workqueue(ixgbev\_f\_driver\_name); if (!ixgbev\_f\_wq) { pr\_err("%s: Failed to create  
workqueue\n", ixgbev\_f\_driver\_name); return -ENOMEM; }  
Copyright (C) 2012-2013, Guennadi Liakhovetski <g.liakhovetski@gmx.de> \* \* ATTENTION: This is a temporary API and it shall be replaced by the  
generic \* clock API, when the latter becomes widely ava  
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 Copyright (C) 2000 by Massimo Piccioni <dafastidio@libero.it>  
 Copyright (c) by Francisco Moraes <fmoraes@nc.rr.com> \* Driver EMU10K1X chips \* \* Parts of this code were adapted from audigy.c driver which is  
 Copyright © 1991, 1992 Linus Torvalds \* \* and parts of the affs filesystem additionally  
 Copyright (C) 2008 by Nokia Corporation \*/  
 Copyright (C) 2011-2012 Van Jacobson <van@pollere.net> \* \* Implemented on linux by :  
 Copyright 2008 Juergen Beisert, kernel@pengutronix.de  
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 Copyright 1997--1999 Martin Mares <mj@ucw.cz> \* \* PCI Express ASPM defines and function prototypes  
 Copyright (C) 2001-2008, Lars Ellenberg <lars.ellenberg@linbit.com>.  
 Copyright (C) 2001 Richard Zidlicky <rz@linux-m68k.org> \* \* Also based on offb.c, which was:  
 Copyright (C) 2007 Eurotech S.p.A. <info@eurotech.it> \*/  
 Copyright (C) 2016, 2018 \* Author: Matt Ranostay <matt.ranostay@konsulko.com> \* \* TODO: bias voltage + polarity control, and multiple chip support  
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 (C) 2011 Omnibond Systems \* \* Changes by Acxiom Corporation to implement generic service\_operation()  
 Copyright (C) 2001 Rusty Russell.  
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 Copyright (c) 2002, 2003 The Regents of the University of Michigan. \* All rights reserved  
 Copyright (C) 2005 Kirk Lapray <kirk\_lapray@bigfoot.com> \* \* Based on code from Jack Kelliher (kelliher@xmission.com)  
 Copyright (C) 2010 - 2016 Cavium, Inc. \* \* This file contains the shared part of the driver for the i2c adapter in \* Cavium Networks' OCTEON processors  
 and ThunderX SOCs.  
 Copyright (C) 2014-2015 Chen-Yu Tsai <wens@csie.org> \* \* Based on code from Allwinner BSP, which is: \*  
 Copyright (c) 2003-2012 QLogic Corporation  
 Copyright(c) 2018 Intel Corporation. \* \* Author(s): Björn Töpel <bjorn.topel@intel.com> \* Magnus Karlsson <magnus.karlsson@intel.com>  
 Copyright 2005 Alessandro Zummo \* \* please send all reports to:  
 Copyright (C) 2008 Igor M. Liplianin <liplianin@me.by> Removed stb6000 specific tuner code and revised some procedures.  
 (C) 2001 by Jay Schulist <jschlst@samba.org>,  
 (C) Copyright 2002-2004 by David Brownell  
 Copyright (C) 2013-2015 Red Hat, Inc. \* Author: Asias He <asias@redhat.com> \* Stefan Hajnoczi <stefanha@redhat.com> \*/#include  
 <linux/miscdevice.h>#include <linux/atomic.h>#include <linu  
 Copyright (c) Roman Volkov <v1ron@mail.ru> \*/  
 Copyright (C) 2015 Intel Deutschland GmbH \*/  
 Copyright (C) 2006-2008 Nokia Corporation. \* \* Authors: Artem Bityutskiy (Битюцкий Артём) \* Adrian Hunter  
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 Copyright 2014 Jon Smirl <jonsmirl@gmail.com>  
 Copyright (C) 2006 Kaiwan N Billimoria <kaiwan@designergraphix.com> \* \* The LM70 communicates with a host processor via an SPI/Microwire Bus \*  
 interface. The complete datasheet is available at Nati  
 Copyright (c) 2002 James Morris <jmorris@intercode.com.au> \* 2002 Adam J. Richter <adam@yggdrasil.com> \* 2004 Jean-Luc  
 Cooke <jlcooke@certaintkey.com> \*/  
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 Copyright (C) 2010 Sheng-Yuan Huang (Nuvoton) (PS00) \* \* Shamelessly ripped from the w83627hf driver  
 Copyright (C) 2008 <srinivasa.deevi at conexant dot com>

```

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Copyright (c) 2013 Chanho Min <chanho.min@lge.com> */#include <linux/init.h>#include <linux/module.h>#include <linux/crypto.h>#include
<linux/vmalloc.h>#include <linux/lz4.h>#include <crypto/in
Copyright (c) 2009 Jochen Friedrich <jochen@scram.de> */
Copyright (c) by James Courtier-Dutton <James@superbug.co.uk> * Added EMU 1010 support. * * BUGS: * --
Copyright 2012-2013 STMicroelectronics Inc. * * Denis Ciocca <denis.ciocca@st.com> * v. 1.0.0
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Copyright (C) 2014 Robert Butora (robert.butora.fi@gmail.com) */
copyright * * WPA code stolen from the ipw2200 driver.
Copyright (C) 2011, 2017 Maciej S. Szmigiero (mail@maciej.szmigiero.name) * * see Documentation/media/dvb-drivers/dvb-usb.rst for more information
Copyright (c) 2006-2007 Greg Kroah-Hartman <greg@kroah.com>
Copyright 1998-2000 Jonathan Teh Soon Yew) * if(temp<169) * return double(temp)*0.427-32.08; * else if(temp>=169 && temp<=202) *
return double(temp)*0.582-58.16; * else * return double(temp)*
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Copyright(C) 2006, Hans J. Koch <hjk@hansjkoeh.de>
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Copyright (C) 2013 Linaro. * * This code is based on glibc cortex strings work originally authored by Linaro * be found @
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Copyright (c) 2016 Trond Myklebust * * I/O and data path helper functionality.
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Copyright 2019 Annaliese McDermond * * Author: Annaliese McDermond <nh6z@nh6z.net>
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Copyright (C) 2001 Jens Axboe <axboe@suse.de> */#ifndef __LINUX_BIO_H#define __LINUX_BIO_H
(C) 1998-2004 Ben Fennema
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Copyright (C) 2000-2002 Joakim Axelsson <gozem@linux.nu> * Patrick Schaaf <bof@bof.de> * Martin Josefsson
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Author: Hanjun Guo <hanjun.guo@linaro.org> */
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```

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*/
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Copyright (C) 2010      Tejun Heo <tj@kernel.org> * * This is the generic async execution mechanism. Work items as are * executed in process
context. The worker pool is shared and * automatically ma
Copyright (C) 1996 Olaf Kirch <okir@monad.swb.de> */
Copyright (c) 2016 Facebook */#include <linux/bpf.h>#include <linux/jhash.h>#include <linux/filter.h>#include <linux/stacktrace.h>#include
<linux/perf_event.h>#include <linux/elf.h>#include <l
Copyright 2016-2017 Advanced Micro Devices, Inc.
Copyright (C) Joerg Reuter DL1BKE (jreuter@yaina.de) */#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include
<linux/in.h>#include <linux/kernel.h>#include <linux/tim
Copyright (C) 2018 Cadence Design Systems, Inc. * * Authors:
Copyright (c) 2019 HiSilicon Limited.#include <crypto/akcipher.h>#include <crypto/dh.h>#include <crypto/internal/akcipher.h>#include
<crypto/internal/kpp.h>#include <crypto/internal/rsa.h>#inclu
Copyright (C) 2003-2009 Jean Delvare <jdelvare@suse.de> * * Heavily inspired from the lm78, lm75 and adm1021 drivers. The LM83 is * a sensor chip
made by National Semiconductor. It reports up to f
Copyright 2018 Christoph Hellwig. * * See ../COPYING for licensing terms.
Copyright (C) 2018-2019 Linaro Ltd. */
Copyright (C) 2002-2005 Roman Zippel <zippel@linux-m68k.org>
Copyright (c) 2005-2007 Takashi Iwai <tiwai@suse.de> */
Copyright (C) 2015 Joachim Eastwood <manabian@gmail.com> */
Copyright (C) 2003 2004 Michel Xhaard mxhaard@magic.fr
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```

Copyright (C) 2010 Zilogic Systems <code@zilologic.com>  
(C) Copyright 2014, 2015 Linaro Ltd. \* Author: Ashwin Chaugule <ashwin.chaugule@linaro.org> \*/  
Copyright (C) 1998-2003 Michael Hunold <michael@mihu.de>  
Copyright message. \* 0.0.17 \* Implement Mic and Line in Capture. \* 0.0.18 \* Add support for mute control on SB Live 24bit (cards w/ SPI DAC) \*

\* This code was initially based on code fr  
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Copyright 2008, Johannes Berg <johannes@sipsolutions.net> \*\* Based on:  
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Copyright (C) 1994 David Carter \*\* with work by William Rucklidge (wjr@cs.cornell.edu) \* Geert Uytterhoeven \*

Jes Sorensen (jds@kom.auc.dk) \* Martin Apel  
Copyright IBM Corporation, 2007  
(C) 1999-2000 Yon Uriarte <yon@astaro.de> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include  
<linux/skbuff.h>#include <linux/in.h>#include <linux/ip.h>  
Copyright (C) 2012 Renesas Electronics Corp. \* Author: Guennadi Liakhovetski <g.liakhovetski@gmx.de> \*/#include <linux/acpi.h>#include  
<linux/kernel.h>#include <linux/mm.h>#include <linux/module  
Copyright (C) 2004-2008 by Eric Van Hensbergen <ericvh@gmail.com>  
Copyright (C) 2013 Huawei Ltd. \* Author: Jiang Liu <liuj97@gmail.com> \*  
Copyright (C) 2013-2017 Wolfram Sang <wsa@the-dreams.de> \*/  
Copyright 2005 Stanislav Marek \* email: pisa@cmp.felk.cvut.cz \*\* Calculates proper bit-timing parameters for a specified bit-rate \* and sample-  
point, which can then be used to set the bit-ti  
Copyright 2006-2009 Solarflare Communications Inc.);  
Copyright (C) 2003 BULL SA.  
Copyright 2007 Sebastian Siewior \*\* Contacts:  
Copyright (c) 2015, Primary Data, Inc. All rights reserved  
Copyright (C) 1996 Larry Ewing (lewing@isc.tamu.edu)  
(C) 2003 Andreas Gruenbacher <agruen@suse.de> \*/#ifndef \_\_LINUX\_NFSACL\_H#define \_\_LINUX\_NFSACL\_H  
Copyright 2014-2015, Qualcomm Atheros, Inc. \*  
Copyright (C) 2007-2009 STMicroelectronics Ltd  
Copyright (C) 1995-99 Simon G. Vogl \* With some changes from Kyösti Mälkki <kmalkki@cc.hut.fi> \* Mux support by Rodolfo Giometti  
<giometti@enneenne.com> and \* Michael Lawnick <michael.lawnick  
Copyright 2005-2006,2013,2017-2018 Ian Kent <raven@themaw.net>  
Copyright 2017-2018 NXP \* Dong Aisheng <aisheng.dong@nxp.com> \*\* Implementation of the SCU based Power Domains  
(C) 2004 Paul Serice - The new inode scheme requires switching \* from iget() to iget5\_locked() which means \* the  
NFS export operations have to be h

Copyright (C) 1993, 1994, 1995, 1996, 1997 Free Software Foundation, Inc.  
Copyright 2011, Integrated Device Technology, Inc. \*/  
COPYRIGHT: return "SET\_COPYRIGHT"; case CX2341X\_ENC\_SET\_EVENT\_NOTIFICATION:  
Copyright (C) 2010-2011 Hans de Goede <hdegoede@redhat.com> \*\* This module is adapted from the in kernel v4l1 cpia driver which is : \*  
Copyright (C) 2018 Western Digital Corporation  
Copyright (c) 2003 James Morris <jmorris@intercode.com.au> \*\* FIXME: deflate transforms will require up to a total of about 436k of kernel \* memory  
on i386 (390k for compression, the rest for deco  
Copyright (C) 2004 David Brownell \*/  
Copyright (c) 2016, Intel Corporation. \*\* IIO driver for ADS1015 ADC 7-bit I2C slave address:  
Copyright (c) 2014-2016 Christoph Hellwig. \*/#include <linux/exportfs.h>#include <linux/iomap.h>#include <linux/genhd.h>#include  
<linux/slab.h>#include <linux/pr.h>  
Copyright 2013-2015 Freescale Semiconductor Inc. \*/#ifndef \_\_FSL\_DPKG\_H#define \_\_FSL\_DPKG\_H\_  
Copyright 2007 Jan D. Louw <jd.louw@mweb.co.za> \*/  
Copyright (C) 2015 Jay Kuri \*/  
Copyright 2012 Intel Corporation \* Author: Josh Triplett <josh@joshtriplett.org> \*\* Based on the bgrt driver:  
Copyright (C) 2010 Willow Garage <http://www.willowgarage.com>  
Copyright (c) 2017 - 2019, Intel Corporation. \*\* Note: This code is based on mptcp\_ctrl.c from multipath-tcp.org, \* authored by: \*  
(C) Copyright 2007-2011 Aaron Maoye <leafy.myeh@reuiimllatech.com>  
Copyright (C) 2018 Lorenzo Bianconi <lorenzo.bianconi83@gmail.com> \*/#include <linux/kernel.h>#include <linux/firmware.h>  
(C) Copyright 2013-2014 David Lanzend  
Copyright (C) 2017 Texas Instruments \* Author: Kishon Vijay Abraham I <kishon@ti.com> \*/



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 Copyright (c) 2016, Intel Corporation \* Authors: Weigang Li <weigang.li@intel.com> \* Giovanni Cabiddu <giovanni.cabiddu@intel.com> \*/#ifndef  
 \_CRYPTO\_ACOMP\_H#define \_CRYPTO\_ACOMP\_H#inclu  
 Copyright (C) 2012 Stephen Warren \*/  
 Copyright 2009-2017 Analog Devices Inc. \* \* http://www.analog.com/ADF7242  
 Copyright (C) Matt Helsley, IBM Corp. 2005 \* Based on cn\_fork.h by Nguyen Anh Quynh and Guillaume Thouvenin  
 Copyright (C) 2010, 2011 Roland Stigge <stigge@antcom.de> \* \* based on ds1621.c by Christian W. Zuckschwerdt <zany@triq.net>  
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 Copyright (C) 2005-2008 Ville Syrjala <syrjala@sci.fi>  
 Copyright (C) 1998-99 Frodo Looijaard <frodol@dds.nl>  
 Copyright (C) 2009-2010 Sven Anders <anders@anduras.de>, ANDURAS AG. \* Watchdog driver part \* (Based partially on fschmd  
 driver,  
 Copyright (C) 1995-06 ICP vortex, Achim Leubner \* See gdth.c for further informations and \* below for supported controller types \* \*  
 <achim\_leubner@adapttec.com>  
 Copyright (C) 2004 Red Hat, Inc., James Morris <jmorris@redhat.com>  
 Copyright (c) 2010-2017 Nikolai Kondrashov  
 Copyright (C) 2007 Atmel Corporation \*/#include <linux/completion.h>#include <linux/debugfs.h>#include <linux/delay.h>#include  
 <linux/gpio/consumer.h>#include <linux/i2c-algo-bit.h>#include <li  
 (C) 2006,2008 Red Hat, Inc., James Morris <jmorris@redhat.com> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include  
 <linux/module.h>#include <linux/skbuff.h>#include <linux/netfilter/x\_tables.h>  
 Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_SYSTEM\_MISC\_H#define \_\_ASM\_SYSTEM\_MISC\_H  
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 Copyright (C) 2006-2009 Hewlett-Packard Development Company, L.P. \* Alex Chiang <achiang@hp.com> \*/  
 Copyright (C) 2007 David Gibson, IBM Corporation. \* \* GPIO address extension:  
 Copyright (c) 2004 Dmitry Torokhov \*/  
 Copyright (C) 2015-2018 Sebastian Moeller <moeller0@gmx.de>  
 Copyright (C) 2005, Thibaut Varene <varenet@parisc-linux.org> \* Based on the OSS AD1889 driver by Randolph Chung <tausq@debian.org> \* \*  
 TODO:  
 Copyright 2006 Stefan Roese <sr at denx.de>, DENX Software Engineering \*  
 Copyright (C) 2004-2006 Marcel Holtmann <marcel@holtmann.org> \*/  
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 Copyright (c) 2005 Andrea Merello <andrea.merello@gmail.com> \* \* Mostly extracted from the rtl8180-sa2400 driver for the \* in-kernel generic  
 ieee802.11 stack.  
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 Copyright © 1997-1999 Janos Farkas <chexum@shadow.banki.hu> \* \* Using parts of the minix filesystem  
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Copyright 2010-2011 Picochip Ltd., Jamie Iles \* http://www.picochip.com \* \* This file implements a driver for the Synopsys DesignWare watchdog device \* in the many subsystems. The watchdog has 16  
Copyright (c) 2008, 2009 open80211s Ltd. \* Author: Luis Carlos Cobo <luisca@cozybit.com> \*/  
(C) 1998-1999 Dave Boynton  
Copyright (C) 2013, 2018 Wolfram Sang <wsa@the-dreams.de> \*/  
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Copyright (c) 2018 by Benjamin Valentin <benpicco@googlemail.com>  
Copyright (C) 2010, Guennadi Liakhovetski <g.liakhovetski@gmx.de> \*/#include <linux/clk.h>#include <linux/delay.h>#include <linux/err.h>#include <linux/hdmi.h>#include <linux/irq.h>#include <li  
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Copyright (c) 2017 BayLibre, SAS. \* Author: Neil Armstrong <narmstrong@baylibre.com> \*/  
Copyright (C) 2006-2006 Harald Welte <laforge@netfilter.org>  
Copyright 2000 Red Hat corp --- All Rights Reserved  
Copyright 2016 Realtek Semiconductor Corp. \* Author: Jack Yu <jack.yu@realtek.com> \*/#include <linux/module.h>#include <linux/moduleparam.h>#include <linux/init.h>#include <linux/delay.h>#inclu  
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Copyright (c) 2012 Samsung Electronics Co., Ltd. \* http://www.samsung.com \*  
Copyright (c) 2016 Facebook \*/#include <linux/cpumask.h>#include <linux/spinlock.h>#include <linux/percpu.h>  
(C) Copyright Yggdrasil Computing, Inc. 2000 \* (usb\_device\_id matching changes by Adam J. Richter)  
Copyright (c) 2004-2012 Patrick McHardy <kaber@trash.net> \* \* SCTP is defined in RFC 2960. References to various sections in this code \* are to this RFC.  
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Copyright (C) 2001, 2002 Paul Diefenbaugh <paul.s.diefenbaugh@intel.com> \* \* This driver fully implements the ACPI thermal policy as described in the \* ACPI 2.0 Specification.  
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Copyright (C) 1992 Rick Sladkey \* \* OS-specific nfs filesystem definitions and declarations  
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Copyright 2007, Red Hat Inc. Peter Zijlstra \* \* see http://programming.kicks-ass.net/kernel-patches/vma\_lookup/btree.patch  
Copyright 2003 mjander \* mjander@users.sourceforge.org  
\*\*\*\*\*/ifnndef \_AU88X0\_WT\_H#define \_AU88X0\_WT\_H  
Copyright (c) 2016-present, Przemyslaw Skibinski, Yann Collet, Facebook, Inc.  
Copyright (C) 2017 Linaro, Ltd., Rob Herring <robh@kernel.org> \* \* Based on hci\_ldisc.c: \*  
(C) Copyright 2009 Intel Corporation \* Author: Arjan van de Ven <arjan@linux.intel.com> \*/#ifnndef \_\_ASYNC\_H\_\_#define \_\_ASYNC\_H\_\_  
Copyright (C) 2015 Marcel Holtmann <marcel@holtmann.org>  
Copyright (c) 2017, Linaro Ltd. \*/#include <linux/firmware.h>#include <linux/module.h>#include <linux/notifier.h>#include <linux/slab.h>#include <linux/interrupt.h>#include <linux/io.h>#includ  
Copyright 2010,2011 Intel Corp. \* Author: Huang Ying <ying.huang@intel.com> \*/#include <linux/kernel.h>#include <linux/export.h>#include <linux/llist.h>  
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Copyright (c) 2009 Intel Corporation \*/  
(C) Copyright 2004 Linus Torvalds \*/#include <linux/pci.h>#include <linux/io.h>  
Copyright (c) 1999 Andreas Gal  
Copyright (c) 2018 Covalent IO, Inc. http://covalent.io \*/#include <uapi/linux/btf.h>#include <linux/kernel.h>#include <linux/types.h>#include <linux/slab.h>#include <linux/bpf.h>#include <linu  
Copyright (C) 2004 Pavel Pisa  
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Copyright IBM Corporation, 2014 \* \* Author: Paul E. McKenney <paulmck@linux.ibm.com>  
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Copyright (C) 2009 Semihalf.

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 Copyright (C) 2018 Paul Kocialkowski <paul.kocialkowski@bootlin.com>  
 Copyright (C) International Business Machines Corp., 2007,2008 \* Author(s): Steve French (sfrench@us.ibm.com) \* \* Contains the routines for  
 mapping CIFS/NTFS ACLs  
 Copyright (c) 2014 Johan Hovold <johan@kernel.org> \* \* Support : Micrel Phys:  
 Copyright (C) 2008 Nokia Corporation \*/#ifndef \_\_WL1251\_IO\_H\_\_#define \_\_WL1251\_IO\_H\_\_  
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 Copyright (c) 2004 Li Shaohua <shaohua.li@intel.com>  
 Copyright (C) 2018 Song Qiang <songqiang1304521@gmail.com> \* \* i2c slave address: 0x20 + SA1 << 1 + SA0.  
 Copyright (c) 2002, 2003, 2004, 2005, 2006, 2007, 2008 \* Phillip Lougher <phillip@squashfs.org.uk> \* \* id.c  
 Copyright (C) 2016 Intel Corporation \* Author: Lv Zheng <lv.zheng@intel.com> \*/  
 Copyright (C) 2008 Darius Augulis <darius.augulis@teltonika.lt> \*  
 Copyright (c) 2013 Eric Leblond <eric@regit.org> \* \* Development of this code partly funded by OISF \* (<http://www.openinfosecfoundation.org/>)  
 Copyright (C) 2000 Gerhard Tonn (ton@de.ibm.com)  
 Copyright (c) 2009-2016 Chelsio Communications, Inc. All rights reserved  
 Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_DEBUG\_MONITORS\_H#define \_\_ASM\_DEBUG\_MONITORS\_H  
 Copyright (C) 2018 - 2019 Intel Corporation \*/  
 Copyright (C) 2018 Amarula Solutions \* Author: Jagan Teki <jagan@amarulasolutions.com> \*/  
 (C) Copyright Deti Fliegl 1999 (new USB architecture)  
 Copyright (C) 2015 Intel Coropration  
 Copyright (c) 2018 Rockchip Electronics Co. Ltd. \*/  
 (C) 2003 - 2010 Dominik Brodowski \* \* This file contains definitions \_only\_ needed by the PCMCIA core modules. \* It must not be included by PCMCIA  
 socket drivers or by PCMCIA device \* drivers.  
 (C) 2002-2006 Netfilter Core Team <coreteam@netfilter.org> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include  
 <linux/netfilter/x\_tables.h>#include <net/netfilter/nf\_na  
 Copyright (C) 2012 Samsung Electronics Co., Ltd.\* Author: Jingoo Han <jg1.han@samsung.com>\*/  
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 Copyright (C) Shaohua Li (shaohua.li@intel.com) \*/  
 Copyright (c) 2009-2010 Stephane Chatty <chatty@enac.fr>  
 Copyright (c) 2010-2011 Jeremy Kerr <jeremy.kerr@canonical.com>  
 Copyright (C) 2016 Fujitsu Technology Solutions GmbH, \* Thilo Cestonaro <thilo.cestonaro@ts.fujitsu.com> \*/#include <linux/err.h>#include  
 <linux/fs.h>#include <linux/hwmon.h>#include <linux/h  
 Copyright (c) 2006 Jiri Kosina \*/  
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 Copyright 2017 \* \* Header file containing the public API for the System Controller (SC) \* Miscellaneous (MISC) function.  
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 (C) Copyright Al Viro 2000, 2001 \*  
 Copyright 2013 Cisco Systems, Inc. and/or its affiliates. All rights reserved  
 Copyright (C) 2009-2010 Jozsef Kadlec (kadlec@netfilter.org) \* \* I've modified Bob's hash to be useful in the Linux kernel, and \* any bugs present are  
 my fault. \* Jozsef  
 Copyright (C) 2012, 2013 Guenter Roeck <linux@roeck-us.net> \* \* Supports the following chips: \*  
 (C) 2002 by Brian J. Murrell <netfilter@interlinx.bc.ca> \* based on a copy of HW's ip\_nat\_irc.c as well as other modules  
 Copyright 2012 The Nouveau community  
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 Copyright (c) 2009 Intel Corp. \* Author: Huang Ying <ying.huang@intel.com> \*/  
 Copyright (c) 2016, EPAM Systems \*/  
 (C) 1999 Andrea Arcangeli <andrea@suse.de> (dynamic inode allocation) \*/#include <linux/export.h>#include <linux/fs.h>#include  
 <linux/mm.h>#include <linux/backing-dev.h>#include <linux/hash.h>#  
 Copyright (C) 2011 Samsung Electronics \* MyungJoo Ham <myungjoo.ham@samsung.com> \*/  
 Copyright (C) 2008 Google, Inc. \* Author: Mike Lockwood <lockwood@android.com> \* \* Modified by MyungJoo Ham <myungjoo.ham@samsung.com>  
 to support extcon \* (originally switch class is supported)  
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(C) 2000 by Matthew G. Marsh <mgm@paktronix.com> \* \* See RFC2474 for a description of the DSCP field within the IP Header.  
Copyright (C) 2007-2008 Peter Stokes <linux@dadeos.co.uk> \*/  
Copyright (c) 2017 Microsemi Corporation \*/  
Copyright (C) 2016 - Marcin Malagowski <mrc@bourne.st> \*/#include <linux/delay.h>#include <linux/device.h>#include <linux/err.h>#include  
<linux/i2c.h>#include <linux/io.h>#include <linux/kernel  
Copyright (C) 1999, Trond Myklebust <trond.myklebust@fys.uio.no> \*/  
Copyright (C) 2016 Shuah Khan <shuahkh@osg.samsung.com>  
Copyright (C) 2009 Adams.Xu <adams.xu@azwave.com.cn> \* \* see Documentation/media/dvb-drivers/dvb-usb.rst for more information  
Copyright (C) 2012, Michel Lespinasse <walken@google.com> \*/  
(C) Copyright 1994 - 1997 Marco van Wieringen \*/  
Copyright (C) 1996 Olaf Kirch, <okir@monad.swb.de> \* \* TCP NFS related read + write fixes  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_FP\_H#define \_\_ASM\_FP\_H  
Copyright (c) by Jaroslav Kysela <perex@perex.cz> \* Creative Labs, Inc. \* Routines for effect processor FX8010 \*  
Copyright (C) 2015, Google, Inc.  
Copyright (C) 2018 HUAWEI, Inc. \* <http://www.huawei.com/> \* Created by Gao Xiang <gaoxiang25@huawei.com> \*/#ifndef  
\_\_EROFS\_FS\_ZDATA\_H#define \_\_EROFS\_FS\_ZDATA\_H  
Copyright 2013 Ilia Mirkin  
Copyright (C) 2016 secunet Security Networks AG \* Author: Steffen Klassert <steffen.klassert@secunet.com> \* \* ESP GRO support  
Copyright 2000,2001,2002 Red Hat. \* \* Written by Benjamin LaHaise <bcl@kvack.org>  
(C) Copyright 2017 Sootech SA \*/  
Copyright (C) 2005-2006 Intel Corporation \* Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com> \* \* Initialize the Radio Control interface Driver.  
Copyright (c) 2003 Zinx Verituse <zinx@epicsol.org>  
Copyright (C) 2012 Parallels Inc. and Google Inc. \* Authors: Glauber Costa and Suleiman Souhlal \* \* Native page reclaim \* Charge lifetime sanitation \*  
Lockless page tracking & accounting \* Unifi  
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Copyright (C) 2009 Nokia Corporation \* \* Contact: Luciano Coelho <luciano.coelho@nokia.com>  
Copyright (c) 2015, Applied Micro Circuits Corporation \* Author: Feng Kan fkan@apm.com \*/#include <linux/acpi.h>#include <linux/delay.h>#include  
<linux/interrupt.h>#include <linux/io.h>#include  
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Copyright (C) 2006-2008 Nokia Corporation. \* \* Authors: Adrian Hunter \* Artem Bityutskiy (Битюцкий Артём)  
Copyright (C) 2017 Sean Wang <sean.wang@mediatek.com> \*/#include <linux/etherdevice.h>#include <linux/if\_bridge.h>#include  
<linux/iopoll.h>#include <linux/mdio.h>#include <linux/mfd/syscon.h>#i  
Copyright (c) 2013 Trond Myklebust <Trond.Myklebust@netapp.com> \*/#undef TRACE\_SYSTEM#define TRACE\_SYSTEM nfs4  
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 multi session handling. \* 1995 Mark Dob  
 Copyright © 2006, Intel Corporation. \*/#ifndef \_ASYNC\_TX\_H\_#define \_ASYNC\_TX\_H\_#include <linux/dmaengine.h>#include  
 <linux/spinlock.h>#include <linux/interrupt.h>  
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 Copyright(C) 2006-2007, Timesys Corp., Thomas Gleixner \*/#include <linux/cpu.h>#include <linux/err.h>#include <linux/hrtimer.h>#include  
 <linux/interrupt.h>#include <linux/percpu.h>#include <lin  
 (C) 1999-2001 Marc Boucher <marc@mbsi.ca>  
 Copyright (c) 2018-2019, Vladimir Oltean <olteanv@gmail.com> \*/#ifndef \_LINUX\_PACKING\_H#define \_LINUX\_PACKING\_H  
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 <nbd@nbd.name> \* Lorenzo Bianconi <lorenzo@ke  
 Copyright (C) 2008 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/slab.h>#include <linux/random.h>#include  
 <linux/export.h>  
 Copyright (C) 2006 Tomasz Michal Lukaszewski (FIXME: add e-mail)  
 Copyright (C) 2018 IVI Foundation, Inc. \*/  
 Copyright (C) 2010 IBM Corporation  
 Copyright (c) 2014 Broadcom Corporation \*/  
 Copyright (c) 2015, Linaro Limited \*/#include <linux/device.h>#include <linux/dma-buf.h>#include <linux/genalloc.h>#include <linux/slab.h>#include  
 <linux/tee\_drv.h>#include "tee\_private.h"  
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 https://linuxtv.org  
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 Copyright 2007 Stephane Marchesin  
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Copyright (C) 2010 Dell Inc. \* by Narendra K <Narendra\_K@dell.com>, \* Jordan Hargrave <Jordan\_Hargrave@dell.com> \*\* PCI Firmware Specification  
Revision 3.1 section 4.6.7 (DSM for Naming a \* PCI  
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<mchehab@kernel.org> (version 2) \* Alan Cox, <alan@lxorguk.ukuu.org.uk> (v  
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Copyright (C) 2013, Intel Corporation \* Author: Andy Shevchenko <andriy.shevchenko@linux.intel.com> \*/  
Copyright (C) 2017 Linaro Ltd. \*/#include <linux/types.h>#include <media/v4l2-ctrls.h>  
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Copyright (C) 2011 James Nuss <jamesnuss@nanometrics.ca> \*/  
Copyright (C) 2013-2015 Corentin LABBE <clabbe.montjoie@gmail.com> \*\* This file add support for AES cipher with 128,192,256 bits \* keysize in CBC  
and ECB mode. \* Add support also for DES and 3DES  
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Copyright (C) 2009-2016 Cavium, Inc. \*/  
Copyright (C) 2015 Intel Corporation. \*\* Simple file API to load FW that includes mixers, coefficients, DAPM graphs, \* algorithms, equalisers, DAIs,  
widgets, FE caps, BE caps, codec link caps etc.  
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and the health of each NFS mount point. \* Gene  
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 Copyright (c) 2012-2015,2017 Qualcomm Atheros, Inc.  
 Copyright (C) 1994 by Florian La Roche \* - Do not copy data too often around in the kernel. \* - In nfs\_file\_read the return value of kmalloc wasn't checked. \* - Put in a better version of rea  
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 <linux/types.h>#include <linux/slab.h>#include <linux/std  
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 Copyright (C) 2015 Linaro Ltd. \*/#include <linux/platform\_device.h>#include <linux/init.h>#include <linux/cpumask.h>#include  
 <linux/export.h>#include <linux/dma-direct.h>#include <linux/dma-map  
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 (pavel@ucw.cz), \* John Tyner (jty  
 Copyright (c) 2010-2011, Jarod Wilson <jarod@redhat.com> \* \* Based on the original lirc\_mceusb and lirc\_mceusb2 drivers, by Dan \* Conti, Martin  
 Blatter and Daniel Melander, the latter of which was  
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 (C) using AArch64 CRC instructions \*  
 Copyright (C) 2012,2013 - ARM Ltd \* Author: Marc Zyngier <marc.zyngier@arm.com> \* \* Based on arch/arm/kvm/coproc\_a15.c:  
 Copyright (C) 2005 Mike Isely <isely@pobox.com> \*/#ifndef \_\_PVRUSB2\_HDW\_H#define \_\_PVRUSB2\_HDW\_H  
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 Copyright (c) by James Courtier-Dutton <James@superbug.demon.co.uk> \* Driver p16v chips \* Version: 0.25 \* \* FEATURES currently supported:  
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Copyright (C) 2002 SGI - Silicon Graphics, Inc <linux-xfs@oss.sgi.com> \*/#ifndef \_POSIX\_ACL\_XATTR\_H#define \_POSIX\_ACL\_XATTR\_H  
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Copyright (c) 2017 Facebook \*/#include <linux/bpf.h>#include <linux/slab.h>#include <linux/vmalloc.h>#include <linux/etherdevice.h>#include  
<linux/filter.h>#include <linux/sched/signal.h>#incl  
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subsystem header files.  
Copyright (c) 1997 Dan Malek (dmalek@jlc.net) \* \* Right now, I am very wasteful with the buffers. I allocate memory \* pages and then divide them  
into 2K frame buffers. This way I know I \* have b  
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Copyright (C) 2013 Christian Vogelgsang \*/  
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Copyright (C) 1997 Geert Uytterhoeven  
Copyright (C) 2007, Thomas Gleixner <tglx@linutronix.de> \* \* Adaptive scheduling granularity, math enhancements by Peter Zijlstra  
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Copyright (C) 2001 - 2002 Greg Kroah-Hartman (greg@kroah.com) \* \* Thanks to Randy Dunlap for the original version of this code.  
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Lothar Waßmann <LW@KARO-electronics.de> (DT support) \*/  
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(USB dongle) or WHCI PCI \* card delivers a stream of  
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Copyright (C) 2011-2012 Mike Turquette, Linaro Ltd <mturquette@linaro.org> \* \* Simple multiplexer clock implementation  
Copyright (c) 2003-2004 LSI Logic Corporation. \* \* FILE : mbox\_defs.h  
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Copyright (c) 2000 Daniel Egger <egger@suse.de>  
Copyright (C) IBM Corporation, 2010-2012 \* Author: Srikar Dronamraju <srikar@linux.vnet.ibm.com> \*/#define pr\_fmt(fmt) "trace\_uprobe: " fmt  
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Copyright (c) 2007 Steven Toth <stoth@linuxtv.org> \*/  
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Copyright (C) 2014 Freescale Semiconductor, Inc.// Dong Aisheng <b29396@freescale.com>  
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Copyright (C) 2016 Google, Inc \* \* This device driver implements a TCG PTP FIFO interface over SPI for chips \* with Cr50 firmware. \* It is based on tpm\_tis\_spi driver by Peter Huewe and Christophe  
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Copyright 2007, 2011, Broadcom Corporation  
Copyright (C) 2017 Linaro Ltd. \*/#include <linux/hash.h>#include <linux/list.h>#include <linux/slab.h>#include <media/videobuf2-v4l2.h>  
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Copyright (C) 2003-2007 Micronas \*/  
Copyright (C) 2011 Sascha Hauer <s.hauer@pengutronix.de>  
Copyright(c) 2019 HiSilicon Limited.#include <linux/bitfield.h>#include <linux/dmaengine.h>#include <linux/init.h>#include <linux/iopoll.h>#include <linux/module.h>#include <linux/pci.h>#includ  
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Copyright (C) 2011 Tejun Heo <tj@kernel.org> \* \* REQ\_{PREFLUSH|FUA} requests are decomposed to sequences consisted of three \* optional steps - PREFLUSH, DATA and POSTFLUSH - according to the reque  
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Copyright 2017 \* \* Header file containing types used across multiple service APIs.  
Copyright (C) 2008 Adam Nielsen <a.nielsen@shikadi.net> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include <linux/skbuff.h>#include <linux/netfilter/x\_tables.h>#includ  
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Copyright (c) 2017 Savoir-faire Linux Inc. \* Vivien Didelot <vivien.didelot@savoirfairelinux.com> \*/  
Copyright (C) 2018 Western Digital Corporation \*/  
Copyright (C) Linus Torvalds, 1991, 1992 \*/  
Copyright (C) 2004 Nadia Yvette Chambers \*/#include <linux/ring\_buffer.h>#include <generated/utsrelease.h>#include <linux/stacktrace.h>#include <linux/writeback.h>#include <linux/kallsyms.h>#in  
Copyright (c) 2007 Dave Airlie <airlied@linux.ie> \*/  
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Copyright (c) 2011 Jiri Pirko <jpirko@redhat.com> \*/  
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Copyright (C) 2009 Matthias Fuchs <matthias.fuchs@esd-electronics.com>. \* \* References:  
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Copyright(C) 2006-2007 Timesys Corp., Thomas Gleixner \* \* High-resolution kernel timers  
(C) Copyright 1995 1996 Linus Torvalds \*/#include <linux/vmalloc.h>#include <linux/mm.h>#include <linux/sched.h>#include <linux/io.h>#include  
<linux/export.h>#include <asm/cacheflush.h>#includ  
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Copyright (C) 2000 Takashi Iwai <tiwai@suse.de> \* \* Routines for control of EMU10K1 WaveTable synth  
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<zхудacai@hisilicon.com> \* Gabriele Paoloni <gabriele  
Copyright (C) Steven Whitehouse \* New nbd\_end\_request() for compatibility with new linux block \* layer code. \* 2003/06/24 Louis D.  
Langholtz <ldl@aros.net> \* Remov  
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Copyright (C) 2018-2019 Intel Corporation \*/#ifndef \_\_NET\_WIRELESS\_CORE\_H#define \_\_NET\_WIRELESS\_CORE\_H#include <linux/list.h>#include  
<linux/netdevice.h>#include <linux/rbtree.h>#include <linux  
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(C) 2001 Brad Hards  
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Copyright 2012 Marvell International Ltd. \*/#include <linux/dmaengine.h>#include <linux/dma-mapping.h>#include <linux/dmapool.h>#include  
<linux/err.h>#include <linux/init.h>#include <linux/inte  
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(C) \* 11101: sent, \* recv\_ack (B) or implicit "ack" (A), \* still waiting for the barrier ack. \* master\_bio may already be completed  
and invalidated. \* 11100: write acked  
Copyright (c) 2002, 2003 Philip Pokorny <ppokorny@penguincomputing.com>  
Copyright (c) 2014 The Linux Foundation \*/#include <linux/dma-direct.h>#include <linux/dma-noncoherent.h>#include <linux/dma-  
contiguous.h>#include <linux/init.h>#include <linux/genalloc.h>#incl

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Copyright 2004,2007,2012 Freescale Semiconductor, Inc \*/  
(C) 2006-2012 \*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/ipv6.h>#include <linux/netfilter.h>#include  
<linux/netfilter\_ipv6.h>#include <linux/export.h>#include <net/addrc  
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Copyright (C) 2017 Linaro Ltd. \*/#ifndef \_\_HFI\_H\_\_#define \_\_HFI\_H\_\_  
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Copyright (C) 2008 Thomas Chou <thomas@wytron.com.tw> \* \* Based on sa1111ps2.c, which is:  
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Copyright (C) 2004, 2005, 2006 Red Hat, Inc., Ingo Molnar <mingo@redhat.com> \* \* This file contains mutex debugging related internal prototypes, for  
the \* !CONFIG\_DEBUG\_MUTEXES case. Most of them a  
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Copyright (c) 2000 The Regents of the University of Michigan. \* All rights reserved  
Copyright (C) 2008 Pierrick Hascoet <pierrick.hascoet@abilis.com> \*/#ifndef \_AS10X\_HANDLE\_H#define \_AS10X\_HANDLE\_Hstruct  
as10x\_bus\_adapter\_t;struct as102\_dev\_t;  
Copyright (C) 2008 Nokia Corporation  
Copyright (C) 1995 Linus Torvalds  
Copyright (c) 2004 Ron Lee (ron@debian.org) \* rewritten for kernel 2.6 \* \* cPad display character device part is not included. It can be found at \*  
<http://jan-steinhoff.de/linux/synaptics-usb.ht>  
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Copyright Jonathan Naylor G4KLX (g4klx@g4klx.demon.co.uk) \*/#include <linux/module.h>#include <linux/proc\_fs.h>#include  
<linux/kernel.h>#include <linux/interrupt.h>#include <linux/fs.h>#include  
(C) 1998 Dave Boynton  
Copyright (C) 2012 Stephane Grosjean <s.grosjean@peak-system.com> \* \* Derived from the PCAN project file driver/src/pcan\_pci.c: \*  
Copyright (c) 1995-2006, 2013-2014, Ericsson AB  
Copyright (C) 2014 Red Hat  
Copyright (c) 2003 Matthew Wilcox <matthew@wil.cx> \* \* This file is where we call all the ethtool\_ops commands to get \* the information ethtool  
needs.  
Copyright (C) 2007 David S. Miller (davem@davemloft.net) \*/  
Copyright (C) 2008 Hartmut Hackmann  
Copyright (C) 2005 Tower Technologies \* Author: Alessandro Zummo <a.zummo@towertech.it> \* \* class skeleton from drivers/hwmon/hwmon.c  
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Copyright (c) 2017 Maciej S. Szmigiero <mail@maciej.szmigiero.name>  
Copyright (c) 2008 Jonathan Cameron \*/  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_PTRACE\_H#define \_\_ASM\_PTRACE\_H  
Copyright (c) Tomasz Duszynski <tduszyns@gmail.com> \* \* Data sheets:  
(C) Copyright 1999 Roman Weissgaerber, weissg@vienna.at  
copyright ;- ) \* \* PS wx handler mostly stolen from hostap, copyright who  
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Copyright (c) 2010 Jarod Wilson <jarod@wilsonet.com> \* \* This driver was based on the work of Greg Wickham and Adrian \* Dewhurst. It was  
substantially rewritten to support correct signal \* gaps an  
Copyright 2009 Sysgo AG \* Thomas Moll <thomas.moll@sysgo.com> \* - Added Input- Output- enable functionality, to allow full communication \*/  
Copyright (C) 2008-2014 Red Hat Inc, Steven Rostedt <srostedt@redhat.com> \* \* The trace\_seq is a handy tool that allows you to pass a descriptor  
around \* to a buffer that other functions can write  
Copyright (c) 2010 Patrick McHardy <kaber@trash.net> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include  
<linux/gfp.h>#include <linux/skbuff.h>#include <linux/netfilter

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Thompson <mcthomps@us.ibm.com> \*/  
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Copyright (c) Jean-Francois Dive <jef@linuxbe.org> \*/#include <crypto/internal/hash.h>#include <linux/init.h>#include <linux/module.h>#include  
<linux/mm.h>#include <linux/cryptohash.h>#include  
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Copyright (C) 2005-2008 Nippon Telegraph and Telephone Corporation. \* \* Written by Seiji Kihara, Amagai Yoshiji, and Ryusuke Konishi. \* Revised by  
Ryusuke Konishi.  
Copyright (c) 2016 Linus Walleij <linus.walleij@linaro.org> \* \* Driver for Bosch Sensortec BMP180 and BMP280 digital pressure sensor.  
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(C) 2004-2006 Luc Saillard (luc@saillard.org)  
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Thompson <mcthomps@us.ibm.com> \* Tyler H  
Copyright (C) Fuzhou Rockchip Electronics Co.Ltd \* Author: Jacob Chen <jacob-chen@iotwrt.com> \*/#ifndef \_\_RGA\_HW\_H\_\_#define \_\_RGA\_HW\_H\_\_  
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Copyright (C) 1992,1993 Rick Sladkey <jrs@world.std.com>  
Copyright (C) 2014, Stephan Mueller <smueller@chronox.de> \* \* This file provides the user-space API for AEAD ciphers.  
Copyright 2015 Andrea Venturi <be17068@iperbole.bo.it>  
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Copyright(C) 2005, Red Hat, Inc., Ingo Molnar \* \* data type definitions, declarations, prototypes  
Copyright (C) 2001 Jens Axboe <axboe@kernel.dk> \*/#include <linux/mm.h>#include <linux/swap.h>#include <linux/bio.h>#include  
<linux/blkdev.h>#include <linux/uid.h>#include <linux/iocontext.h>#  
Copyright (C) 2018 Gao Xiang <gaoxiang25@huawei.com> \*/#ifndef \_\_EROFS\_FS\_TAGPTR\_H#define \_\_EROFS\_FS\_TAGPTR\_H  
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Copyright 2012 IBM Corporation \* \* Author: Ashley Lai <ashleydlai@gmail.com> \* Nayna Jain <nayna@linux.vnet.ibm.com>  
Copyright 2019 Analog Devices Inc. \*/#include <linux/i2c.h>#include <linux/module.h>#include <linux/regmap.h>  
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Copyright (c) 2007 Michel Ludwig <michel.ludwig@gmail.com>// - Fix SMBus Read Byte command  
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(C) 1991 Linus Torvalds - minix filesystem \*/  
Copyright (C) 2010 Malcolm Priestley \*/  
Copyright (C) 2004, 2013 Intel Corp. \* Author: Rafael J. Wysocki <rafael.j.wysocki@intel.com> \*/#include <linux/acpi.h>#include <linux/container.h>  
copyright information. \* 08-25-16 02.00.14 Added new values for the Flags field of Toolbox Clean \* Tool Request Message. \* 07-22-18  
02.00.15 Added defines for new TOOLBOX\_PC  
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Copyright (c) 2001 Haroldo Gamal <gamal@alternex.com.br> \*/  
Copyright (c) 2020, Nikolay Aleksandrov <nikolay@cumulusnetworks.com>#include <linux/kernel.h>#include <linux/netdevice.h>#include  
<linux/rtnetlink.h>#include <linux/slab.h>  
Copyright (C) 2002 Richard Henderson  
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Copyright (c) 2012 Broadcom Corporation \*/#ifndef WL\_CFGP2P\_H\_#define WL\_CFGP2P\_H\_  
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Copyright 2012 Google, Inc. \*/  
Copyright (C) 1996 Mike Shaver (shaver@zeroknowledge.com) \*/#include <linux/mm.h>#include <linux/sysctl.h>#include <linux/init.h>#include  
<net/ax25.h>#include <net/netrom.h>  
Copyright (c) 2001-2005 Greg Ungerer (gerg@snapgear.com) \* \* Bug fixes and cleanup by Philippe De Muyter (phdm@macqel.be)  
Copyright (c) 2008 Intel Corporation \* Author: Matthew Wilcox <willy@linux.intel.com> \* \* This file implements counting semaphores. \* A counting  
semaphore may be acquired 'n' times before sleeping  
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Copyright (C) 2012 Texas Instruments Incorporated - http://www.ti.com \* Author: Sebastian Andrzej Siewior <bigeasy@linutronix.de> \* \* A lot of code  
borrowed from the Linux xHCI driver.  
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<linux/iio/iio.h>#include <linux/module.h>#include <linux/reg  
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Copyright (C) 2001 Ingo Molnar <mingo@redhat.com> \* \* This file contains the implementation of an IRQ-safe, crash-safe \* kernel console  
implementation that outputs kernel messages to the \* net  
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Copyright (C) 2004 IBM Inc. \* \* debugfs is for people to use instead of /proc or /sys. \* See Documentation/filesystems/ for more details.  
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Copyright (C) 2014 Yunzhi Li <lyz@rock-chips.com>  
Copyright (C) 2013 Antti Palosaari <crope@iki.fi> \* \* GNU Radio plugin "gr-kernel" for device usage will be on:  
Copyright (c) 2017 Yotam Gigi <yotamg@mellanox.com> \* \* Refer to: draft-ietf-forces-interfelfb-03 and netdev01 paper:  
Copyright 2017-2018 NXP \*/#ifndef \_DPSECI\_H#define \_DPSECI\_H  
Copyright (C) 2009 Nokia Corporation and/or its subsidiary(-ies). \* \* Contact: Samu Onkalo <samu.p.onkalo@nokia.com>  
Copyright (C) 2014 Red Hat Inc, Steven Rostedt <rstedt@redhat.com> \* \* The seq\_buf is a handy tool that allows you to pass a descriptor around \*  
to a buffer that other functions can write to. It  
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Copyright © 2014 Intel Corporation  
Copyright (C) IBM Corporation, 2010-2011 \* Author: Srikar Dronamraju \*/#define pr\_fmt(fmt) "trace\_probe: " fmt  
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Copyright (C) 2005-2006 Micronas USA Inc. \*/  
Copyright (C) 2010 Red Hat, Inc., Peter Zijlstra \* \* Provides a framework for enqueueing and running callbacks from hardirq \* context. The enqueueing  
is NMI-safe.  
Copyright (c) 2007-2008 Oliver Neukum  
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Copyright (C) Andrew Tridgell 2001 \*/  
Copyright (c) 2015-2016 HGST, a Western Digital Company. \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/blkdev.h>#include  
<linux/module.h>#include "nvmet.h"  
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Copyright (c) 2013-2014, NVIDIA CORPORATION. All rights reserved  
Copyright (C) 2016 Linaro Ltd. \* Author: Linus Walleij <linus.walleij@linaro.org> \* \* This hardware was found in the very first Nexus One handset from  
Google/HTC \* and an early endavour into mobil  
Copyright (C) IBM Corporation, 2017 \* \* Author: Paul McKenney <paulmck@linux.ibm.com>  
Copyright (c) by Uros Bizjak <uros@kss-loka.si> \* \* OPL2/OPL3/OPL4 FM routines for internal percussion channels  
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\* hfsplus ioctl  
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Copyright (C) 2008 Felix Fietkau <nbd@openwrt.org>  
Copyright 2017 Intel Deutschland GmbH  
Copyright (C) 2006, OmniVision \*/  
Copyright (C) 2012 Andrew Lunn \*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/platform\_device.h>#include  
<linux/of\_platform.h>#include <linux/module.h>#include <linux/reboot.  
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Copyright (C) 2006, 2014, Intel Corp. \* Author: Kristen Carlson Accardi <kristen.c.accardi@intel.com> \* Rafael J. Wysocki  
<rafael.j.wysocki@intel.com> \*/  
Copyright 2019 Linaro, Ltd., Rob Herring <robh@kernel.org>#ifndef \_\_PANFROST\_FEATURES\_H\_\_#define \_\_PANFROST\_FEATURES\_H\_\_  
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Copyright (C) 2018 Netronome Systems, Inc.  
Copyright (c) 2012 Savoir-faire Linux Inc. \* Vivien Didelot <vivien.didelot@savoirfairelinux.com> \* \* For further information, see the  
Documentation/hwmon/ads7828.rst file.  
Copyright (C) 2016 Linaro Ltd \*/#include <linux/module.h>#include <linux/ulpi/driver.h>#include <linux/ulpi/regs.h>#include  
<linux/phy/phy.h>#include <linux/pinctrl/consumer.h>#include <linux/p  
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Copyright 2012 Linaro Ltd. \* Anton Vorontsov <anton.vorontsov@linaro.org> \* \* Based on ideas from Andrew Morton, David Rientjes,  
KOSAKI Motohiro, \* Leonid Moiseichuk, Mel Gorman, Minchan Kim an  
Copyright (C) 2001 \* Brad Boyer (flar@allandria.com)  
Copyright (C) 2002, 2018 Red Hat, Inc. All Rights Reserved  
Copyright (C) 1998-1999, Stephen Tweedie and Bill Hawes \* \* Manage the dynamic fd arrays in the process files\_struct.  
Copyright (C) 2015-2016 Synopsys, Inc. (www.synopsys.com) \* \* Authors: Joao Pinto <Joao.Pinto@synopsys.com>

(C) 1998-2001 Ben Fennema \* \* HISTORY  
Copyright 2010-11 Wolfson Microelectronics plc \* \* Author: Dimitris Papastamos <dp@opensource.wolfsonmicro.com>  
Copyright (C) 2000, 2003 Hewlett-Packard Co \* David Mosberger-Tang <davidm@hpl.hp.com> \* \* 03/05/07 davidm Switch from PCI-DMA to  
generic device DMA API. \* 00/12/13 davidm Rename to swiotlb.c and  
Copyright (C) 1997 Richard G \* \* binfmt\_misc detects binaries via a magic or filename extension and invokes \* a specified wrapper. See  
Documentation/admin-guide/binfmt-misc.rst for more details.  
Copyright (C) 2018 HUAWEI, Inc. \* http://www.huawei.com/ \* Created by Gao Xiang <gaoxiang25@huawei.com> \*/#ifndef  
\_\_EROFS\_FS\_ZPVEC\_H#define \_\_EROFS\_FS\_ZPVEC\_H  
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Copyright (c) 2012 Samsung Electronics Co., Ltd. \* http://www.samsung.com/ \*/#include <linux/fs.h>#include <linux/f2fs\_fs.h>#include  
<linux/mpage.h>#include <linux/backing-dev.h>#in  
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(C) 2001 NeilBrown <neilb@cse.unsw.edu.au> \* \* This driver for the Micro Memory PCI Memory Module with Battery Backup  
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(C) Copyright 1995 - 1997 Marco van Wieringen - ELM Consultancy B.V. \* \* Plugged two leaks. 1) It didn't return acct\_file into the free\_filps if \* the file  
happened to be read-only. 2) If the acc  
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Copyright (c) 2006 Silicon Graphics, Inc. \* by Greg Banks <gnb@melbourne.sgi.com> \*/  
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Copyright (C) 2000-2002 Inside Out Networks  
Copyright (C) 2004, David Woodhouse \*\*\*\*\*/  
Copyright (c) 2004 Red Hat, Inc., James Morris <jmorris@redhat.com>\*/#ifndef \_\_LINUX\_XATTR\_H#define \_\_LINUX\_XATTR\_H  
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aeb@cw.nl - May 1994 \* Restrict VT switching  
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copyright information. \* \* This file contains functions for initializing the input-device and for handling remote-control-queries.  
 Copyright (C) 2013 Texas Instruments  
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 Copyright (C) 2014-2016 Zi Shen Lim <zlim.lnx@gmail.com> \*/#include <linux/bitops.h>#include <linux/bug.h>#include <linux/compiler.h>#include  
 <linux/kernel.h>#include <linux/mm.h>#include <linu  
 Copyright (c) 2017 Synopsys, Inc. and/or its affiliates.// stmmac Support for 5.xx Ethernet QoS cores  
 Copyright (c) 2017, Microsemi Corporation  
 Copyright (C)2003 USAGI/WIDE Project \* \* Authors:  
 Copyright (c) 2016 Quantenna Communications. All rights reserved  
 Copyright (C) 2004 Intel Corporation <matthew.e.tolentino@intel.com> \* \* This code takes all variables accessible from EFI runtime and \* exports them  
 via sysfs  
 Copyright (C) 2006 Broadcom Corporation.  
 Copyright (C) 2007, 2008 Wolfson Microelectronics PLC. \* \* Author: Liam Girdwood <lrg@slimlogic.co.uk>  
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 Copyright (c) 2007 Jiri Kosina \*/#ifndef \_HIDRAW\_H#define \_HIDRAW\_H  
 Copyright (C) Terry Dawson VK2KTJ (terry@animats.net) \*/#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include  
 <linux/in.h>#include <linux/kernel.h>#include <linux/ti  
 Copyright (C) 2011-2012 Linaro Ltd <mturquette@linaro.org> \*/#ifndef \_\_LINUX\_CLK\_PROVIDER\_H#define \_\_LINUX\_CLK\_PROVIDER\_H  
 Copyright (c) 2012 Benjamin Tissoires <benjamin.tissoires@gmail.com>  
 Copyright (C) 2016 Martin Sperl \*/  
 Copyright (C) 2007 Herbert Valerio Riedel <hvr@gnu.org> \* \* g762: minimal datasheet available at:  
 Copyright (C) 2002\*/  
 Copyright (c) 2015, Intel Corporation \* Authors: Tadeusz Struk <tadeusz.struk@intel.com> \*/#ifndef \_CRYPTO\_AKCIIPHER\_H#define  
 \_CRYPTO\_AKCIIPHER\_H#include <linux/crypto.h>  
 Copyright (c) 2009 Pavel Hofman <pavel.hofman@ivitera.com> \*/  
 Copyright(C) 2010 Jarod Wilson <jarod@wilsonet.com> \* Portions based on the original lirc\_imon driver,  
 Copyright (C) 2014 Red Hat, Inc., Johannes Weiner \*/  
 Copyright (2016) Chen-Yu Tsai \* \* Author: Chen-Yu Tsai <wens@csie.org>  
 Copyright (c) 2012 Coraid, Inc. See COPYING for GPL terms./\* \* aoemain.c \* Module initialization routines, discover timer  
 Copyright 2004-2009 Freescale Semiconductor, Inc. All Rights Reserved  
 Copyright 2019 Analog Devices Inc. \*/#include <linux/bitfield.h>#include <linux/bits.h>#include <linux/clock.h>#include <linux/device.h>#include  
 <linux/hwmon.h>#include <linux/hwmon-sysfs.h>#inc  
 Copyright (C) 2001 Standard Microsystems Corporation \* Developed by Simple Network Magic Corporation  
 Copyright (C) 2012 Federico Vaga <federico.vaga@gmail.com> \* \* Borrowed from c\_can\_platform.c  
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 Copyright 2017-2018 Qiang Yu <yuq825@gmail.com>  
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 Copyright (C) 2007 Andrei Tanas <andrei@tanac.ca> (suspend/resume support)  
 Copyright (C) 2017-2018 Hilisicon Electronics Co., Ltd. \* http://www.huawei.com \* \* Authors: Yu Chen <chenyu56@huawei.com>  
 Copyright (C) 2019 Xilinx, Inc. \* \* Description: \* This driver is developed for SDFEC16 IP. It provides a char device  
 Copyright (c) by Jaroslav Kysela <perex@perex.cz>, \* Creative Labs, Inc. \* Definitions for EMU10K1 (SB Live!) chips \*/#ifndef  
 \_\_SOUND\_EMU10K1\_H#define \_\_SOUND\_EMU10K1\_H  
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 Copyright (C) 2004 Neil Brown \* \* faultly-device-simulator personality for md  
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 Copyright (c) 2012 Samsung Electronics Co., Ltd. \* http://www.samsung.com/ \*/#include <linux/fs.h>#include <linux/f2fs\_fs.h>#include

```

<linux/bio.h>#include <linux/blkdev.h>#include <
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Copyright (C) 2002 David S. Miller (davem@redhat.com) * * Changes:
Copyright (C) 2012 ST-Ericsson SA * Written on behalf of Linaro for ST-Ericsson * Based on bits of regulator core, gpio core and clk core * * Author:
Linus Walleij <linus.walleij@linaro.org>
Copyright (C) 2016-2017 Micron Technology, Inc. * * Authors:
Copyright (C) 2015 ARM Ltd. * Author: Lorenzo Pieralisi <lorenzo.pieralisi@arm.com> */#include <linux/acpi.h>#include <linux/irq.h>#include
<linux/irqdomain.h>#include <linux/of.h>
Copyright (c) 1999-2000 by Takashi Iwai <tiwai@suse.de> */
Copyright (C) 2009 Tejun Heo <tj@kernel.org> *
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Copyright (C) 2005-2008 Jean Delvare <jdelvare@suse.de> * * Based on the lm90 driver, with some ideas taken from the lm_sensors * lm92 driver as
well.
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Thomas Gleixner suggestions.
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Copyright (c) 2011 DENX Software Engineering GmbH */#include <linux/kernel.h>#include <linux/errno.h>#include <linux/init.h>#include
<linux/module.h>#include <linux/mii.h>#include <linux/phy.h>
Copyright 2016 NXP */
Copyright (c) 2012 Massachusetts Institute of Technology * * Adapted from code in net/8021q/vlan_gvrp.c
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Copyright (c) 2001 Chris Atenasio <chris@crud.net>
(C) 2005 by Harald Welte <laforge@netfilter.org> */
Copyright (C) 2015-2018 Linaro Ltd. */#include <linux/clk.h>#include <linux/media-bus-format.h>#include <linux/media.h>#include
<linux/module.h>#include <linux/platform_device.h>#include <linux
Copyright (C) 2014 Pavel Machek <pavel@denx.de> * * You can get hardware description at * http://www.ti.com/lit/ds/symlink/bq32000.pdf
Copyright (C) Frederic Rible F1OAT (frible@teaser.fr) */
COPYRIGHT (1 << 5)#define m_AUDIO_STATUS_ADDITION (3 << 2)#define m_AUDIO_STATUS_CLK_ACCURACY (2 << 0)#define
v_AUDIO_STATUS_NLPCM(n) ((n & 1) << 7)#define AUDIO_N_H 0x3f#define AUDIO_N_M 0
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```

```

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Copyright (c) 2018-2019 HiSilicon Limited. #include <linux/acpi.h> #include <linux/aer.h> #include <linux/bitops.h> #include <linux/debugfs.h> #include
<linux/init.h> #include <linux/io.h> #include <l
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Copyright (C) 2010-2011 Texas Instruments Incorporated - http://www.ti.com * * Authors: Felipe Balbi <balbi@ti.com>, * Sebastian Andrzej Siewior
<bigeasy@linutronix.de>
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Copyright (C) 2016 Marek Vasut <marex@denx.de> * * Details about this controller can be found at * http://www.ifi-pld.de/IP/CANFD/canfd.html
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Copyright (C) 2016 Matt Ranostay <matt@ranostay.consulting> * * Based on the original work pwrseq_simple.c
Copyright (C) 2017 Hisilicon Limited * Author: Shaokun Zhang <zhangshaokun@hisilicon.com> * Anurup M <anurup.m@huawei.com> * * This
code is based on the uncore PMUs like arm-cci and arm-cc
Copyright 2002 H. Peter Anvin */ #include <linux/kernel.h> #include <linux/interrupt.h> #include <linux/module.h> #include <linux/dma-
mapping.h> #include <linux/raid/pq.h> #include <linux/async_tx.h>
Copyright (C) 2009 Laurent Pinchart <laurent.pinchart@skynet.be> * * This file holds USB constants and structures defined by the USB Device * Class
Definition for Video Devices. Unless otherwise st
Copyright (C) 2017 Google, Inc. * Thiebaud Weksteen <tweek@google.com> */
Copyright (c) 2002-2003, Jouni Malinen <j@w1.fi> * * Adaption to a generic IEEE 802.11 stack by James Ketrenos * <jketreno@linux.intel.com>
Copyright (C) 2014-2016 Freescale Semiconductor, Inc. * Author: German Rivera <German.Rivera@freescale.com> *
Copyright (c) 2009 Secret Lab Technologies, Ltd. * * This file provides helper functions for extracting PHY device information * out of the OpenFirmware
device tree and using it to populate an mii_
Copyright Management Information (UDF 2.60 3.3.4.5.1.2) struct DVDCopyrightImpUse { __le16 headerChecksum; uint8_t
CGMSInfo; uint8_t dataType; uint8_t protectionSystemInfo[4];} __packed;
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<gary.hook@amd.com> * Author: Brijesh Singh <brijesh.singh@amd.com>
Copyright 2013-2014 Freescale Semiconductor, Inc. * * Driver for the Freescale eDMA engine with flexible channel multiplexing * capability for DMA
request sources. The eDMA block can be found on so
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Copyright 2013 Freescale Semiconductor, Inc. * * clock driver for Freescale QorIQ SoCs.
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Copyright (C) 2006, 2007 Christopher Pascoe <c.pascoe@itee.uq.edu.au> */
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Copyright (C) 2017 Intel Corporation \*/#include <linux/fpga/fpga-bridge.h>#include <linux/fpga/fpga-mgr.h>#include <linux/fpga/fpga-  
region.h>#include <linux/idr.h>#include <linux/kernel.h>#incl  
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Copyright (C) 2011-2013 Intel Corporation  
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Copyright (C) 2017 Hans de Goede <hdegoede@redhat.com> \*/  
Copyright (C) 2000-2001 Deep Blue Solutions Ltd. \*/#include <linux/module.h>#include <linux/tty.h>#include <linux/tty\_flip.h>#include  
<linux/slab.h>#include <linux/sched/signal.h>#include <linu  
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<linux/trace.h>#include <linux/slab.h>#include <linux/fs.h  
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Copyright (c) 2003 Christoph Hellwig <hch@lst.de> \* - new-style, hotplug-aware pci probing and scsi registration \* \* Version : v2.00.4 Mon Nov 14  
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sending me one to hack on, without \* which the  
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<linux/kernel.h>#include <linux/interrupt.h>#include <linux/fs.h>#inc  
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Copyright (C) 2007 Cambridge Silicon Radio Ltd. */#include <linux/kernel.h>#include <linux/export.h>#include "include/umc.h"
Copyright (C) 2014 Linaro Ltd. * Author: Rob Herring <robh@kernel.org> * * Based on 8250 earlycon:
Copyright (c) 2005, 2006, 2008 Patrick McHardy <kaber@trash.net> */#include <linux/kernel.h>#include <linux/init.h>#include
<linux/sysctl.h>#include <linux/spinlock.h>#include <linux/skbuff.h>#
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Copyright (C) 2006 Torsten Erbjerg Rasmussen <tr@newtec.dk> * - nvram size determined from resource * - this ds1742 driver now supports ds1743.
*/
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<linux/string.h>#include <linux/spinlock.h>#include <linux/rcupdate  
Copyright (C) 2015-2016 Russell King \* \* Network PHYs can appear on I2C buses when they are part of SFP module. \* This driver exposes these PHYs to  
the networking PHY code, allowing \* our PHY driv  
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specification, and can be driven by uvcvideo. Howev  
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(C) 2007 Laszlo Attila Toth <panther@balabit.hu> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/kernel.h>#include  
<linux/module.h>#include <linux/skbuff.h>#include <linux/netdevice  
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<linux/tty.h>#include <linux/tty\_driver.h>#include <linux/poll.h>  
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checks and added DM partitions
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<linux/module.h>#include <linux/usb.h>#includ
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```

```

<linux/export.h>#include <linux/slab.h>#include <linux/
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<linux/netdevice.h>#include <linux/export.h>#include <asm/unalignt
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F. Padovan, February 2009 * Break out common bits to
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<linux/string.h>#include <sound/core.h>#include <s  
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<linux/init.h>#include <linux/serial\_core.h>  
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<linux/irqdomain.h>#include <linux/interrupt.h>#includ  
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<linux/mtd/partitions.h>#include <linux/etherdevice.h>#include  
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 Copyright (c) 2005 - James Bottomley <James.Bottomley@steeleye.com> \* \* The basic idea here is to allow any "device controller" (which \* would most often be a Host Bus Adapter to use the services o  
 Copyright 2016 NXP \*/#ifndef \_FSL\_DPNI\_CMD\_H#define \_FSL\_DPNI\_CMD\_H  
 Copyright © 1996 Hans-Joachim Widmaier \* \* Changes \*  
 release \* Jun 1997 2.1.43+ changes \* Changed for 2.1.19 modules \* Jan 1997 Initial  
 Proper page locking in readpage \* Change  
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 Copyright (C) 2019 ARM Ltd. \*/  
 Copyright (C) 2013 Intel Corporation \*/  
 Copyright (C) 2018 Stefan Wahren <stefan.wahren@i2se.com> \*/#include <linux/device.h>#include <linux/err.h>#include <linux/hwmon.h>#include <linux/module.h>#include <linux/platform\_device.h>#in  
 Copyright (c) 2010 Stefan Achatz <erazor\_de@users.sourceforge.net> \*/  
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 Copyright (c) 2005, Wind River Systems \* All rights reserved  
 Copyright (c) 2004 Matthieu Castet <castet.matthieu@free.fr>  
 Copyright 2019 Linaro, Ltd, Rob Herring <robh@kernel.org>#include <linux/atomic.h>#include <linux/bitfield.h>#include <linux/delay.h>#include <linux/dma-mapping.h>#include <linux/interrupt.h>#in  
 (C) 1991, 1992 by Linus Torvalds \*/  
 Copyright (C) 1999 Roman Weissgaerber \* \* The SL811HS controller handles host side USB (like the SL11H, but with \* another register set and SOF generation) as well as peripheral side USB \* (like t  
 Copyright 1998-2000 Jonathan Teh Soon Yew): \* voltagefactor[0]=1.25/2628; (2628/1.25=2102.4) // Vccp \* voltagefactor[1]=1.25/2628; (2628/1.25=2102.4) // +2.5V \* voltagefactor[2]=1.67/2628; (262  
 Copyright 2006-2007 Pierre Ossman \*/  
 (C) 2008-2009 Pablo Neira Ayuso <pablo@netfilter.org> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include <linux/skbuff.h>#include <linux/jhash.h>#include <linux/ip.h>  
 Copyright (c) 2015 by Kamil Debski \*/  
 Copyright (C) 2017-2019 Microchip Technology Inc. \*/  
 (C) 2000-2002 Harald Welte <laforge@netfilter.org>  
 Copyright (C) 2009, Intel Corp. \* Author: Huang Ying <ying.huang@intel.com> \*/  
 Copyright (c) 2012 Samsung Electronics Co., Ltd. \* http://www.samsung.com/ \*/#include <linux/fs.h>#include <linux/f2fs\_fs.h>#include <linux/pagemap.h>#include <linux/sched.h>#includ  
 Copyright (C) 2009 Alan Stern (stern@rowland.harvard.edu) \*/  
 (C) Copyright 1996 Linus Torvalds \* \* Address space accounting code <alan@lxorguk.ukuu.org.uk>  
 Copyright (c) 2014 Broadcom Corporation \*/#ifndef BRCMF\_CHIP\_H#define BRCMF\_CHIP\_H  
 Copyright (C) 2008, 2009 PMC Sierra Inc \*/#include <linux/fs.h>#include <linux/init.h>#include <linux/types.h>#include <linux/errno.h>#include <linux/kernel.h>#include <linux/ioport.h>#include  
 Copyright (c) 2011 Werner Almesberger <werner@almesberger.net> \* \* USB initialization is  
 Copyright (c) 2018 NVIDIA CORPORATION. All rights reserved  
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```

<simon.kallweit@intefo.ch> * * Bosch C_CAN controller is compliant to CAN
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Copyright (C) 1998,99 Takashi Iwai */
Copyright (c) 2009 Patrick McHardy <kaber@trash.net> * * Development of this code funded by Astaro AG (http://www.astaro.com/)
Copyright 2013 Intel Corporation; author Matt Fleming */
Copyright (C) 2007 FUJITA Tomonori <tomof@acm.org> */#include <linux/init.h>#include <linux/module.h>#include <linux/jiffies.h>#include
<linux/err.h>#include <linux/slab.h>#include <linux/strin
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Copyright (C) 2011-2012 - NVIDIA, Inc. */
Copyright (C) 2016-2017 Texas Instruments Incorporated - http://www.ti.com/ */#ifndef __TAS2770__#define __TAS2770__
Copyright 2015 www.starterkit.ru <info@starterkit.ru> * * Based on:
Copyright (C) 2013 Texas Instruments Incorporated - http://www.ti.com * * Author: Erick Macias <emacias@ti.com> * Author: Felipe Balbi
<balbi@ti.com> * Author: Mark A. Greer <mgreer@animalcreek.co
Copyright (C) 2010 Broadcom Corporation. */
Copyright (c) 2016 Thomas Graf <tgraf@tgraf.ch> */
Copyright (C) 2013 Luis Alves <ljalvs@gmail.com> July, 6th 2013 First release based on cx24116 driver by:
Copyright (C) 2015, 2018 * Author: Matt Ranostay <matt.ranostay@konsulko.com> * * TODO: enable pulse length controls via device tree properties
Copyright (c) 2003-2004, Jouni Malinen <jkmaline@cc.hut.fi> */
Copyright (c) 2010, 2011 Ericsson AB.
Copyright (c) 2003-2004 LSI Logic Corporation. * * FILE : megaraid_mm.h
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hereby granted, provided that the above * copy
Copyright; struct regid appIdent; struct timestamp recordingDateAndTime; struct regid impIdent; uint8_t
impUse[64]; __le32 predecessorVolDescSeqLocation; __le16 flags; uint8_t reserve
Copyright (C) 2004-2006 Joern Engel <joern@wh.fh-wedel.de> * * Licence: GPL
Copyright (C) 2010 Igor M. Liplianin <liplianin@netup.ru> */#ifndef __ALTERA_CI_H#define __ALTERA_CI_H
Copyright (C) 2014 Freescale Semiconductor. * * Author: Minghuan Lian <Minghuan.Lian@freescale.com>
Copyright (c) 2018 Vitaly Chikunov <vt@altlinux.org>
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Copyright (C) 2014 by Marcel Mol * * - Based on info from http://www.hifi-remote.com * - Ignore Toggle=9 frames * - Ignore XMP-1 XMP-2 difference,
always store 16 bit OBC
Copyright(c) 2009 Realtek Semiconductor Corp. All rights reserved
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<simon.kallweit@intefo.ch> * * TX and RX NAPI implementation has been bor
Copyright (C) 2012 ARM Ltd. */#ifndef __ASM_TLB_H#define __ASM_TLB_H
Copyright 2007 Michael Buesch <m@buesch>
Copyright (C) 2000 Frodo Looijaard <frodol@dds.nl> * Philip Edelbrock <phil@netroedge.com> * Stephen
Rousset <stephen.rousset@rocketlogix.com>
Copyright 1998-2002 by Jes Sorensen, <jes@trained-monkey.org>. * * Thanks to Alteon and 3Com for providing hardware and documentation *
enabling me to write this driver.
Copyright (c) 2005,2006 Mauro Carvalho Chehab <mchehab@kernel.org>
Copyright (c) 2006 Option Wireless n/v * All rights Reserved
Copyright (c) 2016 Andrew Lunn <andrew@lunn.ch> *
Copyright (c) 2012-2014, The Linux Foundation. All rights reserved
Copyright (c) 1999-2000 Takashi iwai <tiwai@suse.de> */
Copyright 2019 Collabora ltd.#include <linux/delay.h>#include <linux/interrupt.h>#include <linux/io.h>#include <linux/platform_device.h>#include

```

```

<linux/pm_runtime.h>#include <linux/dma-resv.h>#
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Copyright (C) 2007, 2008, 2009 Oracle Corporation * Written by: Martin K. Petersen <martin.petersen@oracle.com> */
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Copyright (c) 2014 Christian Gmeiner <christian.gmeiner <at> gmail.com> */
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Copyright (c) 2002-2003 Patrick Mochel
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Copyright (c) 2014 Intel Corporation. All rights reserved
(C) Copyright 1999 Gregory P. Smith (from usb-ohci.c)
Copyright (C) 2009 Steven Rostedt <srostedt@redhat.com> */#include <linux/ring_buffer.h>#include <linux/completion.h>#include
<linux/kthread.h>#include <uapi/linux/sched/types.h>#include <linux/
Copyright (C) 1991, 1992 Linus Torvalds * 1995 Jay Estabrook * * A note on using in_atomic() in here: We can't handle console *
calls from non-schedulable context due to our USB-dependend
Copyright (c) 2002 Mark D. Studebaker <mdsxyz123@yahoo.com> * Aaron M. Marsh <amarsh@sdf.lonestar.org> */
Copyright 2005 Wolfson Microelectronics PLC.
Copyright (C) Josef Gajdusek <atx@atx.name> * * based on other Linux RTC drivers
Copyright (C) 2013 Lubomir Rintel <lkundrak@v3.sk> *
Copyright (C) 2019 Brian Masney <masneyb@onstation.org>
Copyright (C) 2004 Alex Aizman
Copyright (C) 2019 Oracle. All Rights Reserved
Copyright (C) 2018 * Author: Jeff LaBundy <jeff@labundy.com> * * 7-bit I2C slave address: 0x13
Copyright (C) 2001 IBM Corp.
Copyright (C) 2013 Shaohua Li <shli@kernel.org>
Copyright(c) 2015 Intel Deutschland GmbH * * Contact Information:
Copyright (C) 2014-2015 Broadcom
Copyright (C) 2015 Samsung Electronics * Author: Chanwoo Choi <cw00.choi@samsung.com> * * This driver is based on drivers/devfreq/devfreq.c.
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Copyright (C) 2017,2018 Microchip Technology, * Author: Eugen Hristev <eugen.hristev@microchip.com> *
Copyright (c) 2011, 2013 Stefan Kriwanek <dev@stefankriwanek.de> */
Copyright (c) 2015-2019, Linaro Limited */#ifndef OPTEE_SMC_H#define OPTEE_SMC_H
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Copyright (C) 1999 Frodo Looijaard <frodol@dds.nl> * Philip Edelbrock <phil@netroedge.com>
Copyright (C) 2006-2007 Till Harbaum (Till@Harbaum.org) */
Copyright 2007 Intel Corporation * Author: Matthew Wilcox <willy@linux.intel.com> * * This allocator returns small blocks of a given size which are
DMA-able by * the given device. It uses the d
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Copyright (C) 2007, 2008 Rusty Russell IBM Corporation */
Copyright (C) 2010 Sheng-Yuan Huang (Nuvoton) (PS00) * * Shamelessly ripped from the w83627hf driver
Copyright (C) 2005 Mike Isely <isely@pobox.com> */#ifndef __PVRUSB2_IOREAD_H#define __PVRUSB2_IOREAD_H
Copyright (c) 2016, General Electric Company * * based on hid-penmount.c
Copyright (C) 2010, 2011 Ericsson AB. * * Documentation:
Copyright (C) 2005 <bradlch@hotmail.com> * * V4L2 by Jean-Francois Moine <http://moinejf.free.fr>
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Copyright (C) 2000 VA Linux Co
Copyright (C) 2011-2013 Jozsef Kadlec <kadlec@netfilter.org>
(C) 2011 Patrick McHardy <kaber@trash.net> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/in.h>#include <linux/ip.h>#include
<net/ip.h>#include <linux/module.h>#include <linux/skb
COPYRIGHT | IEC958_AES0_CON_EMPHASIS_5015; ctrl->ch_status[1] = IEC958_AES1_CON_DIGDIGCONV_ID; ctrl-
>ch_status[2] = 0x00; ctrl->ch_status[3] = IEC958_AES3_CON_FS_44100 | IEC958_AES3

```



Copyright by Micha \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/pci.h>#include <linux/delay.h>#include "cb710-mmc.h"  
(C) Copyright IBM Corporation 2005. \* Author : Ram Pai (linuxram@us.ibm.com) \*/#include <linux/mnt\_namespace.h>#include  
<linux/mount.h>#include <linux/fs.h>#include <linux/nsproxy.h>#include <u  
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Copyright (c) 2007 Novell Inc. \*/  
Copyright (C) 1998 David S. Miller (davem@redhat.com)  
Copyright (C) 2005 Mike Isely <isely@pobox.com> \*/#ifndef \_\_PVRUSB2\_I2C\_CORE\_H#define \_\_PVRUSB2\_I2C\_CORE\_H  
Copyright (C) 2014 Olli Salonen <olli.salonen@iki.fi> \*/  
Copyright (C) 2001-2002 Greg Kroah-Hartman (greg@kroah.com)  
Copyright (c) 2003 Dimitromanolakis Apostolos <apostol@cs.utoronto.ca> \* \* version 0.82: Stable / not all features work yet (no communication with  
AC97 secondary) \* added 64x/128x oversampl  
Copyright (C) 2016 David Lechner <david@lechnology.com> \* \* Based on uinput.c: Aristeu Sergio Rozanski Filho <aris@cathedrallabs.org>  
Copyright (c) 2014-2016 Jiri Pirko <jiri@mellanox.com>  
Copyright 2012 bct electronic GmbH  
Copyright (C) 2007, 2008 Oracle Corporation \* Written by: Martin K. Petersen <martin.petersen@oracle.com> \*/  
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Copyright (c) 2017-2019 Linaro, Ltd. <ard.biesheuvel@linaro.org> \* \* Based on irq-tegra.c:  
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Copyright (C) 2005 Steven Toth <stoth@linuxtv.org> \* \* Support for KWorld DVB-S 100 by Vadim Catana <skystar@moldova.cc>  
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Copyright 1999 Silicon Integrated System Corporation Revision: 1.08.10 Apr. 2 2006  
Copyright (C) 1995, 1996 Olaf Kirch <okir@monad.swb.de> \* \* CHANGES \* 19-Apr-2000 Chris Evans - Security fix  
Copyright (c) 1998 by Frank van de Pol <fvdpol@coil.demon.nl> \*/#include <linux/time.h>#include <sound/asequencer.h>  
Copyright 1998 by Franco Venturi.  
Copyright (C) 2000 Silicon Graphics, Inc. \* Written by Ulf Carlsson (ulfc@enr.sgi.com) \*  
Copyright (C) 2007 Davide Libenzi <davidel@xmailserver.org> \*  
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Copyright (c) 2016 Yuxuan Shui <yshuiv7@gmail.com>  
Copyright (c) Trond Myklebust \* \* Definitions pertaining to NFS delegated files  
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Copyright (c) 2006 Pototskiy Akex <alex.pototskiy@gmail.com> \* Takashi Iwai <tiwai@suse.de> \* Tobin Davis <tdavis@dsl-  
only.net> \*/  
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(C) Copyright 2003 Hewlett-Packard
Copyright (C) 2019 Renesas Electronics Corp. */
(C) 2002-2004 Netfilter Core Team <coreteam@netfilter.org> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/types.h>#include <linux/udp.h>#include <linux/skb
Copyright (c) 2012 Analog Devices, Inc. * Author: Lars-Peter Clausen <lars@metafoo.de> */
Copyright (C) 2001-2003 Andreas Gruenbacher, <agruen@suse.de> */#include <linux/f2fs_fs.h>#include "f2fs.h"#include "xattr.h"#include "acl.h"
Copyright (C) 2005 Martin Langer <martin-langer@gmx.de>
Copyright (c) 1994-98 by Jaroslav Kysela <perex@perex.cz>, *                               4Front Technologies * * Direct FM control
Copyright (c) 2015-2016 HGST, a Western Digital Company. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/blkdev.h>#include
"nvmet.h"
Copyright (C) 1999 - 2012 Greg Kroah-Hartman (greg@kroah.com)
Copyright (c) 1999-2002 Matthew Dharm (mdharm-usb@one-eyed-alien.net)
Copyright (c) 2006-2016 Herbert Xu <herbert@gondor.apana.org.au> */
Copyright (C) 2011 Atmel */
Copyright (C) 2005 Venkatesh Pallipadi <venkatesh.pallipadi@intel.com> *                               - Added support for C3 on SMP */#define pr_fmt(fmt) "ACPI: "
fmt
Copyright (C) 2012,2013 - ARM Ltd * Author: Marc Zyngier <marc.zyngier@arm.com> */
Copyright (C) 2016, Broadcom */
Copyright (c) 2010-2013, NVIDIA Corporation. */
Copyright (C) 2017-2018 SUSE, Frederic Weisbecker *
Copyright (C) 2004 - 2006 rt2x00 SourceForge Project * <http://rt2x00.serialmonkey.com> * * Module: eeprom_93cx6 * Abstract: EEPROM reader
routines for 93cx6 chipsets. * Supported chipsets: 93c46
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(C) 2015 Pengutronix, Alexander Aring <aar@pengutronix.de>
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Copyright (C) Fuzhou Rockchip Electronics Co.Ltd * Author:Mark Yao <mark.yao@rock-chips.com> * * based on exynos_drm_drv.h
Copyright 2007,2014 Andrea Merello <andrea.merello@gmail.com> * * Based on the r8180 driver, which is:
Copyright (C) 2017-2018 HUAWEI, Inc. *                               http://www.huawei.com/ * Created by Gao Xiang <gaoxiang25@huawei.com> */#include
"internal.h"#include <linux/prefetch.h>
Copyright (C) 2001-2016 Miklos Szeredi <miklos@szeredi.hu> * * This program can be distributed under the terms of the GNU GPL. * See the file
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```

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 Copyright (C) 2013 Red Hat \*/  
 Copyright (c) 2000,2002,2005 Silicon Graphics, Inc.  
 Copyright (c) 2015, Linaro Limited \*/#include <linux/linkage.h>#include <linux/arm-smccc.h>  
 Copyright (c) 2013 Dialog Semiconductor \* \* Author: Adam Thomson <Adam.Thomson.Opensource@diasemi.com>  
 Copyright © 2002 Robert Kaiser <rkaiser@sysgo.de> \*/  
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 Copyright (c) 2017, The Linux Foundation. All rights reserved  
 Copyright (C) 2007 Hewlett-Packard Development Company, L.P. \* Paul Moore <paul@paul-moore.com> \*/#ifndef  
 \_SELINUX\_NETIF\_H\_#define \_SELINUX\_NETIF\_H\_  
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 Copyright (C) 2001 Greg Kroah-Hartman (greg@kroah.com)  
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 from USB Skeleton driver - 0.5  
 Copyright (c) 2011 Johan Hovold <jhovold@gmail.com> \* \* USB Abstract Control Model driver for USB modems and ISDN adapters  
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 (C) 2007 Patrick McHardy <kaber@trash.net> \*/#include <linux/module.h>#include <linux/skbuff.h>#include <linux/gen\_stats.h>#include  
 <linux/jhash.h>#include <linux/rtnetlink.h>#include <linux/ra  
 (C) UV\_Meta\_Top\_Field\_Plane \* (D) UV\_UBWC\_Top\_Field\_Plane \* (E) Y\_Meta\_Bottom\_Field\_Plane \* (F) Y\_UBWC\_Bottom\_Field\_Plane  
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 ericy@andante.org to \* add scatter-gather, multip  
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<surenb@google.com>  
Copyright (C) 2001 Jens Axboe <axboe@suse.de> \*/#include <linux/compat.h>#include <linux/kernel.h>#include <linux/errno.h>#include  
<linux/string.h>#include <linux/module.h>#include <linux/blkde  
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Copyright (c) 2014, Sony Mobile Communications Inc. \* \* This driver is for the multi-block Switch-Mode Battery Charger and Boost \* (SMBB) hardware,  
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Copyright (C) 2006 \* Simon Schulz (ark3116\_driver <at> auctionant.de) \* \* ark3116 \* - implements a driver for the arkmicro ark3116 chipset  
(vendor=0x6547, \* productid=0x0232) (used in a datac  
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Copyright (C) 2012-2013 MundoReader S.L. \* Author: Heiko Stuebner <heiko@sntech.de> \* \* based in parts on Nook zforce driver  
Copyright (c) 2015, Sony Mobile Communications Inc. \*/#include <linux/firmware.h>#include <linux/module.h>#include <linux/slab.h>#include  
<linux/io.h>#include <linux/of\_platform.h>#include <lin  
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<linux/spinlock.h>#include <linux/module.h>#include <linux/
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Copyright (C) 2011, 2012 Cavium, Inc. */#ifndef __LINUX_MDIO_MUX_H#define __LINUX_MDIO_MUX_H#include <linux/device.h>#include
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Copyright (c) 2007 Patrick Boettcher <pb@linuxtv.org> */
Copyright (C) Alexander Popov, Promcontroller 2014 * * Written by Piotr Ziecik <kosmo@semihalf.com>. Hardware description * (defines, structures and
comments) was taken from MPC5121 DMA driver * w
Copyright (C) 2002-2003 Patrick Mochel <mochel@osdl.org>
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<linux/module.h>#include <linux/mm.h>#include <linux/init.h>#include <lin
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<linux/backing-dev.h>#include <linux/init.h>#incl
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<linux/device.h>#include <linux/highmem.h>#include
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VI) */
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<linux/netfilter_ipv4/ip_tables.h>#include <linux/slab.h>#include <net/ip.h>
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Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/sysfs.h>#include <linux/workqueue.h>#include  
<linux/module.h>#include <linux/pci.h>#include "include/u  
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Copyright (C) 2004-2013 Texas Instruments \*/#include <linux/kernel.h>#include <linux/export.h>#include <linux/err.h>#include  
<linux/device.h>#include <linux/module.h>#include <linux/slab.h>#in  
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Copyright (C) 2002 Patrick Mansfield \* \* The general scanning/probing algorithm is as follows, exceptions are \* made to it depending on device specific  
flags, compilation options, and \* global var  
Copyright (c) 2003 Spirent Communications \* and Claus Gindhart <claus.gindhart@kontron.com> \* \* This module has only been tested with the  
MAX6650 chip. It should \* also work with the MAX6651. It d  
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Copyright (C) 2004-2009 EMS Dr. Thomas Wuensche \*/#include <linux/signal.h>#include <linux/slab.h>#include <linux/module.h>#include

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<linux/netdevice.h>#include <linux/usb.h>
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Copyright (c) 2006 Willem Duinker * v4l2 interface modeled after the V4L2 driver * for SN9C10x PC Camera Controllers */
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aside from hashing them and \* parking tasks  
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hardware monitoring features: voltage, fan and temperatu  
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<linux/amba/bus.h>#include <linux/io.h>#include <  
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<linux/acpi.h>#include <linux/pci.h>#include <linux/pnp.h>  
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Copyright (c) 2016-2018 Christoph Hellwig. \*/#include <linux/module.h>#include <linux/compiler.h>#include <linux/fs.h>#include  
<linux/iomap.h>#include "trace.h"  
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Hunter Wu. \* Power Management support by Dave Liu <dave  
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<linux/idr.h>#include <linux/cb710.h>#include <linux/gfp.h>
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VXGE_CONFIG_H#define VXGE_CONFIG_H#include <linux/hardirq.h>#include <linux

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1995 \* Directory entry file type support and  
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MIPS_is_fake_mcount#undef mcount_adjust#undef sift_rel_
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16) \ | ((IEC958_AES3_CON_FS_48000) << 24))
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Copyright (c) 2016 BayLibre, SAS. \* Author: Neil Armstrong <narmstrong@baylibre.com> \*  
Copyright (C) 2016 Synopsys Inc. \*/#include <linux/delay.h>#include <linux/err.h>#include <linux/errno.h>#include <linux/i2c.h>#include  
<linux/interrupt.h>#include <linux/io.h>#include <linux/  
Copyright (C) 2004-2007 Vladimir Lebedev <vladimir.p.lebedev@intel.com>  
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 Copyright (C) 2013-2014 Jens Axboe  
 (C) Copyright 2001, 2002 Rusty Russell IBM Corporation#include <linux/init.h>#include <linux/stringify.h>#include <linux/kernel.h>  
 Copyright (C) 2008 Steven Toth <stoth@linuxtv.org>  
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 (1<<7)  
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 Copyright (C) 2005-2006 Intel Corporation \* Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com> \* \* This contains most of the internal API for  
 UWB. This is stuff used \* across the stack that of  
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 Copyright (c) 2014 JD Cole <jd.cole@plantronics.com>  
 Copyright (C) 2012 Samsung Electronics \* Author: Donggeun Kim <dg77.kim@samsung.com> \* Author: MyungJoo Ham  
 <myungjoo.ham@samsung.com> \* \* based on android/drivers/switch/switch\_class.c  
 Copyright (C) 2014 Intel Corporation \* \* Adjustable fractional divider clock implementation. \* Output rate = (m / n) \* parent\_rate. \* Uses rational best  
 approximation algorithm.  
 Copyright (C) 2001-2002 Greg Kroah-Hartman <greg@kroah.com> \*/#include <linux/kernel.h>#include <linux/errno.h>#include  
 <linux/slab.h>#include <linux/tty.h>#include <linux/tty\_driver.h>#include  
 Copyright (c) 2017 Aaron Bottegal \*/  
 (C) Rusty Russell, IBM 2002 \* \* Generalized futexes, futex requeueing, misc fixes by Ingo Molnar  
 Copyright (c) 2006 Nokia Corporation \* Various changes: Imre Deak <imre.deak@nokia.com> \* \* Using code from:  
 Copyright (C) IBM Corporation, 2006 \* \* Original author, Mel Gorman \* Major cleanups and reduction of bit operations, Andy Whitcroft  
 Copyright who own it \*/#include "rtlib.h"  
 Copyright (c) 2006-2009 Vmware, Inc., Palo Alto, CA., USA  
 Copyright (C) 1994 Wolfgang Stanglmeier \* \* Other major contributions: \*  
 Copyright (C) 2004 MontaVista Software - IPMI Powerdown via sys\_reboot(n");  
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 Copyright (c) 2012 Guenter Roeck <linux@roeck-us.net> \* \* based on max1668.c  
 Copyright (c) by Jaroslav Kysela <perex@perex.cz>, \* Takashi Iwai <tiwai@suse.de> \* Creative Labs, Inc. \* Routines for  
 control of EMU10K1 chips / mixer routines  
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 Copyright (C) 2003 Kevin Thayer <nufan\_wfk@yahoo.com> \* \* Dual output support:

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copyright text 2001jul05 change message format to improve OVERRUN case  
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Copyright (C) 2010 Linus Walleij \* \* MMC host class device management  
Copyright (c) 2017 SKIDATA AG \* \* This work is based on the USB3503 driver by Dongjin Kim and \* a not-accepted patch by Fabien Lahoudere, see:  
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driver are based. Indirect \* thanks also to Maxim  
Copyright (C) 2016-2017 HiSilicon Co., Ltd. http://www.hisilicon.com \* \* Authors: Jianguo Sun <sunjianguo1@huawei.com>  
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<michael@amarulasolutions.com>, \*  
Copyright (C) 2011-2012 ST-Ericsson SA \* Written on behalf of Linaro for ST-Ericsson \* Based on bits of regulator core, gpio core and clk core \* \*  
Author: Linus Walleij <linus.walleij@linaro.org>  
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<linux/slab.h>#include <linux/stddef.h>#include <linux/kernel.h>#in  
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Copyright (c) 2013, Sony Mobile Communications AB.  
Copyright (c) 2019, Linaro Ltd \*/#include <dt-bindings/power/qcom-aoss-qmp.h>#include <linux/clk-provider.h>#include <linux/interrupt.h>#include  
<linux/io.h>#include <linux/mailbox\_client.h>#in  
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Copyright(c) 2016 Intel Deutschland GmbH  
Copyright 2000,2001 Dell Inc. \*\*\*\*\*/  
Copyright (c) 2010-2012 Mauro Carvalho Chehab \* Driver modified by in order to work with upstream drxx driver, and \* tons of bugs got fixed,  
and converted to use dvb-usb-v2. \*/  
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Copyright (c) 2019 Advantech \* Author: Amy.Shih <amy.shih@advantech.com.tw> \*/  
Copyright (c) 2013 Trond Myklebust <Trond.Myklebust@netapp.com> \*/#include <linux/nfs\_fs.h>#include "nfs4\_fs.h"#include "internal.h"#include  
"nfs4session.h"#include "callback.h"#include "pnfs.h  
Copyright (c) 2014, Applied Micro Circuits Corporation \* Authors: Iyappan Subramanian <isubramanian@apm.com> \* Ravi Patel <rapatel@apm.com>  
\* Keyur Chudgar <kchudgar@apm.com> \*/  
Copyright (c) 2012 Samsung Electronics Co., Ltd. \* http://www.samsung.com/ \*/\* start node id of a node block dedicated to the given node id  
\*/#define START\_NID(nid) (((nid) / NAT\_ENTR  
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\* Author: Rafael J. Wysocki <rafael.j.wysocki@intel.c  
Copyright (C) 1991, 1992 Linus Torvalds \* \* Big-endian to little-endian byte-swapping/bitmaps by \* David S. Miller (davem@caip.rutgers.edu),  
1995  
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Copyright (C) 2012,2013 - ARM Ltd \* Author: Marc Zyngier <marc.zyngier@arm.com> \* \* Derived from arch/arm/kvm/guest.c:  
Copyright (c) by David Shust, dshust@shuststring.com. This chip has nonstandard and extended behaviour with regard to its S/PDIF output. The  
AC'97 spec states that the S/PDIF signal is to be out  
Copyright (C) 2003 Vadim Catana, skystar@moldova.cc \* \* Acknowledgements:  
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Copyright (C) 2018 Pengutronix, Sascha Hauer <s.hauer@pengutronix.de> \*/  
Copyright (C) 2005 Chuck Lever <cel@netapp.com> \* \* RPC client per-operation statistics provide latency and retry \* information about each type  
of RPC procedure in a given RPC program. \* These  
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 Copyright (c) Clemens Ladisch <clemens@ladisch.de> \* \* based on btaudio.c by Gerd Knorr <kxrel@bytesex.org>  
 Copyright (C) 2015, 2018 \* Author: Matt Ranostay <matt.ranostay@konsulko.com> \* \* TODO: gesture + proximity calib offsets  
 (C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handling of directories  
 Copyright (c) 2011 Florian Westphal <fw@strlen.de> \* \* based on fib\_frontend.c; Author: Alexey Kuznetsov, <kuznet@ms2.inr.ac.ru>  
 Copyright (C) Fuzhou Rockchip Electronics Co., Ltd. \* Author: Andy Yan <andy.yan@rock-chips.com> \* Yakir Yang <ykk@rock-chips.com> \*  
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 COPYRIGHT; switch (bits case 0x10: diga->status[0] |= IEC958\_AES0\_CON\_EMPHASIS\_NONE; break; case 0x00: diga-  
 >status[0] |= IEC958\_AES0\_CON\_EMPHASIS\_5015; break; } if (!(bits & 0x80)) diga-  
 Copyright (C) 1995, 1996 Olaf Kirch <okir@monad.swb.de>  
 Copyright (C) 2010, 2013 Intel Corporation \* Author: Zhao Yakui <yakui.zhao@intel.com> \* Lv Zheng <lv.zheng@intel.com> \*/  
 Copyright (C) 2010 Hans Verkuil <hverkuil@xs4all.nl>  
 Copyright (C) 2010 Tomohiro Kusumi <kusumi.tomohiro@jp.fujitsu.com> \*/#include <linux/kernel.h>#include <linux/trace\_seq.h>#include  
 <asm/unaligned.h>#include <trace/events/scsi.h>  
 Copyright (C) 2018 Vasily Khoruzhick <anarsoul@gmail.com> \* \* Based on sun8i-codec-analog.c  
 Copyright 2011 Intel Corporation; author Matt Fleming \*  
 Copyright (c) 2015 Qualcomm Atheros, Inc. \*/  
 Copyright (C) Frederic Rible F1OAT (frible@teaser.fr) \*/#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include  
 <linux/in.h>#include <linux/kernel.h>#include <linux/ti  
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 Copyright (C) 2013, LG Electronics, Kyungsik Lee <kyungsik.lee@lge.com>  
 Copyright (C) International Business Machines Corp., 2000-2001 \*/#ifndef \_H\_JFS\_INODE#define \_H\_JFS\_INODE  
 Copyright (C) 2006 Applied Data Systems \*/#include <linux/module.h>#include <linux/init.h>#include <linux/dmaengine.h>#include  
 <linux/slab.h>#include <sound/pcm.h>#include <sound/pcm\_params.h>  
 Copyright 2016 Jonathan Cameron <jic23@kernel.org> \* \* Based on a mashup of the hrtimer trigger and continuous sampling proposal of \* Gregor  
 Boirie <gregor.boirie@parrot.com>  
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 Copyright (C) 1995-2000 Simon G. Vogl \* \* With some changes from Frodo Looijaard <frodol@dds.nl>, Kyösti Mälkki \* <kmalkki@cc.hut.fi> and Jean  
 Delvare <jdelvare@suse.de>  
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 Copyright (C) 2006 IBM Corporation \* \* Author: Serge Hallyn <serue@us.ibm.com>  
 Copyright (C) 1991, 1992, 1999 Linus Torvalds \*/  
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 Copyright (C) 2010-2011 Texas Instruments, Inc. \*/  
 Copyright (C) 2009 Red Hat, Inc. \* Author: Michael S. Tsirkin <mst@redhat.com> \* \* Since the driver does not declare any device ids, you must allocate  
 \* id and bind the device to the driver yourself  
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 Copyright (c) 2008 Jonathan Cameron \*/#include <linux/irq.h>#include <linux/module.h>#include <linux/atomic.h>  
 Copyright(C) 2007, Red Hat, Inc., Ingo Molnar \*/#include "sched.h"

```

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Copyright (C) 2015, IBM Corporation * Author: Laurent Dufour <ldufour@linux.vnet.ibm.com> * #ifndef _LINUX_MM_ARCH_HOOKS_H #define
_LINUX_MM_ARCH_HOOKS_H
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Copyright (c) 2011 David George <david.george@ska.ac.za> */
Copyright (c) 2018-2019, Vladimir Oltean <olteanv@gmail.com> */ #include <linux/packing.h> #include <linux/module.h> #include
<linux/bitops.h> #include <linux/errno.h> #include <linux/types.h>
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Copyright (C) 2008 <srinivasa.deevi at conexant dot com> * Based on em28xx driver */
Copyright (C) 2017 Pengutronix, Juergen Borleis <kernel@pengutronix.de> */ #include <linux/kernel.h> #include <linux/module.h> #include
<linux/gpio/consumer.h> #include <linux/regmap.h> #include <lin
Copyright (C) 2006 Red Hat, Inc. All rights reserved
Copyright (c) 2011, Tom Herbert <therbert@google.com> */ #include <linux/types.h> #include <linux/kernel.h> #include <linux/jiffies.h> #include
<linux/dynamic_queue_limits.h> #include <linux/compiler
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Copyright (C) 2016 Eduardo Valentin <edubezval@gmail.com> * * Highly based on original thermal_core.c
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Copyright (C) 2014 Elan Microelectronics Corporation. * Scott Liu <scott.liu@emc.com.tw> * * This code is partly based on hid-multitouch.c: *
Copyright (C) 2002 Trond Myklebust <trond.myklebust@fys.uio.no> */
Copyright (C) 2011-2012 Stephane Grosjean <s.grosjean@peak-system.com> */ #include <linux/netdevice.h> #include <linux/usb.h> #include
<linux/module.h>
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Copyright (c) 2009 Trond Myklebust <Trond.Myklebust@netapp.com> * * Resolves DNS hostnames into valid ip addresses
Copyright (C) 2016 Cadence Design Systems - http://www.cadence.com * * Authors: Rafal Ozieblo <rafalo@cadence.com> * Bartosz Folta
<bfolta@cadence.com>
Copyright (C) 2016-2020 B.A.T.M.A.N. contributors: * * Matthias Schiffer
Copyright 2012 Sony Corporation
Copyright (c) Dmitry Torokhov <dtor@mail.ru> */
Copyright (C) 2010-17 Digital Devices GmbH\n"); stat = ddb_init_ddbridge(); if (stat < 0) return stat; stat =
pci_register_driver(&ddb_pci_driver); if (stat < 0) ddb_exit_ddbridge(0, stat);

```

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 Copyright (C) 2011-2012 Mike Turquette, Linaro Ltd <mturquette@linaro.org> \* \* Gated clock implementation  
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 Copyright (C) 2010 Jean-Francois Moine (http://moinejf.free.fr) \* \*Notes: \* t613 + tas5130A \* \* Focus to light do not balance well as in win. \*  
 Quality in win is not good, but its kinda better.  
 Copyright (c) 2012, Intel Corporation. \*/#include <linux/device.h>#include <linux/platform\_device.h>#include <linux/module.h>#include  
 <linux/interrupt.h>#include <linux/irq.h>#include <linux/sl  
 Copyright (C) 2007 Patrick McHardy <kaber@trash.net> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include  
 <linux/skbuff.h>#include <linux/ip.h>#include <linux/gfp.h>#i  
 Copyright (C) 2018 Western Digital Corporation \*/#ifndef SCSI\_BSG\_UFS\_H#define SCSI\_BSG\_UFS\_H  
 Copyright (c) 2007 Ilyes Gouta. Based on the m5603x Linux Driver Project.  
 Copyright (C) 2012 Samsung Electronics Co., Ltd(http://www.samsung.com)  
 Copyright 2015-2016 Freescale Semiconductor Inc.  
 Copyright (C) 2010-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: July 10th 2012 \* -Decoupled the driver from arch/arc \* +Using  
 platform\_get\_resource() for irq/membase (thx to bfin\_uart.c  
 Copyright (c) 2019 BayLibre, SAS \* Author: Neil Armstrong <narmstrong@baylibre.com> \*/  
 Copyright (C) 2015-2018 Linaro Ltd. \*/#include <linux/clk.h>#include <linux/completion.h>#include <linux/interrupt.h>#include  
 <linux/iommu.h>#include <linux/mutex.h>#include <linux/of.h>#inclu  
 Copyright 2003,2004 Andi Kleen SuSE Labs \*/#ifndef LINUX\_MEMPOLICY\_H#define LINUX\_MEMPOLICY\_H 1  
 COPYRIGHT) val val |= ((new >> 8) & 0xff) << 4;// category + original switch ((new >> 24) & 0xff) { case  
 IEC958\_AES3\_CON\_FS\_44100: val |= 0<<12; break; case IEC958\_AES3\_CON\_FS\_48000: val  
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 (C) 2003 Ardis Technologies <roman@ardistech.com> \*  
 Copyright (C) 2011 Oliver Endriss <o.endriss@gmx.de> \* \* the project's page is at https://linuxtv.org  
 (C) 2002 David Woodhouse <dwmw2@infradead.org>  
 Copyright (C) 2004-5 Patrick Boettcher (patrick.boettcher@posteo.de) \* \* This code is partially based on the previous dib3000mc.c .  
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 Copyright (C) 1995-99 Simon G. Vogl \* With some changes from Kyösti Mälkki <kmalkki@cc.hut.fi> and even \* Frodo Looijaard <frodol@dds.nl> \*/  
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 Copyright (c) 2013, Applied Micro Circuits Corporation \* Author: Loc Ho <lho@apm.com> \*/#include <linux/module.h>#include  
 <linux/spinlock.h>#include <linux/io.h>#include <linux/of.h>#include <l  
 Copyright (c) 2004 James Courtier-Dutton <James@superbug.demon.co.uk> \* Driver CA0106 chips. e.g. Sound Blaster Audigy LS and Live 24bit \*  
 Version: 0.0.22 \* \* FEATURES currently supported:  
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 Copyright (C) 2004, 2005, 2006 Red Hat, Inc., Ingo Molnar <mingo@redhat.com> \* \* Wait/Die implementation:  
 Copyright 2015-2017 Google, Inc \* \* USB Type-C Port Controller Interface.  
 Copyright (C) 2004 Trond Myklebust \* \* NFSv4 callback procedures  
 Copyright (C) 2009 Bob Copeland (me@bobcopeland.com) \*/#include <linux/interrupt.h>#include <linux/module.h>#include  
 <linux/mod\_devicetable.h>#include <linux/mmc/sdio\_func.h>#include <linux/mmc/  
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 (C) 2002 - 2003 Dominik Brodowski <linux@brodo.de> \*/#ifndef \_LINUX\_CPUFREQ\_H#define \_LINUX\_CPUFREQ\_H  
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 Copyright (C) 1991, 1992 Linus Torvalds \* \* minix directory handling functions  
 Copyright (c) 2005 Misha Zhilin <misha@epiphany.com> \*/  
 Copyright (c) 2005, 2006 Danny van Dyk <kugelfang@gentoo.org>  
 Copyright (C) 2014 ARM Ltd. \*/#include <linux/types.h>  
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 (C) Linux Foundation 2008-2013 \* Unified interface for all slab allocators \*/  
 Copyright (C) 2001-2003 Andreas Gruenbacher, <agruen@suse.de> \*/#ifndef \_\_F2FS\_ACL\_H\_\_#define \_\_F2FS\_ACL\_H\_\_  
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 Copyright (c) 2012 David Dillow <dave@thedillows.org>  
 Copyright (C) 2007 Tomi Orava (tomimo@ncircle.nullnet.fi) \* \* Based on the dvb-usb-framework code and the \* original Terratec Cinergy T2 driver by: \*  
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 Copyright (c) 2012, Intel Corporation. \*/#include <linux/device.h>#include <linux/platform\_device.h>#include <linux/module.h>#include

```
<linux/interrupt.h>#include <linux/irq.h>#include <linux/ke
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Copyright (C) 2011 Hans Verkuil <hverkuil@xs4all.nl> */
Copyright (C) 2008 Texas Instruments, Inc.
Copyright (c) 2008 Richard Lemon
Copyright (c) 1999 Martin Mares <mj@ucw.cz> * * Init/reset quirks for USB host controllers should be in the USB quirks * file, where their drivers can
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Copyright (C) 2008 IBM Corporation * 'find_last_bit' is written by Rusty Russell <rusty@rustcorp.com.au> * (Inspired by David Howell's find_next_bit
implementation) * * Rewritten by Yury Norov <yu
Copyright (C) 2010-2012 Matthias Fuchs <matthias.fuchs@esd.eu>, esd gmbh */#include <linux/signal.h>#include <linux/slab.h>#include
<linux/module.h>#include <linux/netdevice.h>#include <linux/us
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```

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Copyright (C) 2014,2016 Maksim Salau <maksim.salau@gmail.com> \* \* Protocol of the adaptor is described in the application note U19660EJ1V0AN00 \*  
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(C) 1999 Dave Airlie, University of Limerick, Ireland <airlied@linux.ie> \* \* Rewrite of larges part of the code in order to stabilize TCP stuff. \* Fix  
behaviour when socket buffer is full.  
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bma023.c. \* Device behaviour based on a misc driver po  
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<linux/textsearch.h>#include <linux/skbuff.h>#include <linu  
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Copyright (c) 2016 Facebook \*/#include <linux/kernel.h>#include <linux/types.h>#include <linux/slab.h>#include <linux/bpf.h>#include  
<linux/bpf\_perf\_event.h>#include <linux/filter.h>#include <  
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Copyright (C) 1999 - 2004 \* Greg Kroah-Hartman (greg@kroah.com) \* \* See Documentation/usb/usb-serial.rst for more information on using this \*  
driver  
Copyright status#define AC97\_ALC650\_PRE 0x0038 /\* Preemphasis status \*/#define AC97\_ALC650\_PRE\_SHIFT 3#define  
AC97\_ALC650\_MODE 0x00C0 /\* Preemphasis status \*/#define  
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plus single list \* pointer as suggested by Josh Tr  
Copyright (C) Jonathan Naylor G4KLX (g4klx@g4klx.demon.co.uk)  
Copyright (C) 2016 Linaro Ltd \*/#include <linux/module.h>#include <linux/ulpi/driver.h>#include <linux/ulpi/regs.h>#include <linux/clk.h>#include  
<linux/regulator/consumer.h>#include <linux/of\_<br>Copyright (c) 2008, John W. Linville <linville@tuxdriver.com> \* \* Some bits copied from old ieee80211 component, w/ original copyright \* notices  
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<linux/trace.h>#include <linux/sched/rt.h>#include <linux/sche  
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- fil

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Copyright (c) 2004 James Courtier-Dutton <James@superbug.demon.co.uk> * Driver CA0106 chips. e.g. Sound Blaster Audigy LS and Live 24bit *
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```

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compressed memory.
Copyright (c) 2016 Joel Holdsworth * * This driver adds support to the FPGA manager for configuring the SRAM of * Lattice iCE40 FPGAs through slave
SPI.
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<linux/device.h>#include <linux/mutex.h>#include <linux/err.h>#in
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Computer Networking Conference papers. The diagrams have
Copyright (c) 2006 Tejun Heo <teheo@suse.de> */#include <linux/memblock.h> /* for max_pfn */#include <linux/acpi.h>#include <linux/dma-
direct.h>#include <linux/dma-noncoherent.h>#include <linux
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<linux/mutex.h>#include <linux/nvmem-provider.h>#include
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 vm\_area\_struct', July 1994. \* Fixed the shm swap deallocation (s  
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 almost re-written \*/  
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 <linux/errno.h>#include <linux/gpio/driver.h>#include <linux/inte  
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 integrated media GmbH \*  
 Copyright (C) 2014 Amlogic, Inc. \*/#include <linux/err.h>#include <linux/module.h>#include <linux/io.h>#include <linux/platform\_device.h>#include  
 <linux/hw\_random.h>#include <linux/slab.h>#inc  
 Copyright (C) 2007 Red Hat, Inc., Peter Zijlstra \*/#include "sched.h"  
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 Copyright (C) 2010 NEC corporation \*/#include <linux/kernel.h>#include <linux/gfp.h>#include <linux/mm.h>#include <linux/mutex.h>#include  
 "avc.h"#include "services.h"  
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 Copyright 2018-2020 Intel Corporation \*/  
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 <linux/of.h>#include <linux/regmap.h>#include <linux/spi/spi.h>  
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Copyright (c) 2008-2009 Eurotech S.p.A. <info@eurotech.it> \* \* This module supports the PCA954x and PCA954x series of I2C multiplexer/switch \*  
chips made by NXP Semiconductors. \* This includes the  
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<linux/kernel.h>#include <linux/module.h>#include <linux/s  
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(C) 2001 \* by Karsten Merker <merker@linuxtag.org> \* and Maciej W. Rozycki <macro@ds2.pg.gda.pl> \* Later on, I had access to the device's  
documentation (referenced below). \*/  
(C) 2008, 2009 by Marc Kleine-Budde <kernel@pengutronix.de> \* \* Bosch C\_CAN controller is compliant to CAN protocol version 2.0 part A and B. \*  
Bosch C\_CAN user manual can be obtained from:  
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Copyright 2016 NXP */#include <linux/kernel.h>#include <linux/errno.h>#include <linux/fsl/mc.h>#include "dpni.h"#include "dpni-cmd.h"
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upstreaming attempt by:
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Copyright (c) 2016 Facebook */#include <linux/bpf.h>#include <linux/btf.h>#include <linux/jhash.h>#include <linux/filter.h>#include
<linux/rculist_nulls.h>#include <linux/random.h>#include <ua
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Copyright (C) 2015 VanguardiaSur - www.vanguardiasur.com.ar * * Based on the audio support from the tw6869 driver:
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<linux/mutex.h>#include <linux/pm_runtime.h>#incl
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handler receives control and processes the error.
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COPYRIGHT) bits else bits |= 0x08; switch (diga->status[0] & IEC958_AES0_CON_EMPHASIS) {
default:
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COPYRIGHT 0#define DVD_CPM_COPYRIGHTED 1
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Copyright (c) 2006 Michael Buesch <m@bues.ch>
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<linux/kernel.h>#include <linux/cdev.h>#include <linux/ioctl.h>#
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<linux/phy.h>#include <linux/of.h>
Copyright 2007 Intel Corporation <jonathan.lynch@intel.com> */#include <crypto/internal/hash.h>#include <linux/init.h>#include
<linux/module.h>#include <linux/mm.h>#include <linux/types.h>#incl
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<linux/iomap.h>#include <linux/pagemap.h>#include <linux/uio.h>#i
Copyright (C) 2009, 2010, 2011 Amit Shah <amit.shah@redhat.com> */#include <linux/cdev.h>#include <linux/debugfs.h>#include
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Copyright(c) 2002-2010 Exar Corp.\n"); pr_info("Driver version: %s\n", DRV_VERSION);
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COPYRIGHT); snd\_emu10k1x\_ptr\_write(chip, SPCS1, 0, chip->spdif\_bits[1] = SPCS\_CLKACCY\_1000PPM |  
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Copyright 2008 Stuart Bennett  
Copyright (C) 2008 Antoine Jacquet <royale@zerezo.com> \* <http://royale.zerezo.com/dtv5100/> \* \* Inspired by gl861.c and au6610.c drivers  
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[http://gladman.plushost.co.uk/oldsite/cryptography\\_technology/index.php](http://gladman.plushost.co.uk/oldsite/cryptography_technology/index.php) \* See the o  
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 Copyright(c) 2005-2011 by Mauro Carvalho Chehab \* - Added support for a separate Radio tuner \* - Major rework and cleanups at the code \* \* This driver supports many devices and the idea is to let  
 Copyright (C) 2005 Mike Isely <isely@pobox.com> \*/#ifndef \_\_PVRUSB2\_DEVATTR\_H#define \_\_PVRUSB2\_DEVATTR\_H  
 Copyright (c) 2000 by Takashi Iwai <tiwai@suse.de> \* \* Based on nm256\_audio.c OSS driver in linux kernel. \* The original author of OSS nm256 driver wishes to remain anonymous, \* so I just put my a  
 Copyright (C) 1992 Linus Torvalds \* \* Rewritten. Old one was good in 2.2, but in 2.3 it was immoral. --ANK (990903)  
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 Copyright (c) 2015 Tom Herbert <tom@herbertland.com> \*/  
 Copyright (C) 2003 Jozsef Kadlecsek <kadlec@netfilter.org> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include <linux/netfilter\_ipv6/ip6\_tables.h>#include <linux/slab.h>  
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 Copyright (C) 2007-2016 Mauro Carvalho Chehab\n");  
 Copyright (c) by Jaroslav Kysela <perex@perex.cz> \* Creative Labs, Inc. \* Routines for control of EMU10K1 chips / PCM routines  
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 Copyright (c) 2007 Olivier DANET <odanet@caramail.com> \*/  
 Copyright (C) 2016 National Instruments Corp.#include <linux/leds.h>#include <linux/phy.h>#include <linux/phy\_led\_triggers.h>#include <linux/netdevice.h>  
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 (C) 1999-2001 Ben Fennema  
 Copyright (C) 2000 Andrew Henroid  
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 (C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handle opening/closing btree  
 Copyright (c) 2001-2004 Ben Fennema  
 Copyright (c) 2016 Hisilicon.  
 Copyright (C) 2005 Kirk Lapray (kirk.lapray@gmail.com) \* based on nxt2002 by Taylor Jacob <rtjacob@earthlink.net> \* and nxt2004 by Jean-Francois Thibert (jeanfrancois@sagetv.com)\*/  
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Copyright 2009 Johannes Berg <johannes@sipsolutions.net> \* \* If you ever run into a situation in which you have a SW\_ type rkill \* input device, then  
you can revive code that was removed in the pa  
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Copyright 2015 Baylibre SAS.  
Copyright (c) 2008 Erik Andr  
Copyright(c) 2002-2010 Exar Corp.\n"); DBG\_PRINT(ERR\_DBG, "%s: Neterion %s (rev %d)\n", dev->name, sp->product\_name, pdev-  
>revision); DBG\_PRINT(ERR\_DBG, "%s: Driver version %s\n", dev->name,  
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Copyright (C) 1992 Rick Sladkey \* \* structure passed from user-space to kernel-space during an nfs mount  
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the ov511 driver.  
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Copyright (C) 2006 Juerg Haefliger <juergh@gmail.com> \* \* This driver is based on the driver for kernel 2.4 by Mark D. Stuebaker \* and its port to  
kernel 2.6 by Lars Ekman.  
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Copyright (C) 2013 Obsidian Research Corp \* Jason Gunthorpe <jgunthorpe@obsidianresearch.com> \* \* Device file system interface to the TPM  
Copyright (C) 2016 Pengutronix, Philipp Zabel <p.zabel@pengutronix.de> \*  
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 Copyright (C) 2010 Nokia Corporation \* Author: Matti J. Aaltonen <matti.j.aaltonen@nokia.com> \*/  
 Copyright 2002-2003, Stephen Frost, 2.5.x port by laforge@netfilter.org \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include  
 <linux/init.h>#include <linux/ip.h>#include <linux/ipv6.h>#include <  
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 Copyright (c) 2009, Intel Corporation.  
 Copyright (C) 2015 Cavium Inc. \* Author: Ganapatrao Kulkarni <gkulkarni@cavium.com> \*/  
 Copyright (c) 2013, Sony Mobile Communications AB. \*/#ifndef \_\_PINCTRL\_MSM\_H\_\_#define \_\_PINCTRL\_MSM\_H\_\_  
 Copyright (C) 2009 Sergey Tyurin <forum.free-x.de> Updated 2012 by Jannis Achstetter <jannis\_achstetter@web.de>  
 Copyright (c) 2008 Alexey Klimov <klimov.linux@gmail.com> \*/  
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 Copyright (c) 2015 Linaro Ltd. \* www.linaro.org \*/  
 Copyright © CC Computer Consultants GmbH, 2007 - 2008 \*/#include <linux/module.h>#include <linux/skbuff.h>#include <linux/file.h>#include  
 <linux/cred.h>  
 Copyright (c) 2006-2007 MontaVista Software, Inc. \*/#include <linux/export.h>#include <linux/mii.h>#include <linux/phy.h>#include  
 <linux/phy\_fixed.h>  
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 Copyright 2009 Larry Finger <Larry.Finger@lwfinger.net> \*\* Based on the LED handling in the r8187 driver, which is:  
 Copyright (C) 2016, Linaro Ltd. \* Author: Daniel Lezcano <daniel.lezcano@linaro.org> \* Fu Wei <fu.wei@linaro.org> \* Hanjun Guo  
 <hanjun.guo@linaro.org> \*/  
 Copyright (c) 2008 Jonathan Cameron  
 Copyright 2005-2006 Openedhand Ltd.// Author: Richard Purdie <rurpurdie@openedhand.com>  
 copyright information \*/#include <media/tuner.h>#include "flexcop.h"#include "mt312.h"#include "stv0299.h"#include "s5h1420.h"#include  
 "itd1000.h"#include "cx24113.h"#include "cx24123.h"#inc  
 Copyright 2018 Noralf Tr  
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 Copyright (c) 2008, Swedish Institute of Computer Science. \* All rights reserved  
 Copyright (C) 2001 Stefan Gmeiner <riddlebox@freesurf.ch> \* start merging tconfig and gpm code to a xfree-input module \* adding some  
 changes and extensions (ex. 3rd and 4th button) \*  
 Copyright (c) 2017-2019 Pensando Systems, Inc. All rights reserved  
 Copyright 2012, Fabio Baltieri <fabio.baltieri@gmail.com> \*\* Based on ledtrig-timer.c by Richard Purdie <rurpurdie@openedhand.com>  
 Copyright (C) 2004-2005 Richard Purdie \* - omap\_ts.[hc], ads7846.h, ts\_osk.c  
 Copyright (C) 2003 Open Source Development Lab \*\* The driver model core calls device\_pm\_add() when a device is registered. \* This will initialize the  
 embedded device\_pm\_info object in the device  
 copyright 1997-2001 by Donald Becker.  
 Copyright (C) 2009 Eric Paris <Red Hat Inc> \* notify was largely rewritten to make use of the fsnotify infrastructure \*/  
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Copyright IBM Corporation, 2008 \* \* Authors: Dipankar Sarma <dipankar@in.ibm.com> \* Manfred Spraul <manfred@colorfullife.com> \* Paul E. McKenney <paulmck@linux.ibm.com> Hierarchical version  
Copyright (C) 2018-2019 Intel Corporation \*/#include <linux/kernel.h>#include <linux/slab.h>#include <linux/module.h>#include <linux/netdevice.h>#include <linux/wireless.h>#include <linux/nl802  
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(C) 1999-2004 Ben Fennema  
Copyright (C) 2016 Martin Sperl <kernel@martin.sperl.org> \* \* Based on 8250\_lpc18xx.c:  
Copyright (c) Andrew Tridgell 1997-1998. \* Modified by Steve French (sfrench@us.ibm.com) 2002  
(C) 2000 David Brown <usb-storage@davidb.org> \* \* This driver was developed with information provided in FREECOM's USB \* Programmers Reference Guide. For further information contact Freecom \* (ht  
Copyright (C) 2008 Intel Corp. \* \* Author: Sarah Sharp \* Some code borrowed from the Linux EHCI driver.  
Copyright (C) 2000-2001 Ralph Metzler <ralph@convergence.de>  
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Copyright (C) 2003 \* Ethan Benson <erbenson@alaska.net> \* partially derived from linux/fs/ext2/ioctl.c  
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Copyright (C) 2003 Red Hat, Inc., James Morris <jmorris@redhat.com> \*/#include <linux/types.h>#include <linux/stddef.h>#include <linux/kernel.h>#include <linux/slab.h>#include <linux/fs.h>#incl  
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Copyright (C) 1991, 1992, 1993, 1994 Linus Torvalds \* \* zoned VM statistics  
Copyright (C) 2010 Maxim Levitsky <maximlevitsky@gmail.com> \*/#include <linux/spinlock.h>  
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Copyright (c) 2006-2010 Patrick McHardy <kaber@trash.net> \*/#include <linux/module.h>#include <linux/moduleparam.h>#include <linux/types.h>#include <linux/kernel.h>#include <linux/in.h>#include  
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Copyright (C) 2008 Mercury IMC Ltd \* Written by Mark Jackson <mpfj@mimc.co.uk> \* \* NOTE: Currently this driver only supports the bare minimum for

```

read * and write the RTC. The extra features provi
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is take from Gerd Hoffmann <kxaxel@redhat.com>'s *
Copyright (c) 2009 Wolfgang Grandegger <wg@grandegger.com>
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5836 * * For more information, please consult
Copyright (C) 2003-2004 Robert Schwebel, Benedikt Spranger
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(C) 2007 Sukadev Bhattiprolu <sukadev@us.ibm.com>, IBM * Many thanks to Oleg Nesterov for comments and help *
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Copyright abandoned, Michael Chastain, <mailto:mec@shout.net>. * This is a C version of syncdep.pl by Werner Almesberger. *
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Copyright (C) 2018 Texas Instruments Incorporated - http://www.ti.com/ */
Copyright (c) 2013 Hisilicon Limited. */#include <linux/sched.h>#include <linux/device.h>#include <linux/dma-mapping.h>#include
<linux/dmapool.h>#include <linux/dmaengine.h>#include <linux/init
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Copyright (C) 2006-2008 Steven Toth <stoth@hauppauge.com>
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-----
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<linux/uaccess.h>#include <asm/kvm_mmu.h>#include <asm/c
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Copyright (c) 2015-2016, Linaro Limited */#include <linux/device.h>#include <linux/dma-buf.h>#include <linux/fdtable.h>#include
<linux/idr.h>#include <linux/sched.h>#include <linux/slab.h>#inc
Copyright (C) 2008 Sebastian Haas <haas@ems-wuensche.com> */
Copyright (C) 2019 HUAWEI, Inc. * http://www.huawei.com/ * Created by Gao Xiang <gaoxiang25@huawei.com> */#include
"compress.h" #include <linux/module.h>#include <linux/lz4.h>
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Copyright (C) 2017 Linaro Ltd. \*/#include <linux/clk.h>#include <linux/module.h>#include <linux/mod\_devicetable.h>#include  
<linux/platform\_device.h>#include <linux/pm\_runtime.h>#include <linux/  
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Copyright (C) 2008 Paul Hays <Paul.Hays@cattail.ca>  
Copyright (c) 2001 La Monte H.P. Yarroll \* \* These are the definitions needed for the sctp\_ulpevent type. The \* sctp\_ulpevent type is used to carry  
information from the state machine \* upwards to  
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ethernet, so:  
Copyright (c) 2019 Andreas Klinger <ak@it-klinger.de> \* \* For details about the device see:  
Copyright (C) 2013 Eduardo Valentin <eduardo.valentin@ti.com> \*/#include <linux/hwmon.h>#include <linux/thermal.h>#include  
<linux/slab.h>#include <linux/err.h>#include "thermal\_hwmon.h"  
Copyright (C) 2019 NVIDIA Corporation. \* \* Author: Vidya Sagar <vidyas@nvidia.com>  
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Garzik and Anton Vorontsov  
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Copyright (C) 2005 Hans Verkuil <hverkuil@xs4all.nl> \* - Cleanup \* - V4L2 API update \* - sound fixes \*/  
Copyright (C) 2005 Dirk Behme \*/#include <linux/types.h>#include <linux/hwmon.h>#include <linux/err.h>#include <linux/sched.h>#include  
<linux/delay.h>#include <linux/input.h>#include <linux/in  
Copyright (c) 2001-2002 Intel Corp. \* \* This file is part of the SCTP kernel implementation  
Copyright (c) 2005, Bull S.A. \* Written by Pierre Peiffer <pierre.peiffer@bull.net> \*/  
Copyright (c) 2016, Intel Corporation \* Authors: Salvatore Benedetto <salvatore.benedetto@intel.com> \*/#include <linux/kernel.h>#include  
<linux/export.h>#include <linux/err.h>#include <linux/str  
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Copyright (C) Amit Shah <amit.shah@redhat.com>, 2009, 2010, 2011 \*/#ifndef \_\_UAPI\_LINUX\_VIRTIO\_CONSOLE\_H#define  
\_\_UAPI\_LINUX\_VIRTIO\_CONSOLE\_H#include <linux/types.h>#include <linux/virtio\_types.h>  
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Copyright (C) ST-Ericsson SA 2011 \* Author: Lee Jones <lee.jones@linaro.org> for ST-Ericsson. \*/#ifndef \_\_SOC\_BUS\_H#define \_\_SOC\_BUS\_H  
Copyright (C) 2011 by Jarod Wilson <jarod@redhat.com> \*/#include <linux/module.h>



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(C) 2007-2012 Patrick McHardy <kaber@trash.net> \*/#include <linux/module.h>#include <linux/gfp.h>#include <linux/types.h>#include  
<linux/skbuff.h>#include <linux/tcp.h>#include <linux/udp.h>#i  
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Copyright (c) by Jaroslav Kysela <perex@perex.cz> \* \* Routines for control of MPU-401 in UART mode \* \* MPU-401 supports UART mode which is not  
capable generate transmit \* interrupts thus output i  
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Copyright (C) 2005-2006 Intel Corporation \* Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com> \* \* This driver will prepare the i1480 device to  
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Biederman <ebiederm@xmission.com>  
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\*  
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for following ADC chips from Microchip Technology's:  
Copyright (C) 2009 Red Hat, Inc., Ingo Molnar <mingo@redhat.com> \*\* Implements 3 trace clock variants, with differing scalability/precision \*  
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driver  
Copyright (C) 2007 Antonino Daplas <adaplas@pol.net>  
Copyright (C) 2009 Eric Paris <Red Hat Inc> \* dnotify was largely rewritten to use the new fsnotify infrastructure \*/#include <linux/fs.h>#include  
<linux/module.h>#include <linux/sched.h>#include  
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<linux/kernel.h>#include <linux/kprobes.h>#include <linux/extable.h>  
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Copyright (C) 2004-2008 Jean Delvare <jdelvare@suse.de> \* Based on the lm90 driver. \* \* The LM63 is a sensor chip made by National  
Semiconductor. It measures \* two temperatures (its own and one e  
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<linux/moduleparam.h>#include <linux/io.h>#include <s  
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(C) (diagram), 9.2 \* Upon reception of the SHUTDOWN COMPLETE chunk the endpoint will verify \* that it is in SHUTDOWN-ACK-SENT state, if it is not  
the chunk should be \* discarded. If the endpoint is  
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Copyright message. \* 0.0.19 \* Finally fix support for SB Live 24 bit. SB0410 and SB0413. \* The output codec needs resetting, otherwise all output is  
muted. \* 0.0.20 \* Merge "pci\_disabl  
Copyright (c) 2006 Jiri Benc <jbenc@suse.cz>  
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Copyright (C) 2011 Texas Instruments, Inc. \* \* This module exposes the interface to kernel space for specifying \* per-device PM QoS dependencies. It  
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Copyright (c) 2014 Samsung Electronics Co., Ltd. \* Author: Thomas Abraham <thomas.ab@samsung.com> \* \* A CPU clock is defined as a clock  
supplied to a CPU or a group of CPUs. \* The CPU clock is typ  
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Copyright (c) Ahmet \* \* Major (almost complete) rewrite:  
Copyright (c) by James Courtier-Dutton <James@superbug.co.uk> \* Added support for Audigy 2 Value. \* Added EMU 1010 support. \*  
General bug fixes and enhancements. \* \* BUGS: \* --  
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Copyright : %s\n", (status snd_iprintf(buffer, "Emphasis : %s\n", emphasis[(status & SPCS_EMPHASISMASK) >> 3]);
snd_iprintf(buffer, "Mode : %i\n", (status
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<linux/module.h>#include <linux/skbuff.h>#include <linu
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<linux/module.h>#include <linux/skbuff.h>#include <linux
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<linux/module.h>#include <linux/mutex.h>#include <linux/rcupdate.
Copyright (C) 2013 Chanhon Min <chanho.min@lge.com> * * The functions in this file aren't called directly, but are required by * GCC builtins such as

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__builtin_ctz, and therefore they can't be remov
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<linux/vmcache.h>#include <asm/pgtable.h>
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<linux/in.h>#include <linux/kernel.h>#include <linux/tim
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<linux/platform_device.h>#include <linux/videodev2.h
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<linux/kthread.h>#include <linux/scatterlist.h>#  
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<linux/kernel.h>#include <linux/iio/iio.h>#include <linux/interrupt  
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devices around the host kernel. Posted \* Interr  
COPYRIGHT); new new |= ((ucontrol->value.iec958.status[3] & IEC958\_AES3\_CON\_FS) << 24); if ((new &  
IEC958\_AES0\_CON\_EMPHASIS) == IEC958\_AES0\_CON\_EMPHASIS\_5015) val |= 1<<3;  
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"hantro\_jpeg.h"#include "hantro.h"  
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 Copyright (C) 2018 Advanced Micro Devices, Inc. \* Authors:  
 Copyright (C) 2012 Jamie Lentin \*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/delay.h>#include <linux/platform\_device.h>#include <linux/gpio/consumer.h>#include <linux/of\_pl  
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 Copyright 2007-2008 Pierre Ossman \*  
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 Copyright (c) 2006-2008 Nick Kossifidis <mickflemm@gmail.com>  
 Copyright (c) 2006-2008 Dominik Kuhlen <dkuhlen@gmx.net> \* \* TT connect S2-3650-CI Common Interface support, MAC readout  
 Copyright (c) 2008-2009 Michael Buesch <m@bues.ch>  
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 Copyright (c) 2014 Anna Schumaker <Anna.Schumaker@Netapp.com> \*/#ifndef \_\_LINUX\_FS\_NFS\_NFS4\_2XDR\_H#define  
 \_\_LINUX\_FS\_NFS\_NFS4\_2XDR\_H  
 Copyright (C) 2013 Xilinx, Inc. \*/#include <linux/firmware.h>#include <linux/fpga/fpga-mgr.h>#include <linux/idr.h>#include <linux/module.h>#include <linux/of.h>#include <linux/mutex.h>#include  
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 Copyright (c) 2007-2010 The Khronos Group Inc.  
 Copyright (c) 2008 Intel Corporation \* Author: Matthew Wilcox <willy@linux.intel.com> \* \* Please see kernel/locking/semaphore.c for documentation of these functions  
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 Copyright (C) 2008 Igor M. Liplianin <liplianin@me.by> \*/  
 Copyright (C) 2019 Intel Corporation \* Author: Luis Carlos Cobo <luisca@cozybit.com> \*/#include <linux/gfp.h>#include <linux/kernel.h>#include <linux/random.h>#include <linux/rculist.h>  
 Copyright (C) 2013 Linaro Limited \* Author: AKASHI Takahiro <takahiro.akashi@linaro.org> \*/#ifndef \_\_ASM\_FTRACE\_H#define \_\_ASM\_FTRACE\_H  
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 Copyright (C) 2004-2008, LINBIT Information Technologies GmbH.  
 Copyright (C) 2018 Stanislaw Gruszka <stf\_xl@wp.pl> \*/#ifndef \_\_MT76X0\_PHY\_H#define \_\_MT76X0\_PHY\_H  
 Copyright (C) 1992, 1998-2004 Linus Torvalds, Ingo Molnar \* \* This file contains the /proc/irq/ handling code.  
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Copyright (c) 2002, 2003, 2004, 2005, 2006, 2007, 2008 \* Phillip Lougher <phillip@squashfs.org.uk> \* \* symlink.c  
(C) Peter T. Breuer (ptb@ot.uc3m.es) 2003 \* \* bitmap\_create - sets up the bitmap structure \* bitmap\_destroy - destroys the bitmap structure  
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Copyright (c) 1999-2005 Joerg Heckenbach <joerg@heckenbach-aw.de> \* \* This module is part of usbvision driver project. \* Updates to driver  
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Copyright 2010, 2011 David Jander <david@protonic.nl> \*/  
Copyright (C) 2010 OMICRON electronics GmbH \*/#include <linux/module.h>#include <linux/posix-clock.h>#include <linux/poll.h>#include  
<linux/sched.h>#include <linux/slab.h>#include <linux/timeke  
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[http://qanu.de/specs/terratec\\_cinergyT2.pdf](http://qanu.de/specs/terratec_cinergyT2.pdf)  
(C) 2003-2006 Netfilter Core Team <coreteam@netfilter.org>  
(C) 2010-2011 Thomas Graf <tgraf@redhat.com>  
Copyright (C) 2014 ARM Ltd. \*/#include <asm/arch\_timer.h>#include <asm/cache.h>#include <asm/cpu.h>#include <asm/cputype.h>#include  
<asm/cpufeature.h>#include <asm/fpsimd.h>  
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copyright ;- ) \*/#include <linux/etherdevice.h>  
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Copyright (c) 2012 Bryan Schumaker <bjschuma@netapp.com> \*/#include <linux/init.h>#include <linux/module.h>#include <linux/mount.h>#include  
<linux/nfs4\_mount.h>#include <linux/nfs\_fs.h>#include  
Copyright (C) 2018 Song Qiang <songqiang1304521@gmail.com> \* \* Datasheet available at \*  
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 Copyright (c) 2014, The Chromium OS Authors. All rights reserved  
 Copyright (c) 2016 Tom Haynes <loghyc@primarydata.com> \*/#include <linux/sunrpc/svc.h>#include <linux/nfs4.h>  
 Copyright (c) 2007, 2010 George Joseph <george.joseph@fairview5.com> \*/  
 (C) 1998 Red Hat \* TCP NFS related read + write fixes  
 Copyright (C) International Business Machines Corp., 2000-2002 \*/  
 Copyright (c) 2002-2003, Jouni Malinen <jkmaline@cc.hut.fi> \* \* Adaption to a generic IEEE 802.11 stack by James Ketrenos \*  
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 Copyright (c) 2003 Margit Schubert-While <margitsw@t-online.de> \* \* derived in part from w831785ts.c:  
 Copyright (C) 1999 - 2002 Greg Kroah-Hartman (greg@kroah.com) \*/  
 Copyright (c) 1999-2000 Takashi Iwai <tiwai@suse.de> \*/  
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 Copyright 2013-2014 Red Hat Inc.  
 (C) 2013 Viresh Kumar <viresh.kumar@linaro.org> \* \* Oct 2005 - Ashok Raj <ashok.raj@intel.com> \* Added handling for CPU hotplug \* Feb 2006 -  
 Jacob Shin <jacob.shin@amd.com> \* Fix handling for C  
 Copyright (C) 2006, Timesys Corp., Thomas Gleixner <tgx@timesys.com> \* \* This file contains the public data structure and API definitions.  
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 "hashed waitqueues" loudly \* enough at me, Linus  
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 <linux/pci.h>#include <linux/i2c.h>#include <linux/inter  
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 Copyright (c) 2009 Gabor Juhos <juhosg@openwrt.org>  
 Copyright (c) 1998-2001 Vojtech Pavlik \* \* Based on the work of:  
 Copyright (c) 2008 Texas Instruments \*/#include <linux/kernel.h>#include <linux/string.h>#include <linux/errno.h>#include <linux/unistd.h>#include  
 <linux/interrupt.h>#include <linux/init.h>#in  
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 Copyright (C) 2014 Boris Brezillon \* Boris Brezillon <boris.brezillon@free-electrons.com> \*  
 Copyright (C) 1999-2001 Gerard Roudier <groudier@free.fr> \* \* This driver is derived from the Linux sym53c8xx driver.  
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 Copyright (C) 2008-2009 Ralph Metzler <rjkm@metzlerbros.de> \* Modifications for new nGene firmware, \* support for  
 EEPROM-copying, \*

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 Copyright (C) 2013-2015 Eric Dumazet <edumazet@google.com> \* \* Meant to be mostly used for locally generated traffic :  
 Copyright (C) 2003-2010 Jean Delvare <jdelvare@suse.de> \* \* Based on the lm83 driver. The LM90 is a sensor chip made by National \*  
 Semiconductor. It reports up to two temperatures (its own plus up  
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 Copyright 1998 by Egbert Eich <Egbert.Eich@Physik.TU-Darmstadt.DE> \* \* 3. John Fremlin <vii@users.sourceforge.net> and \* Ani Joshi  
 <ajoshi@unixbox.com>  
 Copyright (C) 2018-2019 Corentin LABBE <clabbe@baylibre.com> \* \* This file add support for AES cipher with 128,192,256 bits keysize in \* CBC and  
 ECB mode.  
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 Copyright 2017 Hannes Reinecke, SUSE Linux GmbH <hare@suse.com> \* \* Based on the original DAC960 driver,  
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 Copyright (C) 2008 IBM Corporation \* Author: Mimi Zohar <zohar@us.ibm.com> \* \* File: integrity\_audit.c \* Audit calls for the integrity subsystem  
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 Copyright (C) 2004 Russell King \*/#ifndef \_LINUX\_SERIAL\_8250\_H#define \_LINUX\_SERIAL\_8250\_H  
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 Copyright (C) 2005-6 DiBcom, SA \*/#include "dib0700.h"  
 Copyright (c) 2004 Andras BALI <drewie@freemail.hu> \* \* Heavily based on lm75.c by Frodo Looijaard <frodol@dds.nl>. The LM77 \* is a temperature  
 sensor and thermal window comparator with 0.5 deg  
 Copyright (C) 2005, 2006 Chuck Lever <cel@netapp.com> \*  
 Copyright (C) 2008 Jan Hoogenraad Functions:  
 Copyright (c) 2005 Herbert Xu <herbert@gondor.apana.org.au> \* \* Portions derived from Cryptoapi, by Alexander Kjeldaas <astor@fast.no> \* and  
 Nettle, by Niels Møller.  
 Copyright (C) 2010 by Mauro Carvalho Chehab  
 Copyright (C) 2003-2009 Jean Delvare <jdelvare@suse.de> \* \* Inspired from the lm83 driver. The W83L785TS-S is a sensor chip made \* by Winbond.  
 It reports a single external temperature with a 1 deg  
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copyright asserted, 1 = copyright not asserted#define CS8427_ORIG (1<<0) /* SCMS generation indicator, 0 = 1st generation or higher, 1
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Copyright (C) 2007 Antti Palosaari <crope@iki.fi> * * TODO: * - add smart card reader support for Conditional Access (CA) *
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(C) 2002 by Harald Welte <laforge@netfilter.org>*/#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/in.h>#include
<linux/module.h>#include <linux/skbuff.h>#include <linux/ip.h>#include
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Copyright (C) 2011, 2015, 2016 Intel Corporation. */#include <linux/acpi.h>#include <linux/delay.h>#include <linux/err.h>#include
<linux/errno.h>#include <linux/i2c.h>#include <linux/interrupt.h>
Copyright(c) 2003- 2015 Intel Corporation"#define DRV_AUTHOR " <linuxwifi@intel.com>"
Copyright (c) 2005 Stefano Brivio <st3@riseup.net>
(c) Copyright 1996-1997 Alan Cox <alan@lxorguk.ukuu.org.uk>, * All Rights Reserved
COPYRIGHT)) sbits if (val & AC_DIG1_LEVEL) sbits |= (IEC958_AES1_CON_ORIGINAL << 8); sbits |= val &
(0x7f << 8); } return sbits;
Copyright (C) 2018 Xue Liu <liuxuenetmail@gmail.com> */#include <linux/kernel.h>#include <linux/module.h>#include
<linux/gpio/consumer.h>#include <linux/spi/spi.h>#include <linux/workqueue.h>#i
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(C) * Notes: To disconnect the current associated BSS * Command Mode */struct disconnect_parm { u32 rsvd;
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Copyright (C) 2015 Microchip Technology */#include <linux/kernel.h>#include <linux/module.h>#include <linux/mii.h>#include
<linux/ethtool.h>#include <linux/phy.h>#include <linux/microchipphy.h>
Copyright (C) 2018 ARM Ltd. */#include <linux/device.h>#include <linux/types.h>

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Copyright (c) 2007 Christoph Hellwig <hch@lst.de>  
Copyright (c) by Jaroslav Kysela <perex@perex.cz> \* Universal routines for AK4531 codec \*/  
Copyright (c) 2002 by Takashi Iwai <tiwai@suse.de>  
Copyright (C) 1997, Stephen Tweedie \* \* Provide stub functions for unreadable inodes  
Copyright (C) 2007-2008 Google, Inc.  
Copyright (c) 2016, Cascoda, Ltd.  
Copyright (c) 1999-2002 Vojtech Pavlik \* \* This is a module that converts a tty line into a much simpler \* 'serial io port' abstraction that the input device  
drivers use.  
Copyright (C) 2001-2004 Aurelien Jarno <aurelien@aurel32.net> \* Ported to Linux 2.6 by Aurelien Jarno <aurelien@aurel32.net> with \* the help of Jean  
Delvare <jdelvare@suse.de> \*/  
Copyright (c) 2005-2007 Michael Buesch <m@bues.ch>  
Copyright (C) 2003 Al Borchers (alborchers@steinerpoint.com)  
Copyright (c) 2012 Samsung Electronics Co., Ltd. \* http://www.samsung.com/ \*/#include <linux/fs.h>#include <linux/f2fs\_fs.h>#include  
"f2fs.h"#include "node.h"#include "segment.h"  
Copyright (c) 2019, Jeffrey Hugo \*/  
Copyright(c) 2018 Intel Corporation \* All rights reserved  
Copyright (c) 2016, Intel Corporation \* Authors: Weigang Li <weigang.li@intel.com> \* Giovanni Cabiddu <giovanni.cabiddu@intel.com>  
\*/#include <linux/errno.h>#include <linux/kernel.h>#in  
Copyright (C) 2015, 2016 Linaro Ltd.  
Copyright 2000 by Hans Reiser, licensing governed by reiserfs/README \*/\* Reiserfs block (de)allocator, bitmap-based. \*/  
Copyright (c) 2015-2016 Quantenna Communications. All rights reserved  
Copyright (c) 2014 Lucas Stach <l.stach@pengutronix.de>, Pengutronix \*/  
Copyright (c) 2010 by Kyle Strickland \* (based on kworld-plus-tv-analog.c by \* Mauro Carvalho Chehab) \*/  
Copyright (C) April 2007 Antonino Daplas <adaplas@pol.net>  
Copyright (C) 1999 - 2002 Greg Kroah-Hartman (greg@kroah.com)  
Copyright (C) 2008 David Brownell \*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/bcd.h>#include <linux/slab.h>#include  
<linux/rtc.h>#include <linux/workqueue.h>  
Copyright 2019 Google, LLC \*/  
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Copyright (c) 2003-2006, Cluster File Systems, Inc, info@clusterfs.com \* Written by Alex Tomas <alex@clusterfs.com> \*/  
Copyright (C) 1998 Free Software Foundation, Inc. \* \* The Tiger algorithm was developed by Ross Anderson and Eli Biham. \* It was optimized for 64-bit  
processors while still delivering \* decent pe  
Copyright (c) 2007 Larry Finger <Larry.Finger@lwfinger.net>  
Copyright (C) 2007 Stephen Hemminger <shemminger@linux-foundation.org> \*/  
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Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_STAT\_H#define \_\_ASM\_STAT\_H  
Copyright (C) 2006 Thumtronics Pty Ltd.  
Copyright (c) 1999-2001 Vojtech Pavlik  
Copyright (c) 2013 by Tobias Hoffmann  
Copyright 2005-2006 Openedhand Ltd. \* \* Author: Richard Purdie <rpurdie@openedhand.com>  
Copyright (C) 2010 Mike Christie \*/#ifndef \_\_ISCSI\_BOOT\_SYSFS\_#define \_\_ISCSI\_BOOT\_SYSFS\_  
Copyright (c) 2018 Rockchip Electronics Co. Ltd. \* Author: Elaine Zhang<zhangqing@rock-chips.com> \*/

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Copyright (C) 2008 Alan Stern  
Copyright (C) 2018 Linaro Ltd <ard.biesheuvel@linaro.org>  
Copyright (c) 2002 by Takashi Iwai <tiwai@suse.de> \* \* Many codes borrowed from audio.c by \* Alan Cox (alan@lxorguk.ukuu.org.uk) \*  
Thomas Sailer (sailer@ife.ee.ethz.ch)  
Copyright (c) by Takashi Iwai <tiwai@suse.de>  
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Copyright (C) 2005 Rajesh Shah (rajesh.shah@intel.com)  
Copyright (C) 2005 Patrick Boettcher <pb@linuxtv.org> \*/#ifndef S5H1420\_PRIV#define S5H1420\_PRIV  
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Andreas Bach Aen <abach@stofanet.dk> \* Clifford  
Copyright (C) 2016-2017 Glider bvba \*/  
Copyright (c) 2012 Samsung Electronics Co., Ltd. \* http://www.samsung.com/ \*/#define GC\_THREAD\_MIN\_WB\_PAGES 1 /\*  
\* a threshold to determine \* whether IO subsystem is idle  
Copyright (c) 2003-2004 Paul Miller <pmiller9@users.sourceforge.net> \*/  
Copyright (c) 2002 James Morris <jmorris@intercode.com.au> \*/#include <linux/init.h>#include <linux/module.h>#include <linux/mm.h>#include  
<asm/byteorder.h>#include <linux/crypto.h>#include <li  
Copyright (C) 2009 Red Hat, Inc. \* Author: Michael S. Tsirkin <mst@redhat.com> \* \* virtio-net server in host kernel.  
Copyright (C) 2006-2008 Nippon Telegraph and Telephone Corporation. \* \* Originally written by Koji Sato. \* Two allocators were unified by Ryusuke  
Konishi and Amagai Yoshiji.  
Copyright (c) 2017 Broadcom Limited  
Copyright (C) 2003-2005 Matthew Wilcox (willy@infradead.org)  
Copyright (c) 2000 by Zach Brown <zab@zabbo.net> \* Takashi Iwai <tiwai@suse.de> \* \* Most of the hardware init stuffs are based on  
maestro3 driver for \* OSS/Free by Zach Brown  
copyright (c) 2013 Freescale Semiconductor, Inc. \* Freescale IMX AHCI SATA platform driver \* \* based on the AHCI SATA platform driver by Jeff Garzik  
and Anton Vorontsov  
(C) Copyright 1999 Linus Torvalds \* \* Common interfaces for "ptrace()" which we do not want \* to continually duplicate across every architecture.  
Copyright (C) 2017 Linaro Ltd. \*/#include <linux/clock.h>#include <linux/init.h>#include <linux/interconnect.h>#include <linux/ioctl.h>#include  
<linux/list.h>#include <linux/module.h>#include <li  
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Copyright (C) 2011 Intel Corporation \* Authors: Vinod Koul <vinod.koul@linux.intel.com> \* Pierre-Louis Bossart <pierre-  
louis.bossart@linux.intel.com> \*/  
Copyright (c) 2003-2005 Matthew Wilcox <matthew@wil.cx> \* \* This driver is derived from the Linux sym53c8xx driver.  
(C) 2011 The Chromium OS Authors \* \* In the file "/sys/module/dm\_verity/parameters/prefetch\_cluster" you can set \* default prefetch value. Data are  
read in "prefetch\_cluster" chunks from the \* has  
Copyright (c) 2012-2013, NVIDIA Corporation. \*/  
Copyright (C) 2010 Kent Overstreet <kent.overstreet@gmail.com> \* \* Uses a block device as cache for other block devices; optimized for SSDs. \* All  
allocation is done in buckets, which should match  
Copyright (C) 2009 Greg Kroah-Hartman <gregkh@suse.de>  
Copyright (C) 1996 Paul Mackerras \*/  
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 Copyright (C) 2004 IBM Corporation  
 (C) 2007 EADS Astrium \* \* This driver is based on the Im75 and other Im\_sensors/hwmon drivers  
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 Copyright (C) 2010 Ivo van Doorn <IvDoorn@gmail.com>  
 Copyright (c) 2014-2015 Scott Feldman <sfeldma@gmail.com> \*/#ifndef \_LINUX\_SWITCHDEV\_H\_#define \_LINUX\_SWITCHDEV\_H\_  
 Copyright 2019 Intel Corporation, Inc. \* \* Authors:  
 Copyright (C) 2005 Trond Myklebust <Trond.Myklebust@netapp.com> \* - Modified by David Howells <dhowells@redhat.com> \* \* NFS namespace  
 Copyright 2007-2008, Intel Corporation  
 Copyright (C) 2004-2005 Deep Blue Solutions Ltd. \* \* History:  
 Copyright (C) 2018-2019 Intel Corporation \*/#include <linux/export.h>#include <linux/bitops.h>#include <linux/etherdevice.h>#include  
 <linux/slab.h>#include <linux/ieee80211.h>#include <net/cfg8  
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 Copyright (c) 2000 Jaroslav Kysela <perex@perex.cz> \* 2002 Takashi Iwai <tiwai@suse.de> \*/  
 Copyright (C) 2015-2016 Altera Corporation \* by Alan Tull <atull@opensource.altera.com> \*/#include <linux/fpga/altera-pr-ip-core.h>#include  
 <linux/module.h>#include <linux/of\_device.h>  
 Copyright (c) 1999 Pavel Machek <pavel@ucw.cz>  
 Copyright (C) 2011 Google, Inc.  
 Copyright (c) 2019 Kuninori Morimoto <kuninori.morimoto.gx@renesas.com> \*/#ifndef \_\_SOC\_COMPONENT\_H#define \_\_SOC\_COMPONENT\_H  
 Copyright (c) 1998-1999 by Frank van de Pol <fvdpol@coil.demon.nl> \*/#ifndef \_\_SND\_SEQ\_QUEUE\_H#define \_\_SND\_SEQ\_QUEUE\_H  
 Copyright (C) 1996, 1997 Ingo Molnar, Miguel de Icaza, Gadi Oxman  
 Copyright (C) 2016 ARM Ltd. \* Author: James Morse <james.morse@arm.com> \*/#include <linux/linkage.h>#include <linux/errno.h>  
 Copyright 2016 Chen-Yu Tsai <wens@csie.org> \* \* Based on the Allwinner SDK driver, released under the GPL.  
 Copyright (C) 2003 by Chuck Lever <cel@netapp.com> \* \* High-performance uncached I/O for the Linux NFS client  
 Copyright (C) 1995-1997, Olaf Kirch <okir@monad.swb.de> \*  
 Copyright (c) 2003, 2004, 2005 Francois Romieu <romieu@fr.zoreil.com>  
 COPYRIGHT); snd\_emu10k1x\_ptr\_write(chip, SPCS2, 0, chip->spdif\_bits[2] = SPCS\_CLKACCY\_1000PPM |  
 SPCS\_SAMPLERATE\_48 | SPCS\_CHANNELNUM\_LEFT | SPCS\_SOURCENUM\_UNSPEC |  
 Copyright (c) 2002, 2003, 2004, 2005, 2006, 2007, 2008 \* Phillip Lougher <phillip@squashfs.org.uk> \* \* export.c  
 Copyright (C) 2012 Michael D. Taht <dave.taht@bufferbloat.net>  
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 Copyright (C) 2006. Bob Jenkins (bob\_jenkins@burtleburtle.net) \* \* http://burtleburtle.net/bob/hash/  
 Copyright (C) 2006 David Gibson, IBM Corporation. \*/#include <fdt.h>  
 Copyright (C) 2009 Bart Zolnierkiewicz <bzolnier@gmail.com> \* <http://rt2x00.serialmonkey.com> \*/  
 Copyright (C) 2015-2018 - ARM Ltd \* Author: Marc Zyngier <marc.zyngier@arm.com> \*/  
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 Copyright (C) 1995 Geert Uytterhoeven \*  
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 Copyright (c) 2012-2013 Red Hat, Inc \* \* This code is partly based on hid-egalax.c: \*  
 Copyright (C) 2014 Advanced Micro Devices, Inc. \* Authors: Suravee Suthikulpanit <suravee.suthikulpanit@amd.com> \* Harish Kasiviswanathan  
 <harish.kasiviswanathan@amd.com> \* Brandon Anderson  
 Copyright (c) 2014 Broadcom Corporation \*/#ifndef CONFIG\_OFvoid brcmf\_of\_probe(struct device \*dev, enum brcmf\_bus\_type bus\_type, struct  
 brcmf\_mp\_device \*settings);#elsestatic void brcmf\_of\_p  
 Copyright (c) 2008-2009, NVIDIA Corporation. \* \* Bits taken from arch/arm/mach-dove/pcie.c  
 (C) Copyright 2000 Yggdrasil Computing, Inc. (port of new PCI interface \* support from usb-ohci.c by Adam Richter, adam@yggdrasil.com).  
 Copyright (C) 2004-2006 Silicon Graphics, Inc. \*  
 Copyright 2012 Intel Corporation; author H. Peter Anvin \*  
 Copyright 2009, Boris Hajduk <boris@hajduk.org> \* \* ch341.c implements a serial port driver for the Winchiphead CH341.  
 Copyright 2013 Freescale Semiconductor, Inc. \* \* CPU Frequency Scaling driver for Freescale QorIQ SoCs.

Copyright (C) 2019 Axis Communications AB \* \* Based on ttyprintk.c:  
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 Copyright (C) Dave Airlie 2005  
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 Copyright (c) 2010 by Jarod Wilson <jarod@redhat.com> \* \* See <http://mediacenterguides.com/book/export/html/31> for details on \* key mappings.  
 Copyright (c) 2000-2003,2005 Silicon Graphics, Inc.  
 (C) Adam Belay. \*/#include <linux/module.h>#include <linux/pci.h>#include <linux/pnp.h>#include <linux/string.h>#include  
 <linux/kernel.h>#include <linux/serial\_core.h>#include <linux/bitops.h>  
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 Copyright Matthew Wilcox for Intel Corp, 2010  
 Copyright (c) 2016 Facebook \*/#ifndef \_\_PERCPU\_FREELIST\_H\_\_#define \_\_PERCPU\_FREELIST\_H\_\_#include <linux/spinlock.h>#include  
 <linux/percpu.h>  
 Copyright 2014, Broadcom Corporation  
 Copyright 2015 Realtek Semiconductor Corp. \* Author: Oder Chiou <oder\_chiou@realtek.com> \*/  
 Copyright (c) 2006 Patrick McHardy <kaber@trash.net> \* \* Based on ipt\_random and ipt\_nth by Fabrice MARIE <fabrice@netfilter.org>.  
 Copyright 2001, 2002 Red Hat. \* \* Mmmm, chocolate vanilla mocha...  
 (C) IP\_VS\_DBG\_ADDR((C)->af, ntohs((C)->cport), IP\_VS\_DBG\_ADDR((C)->af, &((C)->vaddr)),  
 \ ntohs((C)->vport), \ IP\_VS\_DBG\_ADDR((C)->daf, &((C)->daddr)), \ ntohs((C)-  
 >dport),  
 Copyright (C) 2005 Kihon Technologies Inc., Steven Rostedt  
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 Copyright (c) 2010 Chase Douglas <chase.douglas@canonical.com> \*/  
 Copyright 2015 Heiko Stuebner <heiko@sntech.de> \*/  
 Copyright (C) 1999 Paul \*/#ifndef \_\_LINUX\_BRIDGE\_EFF\_H#define \_\_LINUX\_BRIDGE\_EFF\_H  
 Copyright (C) 2009 Tom Zanussi <tzanussi@gmail.com> \*/  
 Copyright (c) 2013 Olivier Scherler \*/  
 Copyright 2001 David Brownell  
 Copyright 2004-2005 Andrea Merello <andrea.merello@gmail.com>, et al. \* \* Thanks to Realtek for their support!  
 Copyright (c) 2015, Intel Corporation. \* \* IIO driver for STK3310/STK3311. 7-bit I2C address: 0x48.  
 Copyright (c) 2017 Cadence// Cadence PCIe host controller driver.// Author: Cyrille Pitchen <cyrille.pitchen@free-electrons.com>  
 Copyright(C) 2006, Greg Kroah-Hartman <greg@kroah.com> \* \* Userspace IO  
 Copyright(c) Pierre Ossman\n");  
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 <sowjanya@cdac.in> \*/  
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Copyright(c) 2015 Intel Mobile Communications GmbH  
Copyright (c) 2013 - 2015 Linaro Ltd.  
Copyright (C) 2004 Kay Sievers <kay.sievers@vrfy.org> \*/  
Copyright 2007 Pierre Ossman \*/#ifndef \_MMC\_CORE\_BUS\_H#define \_MMC\_CORE\_BUS\_H  
Copyright (c) 2010 Dmitry Torokhov \* Input handler conversion \*/  
Copyright Red Hat, Inc. 2011 \* \* Authors:  
Copyright (C) Microsoft Corporation \* \* Implements a firmware TPM as described here:  
Copyright (c) 2012 Red Hat, Inc \* \* This code is partly based on "USB HID support for Linux": \*  
Copyright (c) 2005 - James Bottomley <James.Bottomley@steeleye.com> \*/  
Copyright 2014 Chen-Yu Tsai \* \* Chen-Yu Tsai <wens@csie.org>  
Copyright (c) 2019 Baylibre SAS. \* Author: Neil Armstrong <narmstrong@baylibre.com> \*/#include <linux/clock-provider.h>#include  
<linux/platform\_device.h>#include <linux/reset-controller.h>#include  
(C) 2005 by Christian Hentschel <chentschel@arnet.com.ar> \* based on RR's ip\_contrack\_ftp.c and other modules.  
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Copyright(c) 2016 Intel Deutschland GmbH \* All rights reserved  
(C) 2008-2013 Christoph Lameter <cl@linux.com> \* \* Optimized manipulation for memory allocated through the per cpu \* allocator or for addresses of  
per cpu variables.  
Copyright (c) 2000, 2001 Anders Torger <torger@ludd.luth.se> \* \* Thanks to Henk Hesselink <henk@anda.nl> for the analog volume control \*  
code.  
Copyright (c) 2002 Mark D. Studebaker <mdsxyz123@yahoo.com> \* With assistance from Trent Piepho <xyzyz@speakeasy.org> \*/  
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Copyright (C) 2000-2002 Russell King  
Copyright (C) 2012 Texas Instruments. All rights reserved  
Copyright (c) 2016,2017 Daniel Mack  
Copyright (C) 2018 WMWare Inc. \* \* This file contains the main data structure and API definitions.  
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Copyright (C) 2004, 2005 IBM, John Stultz (johnstul@us.ibm.com) \*/#include <linux/clocksource.h>#include <linux/jiffies.h>#include  
<linux/module.h>#include <linux/init.h>  
Copyright (C) 2004-2005, Kyle McMartin <kyle@parisc-linux.org>  
(C) 2002 Andreas Gruenbacher, <a.gruenbacher@computer.org>\*/  
Copyright (C) 1993 -- 1997 Drew Eckhardt, Frederic Potter, \* David Mosberger-Tang  
Copyright (C) 2014 Linaro Limited \* Author: AKASHI Takahiro <takahiro.akashi@linaro.org> \*/#ifndef \_ASM\_GENERIC\_SECCOMP\_H#define  
\_ASM\_GENERIC\_SECCOMP\_H  
Copyright (C) 2005-2006 Intel Corporation \* Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com> \* \* Implementation of the code for parsing the  
firmware file (extract \* the headers and binary cod  
Copyright (C) 2015, 2016 ARM Ltd. \*/#ifndef \_\_KVM\_ARM\_VGIC\_MMIO\_H\_\_#define \_\_KVM\_ARM\_VGIC\_MMIO\_H\_\_  
(C) 2002 by Harald Welte <laforge@netfilter.org> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include  
<linux/skbuff.h>#include <linux/ip.h>#include <linux/ipv6.h>#incl  
Copyright (c) 1997 Dan Malek (dmalek@jlc.net). \*/#define BD\_SC\_EMPTY ((ushort)0x8000) /\* Receive is empty \*/#define BD\_SC\_READY  
((ushort)0x8000) /\* Transmit is ready \*/#define BD\_SC\_WRAP ((ushort)  
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Copyright (c) 2005-2006 Varma Electronics Oy  
Copyright (C) 2004 Compulab Ltd. \* \* Driver for the V3020 RTC  
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Copyright (C) 2003 Winfried Ritsch (IEM) \* based on hdsp.h from Thomas Charbonnel (thomas@undata.org) \*  
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Copyright (C) 2011 Sascha Hauer, Pengutronix <s.hauer@pengutronix.de> \*/#include <linux/module.h>#include <linux/clk-provider.h>#include  
<linux/slab.h>#include <linux/err.h>#include <linux/of.h>  
Copyright (c) 2002, 2003, 2004, 2005, 2006, 2007, 2008, 2009 \* Phillip Lougher <phillip@squashfs.org.uk> \* \* decompressor.h  
Copyright (c) 2014, Applied Micro Circuits Corporation \* Author: Feng Kan <fkan@apm.com> \* Author: Hieu Le <hnle@apm.com> \* \* This driver  
provides support for X-Gene SLIMpro I2C device access \* u  
Copyright IBM Corp. 2007-2010 Mel Gorman <mel@csn.ul.ie> \*/#include <linux/cpu.h>#include <linux/swap.h>#include <linux/migrate.h>#include  
<linux/compaction.h>#include <linux/mm\_inline.h>#inclu  
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Copyright (c) 2009 Takashi Iwai <tiwai@suse.de> \*/  
Copyright (C) 2004, 2010 Nokia Corporation \* Written by Timo Teras <ext-timo.teras@nokia.com> \* \* Converted to x\_tables and forward-ported to  
2.6.34 \* by Luciano Coelho <luciano.coelho@nokia.com>  
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Copyright (c) 2008 Spansion Inc. \* Frank Edelhaeuser <frank.edelhaeuser at spansion.com> \* (converted to "new style" I2C driver model, removed  
checkpatch.pl warnings) \* \* Based on ad7418.c  
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by:  
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Copyright (c) 2009 Nokia Corporation \* Roger Quadros <ext-roger.quadros@nokia.com> \* \* This is useful for systems with mixed controllable and \*  
non-controllable regulators, as well as for allowing  
Copyright (C) 2012 ARM Ltd. \* Modified by Will Deacon <will.deacon@arm.com> \*/  
Copyright (C) 2016-2017 Christoph Hellwig. \*/#include <linux/interrupt.h>#include <linux/kernel.h>#include <linux/slab.h>#include  
<linux/cpu.h>#include <linux/sort.h>  
Copyright (C) 2019 Guillaume La Roque <glaroque@baylibre.com> \* \* Register value to celsius temperature formulas:  
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Copyright (c) 2006, Michael Wu <flamingice@sourmilk.net> \* \* Based on the islm (softmac prism54) driver, which is:  
Copyright: (C) 2017 Conexant Systems, Inc.// Author: Simon Ho, <Simon.ho@conexant.com>  
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Copyright (c) 2001-2003 Patrick Mochel <mochel@osdl.org>  
(C) 1998 Stephen Tweedie \*/#include <linux/mm.h>#include <linux/gfp.h>#include <linux/kernel\_stat.h>#include <linux/swap.h>#include  
<linux/swapops.h>#include <linux/init.h>#include <linux/page  
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of help from Eric Jarman <ejarman@acm.org>. \* Extende  
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Copyright (c) 2015-2016 HGST, a Western Digital Company. \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include  
<linux/init.h>#include <linux/slab.h>#include <rdma/mr\_poo  
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Copyright (C) 2007 MontaVista Software, Inc., <source@mvista.com> \* \* OMAP ALSA SoC DAI driver using McBSP port  
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Copyright by Michael T. Mayers (c) Jan 09, 2000 \* mail: michael@tweakoz.com \* Thanks to John Galbraith \* \* This driver is for the 'Mark Of  
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 Copyright (C) 2013-2016 Red Hat \* Author: Rob Clark <robdclark@gmail.com> \*/  
 Copyright (c) 2016-2018 Cavium Inc. \*/#include "qedf.h"  
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 Copyright (C) 2015 VanguardiaSur - [www.vanguardiasur.com.ar](http://www.vanguardiasur.com.ar) \* \* Based on original driver by Krzysztof Ha?asa:  
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 \*/#include <linux/module.h>#include <linux/sched.h>#includ  
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 Copyright (C) 2015: Linus L \* \* Based on the MLD support added to br\_multicast.c by YOSHIFUJI Hideaki.  
 Copyright (c) 1999 by Takashi Iwai <tiwai@suse.de> \*  
 Copyright (C) 2018 - 2019 Intel Corporation \*/#ifndef \_\_PMSR\_H#define \_\_PMSR\_H#include <net/cfg80211.h>#include "core.h"#include "nl80211.h"#include "rdev-ops.h"  
 Copyright (C) 2015-2018 Linaro Ltd. \*/#include <linux/clk.h>#include <linux/completion.h>#include <linux/interrupt.h>#include <linux/io.h>#include <linux/kernel.h>#include <linux/of.h>#include  
 Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_IO\_H#define \_\_ASM\_IO\_H  
 Copyright (c) <2013-2014> Linaro Ltd.// <http://www.linaro.org>  
 Copyright (C) 1999 Stephane Eranian <eranian@hpl.hp.com> \*/#ifndef \_UAPI\_LINUX\_RTC\_H#define \_UAPI\_LINUX\_RTC\_H  
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 Copyright (C) 2009 Andy Walls <awalls@md.metrocast.net> \* \* However, the cx23885\_input\_{init,fini} functions contained herein are \* derived from Linux kernel files linux/media/video/.../...-inpu  
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 Copyright (C) 2011 Jarod Wilson <jarod@redhat.com> \* \* Special thanks to Fintek for providing hardware and spec sheets. \* This driver is based upon the nvuoton, ite and ene drivers for \* similar h  
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 Copyright (c) 2012 Bj \* \* The probing code is heavily inspired by cdc\_ether, which is:  
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 Copyright 2009-2010 Integrated Device Technology, Inc. \* Alexandre Bounine <alexandre.bounine@idt.com> \* - Added EM support \* - Modified switch operations initialization. \*  
 Copyright (C) 2008-2009 Ira W. Snyder <iws@ovro.caltech.edu> \* \* Aligned with ina2xx driver  
 copyright of the respective owners.\*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/module.h>#include <linux/string.h>#include <linux/slab.h>#include <linux/delay.h>#inc  
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 Copyright (C) 2002 by Concurrent Computer Corporation \* \* Small id to pointer translation service avoiding fixed sized \* tables.  
 Copyright (C) 2003 MontaVista Software Inc. \* Author: Jun Sun, jsun@mvista.com or jsun@junsun.net \* \* 031022 - [jsun] add run-time configure and  
 partition setup  
 Copyright (C) 2005-2007 Micronas \*  
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 Copyright (C) 2018 - 2019 Intel Corporation \* Authors: Luis Carlos Cobo <luisca@cozybit.com> \* Javier Cardona <javier@cozybit.com> \*/  
 COPYRIGHT) val if (sbits & (IEC958\_AES1\_CON\_ORIGINAL << 8)) val |= AC\_DIG1\_LEVEL; val |= sbits &  
 (IEC958\_AES1\_CON\_CATEGORY << 8); } return val;  
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 locking sane, Robert Love \* 2000-10-05 Implemented  
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 <linux/slab.h>#include "cifs\_fs\_sb.h"#include "  
 Copyright (C) 2004 Jan M. Hochstein \* <hochstein@algo.informatik.tu-darmstadt.de> \*/#include <linux/device.h>#include <linux/kernel.h>#include  
 <linux/module.h>#include <linux/usb.h>#include <li  
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Copyright (c) by Francisco Moraes <fmoraes@nc.rr.com> \*/#include <linux/delay.h>#include <linux/init.h>#include <linux/interrupt.h>#include  
<linux/pci.h>#include <linux/slab.h>#include <linux/m  
Copyright (c) 2016-2018 Cavium Inc. \*/#ifndef \_SCSI\_FW\_FUNCS\_H#define \_SCSI\_FW\_FUNCS\_H#include <linux/qed/common\_hsi.h>#include  
<linux/qed/storage\_common.h>#include <linux/qed/fcoe\_common.h>  
Copyright (c) 2017 HiSilicon Technologies Co., Ltd. \* \* Simple HiSilicon phase clock implementation.  
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without fee is hereby granted, provided tha  
Copyright 2015 Carnegie Mellon University \* \* Expose entries from QEMU's firmware configuration (fw\_cfg) device in \* sysfs (read-only, under  
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The ROCKCHIP Type-C PHY has two PLL clocks. The fir  
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accounting right. \* Occurs in sever  
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Copyright (c) by Francisco Moraes <fmoraes@nc.rr.com> \*/#include <linux/delay.h>#include <linux/init.h>#include <linux/interrupt.h>#include  
<linux/pci.h>#include <linux/slab.h>#include <linux/v  
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Copyright 2005, Red Hat, Inc., Ingo Molnar  
(C) \* A(W1) \* C(C) \* \* Which would create W1->C->W1 dependencies, even though there is no \* actual deadlock  
possible. There are two solutions, using a \* read-recursive acquire on the wor  
Copyright (C) 2008 Greg Kroah-Hartman <gregkh@suse.de>  
Copyright (C) 1994 by Florian La Roche \* \* This module implements the Address Resolution Protocol ARP (RFC 826), \* which is used to convert IP  
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Copyright (C) 2016 Linaro, Inc. \* \* Author: Baolin Wang <baolin.wang@linaro.org>  
Copyright (c) 2010 Rafael J. Wysocki <rjw@sisk.pl>, Novell Inc. \*/#define pr\_fmt(fmt) "PM: " fmt  
(C) 2003 Yasuyuki Kozakai @USAGI <yasuyuki.kozakai@toshiba.co.jp> \*/  
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Copyright (C) 1992 Krishna Balasubramanian \* \* Removed all the remaining kernel mess \* Catch the -EFAULT stuff properly \* Use GFP\_KERNEL for  
messages as in 1.2 \* Fixed up the unchecked user spac  
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Copyright (C) 2003 Jozsef Kadlec <kadlec@netfilter.org> \*/#define pr\_fmt(fmt) "KUILD\_MODNAME": " fmt#include <linux/module.h>#include  
<linux/netfilter\_ipv4/ip\_tables.h>#include <linux/slab.h>  
Copyright (C) 2016 Martin Blumenstingl <martin.blumenstingl@googlemail.com> \* \* Some devices (like the Cisco Meraki Z1 Cloud Managed Teleworker  
Gateway) \* need to be able to initialize the PCIe wif  
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<linux/delay.h>#include <linux/slab.h>#include <linux/module.h>#include  
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IEC958_AES1_CON_ORIGINAL;    return 0;}
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<linux/interrupt.h>#include <linux/sched_clock.h>#i
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Copyright (C) 2013 Huawei Ltd. * Author: Jiang Liu <liuj97@gmail.com> * * Based on arch/arm/include/asm/jump_label.h
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Copyright 2007 Holger Schurig * * This hardware has more or less no CMD53 support, so all registers * must be accessed using
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examining the MSFT drivers' debug output \* (insanely lef  
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Copyright 2015 Maciej Szmigiero <mail@maciej.szmigiero.name> \* \* Based on ad7414.c:  
Copyright (C) 2012 Anssi Hannula <anssi.hannula@iki.fi> \* \* This keymap is for several Medion X10 remotes that have the Windows MCE \* button. This  
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Linus Walleij <linus.walleij@linaro.org>
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Copyright (C) 2004 Trond Myklebust * * NFSv4 callback encode/decode procedures
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COPYRIGHT; ucontrol->value.iec958.status IEC958_AES1_CON_ORIGINAL; ucontrol->value.iec958.status[3] =
IEC958_AES3_CON_FS; return 0;}
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Copyright (C) 2008-2014 Mathieu Desnoyers */#include <linux/module.h>#include <linux/mutex.h>#include <linux/types.h>#include
<linux/jhash.h>#include <linux/list.h>#include <linux/rcupdate.h>#
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(C) Copyright Al Viro 2002,2003 * * NOTE: data structure needs to be changed. It works, but for large dev_t * it will be too slow. It is isolated, though,
so these changes will be * local to tha
Copyright (C) 2017 Icenowy Zheng <icenowy@aosc.io> * * Based on sun4i_backend.c, which is:

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(C) ordering is still observed by the pmu driver. */ if (!rb->aux_overwrite) {          aux_tail = READ_ONCE(rb->user_page->aux_tail);
handle->wakeup = rb->aux_wakeup + rb->aux_watermark;          if (aux_head
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Copyright 2012 Steffen Trumtrar <s.trumtrar@pengutronix.de> * * description of display timings
COPYRIGHT) {          snd_iprintf(buffer, "permitted\n");          } else {          snd_iprintf(buffer, "protected\n");          }
snd_iprintf(buffer, "Emphasis: ");          if ((status[0] & IEC958_AES0_CON_EMPHASIS) != IEC958_AE
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Copyright (c) 2005 Herbert Xu <herbert@gondor.apana.org.au> */#ifndef _CRYPTO_INTERNAL_H#define _CRYPTO_INTERNAL_H
(C) 2005-2012 by Pablo Neira Ayuso <pablo@netfilter.org> * * Initial connection tracking via netlink development funded and * generally made possible
by Network Robots, Inc. (www.networkrobots.com)
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Copyright © 2004 Ferenc Havasi <havasi@inf.u-szeged.hu>, *          University of Szeged, Hungary * * Created by Arjan van de Ven
<arjan@infradead.org>
(C) 2003 Ardis Technologies <roman@ardistech.com> * * Inode handling routines
Copyright (C) 2013 Intel Corporation
Copyright (c) 2010-2012 Benjamin Tissoires <benjamin.tissoires@gmail.com>
Copyright (C) 2015-2018 Linaro Ltd. */#include <linux/slab.h>#include <media/media-entity.h>#include <media/v4l2-dev.h>#include <media/v4l2-
device.h>#include <media/v4l2-ioct.h>#include <media
Copyright (C) 1993, 1994, 1995, 1999 Eric Youngdale
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Copyright (c) 2011-2012,2017 Qualcomm Atheros, Inc.
Copyright 2018 David Lechner <david@lechnology.com> * * Based on mi0283qt.c:
copyright; for (;;)          while (*++c != '$')          if (*c == '\0')          exit(0);          while (*++c != '$!')
putc(*c, stderr);          putc('\n', stderr);          }
Copyright (C) 2002 Deep Blue Solutions Ltd, All Rights Reserved
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```

```

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<linux/kernel.h>#include <linux/leds.h>#include <linux/m
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Copyright 2019 Analog Devices Inc. */#include <linux/kernel.h>#include <linux/bitfield.h>#include <linux/delay.h>#include <linux/errno.h>#include
<linux/init.h>#include <linux/module.h>#include
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<linux/iopoll.h>#include <linux/kernel.h>#include
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Copyright (C) 2006,2007 Genpix Electronics (genpix@genpix-electronics.com) * * Thanks to GENPIX for the sample code used to implement this module.
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Copyright (C) 2017 Josh Poimboeuf <jpoimboe@redhat.com> * * Some of this code was taken out of recordmcount.h written by: *
Copyright (c) 2016, Intel Corporation * Author: Giovanni Cabiddu <giovanni.cabiddu@intel.com> */#ifndef __CRYPTO_SCOMP_INT_H#define
__CRYPTO_SCOMP_INT_H#include <linux/crypto.h>
(C) 2012 Christoph Lameter <cl@linux.com> */#include <linux/slab.h>
Copyright (C) 2006 Nicolas Boichat (nicolas@boichat.ch) */
Copyright (C) 2014 Endless Mobile * * Written by:
Copyright (c) 2003 */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include <linux/kernel.h>
(C) 2011 Patrick McHardy <kaber@trash.net> */
Copyright (C) 2010 ARM Ltd., Will Deacon <will.deacon@arm.com> */#define pr_fmt(fmt) "hw perfevents: " fmt
Copyright (C) 2013, Obsidian Research Corp. * Jason Gunthorpe <jgunthorpe@obsidianresearch.com> * Device driver for ATMEL I2C TPMs. * * Teddy
Reed determined the basic I2C command flow, unlike ot
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Copyright (C) 2016 Joao Pinto <jpinto@synopsys.com> */
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Copyright © 2001-2007 Red Hat, Inc.
Copyright (C) 2010 Devin Heitmueller <dheitmueller@kernellabs.com> */#include <linux/kernel.h>#include <linux/errno.h>#include

```

```

<linux/slab.h>#include <linux/module.h>#include <linux/mm.h>#inclu
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module is part of usbvision driver project. * Updates t
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Copyright (C) 1999 Paul */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/kmod.h>#include <linux/module.h>#include
<linux/vmalloc.h>#include <linux/netfilter/x_tables.h>#include <lin
Copyright (C) 2008 James Smart, Emulex Corporation */
Copyright IBM Corp. 2016, 2018 * * Author(s): Ursula Braun <ubraun@linux.vnet.ibm.com> * based on prototype from Frank Blaschka
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```



```

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Copyright(c) 2013 - 2015 Intel Mobile Communications GmbH
Copyright (c) 2005 - James Bottomley <James.Bottomley@steeleye.com> * * The basic idea here is to enable a device to be attached to an * arbitrary
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Copyright (C) 2003 Mark Studebaker * * Thanks to Leon Moonen, Steve Cliffe and Grant Coady for their help * in testing and debugging this driver.
Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */#include <linux/module.h>#include <linux/init.h>#include
<linux/fs.h>#include <linux/stafs.h>#include <
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Copyright (C) 2013 Terry Lam <vtlam@google.com>
copyright + flash_ver images, 0 for failure.static u32 chk_cfg(u8 *cfg, u32 length, u32 *flash_ver){ u16 *pw = (u16 *)cfg - 1; u32 sz = 0; u32
len = length;
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Copyright (C) 2010 Alan Stern <stern@rowland.harvard.edu> \*/#include <linux/sched/mm.h>#include <linux/ktime.h>#include  
<linux/hrtimer.h>#include <linux/export.h>#include <linux/pm\_runtime.h>#i  
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Copyright 2015-2017 Google, Inc \* \* USB Power Delivery protocol stack.  
(C) 1996-2000 Thomas Sailer HB9JNX/AE4WA\n");printk(KERN\_INFO "hdcdrv: version 0.8\n"); return 0;}  
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Copyright (C) 2010-2014 Michael Krufky <mkrufky@linuxtv.org> \*/  
Copyright 2002-2007 H. Peter Anvin \*/#include <linux/async\_tx.h>#include <linux/gfp.h>#include <linux/mm.h>#include <linux/random.h>#include <linux/module.h>  
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Copyright 2011 Linus Walleij <linus.walleij@linaro.org>  
Copyright (C) Joerg Reuter DL1BKE (jreuter@yaina.de) \*/#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include <linux/spinlock.h>#include <linux/in.h>#include <linux/k  
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(C) 2015-2018 Kevin Darbyshire-Bryant <kevin@darbyshire-bryant.me.uk>  
Copyright (C) 2004,2019 Greg Kroah-Hartman <greg@kroah.com>  
Copyright (C) 2013-2015 Corentin LABBE <clabbe.montjoie@gmail.com> \* \* This file add support for MD5 and SHA1.  
Copyright (c) 2015 Kontron \* Author: Vadim V. Vlasov <vvlasov@dev.rtsoft.ru> \*  
Copyright (C) 2004 Anil S Keshavamurthy <anil.s.keshavamurthy@intel.com> \* - Added processor hotplug support \*/  
Copyright (c) 2008 Herbert Xu <herbert@gondor.apana.org.au> \*/  
Copyright 2015 Nouveau project  
Copyright (C) 2015-2017 Russell King. \*/#include <linux/interrupt.h>#include <linux/io.h>#include <linux/module.h>#include <linux/platform\_device.h>#include <linux/sched.h>#include <linux/slab.  
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Copyright (c) 2013 Coraid, Inc. See COPYING for GPL terms./\* \* aocmd.c \* Filesystem request handling methods  
Copyright (C) 2006 Steven Toth <stoth@linuxtv.org> Functions:  
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Copyright (c) 2006 Rik Snel <rsnel@cube.dyndns.org> \* \* Based on ecb.c  
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Copyright (C) 2000 Deep Blue Solutions Ltd \*/#include <linux/clk.h>#include <linux/clocksource.h>#include <linux/clockchips.h>#include <linux/err.h>#include <linux/interrupt.h>#include <linux/i  
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Copyright 2016 Karol Herbst  
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copyright by the original authors \* \* Portions of the merged code are based on Host AP (software wireless \* LAN access point) driver for Intersil Prism2/2.5/3.  
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Copyright (C) IBM Corporation, 2002, 2004 \* \* 2002-Oct Created by Vamsi Krishna S <vamsi\_krishna@in.ibm.com> Kernel \* Probes initial  
implementation (includes suggestions from \* Rusty Russell).  
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Copyright (C) 2007 Stefan Kopp, Gchingen, Germany  
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Thompson <mcthomps@us.ibm.com> \*/  
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on other drm code.

```

Copyright (C) 2002 Ralf Baechle DO1GRB (ralf@gnu.org) */#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include
<linux/in.h>#include <linux/kernel.h>#include <linux/mo
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Copyright(C) 2012-2016 Canonical Ltd. * * This driver exports EFI runtime services interfaces into userspace, which * allow to use and test UEFI runtime
services provided by firmware.
Copyright (C) 2018 Lorenzo Bianconi <lorenzo.bianconi83@gmail.com> */#include <linux/kernel.h>#include <linux/firmware.h>#include
<linux/module.h>
Copyright (C) 2001-2003 Andreas Gruenbacher, <agruen@suse.de> */
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Copyright (C) 2017-2018 HUAWEI, Inc. * http://www.huawei.com/ * Created by Gao Xiang <gaoxiang25@huawei.com> */#include "xattr.h"
Copyright (C) 2016, Intel Corporation * Author: Rafael J. Wysocki <rafael.j.wysocki@intel.com> */#include <linux/cpufreq.h>
Copyright (C) 1998, 1999 Frodo Looijaard <frodol@dds.nl> * and Philip Edelbrock <phil@netroedge.com> */
Copyright 2009 John F. Reiser <jreiser@BitWagon.com>. All rights reserved
Copyright (C) 2004 Jens Axboe <axboe@kernel.dk> * * Helper functions for setting/querying io priorities of processes. The * system calls closely
mimick getpriority/setpriority, see the man page fo
Copyright 2001,2002 Jeff Garzik <jgarzik@mandrakesoft.com>
Copyright (C) 2011-2012 Linaro Ltd <mturquette@linaro.org> * * Standard functionality for the common clock API. See Documentation/driver-api/clk.rst
Copyright (C) 1992 Eric Youngdale * Simulate a host adapter with 2 disks attached. Do a lot of checking * to make sure that we are not getting blocks
mixed up, and PANIC if * anything out of t
Copyright (C) 2015 VanguardiaSur - www.vanguardiasur.com.ar *
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Copyright (C) 2003 Vadim Catana <skystar@moldova.cc>:
Copyright 2019 NXP Semiconductors */#include <soc/mscc/ocelot.h>#include <linux/packing.h>#include "dsa_priv.h"
Copyright (C) 2005 - 2006 Mike Christie
(C) State timer broadcast controlstatic void lapic_timer_state_broadcast(struct acpi_processor *pr, struct acpi_processor_cx *cx,
int broadcast){ int state = cx - pr->power.st
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Copyright 1999 Hans Reiser, see reiserfs/README for licensing and copyright * details */
Copyright message. * 0.0.17 * Add iec958 file in proc file system to show status of SPDIF in. * 0.0.18 * Implement support for Line-in capture on
SB Live 24bit. * * This code was initia
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Copyright (C) IBM Corporation, 2002, 2004 * * 2002-Oct Created by Vamsi Krishna S <vamsi_krishna@in.ibm.com> Kernel * Probes initial
implementation ( includes suggestions from * Rusty Russell).
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```

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Copyright (C) 2018 Zoro Li \*/  
Copyright (c) 2004 James Courtier-Dutton <James@superbug.demon.co.uk> \* Driver CA0106 chips. e.g. Sound Blaster Audigy LS and Live 24bit \*  
Version: 0.0.18 \* \* FEATURES currently supported:  
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Copyright (C) 2009 Provigent Ltd. \*/#include <linux/clk.h>#include <linux/delay.h>#include <linux/export.h>#include <linux/errno.h>#include  
<linux/err.h>#include <linux/i2c.h>#include <linux/i  
Copyright (C) 2006-2011 Hermann Kneissel herkne@gmx.de \* \* The latest version of the driver can be found at \*  
http://sourceforge.net/projects/garmin-gps/  
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Copyright (C) 2010 Wang Lei. All Rights Reserved  
Copyright (c) 2006 Herbert Valerio Riedel <hvr@gnu.org> \* \* Support added for SMSC LAN8187 and LAN8700 by steve.glendinning@shawell.net  
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Copyright (c) 2017 Microsoft Corporation \* Author: Matthew Wilcox <willy@infradead.org> \* \* See Documentation/core-api/xarray.rst for how to use the  
XArray.  
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Copyright (C) 2002-2008, Lars Ellenberg <lars.ellenberg@linbit.com>.  
Copyright (c) 2012 Samsung Electronics Co., Ltd. \* http://www.samsung.com/ \*/#include <linux/fs.h>#include <linux/f2fs\_fs.h>#include  
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Copyright (C) 1999 - 2001 \* Greg Kroah-Hartman (greg@kroah.com) \* Bill Ryder (bryder@sgi.com)  
Copyright (C) 2010 Elan Digital Systems Limited \* \* based on USB Skeleton driver - 2.2  
Copyright (c) 2017-2018 Hisilicon Limited.  
Copyright (c) 1999-2000 Takashi Iwai <tiwai@suse.de> \* \* Definitions of OSS compatible headers for Emu8000 device informations  
Copyright (C) 2007 Alex Dubov <oakad@yahoo.com> \* \* Special thanks to Carlos Corbacho for providing various MemoryStick cards \* that made this  
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Copyright (C) 2003-2006 David Hollis <dholllis@davehollis.com>  
Copyright (C) 2011 Intel Corporation \* Author: Andrzej Zaborowski <balrogg@gmail.com> \*/#include <linux/etherdevice.h>#include  
<linux/ethtool.h>#include <linux/mii.h>#include <linux/usb.h>#incl  
Copyright (C) 2018 Kontron Electronics GmbH \* \* Transition to SPI MEM interface:  
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```

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(C) 2004 Nadia Yvette Chambers, Oracle */#include "sched.h"
Copyright (C) 2008 Qumranet, Inc.
Copyright (C) 1991-2002 SciTech Software, Inc. All rights reserved
Copyright (C) 2010 by Ondrej Zary * some parts inspired by the cxacru driver */
Copyright (C) 2013-2014 Jens Axboe */
copyright information. * Added MPI2_BIOSPAGE1_OPTIONS_ADVANCED_CONFIG. * Added AdapterOrderAux fields to BIOS
Page 3. * 03-16-15 02.00.31 Updated for MPI v
Copyright (C) 2005-2012 Jean Delvare <jdelvare@suse.de>
Copyright (C) 2016-2018 Linaro Ltd. */#include <linux/clk.h>#include <linux/delay.h>#include <linux/interrupt.h>#include <linux/io.h>#include
<linux/kernel.h>#include <linux/of.h>#include <lin
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Copyright (c) 2016, Jamal Hadi Salim*/
Copyright (C) 2010 ST-Ericsson SA * * This is a generic driver for ARM AMBA-type serial ports. They * have a lot of 16550-like features, but are not
register compatible. * Note that although they
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Copyright 2005 Andrea Merello <andrea.merello@gmail.com>, et al. * * The driver was extended to the RTL8187B in 2008 by:
Copyright (C) 2004 Trond Myklebust * * NFSv4 callback definitions
Copyright (c) 2017 Alex Manoussakis <amanou@gnu.org>
Copyright (c) 1998 by Frank van de Pol <fvdpol@coil.demon.nl> */#ifndef __SND_SEQ_FIFO_H#define __SND_SEQ_FIFO_H
Copyright (c) 2015 HGST, a Western Digital Company. */#include <linux/module.h>#include <linux/err.h>#include <linux/slab.h>#include
<rdma/ib_verbs.h>
Copyright (c) 2016 Icenowy Zheng <icenowy@aosc.xyz> */
Copyright 2006, 2007, Michael Buesch <m@buesch>
Copyright unasserted) * AN = 0 (Audio data) * P = 0 (Consumer) */ def_bits = SPCS_CLKACCY_1000PPM |
SPCS_SAMPLERATE_48 | SPCS_CHANNELNUM_LEFT | SPCS
Copyright © 2000 Nicolas Pitre <nico@cam.org>
Copyright (C) 2015-2016 Samsung Electronics * Igor Kotrasinski <i.kotrasinski@samsung.com> * * Based on dummy_hcd.c, which is:
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Copyright (c) 2008 Patrick McHardy <kaber@trash.net> */#include <linux/types.h>#include <linux/if_ether.h>#include <linux/if_vlan.h>#include
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It reports up to two temperatures (its own plus an
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Copyright (C) 2002, Linus Torvalds * * 10Sep2002 Andrew Morton * Initial version.
Copyright(c) 2002-2010 Exar Corp. *****/#ifndef
VXGE_MAIN_H#define VXGE_MAIN_H

```



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Copyright 2018 Analog Devices Inc. \*/  
Copyright (C) 2016, Semihalf \* Author: Tomasz Nowicki <tn@semihalf.com> \*\* This file implements early detection/parsing of I/O mapping \*  
reported to OS through firmware via I/O Remapping Table (I  
Copyright (C) 2019 Intel Corporation \*/#include <linux/export.h>#include <net/mac80211.h>#include "ieee80211\_i.h"#include "driver-ops.h"  
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Copyright (C) 2000, 2001, 2002 Ingo Molnar \* Designed by David S. Miller, Alexey Kuznetsov and Ingo Molnar \*/  
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Copyright (C) 2007--2014 Jean Delvare <jdelvare@suse.de> \* \* Chip details at <http://www.national.com/ds/LM/LM85.pdf>  
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Copyright (C) 2017 Dennis Zhou <dennisszhou@gmail.com> \* \* The percpu allocator handles both static and dynamic areas. Percpu \* areas  
are allocated in chunks which are divided into units. There  
Copyright (C) 1996, 1997, 1998 Olaf Kirch <okir@monad.swb.de> \*/  
Copyright (C) 2008 Red Hat Inc, Steven Rostedt <rstrostedt@redhat.com> \* \* - Added format output of fields of the trace point. \* This was based off  
of work by Tom Zanussi <tzanussi@gmail.com>.  
Copyright (c) 2008,2009 Katalix Systems Ltd \*/#include <linux/refcount.h>  
Copyright 2019 Amazon.com, Inc. or its affiliates. All Rights Reserved  
Copyright (C) 2007, CenoSYS (www.cenosys.com).  
Copyright (c) 2004 Dmitry Torokhov <dtor@mail.ru>  
Copyright (C) 2009 HIRANO Takahito <hiranotaka@zng.info> \* \* based on pt1dvr - http://pt1dvr.sourceforge.jp/ \* by Tomoaki Ishikawa  
<tomy@users.sourceforge.jp>  
Copyright (C) 2001-2002 Greg Kroah-Hartman \* \* For questions or problems with this driver, contact Texas Instruments \* technical support, or Al  
Borchers <alborchers@steinerpoint.com>, or \* Peter B  
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Copyright (c) 2016-2017, NVIDIA CORPORATION. All rights reserved  
Copyright (C) 2014 Intel Corporation  
Copyright (C) 2013 Intel Corporation \*/#include <linux/tty.h>#include <linux/tty\_flip.h>#include <linux/serial\_reg.h>#include <linux/dma-mapping.h>  
Copyright (c) 2018 VMware, Inc., Palo Alto, CA., USA  
Copyright (C) Tom Long Nguyen (tom.l.nguyen@intel.com)  
Copyright (c) 2015 Cumulus Networks, Inc. \*/  
Copyright (c) 2019 Google LLC \* \* Updated RFC4106 AES-GCM testing. \* Authors: Aidan O'Mahony (aidan.o.mahony@intel.com) \* Adrian  
Hoban <adrian.hoban@intel.com> \* Gabrie  
Copyright (C) 2002,2003 Florian Schirmer <jolt@tuxbox.org> \*/  
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Copyright 2019 ARM Ltd. \* \* Generic implementation of update\_vsyscall and update\_vsyscall\_tz.  
Copyright (C) 2018-2019 Corentin Labbe <clabbe@baylibre.com> \* \* Core file which registers crypto algorithms supported by the hardware.  
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copyright (C) 2005, Chuck Lever <cel@netapp.com> \*/  
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Copyright (c) 2008 Anssi Hannula <anssi.hannula@gmail.com> \*/  
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Copyright (c) 2019 HiSilicon Limited. #ifndef \_\_HISI\_HPRE\_H#define \_\_HISI\_HPRE\_H  
Copyright Rusty Russell IBM Corporation 2007. #ifndef \_\_KERNEL\_\_ #include <stdint.h> #endif #include <linux/types.h> #include <linux/virtio\_types.h>  
Copyright (C) 2013, Carlo Caione <carlo@caione.org> \*/  
Copyright (C) 2011 Texas Instruments Inc. \*/  
Copyright (C) 2016 Chen-Yu Tsai \* \* Chen-Yu Tsai <wens@csie.org>  
Copyright (c) 2005 Intel Inc. All rights reserved  
Copyright (c) 2012, Intel Corporation. \*/ #ifndef \_HID\_SENSOR\_TRIGGER\_H#define \_HID\_SENSOR\_TRIGGER\_H  
Copyright (c) 2011 Stephen Hemminger <shemminger@vyatta.com>  
Copyright (C) 2017 Martin Blumenstingl <martin.blumenstingl@googlemail.com>  
Copyright (c) by Jaroslav Kysela <perex@perex.cz>, \* Hannu Savolainen 1993-1996, \* Rob Hooft \* \* Routines  
for control of AdLib FM cards (OPL  
Copyright (C) 2005 Yani Ioannou <yani.ioannou@gmail.com> \*/ #ifndef \_LINUX\_HWMON\_SYSFS\_H#define \_LINUX\_HWMON\_SYSFS\_H  
Copyright (C) 2013-2018 Toradex AG <stefan.agner@toradex.com> \*/  
Copyright (c) 2019 HiSilicon Limited. #include <asm/page.h> #include <linux/bitmap.h> #include <linux/debugfs.h> #include <linux/dma-  
mapping.h> #include <linux/io.h> #include <linux/irqreturn.h> #incl  
Copyright (C) 2012-2013 Philippe De Muyter <phdm@macqel.be> \*/  
Copyright (C) 2009 Albert Herranz  
Copyright (C) 2001, 2002, 2003 Patrick Mochel \* \* Based on kobject.h:  
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driver  
Copyright (C) 2017 Google, Inc. \* \* Authors:  
Copyright (c) by Jaroslav Kysela <perex@perex.cz> \* Definitions for Yamaha YMF724/740/744/754 chips \*/  
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Copyright (C) John H Terpstra 1996-2000  
Copyright (C) 2008 Hans Verkuil <hverkuil@xs4all.nl>  
Copyright (C) 2017 Linaro Ltd. \*/ #ifndef \_\_VENUS\_HFI\_HELPER\_H\_\_ #define \_\_VENUS\_HFI\_HELPER\_H\_\_  
Copyright (C) 1998 Jakub Jelinek <jj@ultra.linux.cz>  
Copyright (C) 2013 Frank Sch  
Copyright (c) 2014, 2015 AMD Corporation. \* Authors: Ken Xue <Ken.Xue@amd.com> \* Wu, Jeff <Jeff.Wu@amd.com> \* \* Contact Information:  
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Copyright (C) 2001-5, B2C2 inc. \* \* GPL/Linux driver written by Patrick Boettcher <patrick.boettcher@posteo.de>  
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Copyright (C) 2003 Greg Kroah-Hartman <greg@kroah.com>  
Copyright 2002-2003, Instant802 Networks, Inc. \*/ #include <linux/types.h> #include <linux/bitops.h> #include <linux/ieee80211.h> #include  
<asm/unaligned.h>  
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 Copyright (C) 2008 Hans de Goede <hdegoede@redhat.com>  
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 Copyright (c) 2015 Jiri Pirko <jiri@resnulli.us> \*/  
 Copyright (c) by Jaroslav Kysela <perex@perex.cz>, \* Creative Labs, Inc. \* Definitions for EMU10K1 (SB Live!) chips \*  
 Copyright (c) 2013 Guenter Roeck <linux@roeck-us.net> \*/  
 Copyright (c) 1998 by Frank van de Pol <fvdpol@coil.demon.nl> \*/#ifndef \_\_SND\_SEQ\_INFO\_H#define \_\_SND\_SEQ\_INFO\_H  
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 Copyright (C) 2008 Pierrick Hascoet <pierrick.hascoet@abilis.com> \*/  
 Copyright (C) 2016 Red Hat, Inc. \*\* This is a limited-size FIFO maintaining pointers in FIFO order, with \* one CPU producing entries and another  
 consuming entries from a FIFO.  
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 Copyright (C) 2011-2013 Texas Instruments Inc. \*/  
 Copyright (C) 2013 Eduardo Valentin <eduardo.valentin@ti.com> \*/#ifndef \_\_THERMAL\_HWMON\_H\_\_define \_\_THERMAL\_HWMON\_H\_\_  
 Copyright 2017 Peter Meerwald-Stadler <pmeerw@pmeerw.net> \*\* Datasheet: <https://www.idt.com/document/dst/zopt2201-datasheet> \* 7-bit I2C  
 slave addresses 0x53 (default) or 0x52 (programmed)  
 Copyright (C) 1999, 2002-2003 Hewlett-Packard Co. \* David Mosberger-Tang <davidm@hpl.hp.com> \* Stephane Eranian <eranian@hpl.hp.com>  
 \*/#include <linux/init.h>#include <linux/string.h>#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include  
 Copyright (C) 2012 Dmitry Torokhov <dmitry.torokhov@gmail.com> \*\* Based on PWM beeper driver:  
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 Copyright (C) 2002 Greg Kroah-Hartman (greg@kroah.com) \*/  
 Copyright (c) 2002,2003 by Takashi Iwai <tiwai@suse.de> \*\* STRATEGY \* for playback, we send series of "chunks", which size is equal with the \* IBL  
 size, typically 126 samples. at each end of c  
 Copyright (c) 2011, 2012 Anssi Hannula <anssi.hannula@iki.fi>  
 Copyright (c) by Uros Bizjak <uros@kss-loka.si> \*\* Midi Sequencer interface routines for OPL2/OPL3/OPL4 FM  
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 Copyright (C) 2017 Linaro Ltd. \*/#ifndef \_\_VENUS\_VENC\_H\_\_define \_\_VENUS\_VENC\_H\_\_  
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 Copyright (c) 2016, Intel Corporation \* Authors: Weigang Li <weigang.li@intel.com> \* Giovanni Cabiddu <giovanni.cabiddu@intel.com> \*/#ifndef  
 \_CRYPTO\_ACOMP\_INT\_H#define \_CRYPTO\_ACOMP\_INT\_  
 (C) 2001 Marc Boucher (marc@mbsi.ca).  
 Copyright (C) 2002 - 2005, Areca Technology Corporation All rights reserved  
 Copyright (C) 2013 David Herrmann <dh.herrmann@gmail.com> \*/  
 Copyright (C) 2018, ARM \*\* This file implements parsing of the Processor Properties Topology Table \* which is optionally used to describe the processor  
 and cache topology. \* Due to the relative p  
 Copyright (C) Joerg Reuter DL1BKE (jreuter@yaina.de) \*/#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include  
 <linux/in.h>#include <linux/kernel.h>#include <linux/mod  
 Copyright (C) 1997, 1998 Francois-Rene Rideau <fare@tunes.org>  
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 Copyright 2011-2012 Freescale Semiconductor, Inc \* \* Author: Dipen Dudhat <Dipen.Dudhat@freescale.com>  
 (C) CC Computer Consultants GmbH, 2007 \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/in.h>#include <linux/in6.h>#include  
 <linux/ip.h>#include <linux/ipv6.h>#include <linux/jhash.  
 Copyright (c) 2009-2010 Stephane Chatty \*/  
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 Copyright (c) 2016 Tom Haynes <loghyr@primarydata.com> \* \* The following implements a super-simple flex-file server \* where the NFSv4.1 mds is  
 also the ds. And the storage is \* the same. I.e., wri  
 Copyright (C) 2014, 2015 Intel Corporation \* \* Authors:  
 Copyright 2007-2008 Jaime Velasco Juan <jsagarribay@gmail.com> \*/  
 (C) 2005 by Christian Hentschel <chentschel@arnet.com.ar> \* based on RR's ip\_nat\_ftp.c and other modules.  
 Copyright 2006 Openedhand Ltd. \* \* Author: Richard Purdie <rpurdie@openedhand.com>  
 Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_EXEC\_H#define \_\_ASM\_EXEC\_H  
 Copyright (C) 2016 Intel Corporation \*/  
 Copyright (C) 2019 Linaro Ltd. \* Author: Masami Hiramatsu <mhiramat@kernel.org> \*/  
 Copyright (c) 2007 Freescale Semiconductor, Inc. \*/#include <linux/kernel.h>#include <linux/string.h>#include <linux/errno.h>#include  
 <linux/unistd.h>#include <linux/interrupt.h>#include <linux  
 Copyright (C) 2005 David Brownell  
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 Copyright (C) 2019 Micro Crystal AG \* Author: Alexandre Belloni <alexandre.belloni@bootlin.com> \*/#include <linux/i2c.h>#include  
 <linux/bcd.h>#include <linux/rtc.h>#include <linux/module.h>#inc  
 Copyright (c) 2014 Applied Micro Circuits Corporation. \* \* Author: Tanmay Inamdar <tinamdar@apm.com>.  
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 Copyright (C) 2005 Grant Coady <gcoady.lk@gmail.com> with valuable \* guidance from Jean Delvare \* \* Driver supports  
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 Copyright 2007-2008 Michael Buesch <m@bues.ch>  
 (C) 2007 sgi. Christoph Lameter. \* \* Virtual memory maps allow VM primitives pfn\_to\_page, page\_to\_pfn, \* virt\_to\_page, page\_address() to be  
 implemented as a base offset \* calculation without memor  
 Copyright (C) 2006 Aurelien Jacobs <aurel@gnuage.org > \* \*Based on LzmaDecode.c from the LZMA SDK 4.22 (<http://www.7-zip.org/>)  
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 Copyright (C) 2004-2007 Hans Verkuil <hverkuil@xs4all.nl>  
 Copyright (C) 2010 Hans de Goede <hdegoede@redhat.com> \* \* This driver is based on earlier work of: \*  
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 Copyright (c) 2010 Don Prince <dhprince.devel@yahoo.co.uk> \* \* This driver supports several HID devices: \*  
 Copyright (C) 2000 Deep Blue Solutions Ltd  
 Copyright (c) 2002, 2003, 2004, 2005, 2006, 2007, 2008 \* Phillip Lougher <phillip@squashfs.org.uk> \* \* file.c  
 Copyright 1999 Gregory P. Smith \*/#include <linux/acpi.h>#include <linux/clock.h>#include <linux/dma-mapping.h>#include <linux/err.h>#include <linux/kernel.h>#include <linux/hrtimer.h>#include <  
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 Copyright (C) 2018-2019 Intel Corporation \* All rights reserved  
 Copyright (c) 2012 Hans de Goede <hdegoede@redhat.com> \* \* Based on the ALSA driver for TEA5757/5759 Philips AM/FM radio tuner chips: \*  
 Copyright (C) Barak A. Pearlmutter. \* \* Chopped about by Alan Cox 22/3/96 to make it fit the AF\_UNIX socket problem. \* If it doesn't work blame me, it worked when Barak sent it.  
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 Copyright (C) 2002 Ralf Baechle DO1GRB (ralf@gnu.org) \*/#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include <linux/in.h>#include <linux/kernel.h>#include <linux/ji  
 Copyright (c) 2019 Facebook Inc. \*/  
 Copyright (c) <2013-2014> Linaro Ltd. \* http://www.linaro.org \* \* Author: Guodong Xu <guodong.xu@linaro.org>  
 Copyright (C) 2018 Marek Behun <marek.behun@nic.cz> \*/  
 Copyright (c) 2016 Christoph Hellwig. \*/#include <linux/device.h>#include <linux/blk-mq.h>#include <linux/blk-mq-virtio.h>#include <linux/virtio\_config.h>#include <linux/module.h>#include "blk-  
 Copyright (c) 2000-2006, 2012-2016, Ericsson AB  
 Copyright (C) 2005-2015 Promise Technology Inc. \* \* Written By:  
 Copyright (c) 2012 Samsung Electronics Co., Ltd. \* http://www.samsung.com/ \*/#include <linux/blkdev.h>#include <linux/backing-dev.h>  
 Copyright (C) 2007 AMD (http://www.amd.com) \* Author: Thomas Dahlmann \*/  
 (c) Copyright 1995 Alan Cox <alan@lxorguk.ukuu.org.uk> \* \* Release 0.10.  
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Copyright (C) Jay Lan SGI */
Copyright (c) 2015, Intel Corporation * Authors: Tadeusz Struk <tadeusz.struk@intel.com> */#ifndef _RSA_HELPER_#define _RSA_HELPER_#include
<linux/types.h>
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<scsi/scsi_dh.h>#include <scsi/scsi_device.h>
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(C) Copyright 2005, Intec Automation, *   Mike Lavender (mike@steroidmicros)
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<linux/init.h>#include <linux/kthread.h>#include <linux/ex
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Copyright 2016 Integrated Device Technology, Inc. */
Copyright (C) 2016 Cavium, Inc. */#include <linux/module.h>#include "cptpf.h"
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<linux/skbuff.h>#include <linux/math64.h>#include
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Copyright (C) 2002-2004 Eric Biederman <ebiederm@xmission.com> \*/  
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<linux/i2c.h>#include <linux/interrupt.h>#include <lin  
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Copyright (c) 2013, Applied Micro Circuits Corporation \* Author: Feng Kan <fkan@apm.com> \* Author: Loc Ho <lho@apm.com> \* \* This driver provides  
system reboot functionality for APM X-Gene SoC. \*  
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Copyright (c) 2019, Rishi Gupta <gupt21@gmail.com> \* \* Datasheet: https://www.vishay.com/docs/84366/veml6030.pdf \* Appnote-84367:  
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<linux/smp.h>#include <linux/topology.h>#include <linu  
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Andreas Dilger <adilger@sun.com>  
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Copyright (C) 2018 Red Hat, Inc. \*/#include <linux/errno.h>#include <linux/sched.h>#include <linux/kernel.h>#include <linux/list.h>#include <linux/rcupdate.h>#include  
<linux/slab.h>#include <linux/sched.h>#include <  
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Copyright (c) 2018-2019 Christoph Hellwig \*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/errno.h>#include  
<linux/syscalls.h>#include <linux/compat.h>#include <linux/refcount.  
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Andreas Schwab  
copyright 1989 Regents of the University of California.\n");#endif#endif CONFIG\_SLIP\_SMART printk(KERN\_INFO "SLIP linefill/keepalive  
option.\n");#endif  
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Copyright (C) 2017 Paolo Valente <paolo.valente@linaro.org> \* \* BFQ is a proportional-share I/O scheduler, with some extra \* low-latency capabilities.  
BFQ also supports full hierarchical \* schedul  
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Copyright (c) 2006,2007 Daniel Mack, Tim Ruetz\*/  
Copyright (c) 2010 Mika Laitio <lamikr@pilppa.org> \* \* This driver will read and write the value of 4 counters to w1\_slave file in \* sys filesystem. \*  
Inspired by the w1\_therm and w1\_ds2431 driver  
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Copyright (C) 2016 Jens Axboe <axboe@kernel.dk> \*/#include <linux/kernel.h>#include <linux/fs.h>#include <linux/blkdev.h>#include <linux/blk-  
mq.h>#include <linux/elevator.h>#include <linux/bio.  
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Copyright (c) 2014-2016 Christoph Hellwig. \*/#include <linux/sunrpc/svc.h>#include <linux/blkdev.h>#include <linux/nfs4.h>#include  
<linux/nfs\_fs.h>#include <linux/nfs\_xdr.h>#include <linux/pr.h  
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<linux/seq\_file.h>#include <linux/uaccess.h>#include <linux/trace.h>#i  
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<linux/mfd/syscon.h>#include "meson-aoclk.h"#include "gxbb-aoclk  
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\* one is for firmware access and the second is  
Copyright (c) 2006 Rick Koch <n1gp@hotmail.com> \* \* Based on MicroTouch driver (drivers/input/touchscreen/mtouch.c)  
Copyright (C) 2005-2006 Intel Corporation \* Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com> \* \* FIXME: doc: overview of the API, different  
parts and pointers

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"compress.h"#include <linux/prefetch.h>  
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Copyright (c) 2018-2019 Synopsys, Inc. and/or its affiliates. \* Synopsys DesignWare eDMA PCIe driver \* \* Author: Gustavo Pimentel  
<gustavo.pimentel@synopsys.com>  
Copyright (C) 2010 Marvell International Ltd. \* Zhangfei Gao <zhangfei.gao@marvell.com> \* Kevin Wang <dwang4@marvell.com> \*  
Mingwei Wang <mwwang@marvell.com> \* Philip Rakity <prakity@marvell.c  
Copyright (c) 2015 Benjamin Tissoires \*/  
Copyright (C) 2011 Richard Cochran <richardcochran@gmail.com> \*/  
(C) Copyright 2007, 2009 Tiago Vignatti <vignatti@freedesktop.org>  
Copyright (C) 2006-2008 Nippon Telegraph and Telephone Corporation. \* \* Written by Koji Sato.  
Copyright 2007 Maarten Maathuis  
Copyright (c) 2010 Broadcom Corporation \*/#include <linux/types.h>#include <linux/module.h>#include <linux/if\_ether.h>#include  
<linux/spinlock.h>#include <linux/skbuff.h>#include <linux/netdevi  
Copyright(C) Timesys Corporation 2015  
Copyright (C) 2004 Arcom Control Systems Ltd <linux@arcom.com>  
Copyright (C) 2008, 2005 IBM Corporation.  
Copyright 2013-2015 Maxim Integrated Products  
Copyright (c) 2012 Samsung Electronics Co., Ltd. \* http://www.samsung.com/ \*/#include <linux/fs.h>#include <linux/f2fs\_fs.h>#include  
<linux/stat.h>#include <linux/buffer\_head.h>#inc  
Copyright (C) 2015 Huawei Futurewei Technologies. \*/  
Copyright (C) 2010 Devin Heitmueller <dheitmueller@kernellabs.com> \*/  
Copyright (C) 2017 Oracle. \* All Rights Reserved  
Copyright 2007 Pierre Ossman \*/#ifndef \_MMC\_CORE\_SDIO\_BUS\_H#define \_MMC\_CORE\_SDIO\_BUS\_H  
Copyright 2018 David Lechner <david@lechnology.com>  
(C) Copyright Yggdrasil Computing, Inc. 2000 \* (usb\_device\_id matching changes by Adam J. Richter)  
Copyright (C) 2012 Linaro Ltd. \* \* Author: Dong Aisheng <dong.aisheng@linaro.org>  
Copyright (C) 2000 Al Borchers <borchers@steinerpoint.com> \* \* See Documentation/usb/usb-serial.rst for more information on using this \*  
driver  
Copyright (c) 2017, Impinj, Inc. \* \* i.MX7 System Reset Controller (SRC) driver  
Copyright (C) 2002 Hiroshi Aono (h-aono@ap.jp.nec.com)  
Copyright (c) International Business Machines Corp., 2002,2004  
Copyright(C) 2005, Benedikt Spranger <b.spranger@linutronix.de>  
Copyright (c) by Takashi Iwai <tiwai@suse.de> \* \* A next major update in 2010 (separate timers for playback and capture):  
Copyright (C) 2014 Intel Corporation \* \* DRM universal plane helper functions  
Copyright (C) 2016 Jens Axboe \*/#include <linux/kernel.h>#include <linux/rculist.h>#include <linux/blk-mq.h>  
Copyright (c) 2010 by Mauro Carvalho Chehab  
Copyright (c) 1998-2005 Vojtech Pavlik \*/  
Copyright (c) by Jaroslav Kysela <perex@perex.cz> \* Creative Labs, Inc. \* Routines for control of EMU10K1 chips \*

```

Copyright (C) 2003 MontaVista Software Inc. * Author: Jun Sun, jsun@mvista.com or jsun@junsun.net */
Copyright (C) 2007 Wolfgang Grandegger <wg@grandegger.com>
Copyright (C) 2010 Texas Instruments * Author: Felipe Balbi <balbi@ti.com> */
Copyright (c) 2015 Intel Corporation */
Copyright (c) 1996-2006, 2013-2016, Ericsson AB
Copyright (C) 2012 Seth Jennings <sjenning@linux.vnet.ibm.com>*/
Copyright (c) 2018 Johannes Thumshirn, SUSE Linux GmbH * * This is entirely based on drivers/nvme/host/trace.h
Copyright (c) 2018-2019 The Linux Foundation. All rights reserved
Copyright IBM Corporation, 2019 * * Author: Paul E. Mckenney <paulmck@linux.ibm.com>
Copyright (C) 2011-2012 Red Hat, Inc., Peter Zijlstra */
Copyright (c) 2001-2002 International Business Machines, Corp.
Copyright (C) 2018-2019 Rockwell Collins */
Copyright (C) 2003 Roy Bulter <rbulter@hetnet.nl> * * Based on SAA7126 video encoder driver by Gillem & Andreas Oberritter
Copyright (c) 2007 Cisco Systems, Inc. All rights reserved
Copyright 2003 mjander * mjander@users.sourceforge.org
*****/
Copyright (c) 2015-2016, Integrated Device Technology Inc.
Copyright (C) 2008 Fabio Checconi <fabio@gandalf.sssup.it> * Paolo Valente <paolo.valente@unimore.it> *
Copyright (C) 2009 - 2015 Xilinx, Inc. */
Copyright (C) 2012 Google, Inc * * The ChromeOS EC multi function device is used to mux all the requests * to the EC device for its multiple features:
keyboard controller, * battery charging and r
(C) Copyright 2000 STMicroelectronics */
Copyright (C) 2005-2006, Thomas Gleixner, Russell King * * This file contains the core interrupt handling code. Detailed * information is available in
Documentation/core-api/genericirq.rst
Copyright (C) 2005 Trond Myklebust * * NFSv4-specific filesystem definitions and declarations
Copyright (C) 2010 Google, Inc. * Rewritten by David Rientjes * * The routines in this file are used to kill a process when * we're seriously out of
memory. This gets called from __alloc_pages
Copyright (C) 2004 Clemens Fruhwirth <clemens@endorphin.org>
Copyright (C) 2006 James Smart, Emulex Corporation */#ifndef SCSI_NETLINK_FC_H#define SCSI_NETLINK_FC_H
Copyright (C) 2001-2018 Miklos Szeredi <miklos@szeredi.hu>
Copyright (C) Andrew Tridgell 1992-2000
Copyright (C) 2013, Seth Jennings, IBM * * Concepts based on zcache internal zbud allocator by Dan Magenheimer.
Copyright (c) 2007-2012 Nicira, Inc. */
Copyright (C) 2005-2006 Micronas USA Inc.static int go7007_usb_vendor_request(struct go7007 *go, u16 request, u16 value, u16 index, void
*transfer_buffer, int length, int in){ struct go7007_usb *u
Copyright (C) 2018 NVIDIA Corporation. All rights reserved
Copyright (C) International Business Machines Corp., 2002,2010 * Author(s): Steve French (sfrench@us.ibm.com) * * Contains the routines for
constructing the SMB PDUs themselves
Copyright (c) 2017 Dialog Semiconductor * * Author: Adam Thomson <Adam.Thomson.Opensource@diasemi.com>
Copyright (C) 2007-2009 Arjan Opmeer <arjan@opmeer.net> * * Trademarks are the property of their respective owners.
Copyright (c) 2003 Michal Sojka, for high-speed iso transfers */
Copyright (C) 2007 Alain Degreffe (eczema@ecze.com) *
Copyright (C) People who wrote linux/arch/i386/kernel/sys_i386.c
Copyright (C) 2002 Linus Torvalds.
Copyright (C) 2013-2014 Chelsio Communications. All rights reserved
Copyright (C) 2017 Mobica Limited * * This driver is inspired by the 4.6.2 version of net/can/usb/usb_8dev.c
Copyright (c) 2017 Jesper Dangaard Brouer, Red Hat Inc. */#ifndef __LINUX_NET_XDP_H__#define __LINUX_NET_XDP_H__
Copyright (C) 2004-2010 Markus Grabner (grabner@icg.tugraz.at) * Emil Myhrman (emil.myhrman@gmail.com) */
Copyright (c) 2015 Sabrina Dubroca <sd@queasysnail.net> */
Copyright (C) 2004 Arnd Bergmann (arnd@arndb.de) * * This code is collected from the versions for sparc64, mips64, s390x, ia64, * ppc64 and
x86_64, all of which are based on the original sparc64 v
Copyright (c) 2013-2019, NVIDIA CORPORATION. All rights reserved
Copyright (C) 2005 by Thomas Winischhofer, Vienna, Austria
(C) 2006-2012 Patrick McHardy <kaber@trash.net> * * Limitations:
(C) Copyright 2005 Robert Love */
Copyright (c) 2015-2016 HGST, a Western Digital Company. */#ifndef _NVME_FABRICS_H#define _NVME_FABRICS_H 1
Copyright (c) 2001-2002, 2007 Johann Deneux <johann.deneux@gmail.com> * * USB/RS232 I-Force joysticks and wheels.
Copyright (c) 2018-2019 Synopsys, Inc. and/or its affiliates. * Synopsys DesignWare eDMA core driver * * Author: Gustavo Pimentel
<gustavo.pimentel@synopsys.com>
Copyright 2005, Devicescape Software, Inc. */
Copyright (c) 2013 Christian Ohm * Heavily inspired by various other HID drivers that adjust the report * descriptor.*/

```

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 Copyright (c) Clemens Ladisch <clemens@ladisch.de>  
 Copyright 2009-2011 Analog Devices Inc. \*/  
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 Copyright (c) 2007 Tan Swee Heng <thesweeheng@gmail.com> \* \* Derived from:  
 Copyright © 2007-2008 Intel Corporation \* Jesse Barnes <jesse.barnes@intel.com>  
 copyright by their \* authors. Thanks to Matt Dharm for giving us permission! \* \* This driver implements a SCSI host controller driver and a USB \* device  
 driver. To avoid confusion, all the USB rel  
 Copyright (C) 2012 David Gibson, IBM Corporation. \*/#include "libfdt\_env.h"  
 Copyright (c) 2009 Atheros Communications Inc.  
 Copyright 2009 Geert Uytterhoeven \* \* Based on the A2000 TOD code in arch/m68k/amiga/config.c  
 Copyright (C) 1999-2002 Ralph Metzler \* & Marcus Metzler for convergence integrated media GmbH \* \* 26feb2004 Support for FS Activy  
 Card (Grundig tuner) by \* Michael Dreher <michael@5dot1.  
 Copyright (C) 2012 ST-Ericsson SA \* Written on behalf of Linaro for ST-Ericsson \* This interface is used in the core to keep track of pins. \* \* Author:  
 Linus Walleij <linus.walleij@linaro.org>  
 Copyright (C) 2016 Lorenzo Bianconi <lorenzo.bianconi83@gmail.com> \*/  
 Copyright (C) 2005 Intel Corporation  
 Copyright (c) 2013 Hisilicon Limited. \*/  
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 Copyright (C) 2003 Mark Studebaker \* \* Supports the following chips: \*  
 Copyright 2000 by Hans Reiser, licensing governed by reiserfs/README \* \* Trivial changes by Alan Cox to remove EHASHCOLLISION for compatibility  
 Copyright (c) 2017 Mellanox Technologies. All rights reserved  
 Copyright (c) 2019 Mellanox Technologies.  
 Copyright (C) 2014-2018 Jonathan Morton <chromatix99@gmail.com>  
 Copyright (C) Alex Aizman, 2004 - 2005 \*/#ifndef SCSI\_TRANSPORT\_ISCSI\_H#define SCSI\_TRANSPORT\_ISCSI\_H  
 Copyright (c) 2009-2017, The Linux Foundation. All rights reserved  
 Copyright 2003 mjander \* mjander@users.sourceforge.net  
 \*\*\*\*\*/  
 Copyright (C) 2014 Christoph Hellwig \* \* SCSI queueing library. \* Initial versions: Eric Youngdale (eric@andante.org). \* Based upon  
 conversations with large numbers \*  
 Copyright (c) 2013 Cisco Systems, Inc. \*/  
 Copyright (C) 2015-2016 Marvell International Ltd.  
 Copyright (c) 2015, RapidIO Trade Association \*/  
 Copyright (c) 2004 Jouni Malinen <j@w1.fi> \*/#include <crypto/internal/hash.h>#include <asm/byteorder.h>#include <linux/init.h>#include  
 <linux/module.h>#include <linux/string.h>#include <linux/  
 Copyright (C) 2006 Red Hat, Inc., Ingo Molnar <mingo@redhat.com> \*/#include <linux/sched/task\_stack.h>#include <linux/sched/debug.h>#include  
 <linux/sched.h>#include <linux/kernel.h>#include <lin  
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 Copyright 2003 Digi International (www.digi.com) \*  
 (C) 2003 Ardis Technologies <roman@ardistech.com>  
 Copyright (C) 2003 Maxim Krasnyansky <maxk@qualcomm.com>  
 Copyright (c) 2014,2015 Ken Xue <Ken.Xue@amd.com> \* Jeff Wu <Jeff.Wu@amd.com> \*/  
 Copyright (C) 1995-2000 Simon G. Vogl  
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 Copyright (C) International Business Machines Corp., 2009  
 Copyright (C) 2003 - 2004, 2006 Tresys Technology, LLC  
 Copyright (C) 2013, NVIDIA Corporation. All rights reserved  
 Copyright 2019 Linaro, Ltd., Rob Herring <robh@kernel.org>#ifndef \_\_PANFROST\_ISSUES\_H\_\_#define \_\_PANFROST\_ISSUES\_H\_\_  
 Copyright (c) 2006 by Jing Min Zhao <zhaojingmin@users.sourceforge.net> \* \* See nf\_conntrack\_helper\_h323\_asn1.h for details.  
 Copyright (C) 2004 Novell, Inc. All rights reserved  
 Copyright (C) 2015 ARM Limited \*/  
 Copyright 2004-2006 Pavel Pisa - DCE FELK CVUT cz  
 Copyright 2007, Frank A Kingswood <frank@kingswood-consulting.co.uk>  
 Copyright (C) 2004 Compulab Ltd. \* \* Driver for MAX6902 spi RTC  
 Copyright (c) 2019, Linaro Limited \*/#include "nvmem.h"  
 Copyright (C) 2009 NetUP Inc.  
 Copyright (c) 2003 Takashi Iwai <tiwai@suse.de>  
 Copyright (c) 2006-2007 Nick Kossifidis <mickflemm@gmail.com> \* \* Permission to use, copy, modify, and distribute this software for any \* purpose  
 with or without fee is hereby granted, provided tha

Copyright (C) 1991, 1992 Linus Torvalds \* \* GK 2/5/95 - Changed to support mounting root fs via NFS \* Added initrd & change\_root: Werner Almesberger & Hans Lermen, Feb '96 \* Moan early if gc

Copyright (C) 2013 ARM Ltd.

Copyright (C) 2014 Marvell \* \* Thomas Petazzoni <thomas.petazzoni@free-electrons.com> \* Marcin Wojtas <mw@semihalf.com>

Copyright 2005-2006 Luc Verhaegen

Copyright 2016 Broadcom \*/#ifndef DRIVERS\_PCI\_ECAM\_H#define DRIVERS\_PCI\_ECAM\_H

Copyright (C) 2008 <srinivasa.deevi at conexant dot com> Based on em28xx driver

(C) Copyright 2002,2004 Greg Kroah-Hartman

Linux Kernel v5.4.115 centos:kernel/5.4.115-200.el7/x86\_64

(C) 10#define AC97\_GP\_DRSS\_78 0x0400 /\* LR 7+8 \*/

(C) \* 11101: sent, \* recv\_ack (B) or implicit "ack" (A), \* still waiting for the barrier ack. \* master\_bio may already be completed and invalidated. \* 11100: write acked

(C) 1986-1988 V4.0I 03/16/88

(C) 1986-1989 V5.0C2/14/89

(C) 1986-1989 V6.0A7/28/89

(C) 1986-1990 1800-V1.07/28/89", 5, 50, 6, 0x1fcc

(C) 1986-1990 1800-V2.07/28/89", 5, 50, 6, 0x1fcc

(C) 1986-1990 1800-V2.07/28/89", 72, 50, 6, 0x1fa2

(C) 1986-1990 1800-V2.0", 73, 43, 6, 0x1fa2

(C) 1986-1990 V6.0105/31/90

(C) 1986-1990 V6.0209/18/90

(C) 1986-1990 V7.009/18/90

(C) 1991 1800-V2.0", 72, 39, 6, 0x1fa3

(C) 1991, 1992 by Linus Torvalds \*/

(C) 1991 Linus Torvalds - minix filesystem \*/

(C) 1991 Linus Torvalds - minix filesystem \* 1992, 1993, 1994 Eric Youngdale Modified for ISO 9660 filesystem. \* 1994 Eberhard Mönkeberg - multi session handling. \* 1995 Mark Dob

(C) 1991 Linus Torvalds - minix filesystem \* \* affs directory handling functions

(C) 1991 Linus Torvalds - minix filesystem \* \* affs regular file handling primitives

(C) 1991 Linus Torvalds - minix filesystem \*/#include <linux/sched.h>#include <linux/cred.h>#include <linux/gfp.h>#include "affs.h"

(C) 1991 Linus Torvalds - minix filesystem \* \* Steve Beynon : Missing last directory entries fixed \* (stephen@askone.demon.co.uk) : 21st June 1996

(C) 1992, 1993, 1994 Eric Youngdale Modified for ISO 9660 filesystem. \*

(C) 1992,1993 Brad McLean. See aha1740.c \* for more info \*

(C) 1992, 1993 Eric Youngdale \* \* Rock Ridge Extensions to iso9660

(C) 1992, 1993 Linus Torvalds, (C) 1997 Ingo Molnar \* \* IRQ/IPI changes taken from work by Thomas Radke \* <tomsoft@informatik.tu-chemnitz.de>

(C) 1992, 1993 Linus Torvalds, (C) 1997 Ingo Molnar \* \* moved some of the old arch/i386/kernel/irq.h to here. VY

(C) 1992 Eric Youngdale Modified for ISO 9660 filesystem. \*

(C) 1992 Eric Youngdale Modified for ISO9660 filesystem. \*

(C) 1992 V3.00.004/02/92", 5, 44, 6, 0

(C) 1992 V8.00.004/02/92 \* \* (The cards which do \*NOT\* work are all 8-bit cards -- although some of \* them have a 16-bit form-factor, the upper 8-bits are used only for IRQs \* and are \*NOT\* used f

(C) 1993 by Hamish Macdonald \*

(C) 1993 by Hartmut Schirmer

(C) 1993, Hamish Macdonald, see a2091.c for more info \*

(C) 1993, Hamish Macdonald, see a3000.c for more info \*

(C) 1993, Hamish Macdonald, see mvme147.c for more info \*

(C) 1993, Ralf Baechle, see gvp11.c for more info

(C) 1993 Ray Burr - Amiga FFS filesystem. \* \* Please send bug reports to: hjw@zvw.de

(C) 1993 Ray Burr - Modified for Amiga FFS filesystem. \*

(C) 1993 V3.203/12/93", 5, 44, 7, 0 { "IBM F1 P2 BIOS v1.0011/09/92", 5, 28, 7, 0x1ff3 }, { "IBM F1 P2 BIOS v1.0104/29/93", 5, 28, 7, 0 }, { "Future Domain Corp. V1.0008/18/93", 5, 33, 7

(C) 1994, 1995, 1996,1997 Russell King

(C) 1994, 1995, 1996\* Normunds Saumanis (normunds@fi.ibm.com)

(C) 1994-2001 Russell King

(C) 1994-2001 Russell King \* \* This file contains the ARM-specific time handling details:

(C) 1994 Linus Torvalds \*/

(C) 1994\* Normunds Saumanis (normunds@rx.tech.swh.lv)

(C) 1994 V3.5", 5, 44, 7, 0 { "FUTURE DOMAIN CORP. V3.6008/18/93", 5, 34, 7, 0 }, { "FUTURE DOMAIN CORP. V3.6108/18/93", 5, 34, 7, 0 }, };#define SIGNATURE\_COUNT ARRAY\_SIZE(signatures)

```

(C) 1995-2001 Russell King v0.04\n";
(C) 1995-2003 Linux DECnet Project Team\n";
(C) 1995 by Andreas Busse (andy@waldorf-gmbh.de) * * A driver for the Mac onboard Sonic ethernet chip.
(C) 1995 by Andreas Busse (andy@waldorf-gmbh.de) * * A driver for the onboard Sonic ethernet controller on Mips Jazz * systems (Acer Pica-61, Mips
Magnum 4000, Olivetti M700 and * perhaps others,
(C) 1995 by Andreas Busse (andy@waldorf-gmbh.de) * * A driver for the onboard Sonic ethernet controller on the XT2000.
(C) 1995 by Andreas Busse (andy@waldorf-gmbh.de) * * Core code included by system sonic drivers
(C) 1995 Linus Torvalds * * Alpha division..
(C) 1996,1998 by Thomas Bogendoerfer (tsbogend@alpha.franken.de) * * This driver is based on work from Andreas Busse, but most of * the code is
rewritten.
(C) 1996-1998 by Thomas Sailer, HB9JNX/AE4WA */
(C) 1996-1998 by Thomas Sailer, HB9JNX/AE4WA */#ifndef _HDLCDRV_H#define _HDLCDRV_H
(C) 1996-2000 Thomas Sailer, HB9JNX/AE4WA\n""baycom_par: version 0.9\n";
(C) 1996-2000 Thomas Sailer, HB9JNX/AE4WA\n""baycom_ser_fdx: version 0.10\n";
(C) 1996-2000 Thomas Sailer, HB9JNX/AE4WA\n""baycom_ser_hdx: version 0.10\n";
(C) 1996-2000 Thomas Sailer HB9JNX/AE4WA\n");printk(KERN_INFO "hdlcdrv: version 0.8\n"); return 0;};
(C) 1996 by Thomas Bogendoerfer (tsbogend@bigbug.franken.de) * * This driver is based on work from Andreas Busse, but most of * the code is
rewritten.
(C) 1996 Gordon Chaffee * * Joliet: Microsoft's Unicode extensions to iso9660
(C) 1996 Miguel de Icaza
(C) 1996 P. Braam and M. Callahan
(C) 1996 Peter Braam
(C) 1996 Peter Braam */
(C) 1996, Peter J. Braam
(C) 1996 Philip Blundell * * 10-04-96 RMK Various cleanups & reduced register usage. * 08-04-98 RMK Shifts re-ordered
(C) 1997-1998 by Thomas Sailer, HB9JNX/AE4WA */
(C) 1997-1998 Caldera, Inc.
(C) 1997 Carnegie Mellon University
(C) 1997 Carnegie Mellon University * * Carnegie Mellon encourages users of this code to contribute improvements * to the Coda project. Contact Peter
Braam <coda@cs.cmu.edu>.
(C) 1997 Carnegie Mellon University * * Carnegie Mellon encourages users to contribute improvements to * the Coda project. Contact Peter Braam
(coda@cs.cmu.edu).
(C) 1997 Carnegie Mellon University * * Carnegie Mellon University encourages users of this software to * contribute improvements to the Coda project.
(C) 1997 Linus Torvalds
(C) 1997 M. Kirkwood * Converted to new API by Alan Cox <alan@lxorguk.ukuu.org.uk> * Various bugfixes and enhancements by Russell Kroll
<rkroll@exploits.org> * * Converted to the radio-isa f
(C) 1997 M. Kirkwood * Converted to new API by Alan Cox <alan@lxorguk.ukuu.org.uk> * Various bugfixes and enhancements by Russell Kroll
<rkroll@exploits.org> * * Converted to the radio-isa framewo
(C) 1997 Thomas Schoebel-Theuer, * with heavy changes by Linus Torvalds */
(C) 1998-1999 Dave Boynton
(C) 1998,1999 Kenneth Albanowski, D. Jeff Dionne\n");
(C) 1998-2000 Thomas Sailer, HB9JNX/AE4WA\n""baycom_epp: version 0.7\n";
(C) 1998-2001 Ben Fennema
(C) 1998-2001 Ben Fennema * * HISTORY
(C) 1998-2004 Ben Fennema
(C) 1998-2004 Ben Fennema * * HISTORY
(C) 1998 Alan Cox * * Debugging Andreas Ehliar, Michael Schmitz
(C) 1998 Ben Pfaff *
(C) 1998 Christian Schmidt (chr.schmidt@tu-bs.de)
(C) 1998 Christian Schmidt <schmidt@digadd.de>
(C) 1998 Dave Boynton
(C) 1998 James Banks
(C) 1998 Krzysztof G. Baranowski
(C) 1998 Michael Tesch (tesch@cs.wisc.edu) *
(C) 1998 Red Hat
(C) 1998 Red Hat * TCP NFS related read + write fixes
(C) 1998 Stephen Tweedie */#include <linux/mm.h>#include <linux/gfp.h>#include <linux/kernel_stat.h>#include <linux/swap.h>#include
<linux/swapops.h>#include <linux/init.h>#include <linux/page
(C) 1998 Thomas Bogendoerfer * * This driver is based on tgaafb.c
(C) 1998 Thomas Bogendoerfer (tsbogend@alpha.franken.de)
(C) 1998 Tom Lees <tom@lpsg.demon.co.uk> * Minor reorganizations by David Hinds <dahinds@users.sourceforge.net>

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```

(C) 1999, 2000, 05, 06 Ralf Baechle (ralf@linux-mips.org)
(C) 1999-2000 Kurt Garloff * * Oliver Neukum <oliver@neukum.name> * Ali Akcaagac <aliakc@web.de> * Jamie Lenehan <lenehan@twibble.org>
(C) 1999,2000 Nortel Networks.\n"
(C) 1999, 2000 Silicon Graphics, Inc. */#include <linux/bcd.h>#include <linux/clockchips.h>#include <linux/init.h>#include <linux/kernel.h>#include
<linux/sched.h>#include <linux/sched_clock.h>
(C) 1999-2000 Stelias Computing Inc * * HISTORY
(C) 1999-2000 Yon Uriarte <yon@astaro.de> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/in.h>#include
<linux/module.h>#include <linux/skbuff.h>#include <linux/ip.h>
(C) 1999-2000 Yon Uriarte <yon@astaro.de> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/skbuff.h>#include <linux/in.h>#include <linux/ip.h>
(C) 1999-2001 Ben Fennema
(C) 1999-2001 Ben Fennema * * HISTORY
(C) 1999-2001 by Helge Deller <deller@gmx.de> * * based on parport_pc.c by * Grant Guenther <grant@torque.net> * Phil Blundell
<philb@gnu.org> * Tim Waugh <tim@cyberelk.demon
(C) 1999-2001 by Helge Deller <deller@gmx.de> * * based on parport_pc.c by * Grant Guenther <grant@torque.net> * Phil Blundell
<Philip.Blundell@pobox.com> * Tim Waugh <tim@cybe
(C) 1999-2001 James Morris <jmorros@intercode.com.au> */
(C) 1999-2001 Marc Boucher <marc@mbsi.ca>
(C) 1999-2001 Marc Boucher <marc@mbsi.ca> *
(C) 1999-2001 Michal Ludvig <michal@logix.cz> */
(C) 1999-2001 Paul
(C) 1999-2001 Torben Mathiasen
(C) 1999-2003 Nemosoft Unv.
(C) 1999-2004 Ben Fennema
(C) 1999-2004 Max Krasnyansky <maxk@qualcomm.com>"
(C) 1999-2004 Nemosoft Unv.
(C) 1999-2008 Helge Deller <deller@gmx.de>
(C) 1999 Andrea Arcangeli <andrea@suse.de>
(C) 1999 Andrea Arcangeli <andrea@suse.de>
(C) 1999 Andrea Arcangeli <andrea@suse.de>
(C) 1999 Andrea Arcangeli <andrea@suse.de> (dynamic inode allocation) */#include <linux/export.h>#include <linux/fs.h>#include
<linux/mm.h>#include <linux/backing-dev.h>#include <linux/hash.h>#
(C) 1999 Andrea Arcangeli, SuSE GmbH, andrea@suse.de * Gerhard Wichert, Siemens AG, Gerhard.Wichert@pdb.siemens.de *
(C) 1999 Benjamin Reed * * TODO list is at the wiki: *
(C) 1999 by Joshua M. Thompson (funaho@jurai.org) * Based on via-cuda.c by Paul Mackerras. * * 1999-07-01 (jmt) - First implementation for new
driver architecture.
(C) 1999 by Joshua M. Thompson (funaho@jurai.org) * * Redistribution and use in source and binary forms, with or without * modification, are
permitted provided that the following conditions * are
(C) 1999 Chris Kemp and Tim Watterton
(C) 1999 Dave Airlie, University of Limerick, Ireland <airlied@linux.ie> */
(C) 1999 Dave Airlie, University of Limerick, Ireland <airlied@linux.ie> * * Rewrite of larges part of the code in order to stabilize TCP stuff. * Fix
behaviour when socket buffer is full.
(C) 1999 David A. Hinds */
(C) 1999 David A. Hinds
(C) 1999 David A. Hinds */
(C) 1999 David Airlie, airlied@linux.ie * [07-SEP-99] Bugfixes * * [06-Jan-2002] Russell King <rmk@arm.linux.org.uk> * Converted to new serial core
(C) 1999 David Huggins-Daines <dhd@debian.org> *
(C) 1999 Helge Deller <deller@gmx.de>
(C) 1999 Mihai Spatar
(C) 1999 Nicolas Pitre <nico@fluxnic.net> * * Reorganised to be machine independent.
(C) 1999 Rt-Control, Inc\n");
(C) 1999 Rusty Russell (rusty@rustcorp.com.au). */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt
(C) 1999 Stelias Computing Inc * * HISTORY
(C) 1999 Thomas Bogendoerfer (tsbogend@alpha.franken.de) * * 11/26/2000 -- disabled the existing code because it didn't work for * me in 2.4.
Replaced with a significantly more primitive version
(C) 1999 Thomas Bogendoerfer (tsbogend@alpha.franken.de) * * based on code from Oliver Jowett <oliver@jowett.manawatu.gen.nz>
(C) 1999 Trond Myklebust <trond.myklebust@fys.uio.no> *
(C) 1999 Ulf Carlsson (ulfc@thepuffingruop.com) * * This driver is based on sgicons.c and cons_newport.
(C) (1 << (((C) > 2)
(C) 1:M * ... -->|(A)|--+ | +-----+ +----+ +-->| 1 | +----+ * ... -->| | +-->|(D) 1:3 |-----+ +----+ * pll1.N -->| N |
+-----+ *

```



(C) 2000,2001,2002,2003,2004 Omnikey AG \*

(C) 2000-2001 by Harald Welte <laforge@gnumonks.org>

(C) 2000,2001 by Harald Welte <laforge@netfilter.org> \* \* Hop Limit matching module

(C) 2000-2001 Felix Domke (tmbinc@gmx.net), Gillem (htoa@gmx.net) \*

(C) 2000-2001 Svenning Soerensen <svenning@post5.tele.dk>

(C) 2000-2002 by Harald Welte <laforge@gnumonks.org> \* based on RR's ip\_contrack\_ftp.c

(C) 2000-2002 Harald Welte <laforge@netfilter.org>

(C) 2000-2002 James Morris <jmorris@intercode.com.au>

(C) 2000-2004 by Harald Welte <laforge@netfilter.org> \*/

(C) 2000-2005 by Harald Welte <laforge@gnumonks.org> \* \* Development of this code funded by Astaro AG (<http://www.astaro.com/>)

(C) 2000,2005 by Harald Welte <laforge@netfilter.org> \* \* Hop Limit modification target for ip6tables \* Maciej Soltysiak <solt@dns.toxicfilms.tv>

(C) 2000 Aleph One Ltd (Tak-Shing Chan) \* EtherM integration re-engineered by Russell King. \* \* Changelog:

(C) 2000 Andrea Arcangeli <andrea@suse.de> SuSE \* Queue request tables / lock, selectable elevator, Jens Axboe <axboe@suse.de> \* kernel-doc documentation started by NeilBrown <neilb@cse.unsw.edu.au>

(C) 2000 by Harald Welte <laforge@gnumonks.org>

(C) 2000 by Harald Welte <laforge@netfilter.org>

(C) 2000 by Matthew G. Marsh <mgm@paktronix.com> \* \* See RFC2474 for a description of the DSCP field within the IP Header.

(C) 2000 by Matthew G. Marsh <mgm@paktronix.com> \* This software is distributed under GNU GPL v2, 1991 \* \* See RFC2474 for a description of the DSCP field within the IP Header.

(C) 2000 by R.E.Wolff@BitWizard.nl \* Copied snippets from zatm.c by Werner Almesberger, EPFL LRC/ICA

(C) 2000 Carnegie Mellon University \* \* Carnegie Mellon encourages users of this code to contribute improvements \* to the Coda project <http://www.coda.cs.cmu.edu/> <coda@cs.cmu.edu>.

(C) 2000 ChyGwyn Limited - <http://www.chygwyn.com/> \* \* Author: Steven Whitehouse <steve@chygwyn.com>

(C) 2000 David Brown <usb-storage@davidb.org> \* \* This driver was developed with information provided in FREECOM's USB \* Programmers Reference Guide. For further information contact Freecom \* (ht

(C) 2000 IBM Corp. by Fritz Elfert (felfert@millenux.com)");

(C) 2000 In-System Design, Inc. (support@in-system.com) \* \* The ISD200 ASIC does not natively support ATA devices. The chip \* does implement an interface, the ATA Command Block (ATACB) which provi

(C) 2000 Marc Boucher <marc@mbsi.ca> \*

(C) 2000 Milan Pikula <www@fornax.sk> \* \* sir\_ir - Device driver for use with SIR (serial infra red) \* mode of IrDA on many notebooks.

(C) 2000 Nicolas Pitre <nico@fluxnic.net> \*/#include <linux/module.h>#include <linux/types.h>#include <linux/ioport.h>#include <linux/kernel.h>#include <linux/init.h>#include <linux/errno.h>#i

(C) 2000 Nicolas Pitre <nico@fluxnic.net> \* \* This code is GPL

(C) 2000 Nicolas Pitre <nico@fluxnic.net> \* \* This code is GPL

(C) 2000 Red Hat. GPL \*/

(C) 2000 Red Hat. GPL \*

(C) 2000 Red Hat. GPL\*/

(C) 2000 Red Hat. GPL \* \* 10/10/2000 Nicolas Pitre <nico@fluxnic.net> \* - completely revamped method functions so they are aware and \* independent of the flash geometry (buswidth, interleave,

(C) 2000 Red Hat. GPL See JEDEC (<http://www.jedec.org/>) standard JESD21C (section 3.5) for the standard this probe goes back to.

(C) 2000 Stelias Computing Inc \* \* HISTORY

(C) 2000 -- This code is GPL.

(C) 2000 YATEGASHI Takeshi

(C) 2001, 2002, 2003, 2004 Rusty Russell \* \* This code is licenced under the GPL.

(C) 2001,2002,2003 Benjamin Herrenschmidt (benh@kernel.crashing.org)

(C) 2001,2002,2006 by Jan-Benedict Glaw <jbglaw@lug-owl.de> \* \* This driver is a modified version of Erik Mouw's example proc \* interface, so: thank you, Erik! He can be reached via email at \* <J.

(C) 2001-2002 Andras Kis-Szabo <kisza@sch.bme.hu> \*/

(C) 2001-2002 Andras Kis-Szabo <kisza@sch.bme.hu> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include <linux/skbuff.h>#include <linux/ip.h>#include <linux/ipv6.h>#inc

(C) 2001-2002 Andras Kis-Szabo <kisza@sch.bme.hu> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include <linux/skbuff.h>#include <linux/ipv6.h>#include <linux/types.h>#

(C) 2001-2002 Bj \* \* Developed with the assistance of:

(C) 2001, 2002 by: \* Alan Cox <alan@redhat.com> \* Thomas Hood \* Brian Gerst <bgerst@didntduck.org> \*

(C) 2001-2002 IBM Deutschland Entwicklung GmbH");

(C) 2001-2002 Maciej Soltysiak <solt@dns.toxicfilms.tv> \*/

(C) 2001-2002 Magnus Boden <mb@ozaba.mine.nu>

(C) 2001-2002 Magnus Boden <mb@ozaba.mine.nu> \*/

(C) 2001-2002 Patrick McHardy <kaber@trash.net> \*/

(C) 2001-2003 Bart De Schuymer <bdschuymer@pandora.be> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt

(C) 2001-2003 Red Hat, Inc. \* GPL'd \*/

```

(C) 2001-2004 Dave Jones.
(C) 2001 - 2005 Tensilica Inc. */
(C) 2001,2006 IBM Corporation");
(C) 2001 - 2007 Tensilica Inc. * Kevin Chea <kchea@yahoo.com> * Marc Gauthier <marc@linux-xtensa.org> * Chris Zankel <chris@zankel.net> *
(C) 2001 - 2013 Tensilica Inc. */
(C) 2001 Andreas Gruenbacher, <a.gruenbacher@computer.org>*/
(C) 2001 Andreas Gruenbacher, <a.gruenbacher@computer.org> */#ifndef __F2FS_XATTR_H__#define __F2FS_XATTR_H__
(C) 2001 Brad Hards
(C) 2001 by Jay Schulist <jschlst@samba.org>,
(C) 2001 by Jay Schulist <jschlst@samba.org>
(C) 2001 * by Karsten Merker <merker@linuxtag.org> * and Maciej W. Rozycki <macro@ds2.pg.gda.pl> * Later on, I had access to the device's
documentation (referenced below). */
(C) 2001 Clemson University and The University of Chicago
(C) 2001 Clemson University and The University of Chicago * * Changes by Acxiom Corporation to add proc file handler for pvfs2 client
(C) 2001 Clemson University and The University of Chicago * * Changes by Acxiom Corporation to add protocol version to kernel
(C) 2001 Clemson University and The University of Chicago * * See COPYING in top-level directory.
(C) 2001 Dave Jones, Arjan van de ven.
(C) 2001 Dimitromanolakis Apostolos <apdim@grecian.net> * * Based in the radio Maestro PCI driver. Actually it uses the same chip * for radio but
different pci controller.
(C) 2001 IBM Corp. by Fritz Elfert (felfert@millenux.com)");MODULE_DESCRIPTION("Linux for S/390 IUCV lowlevel driver");
(C) 2001 IBM Corporation by Fritz Elfert (felfert@millenux.com)");MODULE_DESCRIPTION ("Linux for S/390 IUCV network driver");
(C) 2001 IBM Deutschland Entwicklung GmbH by Carsten Otte and "
(C) 2001 Johannes Erdfelt <jerdfelt@valinux.com>
(C) 2001 Marc Boucher (marc@mbsi.ca).
(C) 2001 Marc Boucher (marc@mbsi.ca). */
(C) 2001 Martin Josefsson <gandalf@wlug.westbo.se> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/skbuff.h>#include <linux/netfilter.h>#include <net/netfil
(C) 2001 NeilBrown <neilb@cse.unsw.edu.au> * * This driver for the Micro Memory PCI Memory Module with Battery Backup
(C) 2001 Red Hat, Inc. * * Author: Arjan Van De Ven <arjanv@redhat.com> * Loosly based on i82365.c from the pcmcia-cs package
(C) 2001 Red Hat, Inc. * * GPL'd
(C) 2001 San Mehat <netzwerk@valinux.com>
(C) 2002 - 2003 Dominik Brodowski <linux@brodo.de>
(C) 2002 - 2003 Dominik Brodowski <linux@brodo.de> * * Based upon reverse engineered information, and on Intel documentation * for chipsets
ICH2-M and ICH3-M.
(C) 2002 - 2003 Dominik Brodowski <linux@brodo.de> * * BIG FAT DISCLAIMER: Work in progress code. Possibly *dangerous*
(C) 2002 - 2003 Dominik Brodowski <linux@brodo.de> */#ifndef _LINUX_CPUFREQ_H#define _LINUX_CPUFREQ_H
(C) 2002 - 2003 Dominik Brodowski <linux@brodo.de> * * Library for common functions for Intel SpeedStep v.1 and v.2 support
(C) 2002-2003 Intel Corp * Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com>. *
(C) 2002, 2003 Marc Zyngier <maz@wild-wind.fr.eu.org> */
(C) 2002-2003 Nadia Yvette Chambers, IBM
(C) 2002 - 2004 Dominik Brodowski <linux@brodo.de> */
(C) 2002-2004 Ingo Molnar, Red Hat * * pid-structures are backing objects for tasks sharing a given ID to chain * against. There is very little to them
aside from hashing them and * parking tasks
(C) 2002-2004 Netfilter Core Team <coreteam@netfilter.org>
(C) 2002-2004 Netfilter Core Team <coreteam@netfilter.org> */
(C) 2002-2004 Netfilter Core Team <coreteam@netfilter.org> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/skbuff.h>#include <linux/slab.h>#include <linux/i
(C) 2002-2004 Netfilter Core Team <coreteam@netfilter.org> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/types.h>#include <linux/udp.h>#include <linux/skb
(C) 2002-2005 BUFFALO INC.\n");}
(C) 2002-2005 by Harald Welte <laforge@gnumonks.org>
(C) 2002-2005 Netfilter Core Team <coreteam@netfilter.org> */
(C) 2002-2006 by Harald Welte <laforge@gnumonks.org>
(C) 2002-2006 Netfilter Core Team <coreteam@netfilter.org>
(C) 2002-2006 Netfilter Core Team <coreteam@netfilter.org> */
(C) 2002-2006 Netfilter Core Team <coreteam@netfilter.org> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/netfilter/x_tables.h>#include <net/netfilter/nf_na
(C) 2002-2013 Jozsef Kadlec <kadlec@netfilter.org>
(C) 2002 Alan Stern <stern@rowland.org> */
(C) 2002 Alan Stern <stern@rowland.org> * * Initial work:
(C) 2002 Andreas Gruenbacher, <a.gruenbacher@computer.org>*/

```

```

(C) 2002 Arjan van de Ven <arjanv@redhat.com>
(C) 2002 Atom Create Engineering Co., Ltd.
(C) 2002 Atom Create Engineering Co., Ltd. */#include <linux/module.h>#include <linux/init.h>#include <linux/kdev_t.h>#include
<linux/cdev.h>#include <linux/fs.h>#include <asm/io.h>#include <
(C) 2002 * by Adam Belay <ambx1@neo.rr.com> */
(C) 2002 by Brian J. Murrell <netfilter@interlinx.bc.ca> * based on a copy of HW's ip_nat_irc.c as well as other modules
(C) 2002 by Brian J. Murrell <netfilter@interlinx.bc.ca> * based on HW's ip_contrack_irc.c as well as other modules
(C) 2002 by gaoyonghong
(C) 2002 by Harald Welte <laforge@gnumonks.org>
(C) 2002 by Harald Welte <laforge@gnumonks.org> * * This software is distributed under GNU GPL v2, 1991
(C) 2002 by Harald Welte <laforge@netfilter.org>
(C) 2002 by Harald Welte <laforge@netfilter.org>*/#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/in.h>#include
<linux/module.h>#include <linux/skbuff.h>#include <linux/ip.h>#include
(C) 2002 by Harald Welte <laforge@netfilter.org> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/skbuff.h>#include <linux/ip.h>#include <linux/ipv6.h>#incl
(C) 2002 David Woodhouse <dwmw2@infradead.org>
(C) 2002 Harald Welte <laforge@gnumonks.org>
(C) 2002 Harald Welte <laforge@gnumonks.org> * * This software is distributed under GNU GPL v2, 1991
(C) 2002 Harald Welte <laforge@gnumonks.org> * This software is distributed under GNU GPL v2, 1991 * * See RFC2474 for a description of the DSCP
field within the IP Header.
(C) 2002 Hirokazu Takahashi <taka@valinux.co.jp> */
(C) 2002 Hiroshi Miura <miura@da-cha.org> * All Rights Reserved
(C) 2002 Louis Zhuang <louis.zhuang@intel.com>. * 2007 Alexander Eichner * 2008 Pekka Paalanen <pq@iki.fi> */
(C) 2002 Martin Devera (devik@cdi.cz). */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include <linux/bitops.h>#include
<linux/skbuff.h>#include <linux/math64.h>#include
(C) 2002 Nadia Yvette Chambers, IBM
(C) 2002 Oliver Neukum * * Original author: The Zapman <zapman@interlan.net> * Inspired by, and much credit goes to Michael Rothwell *
<rothwell@interlan.net> for the test equipment, h
(C) 2002 Padraig Brady. <padraig@antefacto.com> * * Based upon datasheets & sample CPUs kindly provided by VIA.
(C) 2002 Pascal Dameme <netinet@freesurf.fr>
(C) 2002 Red Hat
(C) 2002 Samuel Chessman
(C) 2002 Tora T. Engstad * All Rights Reserved
(C) 2002 Zwane Mwaikambo <zwane@commfireservices.com>
(C) 2003, 2004, 2006 Andriy Skulys
(C) 2003,2004 Aurelien Alleaume <slts@free.fr>
(C) 2003-2004 by Harald Welte <laforge@netfilter.org>
(C) 2003-2004 by Harald Welte <laforge@netfilter.org> * based on ideas of Fabio Olive Leite <olive@unixforge.org> * * Development of this code
funded by SuSE Linux AG, http://www.suse.com/
(C) 2003 - 2004 David Woodhouse
(C) 2003 - 2004 Dominik Brodowski */
(C) 2003 - 2004 Dominik Brodowski */
(C) 2003 - 2004 Dominik Brodowski <linux@dominikbrodowski.de> * * Based on code found in * linux/arch/i386/kernel/cpu/cpufreq/speedstep-
centrino.c * and originally developed by Jeremy Fitzharding
(C) 2003-2004 Paul Clements, SteelEye Technology: * - persistent bitmap code */
(C) 2003, 2004 Paul Mundt
(C) 2003,2004 USAGI/WIDE Project <http://www.linux-ipv6.org>
(C) 2003-2005 Netfilter Core Team <coreteam@netfilter.org> */
(C) 2003-2006 Netfilter Core Team <coreteam@netfilter.org>
(C) 2003 - 2008 Dominik Brodowski */
(C) 2003 - 2010 Dominik Brodowski */
(C) 2003 - 2010 Dominik Brodowski * * This file contains definitions _only_ needed by the PCMCIA core modules. * It must not be included by PCMCIA
socket drivers or by PCMCIA device * drivers.
(C) 2003 Andreas Gruenbacher <agruen@suse.de> */#ifndef __LINUX_NFSACL_H#define __LINUX_NFSACL_H
(C) 2003 Andreas Gruenbacher <agruen@suse.de> */#ifndef __UAPI__LINUX_NFSACL_H#define __UAPI__LINUX_NFSACL_H
(C) 2003 Ardis Technologies <roman@ardistech.com>
(C) 2003 Ardis Technologies <roman@ardistech.com> */
(C) 2003 Ardis Technologies <roman@ardistech.com> *
(C) 2003 Ardis Technologies <roman@ardistech.com> * * Export hfs data via xattr
(C) 2003 Ardis Technologies <roman@ardistech.com> * * Format of structures on disk * Information taken from Apple Technote #1150 (HFS Plus
Volume Format)

```

(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handle basic btree node operations  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handle individual btree records  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handle opening/closing btree  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handler routines for unicode strings  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handling of allocation file  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handling of catalog records  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handling of directories  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handling of Extents both in catalog and extents overflow trees  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handling of HFS wrappers around HFS+ volumes  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Inode handling routines  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Option parsing  
(C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Search routines for btrees  
(C) 2003 Aurelien Alleaume <slts@free.fr>  
(C) 2003 Bruno Ducrot  
(C) 2003 by Patrick Mchardy <kaber@trash.net>  
(C) 2003 by Sampsa Ranta <sampsa@netsonic.fi> \*/  
(C) 2003 Christoph Jungegger <disdos@traum404.de> \* \* Some of this code is borrowed from orinoco\_plx.c  
(C) 2003 Dave Jones. \* \* AMD-specific information  
(C) 2003 Dave Jones on behalf of SuSE Labs  
(C) 2003 Dave Jones on behalf of SuSE Labs. \* \* Based upon datasheets & sample CPUs kindly provided by AMD.  
(C) 2003 Dave Jones. \* \* VIA-specific information  
(C) 2003 Herbert Valerio Riedel <hvr@gnu.org>  
(C) 2003 Hiroshi Miura <miura@da-cha.org> \*/  
(C) 2003 IBM Corporation by Martin Schwidewsky (schwidewsky@de.ibm.com)");MODULE\_DESCRIPTION ("Linux for S/390 IUCV special message driver");  
(C) 2003 John Whitney (john.whitney@timesys.com)  
(C) 2003 Jozsef Kadlecsek <kadlec@netfilter.org>  
(C) 2003 Jun Nakajima <jun.nakajima@intel.com>  
(C) 2003 Linux Networx (http://lnxi.com)  
(C) 2003 Luis R. Rodriguez <mcgrof@ruslug.rutgers.edu>  
(C) 2003 Luis R. Rodriguez <mcgrof@ruslug.rutgers.edu> \*/  
(C) 2003 Marc Zyngier <maz@wild-wind.fr.eu.org> \*/  
(C) 2003 Marc Zyngier <maz@wild-wind.fr.eu.org> \* \* Ivan Kokshaysky <ink@jurassic.park.msu.ru> :  
(C) 2003 Marc Zyngier <maz@wild-wind.fr.eu.org> \* modified for SNI usage by Thomas Bogendoerfer \*/  
(C) 2003 Red Hat, Inc.  
(C) 2003 Simtec Electronics\*/  
(C) 2003 Venkatesh Pallipadi <venkatesh.pallipadi@intel.com>.  
(C) 2003 Venkatesh Pallipadi <venkatesh.pallipadi@intel.com>. \* Jun Nakajima <jun.nakajima@intel.com>  
(C) 2003 Venkatesh Pallipadi <venkatesh.pallipadi@intel.com>. \* Jun Nakajima <jun.nakajima@intel.com> \*/  
(C) 2003 Yasuyuki Kozakai @USAGI <yasuyuki.kozakai@toshiba.co.jp> \*/  
(C) 2003 zecke@handhelds.org \* \* based on arch/arm/kernel/apm.c \* factor out the information needed by architectures to provide \* apm status  
(C) 2004,2005 Benjamin Herrenschmidt, IBM Corp. \* \* NAPI and NETPOLL support  
(C) 2004-2006 Luc Saillard (luc@saillard.org)  
(C) 2004-2006 Luc Saillard (luc@saillard.org)  
(C) 2004-2006 Luc Saillard (luc@saillard.org)  
(C) 2004-2006 Sebastian Witt <se.witt@gmx.net> \* \* Based upon reverse engineered information  
(C) 2004-2009 Dominik Brodowski <linux@dominikbrodowski.de>  
(C) 2004-2009 Dominik Brodowski <linux@dominikbrodowski.de> \*/  
(C) 2004 Bruno Ducrot <ducrot@poupinou.org> \* \* Based on code found in \* linux/arch/i386/kernel/cpu/cpufreq/powernow-k8.c \* and originally  
developed by Paul Devriendt  
(C) 2004 by Bart De Schuymer <bdschuym@pandora.be> \*  
(C) 2004 by Eric Lemoine (eric.lemoine@gmail.com) \*  
(C) 2004 Dan Brown <dan\_brown@ieee.org>  
(C) 2004 David Bronaugh <dbronaugh>  
(C) 2004 Dominik Brodowski <linux@brodo.de>  
(C) 2004 Dominik Brodowski <linux@dominikbrodowski.de> \* \* Based on code found in \* linux/include/asm-i386/ist.h and  
linux/arch/i386/kernel/setup.c \* and originally developed by Andy Grover <andr  
(C) 2004 Dominik Brodowski \* \* To use:  
(C) 2004 Hiroshi Miura <miura@da-cha.org>  
(C) 2004 IBM Corporation by Xenia Tkatschow (xenia@us.ibm.com)\n"  
(C) 2004 Intracom, S.A.  
(C) 2004 jornada 720 kbd driver by Alex Lange <chicken@handhelds.org> \*/#include <linux/device.h>#include <linux/errno.h>#include

```

<linux/interrupt.h>#include <linux/input.h>#include <linux/ker
(C) 2004 Kalev Lember <kalev@smartlink.ee> * * Author: David Woodhouse <dwmw2@infradead.org> * Additional Diskonchip 2000 and Millennium
support by Dan Brown <dan_brown@ieee.org> * Diskonchip Mill
(C) 2004 Linux Networx (http://lnxi.com)
(C) 2004 Margit Schubert-While <margitsw@t-online.de> */
(C) 2004 Nadia Yvette Chambers, Oracle
(C) 2004 Nadia Yvette Chambers, Oracle */#include "sched.h"
(C) 2004 NTT DATA Intellilink Co. http://www.intellilink.co.jp/
(C) 2004 Paul Serice - The new inode scheme requires switching * from iget() to iget5_locked() which means * the
NFS export operations have to be h
(C) 2004 Pavel Machek <pavel@ucw.cz> * Based upon datasheets & sample CPUs kindly provided by AMD. * * Valuable input gratefully received from
Dave Jones, Pavel Machek, * Dominik Brodowski, Jac
(C) 2004 Red Hat * * Marcelo Tosatti <marcelo@conectiva.com.br> : SMP fixes
(C) 2004 Rusty Russell <rusty@rustcorp.com.au> IBM Corporation * based on a copy of RR's ip_nat_ftp.c */
(C) 2004 Tom Rini (trini@mvista.com)
(C) 2004 Zou Nan hai <nanhai.zou@intel.com>. */
(C) 2005-2006 Harald Welte <laforge@gnumonks.org>
(C) 2005-2006 Harald Welte <laforge@gnumonks.org> * - add support for poll() * - driver cleanup * - add waitqueues * - adhere to linux
kernel coding style and policies * - support 2.6.13 "ne
(C) 2005, 2006 Linux Networx (http://lnxi.com)
(C) 2005-2006 MontaVista Software, Inc. * Vitaly Bordug <vbordug@ru.mvista.com> */
(C) 2005-2006 Netfilter Core Team <coreteam@netfilter.org>
(C) 2005-2006 Red Hat Inc * Alan Cox <alan@lxorguk.ukuu.org.uk> * * based upon cs5535.c from AMD <Jens.Altmann@amd.com> as
cleaned up and * made readable and Linux style by Wolfgang Zuleger <w
(C) 2005-2006 Red Hat Inc * Alan Cox <alan@lxorguk.ukuu.org.uk> * * The MPIIX is different enough to the PIIX4 and friends that we
give it * a separate driver. The old ide/pci code handles thi
(C) 2005, 2006 Red Hat Inc. * * Author: David Woodhouse <dwmw2@infradead.org> * Tom Sylla <tom.sylla@amd.com>
(C) 2005-2006 Red Hat Inc * * Based on pata-sil680. Errata information is taken from data sheets * and the amd74xx.c driver by Vojtech Pavlik. Nvidia
SATA devices are * claimed by sata-nv.c.
(C) 2005-2006 Red Hat Inc * * Documentation * Most chipset documentation available under NDA only
(C) 2005-2007 Linux Networx (http://lnxi.com)
(C) 2005-2012 by Pablo Neira Ayuso <pablo@netfilter.org> * * Initial connection tracking via netlink development funded and * generally made possible
by Network Robots, Inc. (www.networkrobots.com)
(C) 2005-2012 Patrick McHardy <kaber@trash.net> */
(C) 2005-2017 by Pablo Neira Ayuso <pablo@netfilter.org> * * Initial netfilter messages via netlink development funded and * generally made possible
by Network Robots, Inc. (www.networkrobots.com)
(C) 2005 Benedikt Spranger <b.spranger@linutronix.de> */
(C) 2005 by Christian Hentschel <chentschel@arnet.com.ar> * based on RR's ip_contrack_ftp.c and other modules.
(C) 2005 by Christian Hentschel <chentschel@arnet.com.ar> * based on RR's ip_nat_ftp.c and other modules.
(C) 2005 by Harald Welte <laforge@netfilter.org>
(C) 2005 by Harald Welte <laforge@netfilter.org> */
(C) 2005 Chuck Lever <cel@netapp.com> * * IPv6 support contributed by Gilles Quillard, Bull Open Source, 2005. * <gilles.quillard@bull.net>
(C) 2005 EADS Astrium
(C) 2005 Finn Thain * * Converted to DMA API, added zero-copy buffer handling, and * (from the mac68k project) introduced dhd's support for 16-bit
cards.
(C) 2005 Finn Thain * * Converted to DMA API, and (from the mac68k project) introduced * dhd's support for 16-bit cards.
(C) 2005 Finn Thain * * Converted to DMA API, converted to unified driver model, made it work as * a module again, and from the mac68k project,
introduced more 32-bit cards * and dhd's support for
(C) 2005 Frank Rowand (frowand@mvista.com)
(C) 2005 Frank Rowand (frowand@mvista.com) * PPC32 support restored by Vitaly Wool <vwool@ru.mvista.com> and * Sergei Shtylyov
<sshtylyov@ru.mvista.com>
(C) 2005 Georges Toth <g.toth@e-biz.lu>
(C) 2005 Harald Welte <laforge@gnumonks.org>
(C) 2005 Harald Welte <laforge@netfilter.org> * * This software is distributed under GNU GPL v2, 1991
(C) 2005 John Williams * * MMU code derived from arch/ppc/kernel/head_4xx.S:
(C) 2005 Michel Xhaard mxhaard@magic.fr * * V4L2 by Jean-Francois Moine <http://moinejf.free.fr>
(C) 2005 Nokia Corporation */
(C) 2005 Pablo Neira Ayuso <pablo@eurodev.net> */
(C) 2005 Patrick McHardy <kaber@trash.net>
(C) 2005 Red Hat
(C) 2005 Red Hat <alan@lxorguk.ukuu.org.uk> * * This is a fairly generic MWDMA controller. It has some limitations * as it requires timing reloads

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on PIO/DMA transitions but it is otherwise

```
(C) 2005 Red Hat (http://www.redhat.com)
(C) 2005 Red Hat Inc
(C) 2005 Red Hat Inc * Alan Cox <alan@lxorguk.ukuu.org.uk>
(C) 2005 Red Hat Inc * Alan Cox <alan@lxorguk.ukuu.org.uk> * * based upon
(C) 2005 Red Hat Inc * * based in part upon * linux/drivers/ide/pci/alim15x3.c Version 0.17 2003/01/02
(C) 2005 Red Hat Inc * * Based on * linux/drivers/ide/pci/opti621.c Version 0.7Sept 10, 2002
(C) 2005 Red Hat Inc * * based upon
(C) 2005 Red Hat Inc * * based upon cs5530.c by Mark Lord.
(C) 2005 Red Hat * * Some parts based on ata_piix.c by Jeff Garzik and others.
(C) 2005 Thomas Gleixner <tglx@linutronix.de> * * Simplifications of the original code by * Oleg Nesterov <oleg@tv-sign.ru>
(C) 2005 Thomas Winischhofer.; static const char bootlogo[] = "(o_ //\\ V_ _";
(C) 2005 USAGI/WIDE Project <http://www.linux-ipv6.org> */
(C) 2006-2007 MontaVista Software\n");
(C) 2006-2007 Shaohua Li <shaohua.li@intel.com> * * This code is licenced under the GPL.
(C) 2006-2007 Venkatesh Pallipadi <venkatesh.pallipadi@intel.com> * Shaohua Li <shaohua.li@intel.com> * Adam Belay
<abelay@novell.com> * * This code is licenced under t
(C) 2006-2008 by Openmoko, Inc. * All rights reserved
(C) 2006-2008 by Openmoko, Inc. * Author: Balaji Rao <balajirao@openmoko.org> * All rights reserved
(C) 2006-2008 by Openmoko, Inc. * Author: Harald Welte <laforge@openmoko.org> * Balaji Rao <balajirao@openmoko.org> * All rights reserved
(C) 2006-2008 Harald Welte <laforge@gnumonks.org> *
(C) 2006,2008 Red Hat, Inc., James Morris <jmorris@redhat.com> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include
<linux/module.h>#include <linux/security.h>#include <linux/skbuff.h>#include
(C) 2006,2008 Red Hat, Inc., James Morris <jmorris@redhat.com> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include
<linux/module.h>#include <linux/skbuff.h>#include <linux/netfilter/x_tables.h>
(C) 2006-2008 Takashi Iwai <tiwai@suse.de> */
(C) 2006-2009 VIA Technology, Inc.
(C) 2006-2010 Patrick McHardy <kaber@trash.net> */
(C) 2006-2012 */#include <linux/kernel.h>#include <linux/init.h>#include <linux/ipv6.h>#include <linux/netfilter.h>#include
<linux/netfilter_ipv6.h>#include <linux/export.h>#include <net/addrc
(C) 2006-2012 */#include <linux/kernel.h>#include <linux/netfilter.h>#include <linux/netfilter_ipv4.h>#include <linux/ip.h>#include
<linux/skbuff.h>#include <linux/gfp.h>#include <linux/export
(C) 2006-2012 Patrick McHardy <kaber@trash.net>
(C) 2006-2012 Patrick McHardy <kaber@trash.net> */
(C) 2006-2012 Patrick McHardy <kaber@trash.net> * * Based on the old ipv4-only ipt_ULOG.c:
(C) 2006-2012 Patrick McHardy <kaber@trash.net> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt
(C) 2006-2012 Patrick McHardy <kaber@trash.net> * * Limitations:
(C) 2006-2012 Patrick McHardy <kaber@trash.net> * * TODO: - NAT to a unique tuple, not to TCP source port * (needs netfilter tuple
reservation)
(C) 2006 by Patrick McHardy <kaber@trash.net>
(C) 2006 IBM Corp */
(C) 2006 kogiidena
(C) 2006 Michael Ellerman, IBM Corp */
(C) 2006 MontaVista Software, Inc. * Vitaly Bordug <vbordug@ru.mvista.com> */
(C) 2006 Montavista Software\n");
(C) 2006 Patrick McHardy <kaber@trash.net> */#include <linux/kernel.h>#include <linux/module.h>#include <linux/moduleparam.h>#include
<linux/textsearch.h>#include <linux/skbuff.h>#include <linu
(C) 2006 Red Hat
(C) 2006 Red Hat <alan@lxorguk.ukuu.org.uk> * * Some parts based on ata_piix.c by Jeff Garzik and others.
(C) 2006 Red Hat Inc */
(C) 2006 Red Hat Inc * Alan Cox * * Based heavily on * linux/drivers/ide/pci/cy82c693.c Version 0.40 Sep. 10, 2002
(C) 2006 Red Hat Inc * * The Opti DMA controllers are related to the older PIO PCI controllers * and indeed the VLB ones. The main differences are
that the timing * numbers are now based off PCI c
(C) 2006 Simtec Electronics
(C) 2006 Thomas Gleixner <tglx@linutronix.de>
(C) 2006 Thomas Maier <balagi@justmail.de> *
(C) 2006 Tim Small
(C) 2007, 2008, 2011, 2012 Patrick McHardy <kaber@trash.net> */
(C) 2007,2008 Dmitry Baryshkov * * Based on:
(C) 2007,2008 Jochen Friedrich <jochen@scram.de> */
(C) 2007-2008, MontaVista Software, Inc.
```

```

(C) 2007, 2008 Patrick McHardy <kaber@trash.net> */
(C) 2007,2009,2010 Bartłomiej Zolnierkiewicz * * Based in part on linux/drivers/ide/pci/pdc202xx_old.c
(C) 2007,2009 Bartłomiej Zolnierkiewicz * * Based upon linux/drivers/ide/pci/sis5513.c
(C) 2007-2010 by Jan Engelhardt <jengelh@medozas.de> * * Extracted from xt_TEE.c
(C) 2007,2011 Bartłomiej Zolnierkiewicz * * Based in part on drivers/ide/pci/aec62xx.c
(C) 2007-2012 Patrick McHardy <kaber@trash.net> */#include <linux/module.h>#include <linux/gfp.h>#include <linux/types.h>#include
<linux/skbuff.h>#include <linux/tcp.h>#include <linux/udp.h>#i
(C) 2007 aCarLab (India) Pvt. Ltd. (http://acarlab.com)
(C) 2007 Alexander Shishkin <virtuoso@slind.org>
(C) 2007 Bartłomiej Zolnierkiewicz * * based upon
(C) 2007 by Hans J. Koch <hjk@hansjkoch.de>
(C) 2007 by Hans J. Koch <hjk@hansjkoch.de> * * based on code written by John Morris <john.morris@spirentcom.com>
(C) 2007 by Holger Schurig <hs4233@mail.mn-solutions.de>
(C) 2007 by Patrick McHardy <kaber@trash.net> * * Based on the old ipv4-only ip_queue.c:
(C) 2007 by Sebastian Cla
(C) 2007 Dmitry Adamushko <dmitry.adamushko@gmail.com> * * Group scheduling enhancements by Srivatsa Vaddagiri
(C) 2007 EADS Astrium * * This driver is based on the lm75 and other lm_sensors/hwmon drivers
(C) 2007 Hans J. Koch <hjk@hansjkoch.de>
(C) 2007 jetzbroadband (http://jetzbroadband.com)
(C) 2007 Kees Lemmens (adapted for kernel 2.6.20)
(C) 2007 Laszlo Attila Toth <panther@balabit.hu> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/kernel.h>#include
<linux/module.h>#include <linux/skbuff.h>#include <linux/netdevice
(C) 2007 Linux Networx (http://lnxi.com)
(C) 2007 Martin K. Petersen <mkp@mkp.net>
(C) 2007 Michel Benoit * * Based on rtc-at91rm9200.c by Rick Bronson
(C) 2007 Mike Galbraith <efault@gmx.de> * * Various enhancements by Dmitry Adamushko.
(C) 2007 Montavista Software, Inc. * Vitaly Bordug <vitb@kernel.crashing.org> * * Converted to of_platform_device. Renamed to i2c-cpm.c.
(C) 2007 Patrick McHardy <kaber@trash.net> */#include <linux/module.h>#include <linux/skbuff.h>#include <linux/gen_stats.h>
(C) 2007 Patrick McHardy <kaber@trash.net> */#include <linux/module.h>#include <linux/skbuff.h>#include <linux/gen_stats.h>#include
<linux/jhash.h>#include <linux/rtnetlink.h>#include <linux/ra
(C) 2007 Pavel Emelyanov <xemul@openvz.org>, OpenVZ, SWsoft Inc.
(C) 2007 Red Hat Inc * * Based upon * linux/drivers/ide/pci/cmd640.c Version 1.02 Sep 01, 1996
(C) 2007 Red Hat, Inc.\n");
(C) 2007 Red Hat Inc * * Note: The controller like many controllers has shared timings for * PIO and DMA. We thus flip to the DMA timings in dma_start
and flip back * in the dma_stop function. Thu
(C) 2007 Rodolfo Giometti\n");
(C) 2007 sgi, Christoph Lameter
(C) 2007 SGI, Christoph Lameter
(C) 2007 SGI, Christoph Lameter */#include <linux/kobject.h>
(C) 2007 sgi. Christoph Lameter. * * Virtual memory maps allow VM primitives pfn_to_page, page_to_pfn, * virt_to_page, page_address() to be
implemented as a base offset * calculation without memor
(C) 2007 SoftwareBitMaker
(C) 2007 Sukadev Bhattiprolu <sukadev@us.ibm.com>, IBM * Many thanks to Oleg Nesterov for comments and help *
(C) 2007 United Security Providers
(C) 2007 Venkatesh Pallipadi <venkatesh.pallipadi@intel.com> * Shaohua Li <shaohua.li@intel.com> * Adam Belay <abelay@novell.com>
* * This code is licenced under the GPL.
(C) 2007 www.douglaskthompson.com
(C) 2007 www.softwarebitmaker.com
(C) 2008, 2009, 2010, 2011 by Marc Kleine-Budde <kernel@pengutronix.de> */
(C) 2008, 2009 by Marc Kleine-Budde <kernel@pengutronix.de> * * Bosch C_CAN controller is compliant to CAN protocol version 2.0 part A and B. *
Bosch C_CAN user manual can be obtained from:
(C) 2008-2009 Pablo Neira Ayuso <pablo@netfilter.org> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/skbuff.h>#include <linux/jhash.h>#include <linux/ip.h>
(C) 2008-2013 Christoph Lameter <cl@linux.com> * * Optimized manipulation for memory allocated through the per cpu * allocator or for addresses of
per cpu variables.
(C) 2008 Anatolij Gustschin <agust@denx.de> * DENX Software Engineering */
(C) 2008 Korolev Alexey <akorolev@infradead.org>
(C) 2008 Krzysztof Piotr Oledzki <ole@ans.pl> */
(C) 2008 Manuel Traut <manut@linutronix.de> *
(C) 2008 Vasiliy Leonenko <vasiliy.leonenko@gmail.com> */#include <linux/module.h>#include <linux/types.h>#include <linux/kernel.h>#include
<linux/init.h>#include <linux/errno.h>#include <linux

```

(C) 2008 Vasiliy Leonenko <vasiliy.leonenko@gmail.com> \* Many thanks to Roman Borisov for initial enabling \* \* TODO:  
(C) 2009-2010 Bartłomiej Zolnierkiewicz  
(C) 2009-2010 Bartłomiej Zolnierkiewicz \* \* Based on  
(C) 2009-2010 Bartłomiej Zolnierkiewicz \* \* Some parts based on ata\_piix.c by Jeff Garzik and others.  
(C) 2009 Alexander Clouter <alex@digriz.org.uk> \*/  
(C) 2009 Alexander Clouter <alex@digriz.org.uk>  
(C) 2009 Bartłomiej Zolnierkiewicz \* \* Documentation:  
(C) 2009 Daniel Mack <daniel@caiaq.de> ). \*  
(C) 2009 Google Inc. John(Jung-Ik) Lee <jilee@google.com> \* \* Per Atp867 data sheet rev 1.2, Acard. \* Based in part on early ide code from \* 2003-2004 by Eric Uhrhane, Google, Inc.  
(C) 2009 - Peter Feuerer peter (a) piie.net \* http://piie.net \* 2009 Borislav Petkov bp (a) alien8.de \* \* Inspired by and many thanks to:  
(C) 2009 Samsung Electronics \* Author: Michal Nazarewicz (mina86@mina86.com) \*/  
(C) 2009 Sascha Hauer, Pengutronix  
(C) 2009 Siemens AG \*/#include <linux/fb.h>#include <linux/delay.h>#include <linux/init.h>#include <linux/interrupt.h>#include <linux/module.h>#include <linux/pci.h>#include <linux/slab.h>#if  
(C) 2009 Uwe Kleine-Koenig, Pengutronix \*/  
(C) 2009 Valentin Shtdikov <v.shtdikov@gmail.com>  
(C) 2010-2011 Red Hat, Inc.  
(C) 2010-2011 Red Hat, Inc.\*/  
(C) 2010-2011 Thomas Graf <tgraf@redhat.com>  
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \*/  
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \* \* Based on Len Brown's <lenb@kernel.org> turbostat tool.  
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \* \* Based on SandyBridge monitor. Implements the new package C-states \* (PC8, PC9, PC10) coming with a specific Haswell (family 0  
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \* \* Based on the idea from Michael Matz <matz@suse.de>  
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \* \* Ideas taken over from the perf userspace tool (included in the Linux \* kernel git repo): subcommand builtins and param parsin  
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \* \* Miscellaneous helpers which do not fit or are worth \* to put into separate headers  
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \* \* Output format inspired by Len Brown's <lenb@kernel.org> turbostat tool.  
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \* \* PCI initialization based on example code from:  
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \* \* ToDo: Needs to be done properly for AMD/Intel specifics  
(C) 2010 Bartłomiej Zolnierkiewicz \* \* based upon  
(C) 2010 Eric Dumazet \*/  
(C) 2010 Luotao Fu <l.fu@pengutronix.de> \* All rights reserved  
(C) 2010 Michael J. Cree \* \* Somewhat based on the Sparc code, and to a lesser extent the PowerPC and  
(C) 2010 Pablo Neira Ayuso <pablo@netfilter.org> \*/  
(C) 2010 Red Hat, Inc. \* \* Author: Michael S. Tsirkin <mst@redhat.com>  
(C) 2010 Red Hat Inc \* Author: Michael S. Tsirkin <mst@redhat.com> \* \* This software is distributed under GNU GPL v2, 1991  
(C) 2010 Thomas Renninger <trenn@suse.de> \*/  
(C) 2011 Bartłomiej Zolnierkiewicz \* \* Based in part on linux/drivers/ide/pci/sl82c105.c \* SL82C105/Winbond 553 IDE driver  
(C) 2011 Bartłomiej Zolnierkiewicz \* \* Documentation:  
(C) 2011 DENX Software Engineering, Anatolij Gustschin <agust@denx.de> \*/  
(C) 2011 DENX Software Engineering, Anatolij Gustschin <agust@denx.de> \* \* FIXME: this driver is used on a device-tree probed platform: it \* should be defined as a bit-banged SPI device and probed  
(C) 2011 Hans de Goede <hdegoede@redhat.com>  
(C) 2011 Hans de Goede <hdegoede@redhat.com> \*/  
(C) 2011 Intra2net AG <http://www.intra2net.com> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt  
(C) 2011 Intra2net AG <http://www.intra2net.com> \*/#include <linux/init.h>#include <linux/module.h>#include <linux/kernel.h>#include <linux/skbuff.h>#include <linux/atomic.h>#include <linux/ref  
(C) 2011 Linux Foundation, Christoph Lameter \*/  
(C) 2011 Linux Foundation, Christoph Lameter \* \* Compile with: \*  
(C) 2011 Omnibond Systems \* \* Changes by Acxiom Corporation to implement generic service\_operation()  
(C) 2011 Pablo Neira Ayuso <pablo@netfilter.org>  
(C) 2011 Patrick McHardy <kaber@trash.net> \*/  
(C) 2011 Patrick McHardy <kaber@trash.net> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/in.h>#include <linux/ip.h>#include <net/ip.h>#include <linux/module.h>#include <linux/skb  
(C) 2011 The Chromium OS Authors \*/  
(C) 2011 The Chromium OS Authors \* \* In the file "/sys/module/dm\_verity/parameters/prefetch\_cluster" you can set \* default prefetch value. Data are read in "prefetch\_cluster" chunks from the \* has



(C) 2011 Thomas Renninger <trenn@novell.com> Novell Inc. \*/  
 (C) 2011 Thomas Renninger <trenn@suse.de>, Novell Inc. \*/  
 (C) 2012-2013 by Pablo Neira Ayuso <pablo@netfilter.org> \* \* This software has been sponsored by Sophos Astaro <http://www.sophos.com>  
 (C) 2012-2014 by sysmocom - s.f.m.c. GmbH  
 (C) 2012 by Hans Schillstrom <hans.schillstrom@ericsson.com>  
 (C) 2012 by Lemonage GmbH \* Author: Lars Poeschel <poeschel@lemonage.de> \* All rights reserved  
 (C) 2012 by Pablo Neira Ayuso <pablo@netfilter.org>  
 (C) 2012 by Pablo Neira Ayuso <pablo@netfilter.org> \*/  
 (C) 2012 by Vyatta Inc. <http://www.vyatta.com> \*/  
 (C) 2012 by Vyatta Inc. <http://www.vyatta.com> \*/#include <linux/init.h>#include <linux/module.h>#include <linux/kernel.h>#include  
 <linux/rculist.h>#include <linux/rculist\_nulls.h>#include <li  
 (C) 2012 Christoph Lameter <cl@linux.com> \*/#include <linux/slab.h>  
 (C) 2012 Google, Inc. \* Author: Kent Overstreet <koverstreet@google.com> \* \* This implements a refcount with similar semantics to atomic\_t -  
 atomic\_inc(), \* atomic\_dec\_and\_test() - but percpu.  
 (C) 2012 Michel Lespinasse <>walken@google.com>  
 (C) 2012 MontaVista Software, LLC <source@mvista.com> \* \* Based upon \* linux/drivers/ide/pci/cmd64x.c Version 1.30 Sept 10, 2002  
 (C) 2012 Pablo Neira Ayuso <pablo@netfilter.org> \* \* This software has been sponsored by Vyatta Inc. <http://www.vyatta.com>  
 (C) 2012 Sasha Levin <levinsasha928@gmail.com> \*/  
 (C) 2012 Wolfram Sang, Pengutronix \*/  
 (C) 2013 Astaro GmbH \*/  
 (C) 2013 Daniel Borkmann <dborkman@redhat.com> \*/  
 (C) 2013 James Chapman <jchapman@katalix.com> \*/  
 (C) 2013 Viresh Kumar <viresh.kumar@linaro.org> \* \* Oct 2005 - Ashok Raj <ashok.raj@intel.com> \* Added handling for CPU hotplug \* Feb 2006 -  
 Jacob Shin <jacob.shin@amd.com> \* Fix handling for C  
 (C) 2014-2015 Pratik Patel <pratikp@codeaurora.org> \* \* Serious refactoring, code cleanup and upgrading to the Coresight upstream \* framework by  
 Mathieu Poirier  
 (C) 2014 by Pablo Neira Ayuso <pablo@netfilter.org> \*/  
 (C) 2014 by Pablo Neira Ayuso <pablo@netfilter.org> \* \* Based on code from ebt\_log from: \*  
 (C) 2014 Sittisak <sittisaks@hotmail.com> \*/  
 (C) 2015-2016 Chunyan Zhang <zhang.chunyan@linaro.org> \*/#include <asm/local.h>#include <linux/acpi.h>#include <linux/amba/bus.h>#include  
 <linux/bitmap.h>#include <linux/clk.h>#include <linux/c  
 (C) 2015-2016 Mathieu Poirier <mathieu.poirier@linaro.org> \* \* Guaranteed timing and support for various packet type coming from the \* generic STM  
 API by Chunyan Zhang  
 (C) 2015-2018 Kevin Darbyshire-Bryant <kevin@darbyshire-bryant.me.uk>  
 (C) 2015 Borislav Petkov <bp@alien8.de> \* \* This driver allows to upgrade microcode on x86 processors.  
 (C) 2015 by Il Han <corone.il.han@gmail.com> \*/  
 (C) 2015 Pengutronix, Alexander Aring <aar@pengutronix.de>  
 (C) 2015 Pengutronix, Alexander Aring <aar@pengutronix.de> \*/  
 (C) 2015 Pengutronix, Alexander Aring <aar@pengutronix.de> \* \* Authors:  
 (C) 2015 Red Hat GmbH \* Author: Florian Westphal <fw@strlen.de> \*/  
 (C) 2016 Arnaldo Carvalho de Melo <acme@redhat.com> \*/  
 (C) 2016 by Pablo Neira Ayuso <pablo@netfilter.org> \*/  
 (C) 2016 by Pablo Neira Ayuso <pablo@netfilter.org> \* \* Author: Harald Welte <hwelte@sysmocom.de> \* Pablo Neira Ayuso  
 <pablo@netfilter.org> \* Andreas Schultz <aschultz@travelping.com>  
 (C) 2016 Pengutronix, Alexander Aring <aar@pengutronix.de> \*/  
 (C) 2017-2018 Synopsys, Inc. (www.synopsys.com)  
 (C) 2017 Anju T Sudhakar, IBM Corporation.  
 (C) 2017 Helge Deller <deller@gmx.de> \*/  
 (C) 2017 Hemant K Shaw, IBM Corporation. \*/  
 (C) 2017 Hemant K Shaw, IBM Corporation. \*/#include <linux/kernel.h>#include <linux/platform\_device.h>#include <linux/of.h>#include  
 <linux/of\_address.h>#include <linux/of\_platform.h>#include <l  
 (C) 2017 Hemant K Shaw, IBM Corporation. \*/#include <linux/perf\_event.h>#include <linux/slab.h>#include <asm/opal.h>#include <asm/imc-  
 pmu.h>#include <asm/cputhreads.h>#include <asm/smp.h>#incl  
 (C) 2017 Linus Walleij <linus.walleij@linaro.org> \*/#ifndef \_GEMINI\_ETHERNET\_H#define \_GEMINI\_ETHERNET\_H  
 (C) 2017 Red Hat GmbH \* Author: Florian Westphal <fw@strlen.de> \* \* split from xt\_connlimit.c:  
 (C) 2018 Mojatau, Alexander Aring <aring@mojatau.com>  
 (C) 2018 Uladzislau Rezki (Sony) <urezki@gmail.com> \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/module.h>#include  
 <linux/vmalloc.h>#include <linux/random.h>#include <linux  
 (C) \* 2 - 1) : ((C)  
 (C) Adam Belay. \*/#include <linux/module.h>#include <linux/pci.h>#include <linux/pnp.h>#include <linux/string.h>#include  
 <linux/kernel.h>#include <linux/serial\_core.h>#include <linux/bitops.h>

```

(C) Alex Holden " " <alex@linuxhacker.org> 1998.\n", VERSION);
(C) and server (S): * * Setup of client connection:
(C) Architecture Software * Developer's Manual, Volume 3: System Prgramming Guide, * revision #12 in Table B-1: MSRs in the Pentium 4 and *
Intel Xeon Processors, on page B-4 and B-5. */ fsb_
(C) * A(W1) * C(C) * * Which would create W1->C->W1 dependencies, even though there is no * actual deadlock
possible. There are two solutions, using a * read-recursive acquire on the wor
(C) Balbir Singh, IBM Corp. 2006
(C) Balbir Singh, IBM Corp. 2006 */
(C) Benjamin Reed,
(C) bits in a HPT * entry can be set by hardware at any time up until * the HPTE is invalidated and the TLB invalidation * sequence
has completed. This means that when * removing a HPTE,
(C) __BUG_ON((unsigned long)(C))
(C) by the respective authors, and licensed under the GPL
(C) by Vojtech Pavlik */
(C) (((C) << 11)
(C) Carnegie Mellon University
(C) CC Computer Consultants GmbH, 2007 */
(C) CC Computer Consultants GmbH, 2007 * * based on ...
(C) CC Computer Consultants GmbH, 2007 */ #define pr_fmt(fmt) KBUILD_MODNAME ": " fmt #include <linux/in.h> #include <linux/in6.h> #include
<linux/ip.h> #include <linux/ipv6.h> #include <linux/jhash.
(C) CC Computer Consultants GmbH, 2008 */ #define pr_fmt(fmt) KBUILD_MODNAME ": " fmt #include <linux/module.h> #include
<linux/skbuff.h> #include <linux/ip.h> #include <linux/ipv6.h> #include <linux
(C)(C) Leaguerme (Shenzhen) MicroElectronics Co., Ltd. * * see Documentation/media/dvb-drivers/dvb-usb.rst for more information
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<linux/export.h> #include <asm/cacheflush.h> #includ
(C) Copyright 1995 - 1997 Marco van Wieringen - ELM Consultancy B.V. * * Plugged two leaks. 1) It didn't return acct_file into the free_filps if * the file
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won't always recover a failed machine.
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this is less than 10 seconds, so to allow for * softw
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(c) Copyright 2005 Benjamin Herrenschmidt, IBM Corp. \* <benh@kernel.crashing.org> \* \* The algorithm used is the PID control algorithm, used the same \* way the published Darwin c  
(c) Copyright 2005 Benjamin Herrenschmidt, IBM Corp. \* <benh@kernel.crashing.org> \* \* This core code tracks the list of sensors & controls, register \* clients, and holds the ker  
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(C) Copyright 2007 MIPS Technologies, Inc. \* written by Ralf Baechle <ralf@linux-mips.org> \*/#include <linux/pci.h>#include <linux/export.h>#include <asm/io.h>  
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(C) Copyright 2008 Intel Corporation, Arjan van de Ven. \*/#include <linux/debugfs.h>#include <linux/fs.h>#include <linux/hugetlb.h>#include <linux/io.h>#include <linux/mm.h>#include <linux/high  
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(c) Copyright 2008, RealTEK Technologies Inc. All Rights Reserved  
(C) Copyright 2008 \* Stefano Babic, DENX Software Engineering, sbabic@denx.de. \* \* This driver implements a lcd device for the ILITEK 922x display \* controller. The interface to the display is SPI  
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(C) Copyright 2009 Intel Corporation \* Author: Arjan van de Ven <arjan@linux.intel.com> \*/#ifndef \_\_ASYNC\_H\_\_#define \_\_ASYNC\_H\_\_  
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_PLATFORM_MSIC_H_#define _PLATFORM_MSIC_H_
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<linux/kernel.h>#include <linux/interrupt.h>#include <linux/scatterlist
(C) Copyright 2013, Jonas Jensen <jonas.jensen@gmail.com>
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SOURCEFILE\_NAME "hpios.c" #include "hpi\_internal.h" #include "hpiddebug  
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\_HPIOS\_H\_ #define \_HPIOS\_H\_  
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SOURCEFILE\_NAME "hpi6000.c"  
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SOURCEFILE\_NAME "hpi6205.c"

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SOURCEFILE_NAME "hpimgx.c"#include "hpi_internal.h"#include "hpi_version.
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<linux/mount.h>#include <linux/fs.h>#include <linux/nsproxy.h>#include <u  
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<asm/txx9/pci.h>#include <asm/txx9/tx4927pcie.h>  
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<linux/module.h>#include <linux/io.h>#include <linux/g  
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(C) David Hinds and the Linux wireless
(C) David S. Miller. */
(C) DENX Software Engineering. * * Author: Yuri Tikhonov <yur@emcraft.com>
(C) (diagram), 9.2 * Upon reception of the SHUTDOWN COMPLETE chunk the endpoint will verify * that it is in SHUTDOWN-ACK-SENT state, if it is not
the chunk should be * discarded. If the endpoint is
(C) " DRIVER_AUTHOR "\n");
(C) DVB-S USB2.0");
(C) IBM Corporation, 2011. *      Author: Suzuki Poulouse <suzuki@in.ibm.com> * * - Based on ppc64 code - reloc_64.S
(C) in bits (      MOVQ_R64_XMM   %r12, %xmm1
(C) in bits (      vmovq   %r12, %xmm1      vpslldq $8, %xmm15, %xmm15      # xmm15 = len(A)|| 0x0000000000000000
(C) INESC 1998 */
(C) INESC 1998 *
(C) INESC 1999 */
(C) * in woken_wake_function() such that either we see the wait condition * being true or the store to wq_entry->flags in woken_wake_function()
* follows ours in the coherence order. */  smp_s
(C) IP_VS_DBG_ADDR((C)->af,      ntohs((C)->cport),      \      IP_VS_DBG_ADDR((C)->af, &((C)->vaddr)),      \
ntohs((C)->vport),      \      IP_VS_DBG_ADDR((C)->daf, &((C)->daddr)),      \      ntohs((C)->dport),
\
(C) Jay Lan,      SGI, 2006
(C) Jean Tourrilhes.
(C) Jens Axboe <jens.axboe@oracle.com> 2008 */
(C) Jiri Kosina"\n");out:
(C) Len Brown <len.brown@intel.com>"\n");}
(C) Linus Torvalds * * 'traps.c' handles hardware exceptions after we have saved some state in * 'linux/arch/arm/lib/traps.S'. Mostly a debugging aid,
but will probably * kill the offending pr
(C) Linux Foundation 2008-2013 *      Unified interface for all slab allocators */
(C) Nadia Yvette Chambers, April 2004 */ #include <linux/list.h> #include <linux/init.h> #include <linux/mm.h> #include <linux/seq_file.h> #include
<linux/sysctl.h> #include <linux/highmem.h> #includ
(C) Neil Horman <nhorman@tuxdriver.com> */
(C) * Notes: To disconnect the current associated BSS * Command Mode */ struct disconnect_parm { u32 rsvd;
(C) * Notes: To enter USB suspend mode * Command Mode */ struct usb_suspend_parm {      u32 action; /* 1: sleep, 0: resume */
copyright ;-) */
COPYRIGHT      (0 << 2)
COPYRIGHT) ? 0 : AC97_CXR_COPYRGT;      v |= new & IEC958_AES0_NONAUDIO ? AC97_CXR_SPDIF_AC3 : AC97_CXR_SPDIF_PCM;
change |= snd_ac97_update_bits_nolock(ac97, AC97_CXR_AUDIO_MISC,      AC97_CXR
copyright (0 = copy inhibited) *      D1: 0 = digital audio / 1 = non-digital audio *      * C6 upper channel status (digital serial data description)
(CS4237B,CS4238B) *      D7-D6: sample freque
COPYRIGHT      0#define DVD_CPM_COPYRIGHTED      1
COPYRIGHT      (0)#define EEPROM_COPYRIGHT_LEN      (44)#define EEPROM_CHECKSUM      (62)#define EEPROM_CHECKSUM_REV      (63)#define
EEPROM_MAC      (64)#define EEPROM_MAC_REV      (70)#define EEPROM_SERIAL      (112)#defin
COPYRIGHT      0x00000004      /
COPYRIGHT      0x01#define DVD_STRUCT_DISCKEY      0x02#define DVD_STRUCT_BCA      0x03#define DVD_STRUCT_MANUFACT      0x04
COPYRIGHT      = 0x10,      AUDIO_STATUS_NONAUDIO      = 0x20,      AUDIO_STATUS_PROFESSIONAL = 0x40,      AUDIO_STATUS_LEVEL      =
0x80};
COPYRIGHT      (0x1 << 2)
COPYRIGHT      0xd4#define CX2341X_ENC_SET_EVENT_NOTIFICATION      0xd5#define CX2341X_ENC_SET_NUM_VSYNC_LINES
0xd6#define CX2341X_ENC_SET_PLACEHOLDER      0xd7#define CX2341X_ENC_MUTE_VIDEO      0xd9#define CX
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Copyright 10/16/2005 Tilman Kranz <tilde@tk-sls.de> * Creative Audio MIDI, for the CA0106 Driver * Version: 0.0.1 * * Changelog:
COPYRIGHT      (1 << 2)#define HDMI_I2S_LINEAR_PCM      (0 << 1)#define HDMI_I2S_NO_LINEAR_PCM      (1 << 1)#define
HDMI_I2S_CONSUMER_FORMAT      (0)#define HDMI_I2S_PROF_FORMAT      (1)#define HDMI_I2S_CH_ST_0_CLR
COPYRIGHT      (1<<2)      /*#define IEC958_AES0_CON_EMPHASIS      (7<<3)      /* mask - emphasis */#define IEC958_AES0_CON_EMPHASIS_NONE
(0<<3)      /* none emphasis */#define IEC958_AES0_CON_EMPHASIS_5015      (1<<3)      /* 50/15u
COPYRIGHT      (1<<4)#define AC_DIG1_NONAUDIO      (1<<5)#define AC_DIG1_PROFESSIONAL      (1<<6)#define AC_DIG1_LEVEL
(1<<7)
COPYRIGHT      (1 << 5)#define m_AUDIO_STATUS_ADDITION      (3 << 2)#define m_AUDIO_STATUS_CLK_ACCURACY      (2 << 0)#define
v_AUDIO_STATUS_NLPCM(n)      ((n & 1) << 7)#define AUDIO_N_H      0x3f#define AUDIO_N_M      0
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option.\n");#endif
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 return double(temp)\*0.582-58.16; \* else \* return double(temp)\*  
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 (2628/1.25=2102.4) // +2.5V \* voltagefactor[2]=1.67/2628; (262  
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<linux/kernel.h>#include <linux/nodemask.h>#include <linux/str  
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 >device, BACKPACK\_VERSION, pi->port); printk("%s: Unit: %d  
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<linux/ptrace.h>#include <linux/delay.h>
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<linux/errno.h>#include <linux/reboot.h>#include <linux/slab.h>#include  
Copyright 2007 David Gibson <dwg@au1.ibm.com>, IBM Corporation. \*/#include <linux/kernel.h>#include <linux/of.h>#include <linux/init.h>#include  
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"stdio.h"#include "page.h"#include "ops.h"  
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 <linux/device.h>#include <linux/slab.h>#include <  
 Copyright 2007 Rusty Russell <rusty@rustcorp.com.au> IBM Corporation \*//#define DEBUG#include <linux/netdevice.h>#include  
 <linux/etherdevice.h>#include <linux/ethtool.h>#include <linux/module.h  
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 Copyright 2007 Sony Corporation \*/#undef DEBUG  
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<linux/irq.h>#include <linux/perf\_event.h>#include <linux/ex  
Copyright 2008-2009 Paul Mackerras, IBM Corporation. \*/#include <linux/kernel.h>#include <linux/perf\_event.h>#include <linux/string.h>#include  
<asm/reg.h>#include <asm/cputable.h>  
Copyright 2008-2009 Paul Mackerras, IBM Corporation. \*/#include <linux/kernel.h>#include <linux/sched.h>#include <linux/sched/clock.h>#include  
<linux/perf\_event.h>#include <linux/percpu.h>#incl  
Copyright 2008-2009 Paul Mackerras, IBM Corporation. \*/#include <linux/string.h>#include <linux/perf\_event.h>#include <asm/reg.h>#include  
<asm/cputable.h>  
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Copyright 2008 by Karsten Keil <kkeil@novell.com> * * Module options: *
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Copyright 2008 by Martin Bachem (info@bachem-it.com) * * module params *  debug=<n>, default=0, with n=0xHHHHGGGG *  H - I1 driver flags
described in hfcsusb.h *  G - common mISDN debug
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<linux/init.h>#include <linux/interrupt.h>#include <linux/apm-em
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<linux/module.h>#include <linux/sizes.h>#include "s
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<asm/reg.h>#include <asm/cputable.h>
Copyright © 2009 Paul Mackerras, IBM Corporation. */#include <linux/kernel.h>#include <linux/sched.h>#include <linux/perf_event.h>#include
<linux/percpu.h>#include <linux/uaccess.h>#include <li
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<linux/spinlock.h>#include <linux/init.h>#include <linux/export.h>#inc
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mapping.h>#include <linux/input.h>#include <linux/io.h>#inc
Copyright 2010-2011 Calxeda, Inc. */#include <linux/module.h>#include <linux/mod_devicetable.h>#include <linux/kernel.h>#include
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"efx.h"#include "nic.h"#include "io.h"#include "mcdi.h"#inc
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modules which provide the parameters and scanc
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<linux/kernel.h>#include <linux/mm.h>#include <li  
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<linux/percpu.h>#include <linux/hardirq.h>#include <asm/reg  
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 < linux/interrupt.h >#include < linux/platform\_device.h  
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"string.h"#include "stdio.h"#include "page.h"#include "ops.h  
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<linux/interrupt.h>#include <linux/cpu.h>#include <linux  
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Copyright 2015, 2019 Google LLC \*/  
Copyright 2015 Adam Sampson <ats@offog.org>  
Copyright 2015 Advanced Micro Devices, Inc.  
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Copyright 2015 Advanced Micro Devices, Inc.#include "dm\_services.h"#include "dc.h"#include "dc\_link\_dp.h"#include "dm\_helpers.h"#include  
"opp.h"#ifndef CONFIG\_DRM\_AMD\_DC\_DSC\_SUPPORT#include "dsc  
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Copyright 2015 Amazon.com, Inc. or its affiliates.  
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Copyright 2015 Andrea Venturi <be17068@iperbole.bo.it>  
Copyright 2015 Andrew Lunn <andrew@lunn.ch> \*/  
Copyright 2015, Anshuman Khandual, IBM Corporation. \*/#define \_GNU\_SOURCE#include "dscr.h"  
Copyright 2015, Anshuman Khandual, IBM Corporation. \*/#ifndef \_SELFTESTS\_POWERPC\_DSCR\_DSCR\_H#define  
\_SELFTESTS\_POWERPC\_DSCR\_DSCR\_H  
Copyright 2015, Anshuman Khandual, IBM Corporation. \*/#include "dscr.h"  
Copyright 2015, Anton Blanchard, IBM Corp. \*/

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 Copyright © 2015 Broadcom Corporation \* \* Derived from bcm963xx\_4.12L.06B\_consumer/shared/opensource/include/bcm963xx/63268\_map\_part.h:  
 Copyright 2015 Carnegie Mellon University \* \* Expose entries from QEMU's firmware configuration (fw\_cfg) device in \* sysfs (read-only, under  
 "/sys/firmware/qemu\_fw\_cfg/...").  
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 checking its FPU registers, at some \* p  
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 The check\_fpu function in asm only checks \* t  
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 checking its VMX registers, at some \* p  
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 The check\_vmx function in asm only checks \* t  
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 preemption happened so this test just \* uses many thr  
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 Copyright 2015 IBM Corporation \* Author: Mahesh Salgaonkar <mahesh@linux.vnet.ibm.com> \*/  
 Copyright 2015 IBM Corporation. \*/#include <asm/ppc\_asm.h>#include <asm/export.h>#include <asm/ppc-opcode.h>  
 Copyright 2015 \*/#include <linux/delay.h>#include <linux/io.h>#include <linux/leds.h>#include <linux/module.h>#include <linux/of.h>#include  
 <linux/platform\_device.h>#include <linux/spinlock.h>  
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 Copyright © 2015 Intel Corporation. \* \* Authors: David Woodhouse <dwmw2@infradead.org>  
 Copyright 2015 Intel Deutschland GmbH \*/#include <net/mac80211.h>#include "ieee80211\_i.h"#include "trace.h"#include "driver-ops.h"  
 Copyright 2015 Joachim Eastwood <manabian@gmail.com> \*/  
 Copyright 2015 Joachim Eastwood <manabian@gmail.com> \* \* UNSUPPORTED hardware features:  
 Copyright 2015 Jonas Gorski <jogo@openwrt.org> \*/#include <linux/io.h>#include <linux/leds.h>#include <linux/module.h>#include  
 <linux/of.h>#include <linux/platform\_device.h>#include <linux/spin  
 Copyright 2015 Karol Herbst  
 Copyright 2015 Karol Herbst <nouveau@karolherbst.de>  
 Copyright © 2015 Keith Packard <keithp@keithp.com> \*/  
 Copyright 2015, Laurent Dufour, IBM Corp. \* \* Test the kernel's signal returning code to check reclaim is done if the \* sigreturn() is called while in a  
 transaction (suspended since active is \* al  
 Copyright 2015 LG Electronics Inc.  
 Copyright 2015 Linaro. \*/#include <linux/sched.h>#include <linux/device.h>#include <linux/dmaengine.h>#include <linux/dma-mapping.h>#include  
 <linux/dmapool.h>#include <linux/init.h>#include <l  
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 Copyright 2015 Linaro Limited. \* \* Author: Stanimir Varbanov <svarbanov@mm-sol.com>  
 Copyright 2015 Linaro Ltd.  
 Copyright 2015 Linaro Ltd. \*/  
 Copyright 2015 Linaro Ltd. \*/#include <errno.h>#include <fcntl.h>#include <stdio.h>#include <string.h>#include <stdlib.h>#include  
 <stdint.h>#include <poll.h>#include <sys/types.h>#include <t  
 Copyright 2015 Linaro Ltd. \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/slab.h>#include <linux/sizes.h>#include  
 <linux/cdev.h>#include <linux/fs.h>#include <linux/idr.h>  
 Copyright 2015 Linaro Ltd. \*/#include <linux/kernel.h>#include <linux/module.h>#include <sound/soc.h>#include <sound/pcm\_params.h>  
 Copyright 2015 Linaro Ltd. \*/#undef TRACE\_SYSTEM#define TRACE\_SYSTEM greybus  
 Copyright 2015 Maciej Szmigiero <mail@maciej.szmigiero.name> \* \* Based on ad7414.c:  
 Copyright 2015 Marcus Cooper <codekipper@gmail.com> \* \* Based on the Allwinner SDK driver, released under the GPL.  
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 Copyright 2015 Maxime Ripard \* \* Maxime Ripard <maxime.ripard@free-electrons.com>  
 Copyright 2015 Mentor Graphics Corporation. \*/  
 Copyright 2015 Mentor Graphics Corporation. \* \* vdsomunge - Host program which produces a shared object \* architecturally specified to be usable by  
 both soft- and hard-float \* programs.  
 Copyright 2015, Michael Ellerman, IBM Corp. \*/  
 Copyright 2015, Michael Ellerman, IBM Corp. \* \* This test simply tests that certain syscalls are implemented. It doesn't \* actually exercise their logic in  
 any way.  
 Copyright 2015, Michael Neuling, IBM Corp. \* \* Edited: Rashmica Gupta, Nov 2015  
 Copyright 2015, Michael Neuling, IBM Corp. \* Original: Michael Neuling 19/7/2013 \* Edited: Rashmica Gupta 01/12/2015 \* \* Do some transactions, see  
 if the tar is corrupted. \* If the transaction is  
 Copyright 2015, Michael Neuling, IBM Corp. \* \* Original: Michael Neuling 3/4/2014 \* Modified: Rashmica Gupta 8/12/2015  
 Copyright 2015, Michael Neuling, IBM Corp. \* \* Original: Michael Neuling 4/12/2013 \* Edited: Rashmica Gupta 4/12/2015  
 Copyright 2015, Michael Neuling, IBM Corp. \* \* Test the kernel's signal delievery code to ensure that we don't \* trelaim twice in the kernel signal delivery  
 code. This can happen \* if we trigger  
 Copyright 2015, Michael Neuling, IBM Corp. \* \* Test the kernel's signal return code to ensure that it doesn't \* crash when both the transactional and  
 suspend MSR bits are set in \* the signal conte  
 Copyright 2015 Nouveau project  
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Copyright 2015 Regents of the University of California  
Copyright 2015 Regents of the University of California, Berkeley \* \* See asm-generic/syscall.h for descriptions of what we must do here.  
Copyright 2015 Robert Jarzmik <robert.jarzmik@free.fr> \*/  
Copyright 2015, Sam Bobroff, IBM Corp. \* \* Test the kernel's system call code to ensure that a system call \* made from within an active HTM transaction  
is aborted with the \* correct failure code.  
Copyright 2015 Samuel Pitoiset  
Copyright 2015 Samuel Pitosiet  
Copyright 2015 Simon Arlott \* \* Derived from bcm63138\_nand.c:  
Copyright 2015 Simon Arlott \* \* This is based on the BCM7038 (which supports SMP) but with a single \* enable register instead of separate  
mask/set/clear registers.  
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Copyright 2015 Yoshinori Sato <ysato@users.sourceforge.jp> \*/  
Copyright 2016-17 IBM Corp. \*/  
Copyright 2016-17 IBM Corp. \*/#include <asm/ppc-opcode.h>#include <asm/reg.h>  
Copyright 2016-2017, 2019 NXP \*/  
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Copyright 2016-2017 Google, Inc \* \* Fairchild FUSB302 Type-C Chip Driver  
Copyright 2016,2017 IBM Corporation. \*/  
Copyright 2016,2017 IBM Corporation. \*/#ifndef \_ASM\_POWERPC\_XIVE\_H#define \_ASM\_POWERPC\_XIVE\_H  
Copyright 2016,2017 IBM Corporation. \*/#ifndef \_ASM\_POWERPC\_XIVE\_REGS\_H#define \_ASM\_POWERPC\_XIVE\_REGS\_H  
Copyright 2016,2017 IBM Corporation. \*/#ifndef \_\_XIVE\_INTERNAL\_H#define \_\_XIVE\_INTERNAL\_H  
Copyright 2016-2017 Intel Deutschland GmbH  
Copyright 2016-2017 NXP \*/  
Copyright 2016-2017 NXP \*/#include <linux/init.h>#include <linux/module.h>#include <linux/platform\_device.h>#include  
<linux/etherdevice.h>#include <linux/of\_net.h>#include <linux/interrupt.h>#  
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```

Copyright © 2016 Andrew Lunn <andrew@lunn.ch> */#include <linux/device.h>#include <linux/module.h>#include <linux/mtd/mtd.h>#include
<linux/mtd/partitions.h>#include <linux/mutex.h>#include <li
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Copyright: 2016 Barix AG * Author: Petr Kulhavy <petr@barix.com> * * TODO:
Copyright 2016 Broadcom */
Copyright 2016 Broadcom */#ifndef DRIVERS_PCI_ECAM_H#define DRIVERS_PCI_ECAM_H
Copyright 2016 Broadcom */#include <linux/clk.h>#include <linux/platform_device.h>#include <linux/device.h>#include <linux/of_mdio.h>#include
<linux/module.h>#include <linux/phy.h>#include <li
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Copyright 2016 Chen-Yu Tsai <wens@csie.org> */
Copyright 2016 Chen-Yu Tsai <wens@csie.org> * * Based on the Allwinner SDK driver, released under the GPL.
Copyright 2016, Chris Smart, IBM Corporation. * * Calls to copy_first which are not 128-byte aligned should be * caught and sent a SIGBUS.
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Copyright 2016 Crestez Dan Leonard <leonard.crestez@intel.com> * * SI1132 (7-bit I2C slave address 0x60) * SI1141/2/3 (7-bit I2C slave address
0x5a) * SI1145/6/6 (7-bit I2C slave address 0x60)
Copyright 2016, Cyril Bur, IBM Corp. */
Copyright 2016, Cyril Bur, IBM Corp. * * Sending one self a signal should always get delivered.
Copyright 2016, Cyril Bur, IBM Corp. * * Syscalls can be performed provided the transactions are suspended. * The exec() class of syscall is unique as a
new process is loaded.
Copyright 2016, Cyril Bur, IBM Corp. * * Test the kernel's signal frame code.
Copyright 2016, Daniel Axtens, IBM Corporation. */
Copyright 2016 David Lechner <david@lechnology.com> * * Based on iio/ad7923.c:
Copyright 2016 Elettra-Sincrotrone Trieste S.C.p.A. * * Author: Alessio Igor Bogani <alessio.bogani@elettra.eu>
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Copyright 2016 Ezequiel Garcia <ezequiel@vanguardiasur.com.ar> * * Based on LED IDE-Disk Activity Trigger
© Copyright 2016 Free Electrons * * Author: Boris Brezillon <boris.brezillon@free-electrons.com>
Copyright 2016 Free Electrons NextThing Co. * Quentin Schulz <quentin.schulz@free-electrons.com> * * This driver is based on a previous
upstreaming attempt by:
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Copyright 2016 Freescale Semiconductor, Inc. * * Author: Zhao Qiang <B45475@freescale.com>
Copyright 2016 Freescale Semiconductor, Inc. * * There is no Shared Descriptor for PKC so that the Job Descriptor must carry * all the desired key
parameters, input and output pointers.
copyright 2016 google inc.
Copyright 2016 Google Inc.
Copyright 2016 Google Inc. */
Copyright 2016 Google Inc. */#include <linux/kernel.h>#include <linux/module.h>#include <linux/slab.h>#include <linux/sizes.h>#include
<linux/uaccess.h>#include <linux/greybus.h>
Copyright 2016 Hans Verkuil <hverkuil@xs4all.nl> */
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```

```

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Copyright 2016 HUAWEI TECHNOLOGIES CO., LTD. */
Copyright 2016 HUAWEI TECHNOLOGIES CO., LTD. */
Copyright © 2016 IBM Corporation
Copyright 2016 IBM Corporation. */#include <linux/types.h>#include <linux/kernel.h>#include <linux/irq.h>#include <linux/smp.h>#include
<linux/interrupt.h>#include <linux/cpu.h>#include <linux
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Copyright 2016 Intel Deutschland GmbH
Copyright 2016 Jonathan Cameron <jic23@kernel.org> * * Based on a mashup of the hrtimer trigger and continuous sampling proposal of * Gregor
Boirie <gregor.boirie@parrot.com>
Copyright 2016 Karol Herbst
Copyright 2016 Linaro Ltd.
Copyright 2016 Linaro Ltd. */
Copyright 2016 Linaro Ltd. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt
Copyright 2016 Linaro Ltd. */#include <linux/kernel.h>#include <linux/module.h>#include <linux/pm_runtime.h>#include <sound/soc.h>#include
<sound/pcm_params.h>#include <uapi/linux/input.h>
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Copyright 2016 Maciej W. Rozycki <macro@linux-mips.org>
Copyright 2016 Madhavan Srinivasan, IBM Corporation. */
Copyright 2016 Madhavan Srinivasan, IBM Corporation. */#include "isa207-common.h"
Copyright 2016 Marek Vasut <marek.vasut@gmail.com> */
Copyright 2016 Maxime Ripard * * Maxime Ripard <maxime.ripard@free-electrons.com>
Copyright 2016, Michael Ellerman, IBM Corp. */
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by Matt Evans
Copyright 2016 Naveen N. Rao <naveen.n.rao@linux.vnet.ibm.com> * IBM Corporation */#ifndef _BPF_JIT64_H#define _BPF_JIT64_H
Copyright 2016 Naveen N. Rao <naveen.n.rao@linux.vnet.ibm.com> * IBM Corporation */#include <linux/kprobes.h>#include
<linux/ptrace.h>#include <linux/hardirq.h>#include <linux/preempt.h>#inc
Copyright 2016 Netflix, Inc.
Copyright 2016 Netflix, Inc. */#include <stdio.h>#include <stdlib.h>#include <unistd.h>#include <errno.h>#include <signal.h>#include
<string.h>#include <assert.h>#include <linux/perf_event.h>
Copyright 2016 Noralf Tr
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Copyright 2016 Nuvoton Technology Corp. * Author: David Lin <ctlin0@nuvoton.com> */
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Copyright 2016 Nuvoton Technology Corp. * Author: John Hsu <KCHSU0@nuvoton.com> */
Copyright 2016 NXP */
Copyright 2016 NXP */
Copyright 2016 NXP */#ifndef _FSL_DPNI_CMD_H#define _FSL_DPNI_CMD_H
Copyright 2016 NXP */#include <linux/kernel.h>#include <linux/errno.h>#include <linux/fsl/mc.h>#include "dpni.h"#include "dpni-cmd.h"
Copyright 2016 NXP Semiconductor * * Derived from mpc85xx_edac.c * Author: Dave Jiang <djiang@mvista.com>
Copyright 2016 NXP Semiconductors */
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Copyright 2016 Pengutronix, <kernel@pengutronix.de> */
Copyright 2016 Peter Meerwald-Stadler <pmeerw@pmeerw.net> * * IIO driver for VEML6070 (7-bit I2C slave addresses 0x38 and 0x39)
Copyright 2016 Qtechnology A/S * 2016 Ricardo Ribalda <ricardo.ribalda@gmail.com> */#ifndef __LINUX_PLATFORM_DATA_AD5761_H__#define
__LINUX_PLATFORM_DATA_AD5761_H__
Copyright 2016 Qtechnology A/S * 2016 Ricardo Ribalda <ricardo.ribalda@gmail.com> */#include <linux/kernel.h>#include <linux/module.h>#include
<linux/spi/spi.h>#include <linux/bitops.h>#include
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```

```

Copyright 2016, Rashmica Gupta, IBM Corp. *
Copyright 2016, Rashmica Gupta, IBM Corp. * * This traverses the kernel pagetables and dumps the * information about the used sections of memory to
* /sys/kernel/debug/kernel_pagetables.
Copyright 2016, Rashmica Gupta, IBM Corp. * * This traverses the kernel virtual memory and dumps the pages that are in * the hash pagetable, along
with their flags to * /sys/kernel/debug/kernel_ha
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Copyright 2016 Realtek Semiconductor Corp. * Author: Bard Liao <bardliao@realtek.com> */
Copyright 2016 Realtek Semiconductor Corp. * Author: Jack Yu <jack.yu@realtek.com> */ #include <linux/module.h> #include
<linux/moduleparam.h> #include <linux/init.h> #include <linux/delay.h> #include
Copyright 2016 Realtek Semiconductor Corp. * Author: Oder Chiou <oder_chiou@realtek.com> */
Copyright 2016 Red Hat Inc.
Copyright 2016 Red Hat, Inc. and/or its affiliates. */ #include <linux/kvm_host.h> #include <linux/debugfs.h> #include "lapic.h"
Copyright 2016 Rockwell Collins * * Datasheet:
Copyright 2016 Russell King <rmk
Copyright 2016 Samsung Electronics Co., Ltd. */
Copyright 2016 Samsung Electronics Co., Ltd. * * Author: Beomho Seo <beomho.seo@samsung.com> * Author: Jaechul Lee
<jcsing.lee@samsung.com>
Copyright 2016 Samsung Electronics Co., Ltd. */ #ifndef MAX98504_H_ #define MAX98504_H_
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Copyright 2017-2018 NXP * Author: Dong Aisheng <aisheng.dong@nxp.com> */
Copyright 2017-2018 NXP * Dong Aisheng <aisheng.dong@nxp.com> */
Copyright 2017-2018 NXP * Dong Aisheng <aisheng.dong@nxp.com> * * Implementation of the SCU based Power Domains
Copyright 2017-2018 NXP * * Header file containing the public API for the System Controller (SC) * Power Management (PM) function. This includes
functions for power state * control, clock control,
Copyright 2017-2018 NXP */ #ifndef _DPSECI_H_ #define _DPSECI_H_
Copyright 2017-2018 Qiang Yu <yuq825@gmail.com>
Copyright © 2017-2019 Intel Corporation */
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Copyright 2017-2019 Qiang Yu <yuq825@gmail.com> */
Copyright 2017 Advanced Micro Devices, Inc.
Copyright 2017 Advanced Micro Devices, Inc. * * This file is modified from rt288 machine driver
Copyright © 2017 Amazon.com, Inc. or its affiliates. * * Author: David Woodhouse <dwmw2@infradead.org>
Copyright 2017 Analog Devices Inc. * Author: Lars-Peter Clausen <lars@metafoo.de> */
Copyright 2017 Analog Devices Inc. */ #include <linux/bitfield.h> #include <linux/clk.h> #include <linux/delay.h> #include <linux/device.h> #include
<linux/err.h> #include <linux/gpio/consumer.h> #in

```

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Copyright 2017, Anshuman Khandual, IBM Corp. \* \* Works on architectures which support 128TB virtual \* address range and beyond.  
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Copyright 2017 \* \* Author: Dong Aisheng <aisheng.dong@nxp.com>  
Copyright 2017 \* Author: Dong Aisheng <aisheng.dong@nxp.com> \* \* File containing client-side RPC functions for the MISC service. These \* function  
are ported to clients that communicate to the SC.  
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Copyright: 2017 Cadence Design Systems, Inc. \* \* Author: Boris Brezillon <boris.brezillon@bootlin.com>  
Copyright 2017 Cadence Design Systems Inc. \*/#include <linux/memblock.h>#include <linux/stddef.h>#include <linux/kernel.h>#include  
<linux/init.h>#include <linux/errno.h>#include <linux/reboot.h  
Copyright 2017 Canonical Ltd. \*/  
Copyright 2017 Canonical Ltd. \* \* TODO \* If a task uses change\_hat it currently does not return to the old \* cred or task context but instead creates a  
new one. Ideally the task \* should return  
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Copyright 2017 (c) Samsung Electronics Software, Inc. \* \* Based on the Exynos PRNG driver drivers/crypto/exynos-rng by \* Krzysztof Kozłowski  
<krzk@kernel.org>  
Copyright 2017 CS Systemes d \*  
Copyright 2017 Daniel Axtens, IBM Corporation. \*/  
Copyright 2017, Daniel Axtens, IBM Corporation. \* [based on crc32c-vpmsum\_glue.c] \*/  
Copyright 2017 David Lechner <david@lechnology.com> \*/  
Copyright 2017 David Lechner <david@lechnology.com> \* \* Some code copied from mipi-dbi.c  
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Copyright 2017 Google Inc.  
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Copyright 2017 Google, Inc. \*/  
Copyright 2017 Google Inc \* \* Provides a simple driver to control the ASPEED LPC snoop interface which \* allows the BMC to listen on and save the data  
written by \* the host to an arbitrary LPC I/O  
Copyright 2017 Google, Inc \* \* This driver uses the memory mapper cros-ec interface to communicate \* with the Chrome OS EC about accelerometer  
data or older commands. \* Accelerometer access is pre  
Copyright 2017 Google LLC \* \* Based on Intel IPU4 driver.  
Copyright 2017, Gustavo Romero, Breno Leitao, Cyril Bur, IBM Corp. \* \* Force FP, VEC and VSX unavailable exception during transaction in all \* possible  
scenarios regarding the MSR.FP and MSR.VEC st  
Copyright 2017, Gustavo Romero, IBM Corp. \* \* Check if thread endianness is flipped inadvertently to BE on trap \* caught in TM whilst MSR.FP and  
MSR.VEC are zero (i.e. just after \* load\_fp and loa  
Copyright 2017 Hannes Reinecke, SUSE Linux GmbH <hare@suse.com> \* \* Based on the original DAC960 driver,  
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Copyright 2017 \* \* Header file containing the public API for the System Controller (SC) \* Miscellaneous (MISC) function.  
Copyright 2017 \* \* Header file containing the public System Controller Interface (SCI) \* definitions.  
Copyright 2017 \* \* Header file containing types used across multiple service APIs.  
Copyright 2017 IBM Corp.  
Copyright 2017 IBM Corp. \*/  
Copyright 2017 IBM Corp.#ifndef \_ASM\_PNV\_OCXL\_H#define \_ASM\_PNV\_OCXL\_H  
Copyright 2017 IBM Corp.#ifndef \_\_CHECKER\_\_#define CREATE\_TRACE\_POINTS#include "trace.h"  
Copyright 2017 IBM Corp.#ifndef \_MISC\_OCXL\_H#define \_MISC\_OCXL\_H  
Copyright 2017 IBM Corp.#ifndef \_OCXL\_CONFIG\_H#define \_OCXL\_CONFIG\_H

```

Copyright 2017 IBM Corp. #ifndef _OCXL_INTERNAL_H #define _OCXL_INTERNAL_H
Copyright 2017 IBM Corp. #ifndef _UAPI_MISC_OCXL_H #define _UAPI_MISC_OCXL_H
Copyright 2017 IBM Corp. #include <asm/pnv-ocxl.h> #include <asm/opal.h> #include <asm/xive.h> #include <misc/ocxl-config.h> #include "pci.h"
Copyright 2017 IBM Corp. #include <linux/fs.h> #include <linux/poll.h> #include <linux/sched/signal.h> #include <linux/eventfd.h> #include
<linux/uaccess.h> #include <uapi/misc/ocxl.h> #include <asm/r
Copyright 2017 IBM Corp. #include <linux/interrupt.h> #include <asm/pnv-ocxl.h> #include "ocxl_internal.h" #include "trace.h"
Copyright 2017 IBM Corp. #include <linux/module.h> #include <linux/pci.h> #include <asm/mmu.h> #include "ocxl_internal.h"
Copyright 2017 IBM Corp. #include <linux/pci.h> #include <asm/pnv-ocxl.h> #include <misc/ocxl-config.h> #include "ocxl_internal.h"
Copyright 2017 IBM Corp. #include <linux/sched/mm.h> #include <linux/mutex.h> #include <linux/mm_types.h> #include
<linux/mmu_context.h> #include <asm/copro.h> #include <asm/pnv-ocxl.h> #include <misc
Copyright 2017 IBM Corp. #include <linux/sched/mm.h> #include "trace.h" #include "ocxl_internal.h"
Copyright 2017 IBM Corp. #include <linux/sysfs.h> #include "ocxl_internal.h"
Copyright 2017 IBM Corp. #include "ocxl_internal.h"
Copyright 2017 IBM Corporation
Copyright 2017 IBM Corporation */
Copyright 2017, IBM Corporation * cpufeatures is the new way to discover CPU features with /cpus/features * devicetree. This supersedes PVR based
discovery ("cputable"), and older * device tree
Copyright 2017, IBM Corporation. * * This uses code from arch/sparc/kernel/nmi.c and kernel/watchdog.c
Copyright 2017 IBM Corp. #undef TRACE_SYSTEM #define TRACE_SYSTEM ocxl
Copyright 2017 Icenowy Zheng <icenowy@aosc.io> */
Copyright 2017 Icenowy Zheng <icenowy@aosc.io> *
Copyright 2017 Icenowy Zheng <icenowy@aosc.xyz> */
Copyright 2017 Impinj, Inc * Author: Andrey Smirnov <andrew.smirnov@gmail.com> * * Based on the code of analogous driver: *
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Copyright 2017 John Sperbeck * * Test that an access to a mapped but inaccessible area causes a SEGV and * reports si_code == SEGV_ACCERR.
Copyright 2017 Karol Herbst
Copyright © 2017 Keith Packard <keithp@keithp.com> */
Copyright © 2017 Keith Packard <keithp@keithp.com> */ #include <linux/file.h> #include <linux/uaccess.h>
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Copyright 2017 Linaro Ltd. */
Copyright 2017 Linaro Ltd. * * Author: Baoyou Xie <baoyou.xie@linaro.org>
Copyright 2017 Manivannan Sadhasivam <manivannanece23@gmail.com> * * IIO driver for VL6180 (7-bit I2C slave address 0x29)
Copyright 2017 Maxime Roussin-B * expanded by Silvan Murer <silvan.murer@gmail.com> */
Copyright 2017, Michael Ellerman, IBM Corporation. */
Copyright 2017, Michael Ellerman, IBM Corporation. */ #ifndef _LINUX_SET_MEMORY_H #define _LINUX_SET_MEMORY_H
Copyright 2017, Michael Neuling, IBM Corp. * Original: Breno Leitao <brenohl@br.ibm.com> & * Gustavo Bueno Romero
<gromero@br.ibm.com> * Edited: Michael Neuling * * Force VMX unavailabl
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Copyright 2017 Nuvoton Technology Crop. * * Author: David Lin <ctlin0@nuvoton.com> * Co-author: John Hsu <kchsu0@nuvoton.com> * Co-author:
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Copyright 2017 NXP * * Based on WM8523 ALSA SoC Audio driver written by Mark Brown
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Copyright 2017 Peter Meerwald-Stadler <pmeerw@pmeerw.net> * * 7-bit I2C slave address 0x4c

```

Copyright 2017 Peter Meerwald-Stadler <pmeerw@pmeerw.net> \* \* Datasheet: <https://www.idt.com/document/dst/zopt2201-datasheet> \* 7-bit I2C slave addresses 0x53 (default) or 0x52 (programmed)

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Copyright 2017 Texas Instruments \* \* Author: Milo Kim <milo.kim@ti.com>  
Copyright 2017 Texas Instruments, Inc. \*/#ifndef \_\_DT\_BINDINGS\_CLK\_AM3\_H#define \_\_DT\_BINDINGS\_CLK\_AM3\_H  
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Copyright 2017 Texas Instruments, Inc. \*/#ifndef \_\_DT\_BINDINGS\_CLK\_DM816\_H#define \_\_DT\_BINDINGS\_CLK\_DM816\_H  
Copyright 2017 Texas Instruments, Inc. \*/#ifndef \_\_DT\_BINDINGS\_CLK\_DRA7\_H#define \_\_DT\_BINDINGS\_CLK\_DRA7\_H  
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Copyright 2018-2019 NXP \* \* Based on caamalg.c crypto API driver.  
Copyright 2018-2019 NXP \*/#ifndef DPAA2\_ETH\_DEBUGFS\_H#define DPAA2\_ETH\_DEBUGFS\_H  
Copyright 2018-2019 NXP \*/#include <linux/module.h>#include <linux/debugfs.h>#include "dpa2-eth.h"#include "dpa2-eth-debugfs.h"  
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Copyright 2018 Analog Devices Inc. \*/  
Copyright 2018 Analog Devices Inc. \*/#include <linux/bitfield.h>#include <linux/clk.h>#include <linux/delay.h>#include <linux/device.h>#include <linux/err.h>#include <linux/kernel.h>#include <  
Copyright 2018 Analog Devices Inc. \* \* TODO: Currently CRC is not supported in this driver  
Copyright 2018 Andrej Shadura <andrew@shadura.me>  
Copyright 2018 Angelo Dureghello <angelo@sysam.it> \*/#ifndef \_FSL\_EDMA\_COMMON\_H\_#define \_FSL\_EDMA\_COMMON\_H\_  
Copyright 2018 Anshuman Khandual, IBM Corporation. \*/  
Copyright 2018, Anton Blanchard, IBM Corp. \*/  
Copyright 2018 Arcx Inc \*/  
Copyright 2018 Arm Limited \* Author: Dave Martin <Dave.Martin@arm.com> \*/#include <linux/irqflags.h>#include <linux/sched.h>#include <linux/thread\_info.h>#include <linux/kvm\_host.h>#include <as  
Copyright 2018, Breno Leitao, Gustavo Romero, IBM Corp. \* \* A test case that creates a signal and starts a suspended transaction \* inside the signal handler.  
Copyright 2018, Breno Leitao, Gustavo Romero, IBM Corp. \* \* This test raises a SIGUSR1 signal, and toggle the MSR[TS] \* fields at the signal handler. With MSR[TS] being set, the kernel will \* forc



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Copyright 2018 Cisco Systems, Inc. and/or its affiliates. All rights reserved  
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Copyright 2018 David Lechner <david@lechnology.com> \* \* Based on mi0283qt.c:  
Copyright 2018 DEVIALET \*/  
Copyright 2018 Google, Inc.  
Copyright 2018 Google Inc. \* Author: Eric Dumazet (edumazet@google.com) \* \* Reference program demonstrating tcp mmap() usage, \* and  
SO\_RCVLOWAT hints for receiver.  
Copyright 2018 Google Inc. \* Author: Soheil Hassas Yeganeh (soheil@google.com) \* \* Simple example on how to use TCP\_INQ and TCP\_CM\_INQ.  
Copyright 2018 Google, Inc \* \* This driver uses the cros-ec interface to communicate with the Chrome OS \* EC about counter sensors. Counters are  
presented through \* iio sysfs.  
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Samu Onkalo <samu.onkalo@intel.com> \* Author: To  
Copyright 2018 Google LLC \* \* The corresponding platform device is typically registered in \* drivers/platform/chrome/wilco\_ec/core.c  
Copyright 2018 Google LLC \* \* The Wilco EC is similar to a typical ChromeOS embedded controller. \* It uses the same MEC based low-level  
communication and a similar \* protocol, but with some import  
Copyright 2018 Google LLC \* \* This is the entry point for the drivers that control the Wilco EC. \* This driver is responsible for several tasks:  
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Copyright 2018 Hannes Reinecke, SUSE Linux GmbH <hare@suse.com> \* \* Based on the original DAC960 driver, which has  
Copyright 2018 Hans de Goede <hdegoede@redhat.com> \*/  
Copyright 2018 IBM Corp.  
Copyright 2018 IBM Corp/\* \* A FSI master controller, using a simple GPIO bit-banging interface  
Copyright 2018 IBM Corporation  
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Copyright 2018 IBM Corporation \* Author: Anshuman Khandual <khandual@linux.vnet.ibm.com> \*/  
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Jacob Pan <jacob.jun.pan@linux.intel.com>  
Copyright © 2018 Intel Corporation \*/#include "gt/intel\_gt.h"  
Copyright 2018, Jann Horn <jannh@google.com>  
Copyright 2018 Konsulko Group \* \* Author: Matt Porter <mporter@konsulko.com>  
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 Copyright 2018, Michael Ellerman, IBM Corporation.  
 Copyright 2018, Michael Ellerman, IBM Corporation. \*/  
 Copyright 2018, Michael Ellerman, IBM Corporation. \*/#ifndef \_ASM\_POWERPC\_CODE\_PATCHING\_ASM\_H#define  
 \_ASM\_POWERPC\_CODE\_PATCHING\_ASM\_H  
 Copyright 2018, Michael Ellerman, IBM Corp. \* \* Test that an out-of-bounds branch to counter behaves as expected.  
 Copyright 2018, Murilo Opsfelder Araujo, IBM Corporation. \*/  
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 Copyright 2018 NXP \* Author: Dong Aisheng <aisheng.dong@nxp.com> \* \* Implementation of the SCU IPC functions using MUs (client side).  
 Copyright 2018 NXP. \* Dong Aisheng <aisheng.dong@nxp.com> \*/  
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 Copyright 2018 NXP \* \* Header file for the IPC implementation.  
 Copyright 2018 NXP \*/#ifndef \_\_PTP\_QORIQ\_H\_\_#define \_\_PTP\_QORIQ\_H\_\_  
 Copyright 2018 NXP Semiconductors  
 Copyright 2018 NXP. \* \* This driver supports the fractional plls found in the imx8m SOCs  
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 Copyright 2018 SUSE Linux. \* Author: Coly Li <colyli@suse.de> \*/  
 Copyright 2018 SUSE Linux. \* Author: Coly Li <colyli@suse.de> \*/#include <inttypes.h>#include <stdio.h>  
 Copyright 2018 SUSE Linux GmbH  
 Copyright 2018 Tempo Semiconductor, Inc. // Author: Steven Eckhoff <steven.eckhoff.opensource@gmail.com>  
 Copyright 2018, Thiago Jung Bauermann, IBM Corporation. \*/#include <stdio.h>#include <stdbool.h>#include <semaphore.h>  
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 Copyright 2019 Analog Devices Inc. \* \* https://wiki.analog.com/resources/fpga/docs/axi\_ip \* https://wiki.analog.com/resources/fpga/docs/hdl/regmap  
 Copyright 2019 Analog Devices Inc. \*/#include <linux/bitfield.h>#include <linux/clk.h>#include <linux/device.h>#include <linux/err.h>#include  
 <linux/gcd.h>#include <linux/kernel.h>#include <li  
 Copyright 2019 Analog Devices Inc. \*/#include <linux/kernel.h>#include <linux/bitfield.h>#include <linux/delay.h>#include <linux/errno.h>#include  
 <linux/init.h>#include <linux/module.h>#includ  
 Copyright 2019 Annaliese McDermond \* \* Author: Annaliese McDermond <nh6z@nh6z.net>  
 Copyright 2019 ARM Limited \* \* 32 Bit compat layer by Stefani Seibold <stefani@seibold.net> \* sponsored by Rohde & Schwarz GmbH & Co. KG  
 Munich/Germany  
 Copyright 2019 Arm Limited \* Author: Andrew Murray <Andrew.Murray@arm.com> \*/#include <linux/kvm\_host.h>#include  
 <linux/perf\_event.h>#include <asm/kvm\_hyp.h>  
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Copyright © 2019 Collabora ltd. \*/#ifndef \_\_PANFROST\_DRM\_H\_\_#define \_\_PANFROST\_DRM\_H\_\_  
Copyright 2019 Collabora Ltd#ifndef \_\_PANFROST\_PERFCNT\_H\_\_#define \_\_PANFROST\_PERFCNT\_H\_\_  
Copyright 2019 Collabora ltd.#include <linux/bitfield.h>#include <linux/bitmap.h>#include <linux/delay.h>#include <linux/dma-mapping.h>#include  
<linux/interrupt.h>#include <linux/io.h>#include  
Copyright 2019 Collabora ltd.#include <linux/delay.h>#include <linux/interrupt.h>#include <linux/io.h>#include <linux/platform\_device.h>#include  
<linux/pm\_runtime.h>#include <linux/dma-resv.h>#  
Copyright 2019 Collabora ltd.#include <linux/devfreq.h>#include <linux/platform\_device.h>#include <linux/pm\_opp.h>#include <linux/clk.h>#include  
<linux/regulator/consumer.h>  
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userspace interface \* and other info.  
Copyright 2019 Google LLC \* \* Sysfs properties to view and modify EC-controlled features on Wilco devices. \* The entries will appear under  
/sys/bus/platform/devices/GOOG000C:00/  
Copyright 2019 Google LLC \* \* The Wilco Embedded Controller can create custom events that \* are not handled as standard ACPI objects. These events  
can \* contain information about changes in EC con  
Copyright 2019 Google LLC \* \* The Wilco Embedded Controller is able to send telemetry data \* which is useful for enterprise applications. A daemon  
running on \* the OS sends a command to the EC via  
Copyright 2019 Google LLC \* \* This file is a rework and part of the code is ported from \* drivers/mfd/cros\_ec\_dev.c that was originally written by \* Bill  
Richardson.  
Copyright 2019, Gustavo Romero, Michael Neuling, IBM Corp. \* \* This test will spawn two processes. Both will be attached to the same \* CPU (CPU 0).  
The child will be in a loop writing to FP registe  
Copyright 2019 Hans de Goede <hdegoede@redhat.com> \*/  
Copyright 2019, Hari Bathini, IBM Corporation. \*/  
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Copyright 2019 IBM Corp.#include <linux/idr.h>#include "ocxl\_internal.h"  
Copyright 2019 IBM Corp.#include <linux/module.h>#include "ocxl\_internal.h"  
Copyright 2019 IBM Corp.#include <linux/sched/mm.h>#include "trace.h"#include "ocxl\_internal.h"  
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Copyright 2019 Intel Corporation, Inc. \* \* Authors:  
Copyright © 2019 Intel Corporation \* \* mei\_hdcp.c: HDCP client driver for mei bus  
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Copyright 2019 Linaro, Ltd., Rob Herring <robh@kernel.org>  
Copyright 2019 Linaro, Ltd., Rob Herring <robh@kernel.org>#ifndef \_\_PANFROST\_FEATURES\_H\_\_#define \_\_PANFROST\_FEATURES\_H\_\_  
<linux/dma-mapping.h>#include <linux/atomic.h>#include <linux/bitfield.h>#include <linux/delay.h>#include  
Copyright 2019 Linaro, Ltd, Rob Herring <robh@kernel.org>/\* \* Register definitions based on mali\_midg\_regmap.h  
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Copyright 2019 NXP * Author: Daniel Baluta <daniel.baluta@nxp.com> * * Implementation of the DSP IPC interface (host side)
Copyright 2019 NXP. * * FlexSPI is a flexible SPI host controller which supports two SPI * channels and up to 4 external devices. Each channel supports
* Single/Dual/Quad/Octal mode data transfer
Copyright 2019 NXP * * Header file for the DSP IPC implementation
Copyright 2019 NXP * * Implementation of the SCU IRQ functions using MU.
Copyright 2019 NXP */#include <linux/device.h>#include <linux/debugfs.h>#include <linux/fsl/ptp_qoriq.h>
Copyright 2019 NXP#include <linux/of_mdio.h>#include "enetc_mdio.h"
Copyright 2019 NXP * * Peng Fan <peng.fan@nxp.com>
Copyright 2019 Ondrej Zary * * Original driver by * Rickard E. Faith, faith@cs.unc.edu
Copyright 2019 Ondrej Zary * * The initial developer of the original code is David A. Hinds * <dahinds@users.sourceforge.net>. Portions created by
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Copyright Alan Cox GW4PTS (alan@lxorguk.ukuu.org.uk)
Copyright Alistair Popple, IBM Corporation 2014. */#include <linux/bitops.h>#include <linux/irq.h>#include <linux/irqchip.h>#include
<linux/irqdomain.h>#include <linux/interrupt.h>#include <lin
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= original */
Copyright asserted flag -- do not modify#define SPCS_NOTAUDIODATA 0x00000002 /* 0 = Digital audio, 1 = not audio *//#define
SPCS_PROFESSIONAL 0x00000001 /* 0 = Consumer (IEC-958), 1 = pro (AES3-1992)
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copyright bit#define CM_DAC2SPDO          0x00200000 /* enable wave+fm_midi -> SPDIF/OUT */#define CM_INVIDWEN          0x00100000
/* internal vendor ID write enable, model 039? */#define CM_SETRETRY          0x00100000 /
COPYRIGHT)          bits          else          bits |= 0x08;          switch (diga->status[0] & IEC958_AES0_CON_EMPHASIS) {
default:
COPYRIGHT))          bits          if ((diga->status[0] & IEC958_AES0_CON_EMPHASIS) == IEC958_AES0_CON_EMPHASIS_NONE)
bits |= 0x40;          if (!(diga->status[1] & IEC958_AES1_CON_ORIGINAL))          bits |= 0x80;          if ((d
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The Unicorn' (MOTU) *          MidiTimePiece
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Copyright by Micha */#ifndef LINUX_CB710_MMC_H#define LINUX_CB710_MMC_H
Copyright by Micha */#ifndef LINUX_CB710_SG_H#define LINUX_CB710_SG_H
Copyright by Micha */#include <linux/cb710.h>#include <linux/kernel.h>#include <linux/module.h>
Copyright by Micha */#include <linux/kernel.h>#include <linux/module.h>#include <linux/cb710.h>
Copyright by Micha */#include <linux/kernel.h>#include <linux/module.h>#include <linux/pci.h>#include <linux/delay.h>#include "cb710-mmc.h"
Copyright by Micha */#include <linux/kernel.h>#include <linux/module.h>#include <linux/pci.h>#include <linux/spinlock.h>#include
<linux/idr.h>#include <linux/cb710.h>#include <linux/gfp.h>
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chars caused a compiler warning on HIUX (so I re
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Copyright (C) 1990 - 1992, 1999 Silicon Graphics, Inc. */#ifndef _ASM_FPREGDEF_H#define _ASM_FPREGDEF_H
Copyright (C) 1990, 1999, 2000 Silicon Graphics, Inc. * 2004 pf */#ifndef _ASM_MACH_IP28_SPACES_H#define _ASM_MACH_IP28_SPACES_H

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Copyright (C) 1991, 1992 Linus Torvalds \*/#include <linux/fs.h>#include <linux/ext2\_fs.h>#include <linux/blockgroup\_lock.h>#include <linux/percpu\_counter.h>#include <linux/rbtree.h>

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Copyright (C) 1991, 1992 Linus Torvalds \* \* More specifically, the first copied function was strtobool, which \* was introduced by: \*

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Copyright (C) 1991, 1992 Linus Torvalds \* \* super.c contains code to handle: - mount structures \* - super-block tables \*

- fil

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Copyright (C) 1995-2002 Russell King */#ifndef _ASM_PGTABLE_2LEVEL_HWDEF_H#define _ASM_PGTABLE_2LEVEL_HWDEF_H
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 Copyright (C) 1995-2005 Russell King \*/#include <linux/module.h>#include <linux/kernel.h>#include <linux/errno.h>#include <linux/init.h>#include  
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<linux/slab.h>#include <linux/init.h>#include <linux/m
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<stepan@home.culture.mipt.ru>
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Copyright (c) 1996 John Shifflett, GeoLog Consulting * john@geolog.com * jshiffle@netcom.com */#ifndef WD33C93_H#define WD33C93_H
Copyright(c) 1996 Kars de Jong/* Based on the ide driver from 1.2.13pl8 */
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Copyright (C) 1996 Linus Torvalds */
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Copyright (C) 1996 Mike Shaver (shaver@zeroknowledge.com) */#include <linux/mm.h>#include <linux/slab.h>#include <linux/sysctl.h>#include
<linux/spinlock.h>#include <net/ax25.h>
Copyright (C) 1996 Mike Shaver (shaver@zeroknowledge.com) */#include <linux/mm.h>#include <linux/sysctl.h>#include <linux/init.h>#include
<net/ax25.h>#include <net/netrom.h>
Copyright (C) 1996 Mike Shaver (shaver@zeroknowledge.com) */#include <linux/mm.h>#include <linux/sysctl.h>#include <linux/init.h>#include
<net/ax25.h>#include <net/rose.h>
copyright (C) 1996 okir@monad.swb.de).\n");
Copyright (C) 1996 Olaf Kirch * 04 Aug 1998 Ion Badulescu <ionut@cs.columbia.edu> * FIFO's need special handling in NFSv2 */
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Copyright (C) 1996, Olaf Kirch <okir@monad.swb.de> */
Copyright (C) 1996, Olaf Kirch <okir@monad.swb.de> */#ifndef _LINUX_SUNRPC_DEBUG_H#define _LINUX_SUNRPC_DEBUG_H_
Copyright (C) 1996 Olaf Kirch, <okir@monad.swb.de> * * TCP NFS related read + write fixes
Copyright (C) 1996 Paul Gortmaker
Copyright (C) 1996 Paul Gortmaker (drivers/char/rtc.c)
Copyright (C) 1996 Paul Gortmaker * * This driver allows use of the real time clock (built into * nearly all computers) from user space. It exports the
/dev/rtc * interface supporting various ioct
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Copyright (C) 1996 Paul Mackerras and Mark Abene. * * based on skeletonfb.c:
Copyright (C) 1996 Paul Mackerras * * and on the PowerMac ATI/mach64 display driver: *
Copyright (C) 1996 Paul Mackerras. * * Apr. 21 2002 - BenH Rework bus reset code for new error handler * Add delay after
initial bus reset *
Copyright (C) 1996 Paul Mackerras * * Derived from "arch/i386/mm/init.c"
Copyright (C) 1996 Paul Mackerras. */#ifndef _LINUX_CUDA_H#define _LINUX_CUDA_H
Copyright (C) 1996 Paul Mackerras. */#ifndef _MAC53C94_H#define _MAC53C94_H
Copyright (C) 1996 Paul Mackerras. */#ifndef _MESH_H#define _MESH_H
Copyright (C) 1996 Paul Mackerras. */#include <asm/ppc_asm.h>#include <asm/export.h>#include <asm/cache.h>
Copyright (C) 1996 Paul Mackerras. */#include <asm/processor.h>#include <asm/errno.h>#include <asm/ppc_asm.h>#include
<asm/export.h>#include <asm/kasan.h>
Copyright (C) 1996 Paul Mackerras. */#include <linux/kernel.h>#include <linux/delay.h>#include <linux/types.h>#include <linux/string.h>#include
<linux/slab.h>#include <linux/blkdev.h>#include

```

```

Copyright (C) 1996 Paul Mackerras. */#include <stdarg.h>#include <linux/types.h>#include <linux/errno.h>#include <linux/kernel.h>#include
<linux/delay.h>#include <linux/adb.h>#include <linux/c
Copyright (C) 1996 Paul Mackerras. * * Modified to declare controllers as structures, added * client notification of bus reset and handles PowerBook *
sleep, by Benjamin Herrenschmidt.
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Copyright (c) 1996 Paul Mackerras <paulus@cs.anu.edu.au>
Copyright (C) 1996 Paul Mackerras (paulus@cs.anu.edu.au) */#include <linux/slab.h>#include <linux/seq_file.h>#include <linux/radix-tree.h>#include
<linux/module.h>#include <linux/of.h>#include
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Copyright (C) 1996 Paul Mackerras (paulus@cs.anu.edu.au) * * This file contains the code used to make IRQ descriptions in the * device tree to actual
irq numbers on an interrupt controller * drive
Copyright (C) 1996 Paul Mackerras (paulus@samba.org) * * Derived from "arch/alpha/kernel/setup.c"
Copyright (C) 1996 Paul Mackerras. * * platinumfb.c: Console support for PowerMac "platinum" display adaptor.
Copyright (C) 1996 Paul Mackerras * PPC44x/36-bit changes by Matt Porter (mporter@mvista.com) * * Derived from "arch/i386/mm/init.c"
Copyright (C) 1996 Paul Mackerras. * * The so far unpublished platinumfb.c
Copyright (C) 1996 Paul Mackerras. * * This file contains low-level assembler routines for managing * the PowerPC MMU hash table. (PPC 8xx
processors don't use a * hash table, so this file is
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Copyright (C) 1996 Russell King.
Copyright (C) 1996 Russell King
Copyright (C) 1996 Russell King */
Copyright (C) 1996 Russell King. * * Changelog:
Copyright (C) 1996 Russell King */#define VIDMEM ((char *)SCREEN_START)
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Copyright (C) 1996 Simon G. Vogl 1999 Hans Berglund
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Copyright (C) 1996 Thomas K. Dyas (tdyas@eden.rutgers.edu) */
Copyright (C) 1996 Thomas K. Dyas (tdyas@eden.rutgers.edu) * * Chris Davis (cdavis@cois.on.ca) 03/27/1998 * Added support for the intersil on the
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<jiang.liu@linux.intel.com> * Convert to hierarchical irqdomain */#
Copyright (C) 1997, 1998, 1999, 2000, 2009 Ingo Molnar, Hajnalka Szabo * Moved from arch/x86/kernel/apic/io_apic.c. * Jiang Liu
<jiang.liu@linux.intel.com> * Enable support of hierarchical irqdomai
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 <linux/interrupt.h>#include <linux/kernel.h>#  
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 <linux/types.h>#include <linux/console.h>#include <linux  
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Copyright (C) 1997-1999 Russell King \*/#include <linux/buffer\_head.h>#include <linux/writeback.h>#include "adfs.h"  
Copyright (C) 1997-1999 Russell King \*/#include <linux/module.h>#include <linux/init.h>#include <linux/parser.h>#include <linux/mount.h>#include  
<linux/seq\_file.h>#include <linux/slab.h>#inclu  
Copyright (C) 1997-1999 Russell King \*/#include <linux/slab.h>#include "adfs.h"#include "dir\_fplus.h"  
Copyright (C) 1997-1999 Russell King \* \* Modifications:  
Copyright (C) 1997-1999 Russell King \* \* Structure passed to kernel to tell it about the \* hardware it's running on. See Documentation/arm/setup.rst \*  
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Copyright (C) 1997 - 2000, 2003 Ralf Baechle <ralf@linux-mips.org> \*/#include <linux/kernel.h>#include <linux/pci.h>#include  
<linux/types.h>#include <asm/sni.h>  
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Copyright (C) 1997-2002 Russell King \* \* ARM architecture version 4, Faraday variation. \* This assume an unified TLBs, with a write buffer, and branch  
target buffer (BTB)  
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write buffer.  
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Copyright (C) 1997-2002 Russell King \*/#include <asm/unaligned.h>#include "adfs.h"  
Copyright (C) 1997-2002 Russell King \*/#include <linux/linkage.h>#include <linux/init.h>#include <asm/asm.h>#include  
<asm/memory.h>#include <asm/page.h>#include "proc-macros.S"  
Copyright (C) 1997-2002 Russell King \*/#include <linux/linkage.h>#include <linux/init.h>#include <asm/asm.h>#include <asm/page.h>#include  
"proc-macros.S"  
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 <linux/string.h>#include <linux/ioport.h>#include <linux/proc\_fs.h>#1  
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 Copyright (c) 1997 Dan Malek (dmalek@jlc.net)  
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 Copyright (c) 1997 Dan Malek (dmalek@jlc.net). \*/#define BD\_SC\_EMPTY ((ushort)0x8000) /\* Receive is empty \*/#define BD\_SC\_READY  
 ((ushort)0x8000) /\* Transmit is ready \*/#define BD\_SC\_WRAP ((ushort)  
 Copyright (c) 1997 Dan Malek <dmalek@jlc.net> \* PowerPC 8xx modifications.  
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 Copyright (C) 1997 Dan Malek <dmalek@jlc.net> \* PowerPC 8xx modifications.  
 Copyright (c) 1997 Dan Malek (dmalek@jlc.net) \* \* Right now, I am very wasteful with the buffers. I allocate memory \* pages and then divide them  
 into 2K frame buffers. This way I know I \* have b  
 Copyright (c) 1997 Dan Malek (dmalek@jlc.net) \* \* The I/O on the MPC860 is comprised of blocks of special registers \* and the dual port ram for the  
 Communication Processor Module. \* Within this sp  
 Copyright (C) 1997 Dan Malek (dmalek@jlc.net). \* \* This file contains low-level support and setup for PowerPC 8xx \* embedded processors, including  
 trap and interrupt dispatch.  
 Copyright (c) 1997 Dan Malek (dmalek@jlc.net) \* \* This file contains structures and information for the communication \* processor channels. Some CPM  
 control and status is available \* through the

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Copyright (C) 1997 David S. Miller (davem@caip.rutgers.edu) \* \* Derived from "arch/i386/kernel/signal.c"

Copyright (C) 1997 David S. Miller (davem@caip.rutgers.edu) \* \* These routines maintain argument size conversion between 32bit and 64bit \* environment.

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Copyright (C) 1997 Jakub Jelinek (jj@sunsite.mff.cuni.cz) \*/#ifndef \_SPARC\_TURBOSPARC\_H#define \_SPARC\_TURBOSPARC\_H

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Copyright (C) 1997 Miguel de Icaza (miguel@nuclecu.unam.mx) */
Copyright (C) 1997 Miguel de Icaza (miguel@nuclecu.unam.mx) */#include <linux/init.h>#include <linux/kernel.h>#include <linux/errno.h>#include
<linux/kd.h>#include <linux/selection.h>#include <
Copyright (c) 1997 Nicolas Pitre <nico@cam.org> * * speakup DECTalk PC Internal driver:
Copyright (C) 1997, Olaf Kirch */
Copyright (C) 1997 Olaf Kirch <okir@monad.swb.de> */
Copyright (C) 1997, Olaf Kirch <okir@monad.swb.de> */
Copyright (C) 1997 Paul Mackerras. */
Copyright (C) 1997 Paul Mackerras. * * and indirectly: *
Copyright (C) 1997 Paul Mackerras. * * and indirectly from: *
Copyright (C) 1997 Paul Mackerras. * * BenH: Changed to match those of heathrow (but not all of them). Please * check if I didn't break anything
(especially the media bay).
Copyright (C) 1997 Paul Mackerras. */#ifndef _PPC_MEDIABAY_H#define _PPC_MEDIABAY_H
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Copyright (C) 1997 Paul Mackerras (paulus@samba.org) */
Copyright (C) 1997 Paul Mackerras (paulus@samba.org) */#include <linux/kernel.h>#include <linux/pci.h>#include <linux/init.h>
Copyright (C) 1997 Paul Mackerras. * * pmc-control.c: Console support for PowerMac "control" display adaptor.
Copyright (C) 1997 Paul Mackerras. * * pmc-valkyrie.c: Console support for PowerMac "control" display adaptor.
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Copyright (C) 1997 Russell King * * Modifications:
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Copyright (C) 1997 Sten Wang
Copyright (C) 1997 Sten Wang */
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Copyright (C) 1997, Stephen Tweedie * * Provide stub functions for unreadable inodes
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Copyright (C) 1998, 1999, 2002-2003 Hewlett-Packard Co * David Mosberger-Tang <davidm@hpl.hp.com> */#include <linux/types.h>
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Copyright (C) 1998, 1999 Brent Baccala (baccala@freesoft.org) * * This is the low level driver for the DBRI & MMCODEC duo used for ISDN & AUDIO *
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Copyright (c) 1998-1999 by Frank van de Pol <fvdpol@coil.demon.nl> * Jaroslav Kysela <perex@perex.cz> */
Copyright (c) 1998-1999 by Frank van de Pol <fvdpol@coil.demon.nl> * * MAJOR CHANGES * Nov. 13, 1999 Takashi Iwai <iwai@ww.uni-
erlangen.de> * - Queues are allocated dynamically via ioctl. *
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this driver should be merged into the gener
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Copyright (C) 1998, 1999 Ralf Baechle
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Copyright (C) 1998, 1999 Richard Henderson * * Code for all boards that route the PCI interrupts through the SIO * PCI/ISA bridge. This includes
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 Tsunami family.  
 Copyright (C) 1998,1999 Russell King  
 Copyright (C) 1998,1999 Russell King \* \* Frame buffer code for Acorn platforms  
 Copyright (C) 1998, 1999 Russell King, Phil Blundell \*/#include <linux/ioport.h>#include <linux/kernel.h>#include <linux/init.h>#include  
 <linux/screen\_info.h>#include <linux/io.h>#include <linu  
 Copyright (C) 1998, 1999 Russell King, Phil Blundell \*/#include <linux/module.h>#include <linux/ioport.h>#include <linux/kernel.h>#include  
 <linux/delay.h>#include <linux/init.h>#include <linux/  
 Copyright (C) 1998-1999 Russell King. \* \* This file contains the hardware definitions of the EBSA-285.  
 Copyright (C) 1998-1999, Stephen Tweedie and Bill Hawes \* \* Manage the dynamic fd arrays in the process files\_struct.  
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 driver to Linux-1.2.13.  
 Copyright (c) 1998, 2000 Harald Koerfgen \*/  
 Copyright (C) 1998-2000 Harald Koerfgen  
 Copyright (c) 1998-2000 Harald Koerfgen \*/#include <asm/asm.h>#include <asm/cachectl.h>#include <asm/export.h>#include  
 <asm/fpregdef.h>#include <asm/mipsregs.h>#include <asm/asm-offsets.h>#inc  
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 1.26 2000/09/19 00:11:53 illo Exp \$  
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 Copyright (C) 1998-2000 Michel Aubry  
 Copyright (C) 1998-2000 Michel Aubry, Maintainer  
 Copyright (C) 1998-2000 Phil Blundell \*/#include <linux/kernel.h>#include <linux/pci.h>#include <linux/interrupt.h>#include <linux/mm.h>#include  
 <linux/slab.h>#include <linux/init.h>#include <  
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```

irq.c.
Copyright (C) 1998,2000 Rik van Riel * Thanks go out to Claus Fischer for some serious inspiration and * for goading me into coding this file...
Copyright (C) 1998-2000 Russell King, Dave Gilbert. */#include <linux/module.h>#include <linux/types.h>#include <linux/mm.h>#include
<linux/ioport.h>#include <linux/list.h>#include <linux/init.
Copyright (C) 1998-2000 Russell King * * Integraphics Cyber2000 frame buffer device
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<linux/irq.h>#include <linux/spinlock.h>#inc
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Copyright (C) 1999, 2000 Kaz Kojima * * FIXME! These routines can be optimized in big endian case.
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Copyright (C) 1999, 2000 Kaz Kojima * * Original x86 implementation:
Copyright (C) 1999,2000 Martin Lucina, Tom Zerucha
Copyright (C) 1999,2000 Martin Lucina, Tom Zerucha * * $Id: tgafb.h,v 1.4.2.3 2000/04/04 06:44:56 mato Exp $
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Copyright (C) 1999, 2000 Niibe Yutaka * * In turn started from i386 version:
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Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include <linux/compiler.h>#include <linux/kernel.h>#include <linux/sched.h>#include
<linux/timer.h>#include <linux/smp.h>#include <linux/mmzone
Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include <linux/cpu_pm.h>#include <linux/hardirq.h>#include <linux/init.h>#include
<linux/highmem.h>#include <linux/kernel.h>#include <linux/li
Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include <linux/kernel.h>#include <linux/export.h>#include <linux/pci.h>#include
<linux/smp.h>#include <linux/dma-direct.h>#include <linux/plat
Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include <linux/kernel.h>#include <linux/sched.h>#include <linux/interrupt.h>#include
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APIC code. In particular, we now have separate * handle
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05/22/2000 eranian Added support for stacked register calls * 05
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davidm Tuned to make it perform well both for cached and uncac
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<asm/export.h>#include <asm/fpregdef.h>#include <asm/mipsregs.h>#
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*/#include <linux/init.h>#include <linux/string.h>#include <l
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2002/12/09 Fix rotating register handling (off-by-1 error,
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 Card (Grundig tuner) by \* Michael Dreher <michael@5dot1.  
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Copyright (C) 1999 Niibe Yutaka */#include <linux/irq.h>#include <linux/interrupt.h>#include <linux/module.h>#include
<linux/kernel_stat.h>#include <linux/seq_file.h>#include <linux/trace.h>
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Copyright (C) 1999 Paul */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/kmod.h>#include <linux/module.h>#include
<linux/vmalloc.h>#include <linux/netfilter/x_tables.h>#include <lin
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Copyright (C) 1999 Silicon Graphics, Inc. \*/#include <asm/mipsregs.h>#include <asm/regdef.h>#include <asm/stackframe.h>  
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Copyright (C) 2000,2001,2002,2003,2004 Broadcom Corporation */#include <linux/init.h>#include <linux/kernel.h>#include <linux/export.h>#include
<linux/reboot.h>#include <linux/string.h>
Copyright (C) 2000,2001,2002,2003,2004 Broadcom Corporation */#include <linux/kernel.h>#include <linux/init.h>#include <linux/linkage.h>#include
<linux/interrupt.h>#include <linux/smp.h>#include
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<linux/interrupt.h>#include <linux/ioport.h>#i
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Paul Antoine and Harald Koerfgen.
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Copyright (C) 2000, 2001, 2002, 2003 Broadcom Corporation */#include <linux/export.h>#include <linux/init.h>#include <linux/kernel.h>#include
<linux/reboot.h>#include <linux/string.h>
Copyright (C) 2000, 2001, 2002, 2003 Broadcom Corporation */#include <linux/kernel.h>#include <linux/init.h>#include <linux/linkage.h>#include
<linux/interrupt.h>#include <linux/spinlock.h>#inc
Copyright (C) 2000, 2001, 2002, 2003 David S. Miller (davem@redhat.com) * * Support for Apple GMAC and assorted PHYs, WOL, Power Management
Copyright (C) 2000, 2001, 2002, 2007 Maciej W. Rozycki */#include <linux/init.h>#include <linux/ioport.h>#include <linux/export.h>#include
<linux/screen_info.h>#include <linux/memblock.h>#incl
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FXSR, SSE support by Gareth Hughes * 2000-2002
Copyright (C) 2000, 2001, 2002 Andi Kleen, SuSE Labs */#include <linux/kallsyms.h>#include <linux/kprobes.h>#include <linux/uaccess.h>#include
<linux/utsname.h>#include <linux/hardirq.h>#incl
Copyright (C) 2000, 2001, 2002 Andi Kleen, SuSE Labs */#include <linux/sched/debug.h>#include <linux/kallsyms.h>#include
<linux/kprobes.h>#include <linux/uaccess.h>#include <linux/hardirq.h>#in
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Copyright (C) 2000,2001,2002 Andi Kleen, SuSE Labs (x86-64 port) * * These routines maintain argument size conversion between 32bit and 64bit *
environment. In 2.5 most of this should be moved to a
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Copyright (C) 2000, 2001, 2002 Broadcom Corporation */#ifndef CFE_API_INT_H#define CFE_API_INT_H
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Copyright (C) 2000, 2001, 2002 David A. Schleef <ds@schleef.org> * * PCMCIA support code for this driver is adapted from the dummy_cs.c * driver of
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```

```

<linux/interrupt.h>#include <linux/smp.h>#include <linux/sp
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  Copyright (C) 2000,2001,2004 Broadcom Corporation */#include <linux/clockchips.h>#include <linux/interrupt.h>#include <linux/percpu.h>#include
<linux/smp.h>#include <linux/irq.h>
  Copyright (C) 2000,2001,2004 Broadcom Corporation */#include <linux/clocksource.h>#include <linux/sched_clock.h>
  Copyright (C) 2000,2001,2004 Broadcom Corporation */#include <linux/init.h>
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  Copyright (C) 2000, 2001 Broadcom Corporation */#ifndef __ASM_SIBYTE_SENTOSA_H#define __ASM_SIBYTE_SENTOSA_H
  Copyright (C) 2000, 2001 Broadcom Corporation */#ifndef __ASM_SMP_H#define __ASM_SMP_H
  Copyright (C) 2000, 2001 Broadcom Corporation */#ifndef __ASM_SMP_OPS_H#define __ASM_SMP_OPS_H
  Copyright (C) 2000, 2001 Broadcom Corporation */#include <linux/clockchips.h>#include <linux/interrupt.h>#include <linux/irq.h>#include
<linux/percpu.h>#include <linux/smp.h>
  Copyright (C) 2000, 2001 Broadcom Corporation */#include <linux/clocksource.h>#include <linux/sched_clock.h>
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  Copyright (C) 2000, 2001 by Nicolas Pitre */#include <linux/sched.h>#include <linux/device.h>#include <linux/dmaengine.h>#include
<linux/init.h>#include <linux/interrupt.h>#include <linux/kerne
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  Copyright (C) 2000 - 2001 by Silicon Graphics, Inc. */#include <linux/init.h>#include <linux/sched.h>#include <linux/sched/task_stack.h>#include
<linux/topology.h>#include <linux/nodemask.h>
  Copyright (C) 2000 - 2001 by Silicon Graphics, Inc. */#include <linux/kernel.h>#include <linux/init.h>#include <linux/sched.h>#include
<linux/smp.h>#include <linux/mm.h>#include <linux/export.h>
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<linux/interrupt.h>#include <linux/amba/bus.h>#include <linux/i
  Copyright (C) 2000-2001 Deep Blue Solutions Ltd. */#include <linux/module.h>#include <linux/tty.h>#include <linux/tty_flip.h>#include
<linux/slab.h>#include <linux/sched/signal.h>#include <linu
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*/#include <asm/asmmacro.h>#include <asm/export.h>
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Copyright (C) 2000, 2001 Paolo Alberelli \*/#include <linux/rwsem.h>#include <linux/module.h>#include <linux/smp.h>#include

<linux/user.h>#include <linux/elfcore.h>#include <linux/sched.h>#inc

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<linux/amba/bus.h>#include <linux/io.h>#include <linux/i  
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<linux/device.h>#include <linux/export.h>#include <linux/spinl  
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 <linux/netfilter\_ipv6/ip6\_tables.h>#include <linux/slab.h>#include <net/ipv6.h>  
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Copyright (C) 2000 MIPS Technologies, Inc. */#include <asm/irqflags.h>#include <asm/hazards.h>#include <linux/compiler.h>#include
<linux/preempt.h>#include <linux/export.h>#include <linux/strn
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<linux/delay.h>#include <linux/in6.h>#include
Copyright (C) 2000 Russell King */#include <linux/kernel.h>#include <linux/sched/signal.h>#include <linux/sched/task_stack.h>#include
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<linux/hardirq.h> #include <linux/init.h> #include <linux/kprobes.h> #inclu
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Copyright (C) 2001-2010 Guan Xuetao */#include <linux/init.h> #include <linux/errno.h> #include <linux/interrupt.h> #include <linux/irq.h> #include
<linux/timex.h> #include <linux/clockchips.h>
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<asm/hwcap.h> #include <asm/pgtable-hwdef.h> #include <asm/pgtable.h>
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<asm/page.h> #include <asm/tlbflush.h> #include "proc-macros.S"
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Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/linkage.h> #include <linux/init.h>
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/linkage.h> #include <mach/memory.h>
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/mm.h> #include <linux/gfp.h> #include <linux/highmem.h>
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h> #include <linux/errno.h> #include <linux/sched.h> #include
<linux/slab.h> #include <linux/mm.h> #include <linux/sem.h> #include <linu
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h> #include <linux/kernel.h> #include <linux/errno.h> #include <linux/init.h> #include
<linux/mman.h> #include <linux/nodemask.h> #inclu
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h> #include <linux/kernel.h> #include <linux/stddef.h> #include
<linux/ioport.h> #include <linux/delay.h> #include <linux/utsname.h> #in
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h> #include <linux/mm.h>
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h> #include <linux/mm.h> #include <linux/pagemap.h>
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<linux/mm.h> #include <linux/elf.h> #include <linux/vmalloc.h> #in
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h> #include <linux/sched.h> #include <linux/personality.h> #include
<linux/binfmts.h> #include <linux/elf.h>
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h> #include <linux/sched.h> #include <linux/sched/debug.h> #include
<linux/stacktrace.h>
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<linux/cryptohash.h> #include <linux/delay.h> #include <linux/in6.h> #inc
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h> #include <linux/types.h> #include <linux/kernel.h> #include
<linux/signal.h> #include <linux/sched/signal.h> #include <linux/init.h>
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probably kill the offending process.
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 Copyright (C) 2001 Anton Blanchard <anton@au.ibm.com>, IBM \* \* Communication to userspace based on kernel/printk.c  
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 Copyright (C) 2001 by Ladislav Michl  
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Copyright (C) 2001 Hewlett-Packard (Paul Bame) <bame@debian.org> */
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implementation that outputs kernel messages to the * net

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 <linux/blkdev.h>#include <linux/uid.h>#include <linux/iocontext.h>#  
 Copyright (C) 2001 Jens Axboe <axboe@suse.de> \*/#ifndef \_\_LINUX\_BIO\_H#define \_\_LINUX\_BIO\_H  
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 Copyright (C) 2001 Keith M Wesolowski  
 Copyright (C) 2001 Keith M Wesolowski \*/#include <linux/init.h>#include <linux/kernel\_stat.h>#include <linux/types.h>#include  
 <linux/interrupt.h>#include <linux/irq.h>#include <linux/bitops.h>  
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 \*  
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 Copyright (c) 2001 La Monte H.P. Yarroll \* \* These are the definitions needed for the sctp\_ulpevent type. The \* sctp\_ulpevent type is used to carry  
 information from the state machine \* upwards to  
 Copyright (c) 2001 La Monte H.P. Yarroll \* \* These are the definitions needed for the sctp\_ulpq type. The \* sctp\_ulpq is the interface between the  
 Upper Layer Protocol, or ULP, \* and the core SCT  
 Copyright (c) 2001 La Monte H.P. Yarroll \* \* These functions manipulate an sctp event. The struct ulpevent is used \* to carry notifications and data to  
 the ULP (sockets).  
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<linux/fs.h>#include <linux/smp.h>#include <linux/ptrace.h>
Copyright (C) 2001 MIPS Technologies, Inc. */#include <linux/errno.h>#include <asm/asm.h>#include <asm/asmmacro.h>#include
<asm/irqflags.h>#include <asm/mipsregs.h>#include <asm/regdef.h>#incl
Copyright (C) 2001 MIPS Technologies, Inc. */#include <linux/kernel.h>#include <linux/export.h>#include <linux/pm.h>#include
<linux/types.h>#include <linux/reboot.h>#include <linux/delay.h>
Copyright (C) 2001 MIPS Technologies, Inc. */#include <linux/kernel.h>#include <linux/sched/signal.h>#include <linux/signal.h>#include
<linux/export.h>#include <asm/branch.h>#include <asm/cpu.h
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Copyright (C) 2001 MontaVista Software, Inc. */#include <asm/system_misc.h>#include "hardware.h"#include "iop3xx.h"
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<linux/scatterlist.h>#include <linux/kdebug.h>#include <linu
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 <linux/gpio.h>#include <linux/types.h>#include <linux/init  
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 <linux/kernel.h>#include <linux/io.h>#include <linux/mtd/m  
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 Copyright (C) 2001 Russell King \* \* Generic MCP (Multimedia Communications Port) layer. All MCP locking \* is solely held within this file.  
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 Copyright (C) 2001 Russell King \* \* Perform "ISA" memory and IO accesses. The EBSA110 has some "peculiarities" \* in the way it handles accesses to odd IO ports on 16-bit devices. These \* devices  
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 <linux/string.h>#include <linux/kernel.h>#include <li  
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Copyright (C) 2001 Standard Microsystems Corporation \* Developed by Simple Network Magic Corporation  
Copyright (C) 2001 Stefan Gmeiner <riddlebox@freesurf.ch> \* start merging tpconfig and gpm code to a xfree-input module \* adding some  
changes and extensions (ex. 3rd and 4th button) \*  
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<linux/sched/task.h>#include <linux/sched/task_stack.h>#include <lin
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<asm/pgtable.h>#include <asm/tlbflush.h>#include <
Copyright (C) 2002 - 2009 Paul Mundt */#include <linux/mm.h>#include <linux/init.h>#include <linux/mutex.h>#include <linux/fs.h>#include
<linux/highmem.h>#include <linux/module.h>#include <as
Copyright (C) 2002 - 2009 Paul Mundt */#include <linux/module.h>#include <linux/init.h>#include <linux/mm.h>#include <linux/pm.h>#include
<linux/tick.h>#include <linux/preempt.h>#include <lin
Copyright (C) 2002 - 2009 Paul Mundt */#include <linux/pci.h>#include <linux/io.h>#include <linux/spinlock.h>#include <asm/addrspace.h>#include
"pci-sh4.h"
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<linux/of_address.h>
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Copyright(c) 2002-2010 Exar Corp. *****/#ifndef
VXGE_CONFIG_H#define VXGE_CONFIG_H#include <linux/hardirq.h>#include <linu

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VXGE_MAIN_H#define VXGE_MAIN_H
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VXGE_TRAFFIC_H#define VXGE_TRAFFIC_H
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VXGE_VERSION_H#define VXGE_VERSION_H
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<linux/etherdevice.h>#include <linux/io-64-nonatomic-lo-hi.h>#include <lin
Copyright(c) 2002-2010 Exar Corp. *****/#include
<linux/ethtool.h>#include <linux/slab.h>#include <linux/pci.h>#include <l
Copyright(c) 2002-2010 Exar Corp. *****/#include
<linux/vmalloc.h>#include <linux/etherdevice.h>#include <linux/io-64-nonat
Copyright(c) 2002-2010 Exar Corp.\n");   DBG_PRINT(ERR_DBG, "%s: Neterion %s (rev %d)\n", dev->name,      sp->product_name, pdev-
>revision); DBG_PRINT(ERR_DBG, "%s: Driver version %s\n", dev->name,
Copyright(c) 2002-2010 Exar Corp.\n");   pr_info("Driver version: %s\n", DRV_VERSION);
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<linux/spinlock.h>#include <linux/kallsyms.h>
Copyright (C) 2002 - 2010 Paul Mundt */#include <linux/mm.h>#include <linux/init.h>#include <linux/mutex.h>#include <linux/fs.h>#include
<linux/smp.h>#include <linux/highmem.h>#include <linux
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<linux/sched/debug.h>#include <linux/sched/task_stack.h>#include
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Copyright (C) 2002 ARM Ltd. */#include <linux/smp.h>#include <linux/io.h>#include <linux/of_address.h>#include <linux/delay.h>
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<linux/pagemap.h>#include <linux/unicode.h>
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unsigned long end_brk;#ifdef CONFIG_BINFMT_ELF_FDPIC uns
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<linux/delay.h>#include <linux/tty.h>#include <linux/t
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<linux/platform_data/sa11x0-serial.h>#include <linux/platform_devic
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<linux/errno.h>#include <linux/init.h>
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 <asm/byteorder.h>#include <linux/crypto.h>#include <li  
 Copyright (c) 2002 James Morris <jmorris@intercode.com.au> \*/#include <linux/types.h>#include <linux/crypto.h>#include <linux/errno.h>#include  
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<linux/sched/debug.h>#include <linux/sched/signal.h>#include <asm/traps.h>
Copyright (C) 2002 Maciej W. Rozycki */#include <linux/init.h>#include <linux/kernel.h>#include <linux/signal.h> /* for SIGBUS */#include
<linux/sched.h> /* schow_regs(), force_sig() */#include
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<asm/freq.h>#include <asm/io.h>
Copyright (C) 2002 M. R. Brown <mrbrown@linux-sh.org> */#include <linux/kernel.h>#include <linux/init.h>#include <linux/profile.h>#include
<linux/timex.h>#include <linux/sched.h>#include <lin
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Copyright (c) 2002 Nokia Corp. * * This is part of the SCTP Linux Kernel Implementation.
Copyright (C) 2002 Patrick Mansfield * * The general scanning/probing algorithm is as follows, exceptions are * made to it depending on device specific
flags, compilation options, and * global var
Copyright (c) 2002 Patrick Mochel
Copyright (c) 2002 Paul Davis * Marcus Andersson * Thomas Charbonnel */
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Copyright (C) 2002 Paul Mackerras, IBM Corp. */#include <asm/ppc_asm.h>#include <asm/processor.h>

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Copyright (C) 2002 Paul Mackerras, IBM Corp. */#include <asm/processor.h>#include <asm/ppc_asm.h>#include <asm/export.h>#include <asm/asm-
compat.h>#include <asm/feature-fixups.h>
Copyright (C) 2002 Paul Mackerras, IBM Corp. */#include <asm/processor.h>#include <asm/ppc_asm.h>#include <asm/export.h>#include <asm/asm-
compat.h>#include <asm/feature-fixups.h>#include <asm/k
Copyright (C) 2002 Paul Mackerras, IBM Corp. */#include "ppc_asm.h"
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Copyright (C) 2002 Paul Mundt */#ifndef __ASM_SH_BARRIER_H#define __ASM_SH_BARRIER_H
Copyright (C) 2002 Paul Mundt */#ifndef __TOOLS_LINUX_ASM_SH_BARRIER_H#define __TOOLS_LINUX_ASM_SH_BARRIER_H
Copyright (C) 2002 Paul Mundt */#include <linux/init.h>#include <linux/of_fdt.h>#include <linux/smp.h>#include <linux/io.h>#include
<asm/processor.h>#include <asm/cache.h>
Copyright (C) 2002 Paul Mundt */#include <linux/signal.h>#include <linux/sched.h>#include <linux/kernel.h>#include <linux/errno.h>#include
<linux/string.h>#include <linux/types.h>#include <li
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Copyright (C) 2002 Peter Hettkamp <peter.hettkamp@htp-tel.de> * * large parts based on the bttv driver
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Copyright (c) 2002 Pete Zaitcev (zaitcev@yahoo.com) * - Incorporating suggestions made by Linus Torvalds and Dave Miller */
Copyright (C) 2002 Pete Zaitcev <zaitcev@yahoo.com> * * The algorithm to search a zero bit string is geared towards its application. * We expect a
couple of fixed sizes of requests, so a rotating c
Copyright (c) 2002 Pete Zaitcev (zaitcev@yahoo.com) * * We used to warn all over the code: DO NOT USE prom_printf(), * and yet people do. Anton's
banking code was outputting banks * with prom_prin
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Copyright (C) 2002 Ralf Baechle
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<linux/in.h>#include <linux/kernel.h>#include <linux/ji
Copyright (C) 2002 Ralf Baechle DO1GRB (ralf@gnu.org) */#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include
<linux/in.h>#include <linux/kernel.h>#include <linux/mo
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Copyright (C) 2002 Rick Bronson * * Converted to RTC class model by Andrew Victor
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Copyright (C) 2002 RidgeRun, Inc. * Author: Steve Johnson */#ifndef __ASOC_TI_MCBSP_H#define __ASOC_TI_MCBSP_H
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Copyright (C) 2002, Rohit Seth <rohit.seth@intel.com>
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Copyright (C) 2002 Roman Zippel <zippel@linux-m68k.org> * * Introduced single menu mode (show all sub-menus in one large tree). * 2002-11-06 Petr
Baudis <pasky@ucw.cz>
Copyright (C) 2002 Roman Zippel <zippel@linux-m68k.org>.\n"
Copyright (C) 2002 Roman Zippel <zippel@linux-m68k.org> */%option nostdinit noyywrap never-interactive full ecs%option 8bit nodefault yylineno%x
ASSIGN_VAL_HELP_STRING
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Copyright (C) 2002 Russell King * * Commonly used scsi driver functions.
Copyright (C) 2002 Russell King * * Experimentation shows that on a StrongARM, it appears to be faster * to use the "invalidate whole tlb" rather than
"invalidate single * tlb" for this.
Copyright (C) 2002 Russell King. */#ifndef __ASM_ARM_THREAD_INFO_H#define __ASM_ARM_THREAD_INFO_H
Copyright (C) 2002 Russell King */#include <linux/module.h>#include <linux/init.h>#include <linux/input.h>#include <linux/serio.h>#include
<linux/errno.h>#include <linux/interrupt.h>#include <
Copyright (C) 2002 Russell King. */#include <linux/module.h>#include <linux/serio.h>#include <linux/errno.h>#include <linux/interrupt.h>#include
<linux/ioport.h>#include <linux/device.h>#inclu
Copyright (C) 2002 Russell King. * Modified for nommu by Hyok S. Choi * * Module allocation method suggested by Andi Kleen.
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Copyright (C) 2002 STMicroelectronics Limited * Author : Stuart Menefy * * Started from SH4 version:
Copyright (c) 2002 STMicroelectronics Ltd * Modified from memcpy.S and micro-optimised for SH4 * Stuart Menefy (stuart.menefy@st.com) *
Copyright (C) 2002 Stuart Menefy */#include <linux/io.h>#include <linux/irq.h>#include <linux/interrupt.h>#include <linux/signal.h>#include
<cpu/irq.h>#include <asm/page.h>
Copyright (C) 2002 Stuart Menefy (stuart.menefy@st.com) */#include <asm/cache.h>#include <asm/tlb.h>#include <cpu/mmu_context.h>#include
<cpu/registers.h>
Copyright (C) 2002 Sun Microsystems, Inc. * Tim Hockin <thockin@sun.com> * * A few notes on this MTD map: * * This was developed with a small
number of SCB2 boards to test on.
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Copyright (C) 2002 * Tapio Laxström (tapio.laxstrom@iptime.fi) */#include <linux/kernel.h>#include <linux/errno.h>#include <linux/slab.h>#include
<linux/module.h>#include <linux/usb.h>#includ
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Copyright (c) 2002 Thibaut Varene <varenet@parisc-linux.org> * * Pieces of code based on linux-2.4's hp_mouse.c & hp_keyb.c
Copyright (C) 2002 Thibaut Varene <varenet@parisc-linux.org> * * TODO: - handle processor number on SMP systems (Reporting Entity ID) *

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-

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handle message ID * - handle timestamps
Copyright (c) 2002 Thiemo Seufer */#include <linux/tty.h>#include <linux/major.h>#include <linux/init.h>#include <linux/console.h>#include
<linux/fs.h>#include <asm/setup.h>#include <asm/sgial
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Copyright (c) 2002 Thomas Gleixner <tgxl@linutronix.de> * * Converted to platform driver by Janusz Krzysztofik <jkrzysz@tis.icnet.pl> * Partially
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Copyright (C) 2002 Trond Myklebust * * structure passed from user-space to kernel-space during an nfsv4 mount
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Copyright (C) 2002 Vivien Chappelier */#include <linux/module.h>#include <linux/init.h>#include <linux/serio.h>#include <linux/errno.h>#include
<linux/interrupt.h>#include <linux/ioport.h>#inc
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Copyright (c) 2002 Vladimir Dergachev * * This 2.2.0 version is a rewrite / cleanup of the 2.1.1 driver, including * porting to the 2.6 kernel interfaces,
along with other modification * to bet
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__ASM_MACH_MSP71XX_CPU_FEATURE_OVERRIDES_H
Copyright (C) 2003, 04, 07 Ralf Baechle (ralf@linux-mips.org) */#ifndef __ASM_MACH_SIBYTE_CPU_FEATURE_OVERRIDES_H#define
__ASM_MACH_SIBYTE_CPU_FEATURE_OVERRIDES_H
Copyright (C) 2003, 04, 07 Ralf Baechle (ralf@linux-mips.org) * * SNI RM200 C apparently was only shipped with R4600 V2.0 and R5000 processors.
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Copyright (C) 2003, 04 Ralf Baechle (ralf@linux-mips.org) */#include <linux/kernel.h>#include <linux/mm.h>#include <linux/pci.h>#include
<linux/init.h>#include <linux/types.h>#include <linux/dm
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* * Additions/Fixe
Copyright (C) 2003, 06 Ralf Baechle (ralf@linux-mips.org) */#include <linux/bcd.h>#include <linux/i8253.h>#include <linux/init.h>#include
<linux/irq.h>#include <linux/kernel.h>#include <linux/i
Copyright (C) 2003, 07 Ralf Baechle */#ifndef __ASM_MACH_IP22_CPU_FEATURE_OVERRIDES_H#define
__ASM_MACH_IP22_CPU_FEATURE_OVERRIDES_H
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Copyright (C) 2003, 2004, 2005 Paul Mundt */#include <linux/init.h>#include <linux/module.h>#include <linux/spinlock.h>#include
<linux/proc_fs.h>#include <linux/seq_file.h>#include <linux/list
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<linux/ptrace.h>#include <linux/stddef.h>
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 Copyright (c) 2003, 2004 Maciej W. Rozycki \* \* Common time service routines for MIPS machines.  
 Copyright (C) 2003, 2004 Maciej W. Rozycki \*/#ifndef \_ASM\_MIPSREGS\_H#define \_ASM\_MIPSREGS\_H  
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Copyright (C) 2003-2004, Paul Clements, SteelEye Technology, Inc.: * - added disk storage for bitmap * - changes to allow various bitmap chunk sizes */
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<linux/interrupt.h>#include <mach/sysasic.h>#include <mach/dma.h>#inclu
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<asm/processor.h>#include <asm/unistd.h>#incl
Copyright (C) 2003, 2004 Richard Curnow */#include <linux/sched.h>#include <linux/sched/debug.h>#include <linux/kernel.h>#include
<linux/string.h>#include <linux/errno.h>#include <linux/ptrace
Copyright (C) 2003, 2004 Richard Curnow */#include <linux/threads.h>#include <asm/processor.h>#include <asm/page.h>
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Copyright (C) 2003-2005 SAN People (Pty) Ltd\*/#include <linux/module.h>#include <linux/slab.h>#include <linux/delay.h>#include <linux/device.h>#include <linux/mutex.h>#include <linux/err.h>#in  
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<asm/assembler.h>#include <asm/asm-offsets.h>#include <asm/hwcap.h>#incl
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<net/mac80211.h>
Copyright (C) 2003-2006, Marvell International Ltd. */#include <linux/wait.h>#include <linux/timer.h>
Copyright (C) 2003 - 2006 Paul Mundt */#include <linux/init.h>#include <linux/kernel.h>#include <linux/module.h>#include
<linux/interrupt.h>#include <asm/cacheflush.h>#include <mach/sysasic.h>
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<linux/platform_device.h>#include <linux/of.h>#include <linux/of_device
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Copyright (C) 2003 - 2007 Paul Mundt */#include <linux/init.h>#include <linux/io.h>#include <linux/string.h>#include <asm/processor.h>#include
<asm/cache.h>#include <asm/tlb.h>
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Copyright (C) 2003-2007 Yoichi Yuasa <yuasa@linux-mips.org> */#include <linux/cpu.h>#include <linux/errno.h>#include <linux/init.h>#include
<linux/ioport.h>#include <linux/kernel.h>#include <l
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 <linux/io.h>#include <linux/ioport.h>#include <linux/in  
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 It reports a single external temperature with a 1 deg  
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 <linux/gpio/driver.h>#include <linux/init.h>#include <linux/interrupt.h>#inc  
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 <linux/string.h>  
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Copyright (C) 2003 by Ralf Baechle */#include <linux/export.h>#include <linux/init.h>#include <linux/mm.h>#include <asm/fixmap.h>#include
<asm/pgtable.h>#include <asm/pgalloc.h>#include <asm/t
Copyright (C) 2003 by Ralf Baechle */#include <linux/init.h>#include <linux/mm.h>#include <linux/memblock.h>#include <linux/highmem.h>#include
<asm/fixmap.h>#include <asm/pgtable.h>#include <a
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 Copyright (C) 2003 Dave Engebretsen <engebret@us.ibm.com> \*/  
 Copyright (C) 2003 Dave Engebretsen <engebret@us.ibm.com> \*/  
 Copyright (c) 2003 Dave Engebretsen <engebret@us.ibm.com> \* \* 32-bit and 64-bit versions merged by Paul Mackerras <paulus@samba.org>  
 Copyright (C) 2003 Dave Engebretsen <engebret@us.ibm.com> \*/#ifndef \_\_ASM\_POWERPC\_FIRMWARE\_H#define \_\_ASM\_POWERPC\_FIRMWARE\_H  
 Copyright (c) 2003 Dave Engebretsen \* Will Schmidt willschm@us.ibm.com  
 Copyright (C) 2003 David Borowski. \*/  
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 Copyright (c) 2003 David Borowski <david575@golden.net> \* \* All rights reserved  
 Copyright (C) 2003 David Borowski. \*/#ifndef \_SPEAKUP\_PRIVATE\_H#define \_SPEAKUP\_PRIVATE\_H  
 Copyright (C) 2003 David Borowski. \* \* specifically written as a driver for the speakup screenreview \* package it's not a general device driver. \* This driver is for the Keynote Gold internal synthe  
 Copyright (C) 2003 David Borowski. \* \* specifically written as a driver for the speakup screenreview \* package it's not a general device driver. \* This driver is for the RC Systems DoubleTalk PC int  
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 Copyright (C) 2003 David Borowski. \* \* this code is specifically written as a driver for the speakup screenreview \* package and is not a general device driver.  
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 Copyright (C) 2003 David S. Miller (davem@redhat.com) \*/  
 Copyright (C) 2003 David S. Miller (davem@redhat.com) \* \* Many thanks to Dominik Brodowski for fixing up the cpufreq \* infrastructure in order to make this driver easier to implement.  
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 Copyright (C) 2003 Deep Blue Solutions Ltd \*/#include <linux/kernel.h>#include <linux/amba/mmci.h>#include <linux/io.h>#include <linux/irqchip.h>#include <linux/of\_irq.h>#include <linux/of\_addr  
 Copyright (c) 2003 \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include <linux/kernel.h>  
 Copyright (c) 2003 Dimitromanolakis Apostolos <apostol@cs.utoronto.ca>  
 Copyright (c) 2003 Dimitromanolakis Apostolos <apostol@cs.utoronto.ca> \* \* version 0.82: Stable / not all features work yet (no communication with AC97 secondary) \* added 64x/128x oversampl  
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 Copyright (C) 2003 \* Ethan Benson <erbenson@alaska.net> \* partially derived from linux/fs/ext2/ioctl.c  
 Copyright (c) 2003 Evgeniy Polyakov <zbr@ioremmap.net> \*/  
 Copyright (c) 2003 Felix Domke <tmbinc@elitedvb.net>  
 Copyright (c) 2003 Felix Domke <tmbinc@elitedvb.net> \*/#include <linux/init.h>#include <linux/slab.h>#include <linux/wait.h>#include <linux/fs.h>#include <linux/module.h>#include <linux/usb.h>  
 Copyright (C) 2003 Fenghua Yu <fenghua.yu@intel.com> \* - Change pt\_regs\_off() to make it less dependent on pt\_regs structure. \*/  
 Copyright (C) 2003 Frank Mori Hess <fmhess@users.sourceforge.net> \*/  
 Copyright (C) 2003 Fruhwirth Clemens <clemens@endorphin.org>  
 Copyright (C) 2003 Geert Uytterhoeven <geert@linux-m68k.org> \*/  
 Copyright (C) 2003 Geert Uytterhoeven <geert@linux-m68k.org> \* \* Serial\_console ascii image can be any size, \* but should contain %s to display the version

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Copyright (C) 2003 Geert Uytterhoeven \* \* Loosely based on drivers/pci/pci-sysfs.c  
Copyright (C) 2003 Georges Menie \* \* This driver assumes an already configured controller (e.g. from config.c) \* Keep the code clean of board specific initialization.

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Copyright (c) 2003 Greg Kroah-Hartman (greg@kroah.com) \* \* and from the Apple USB BCM5974 multitouch driver,  
Copyright (C) 2003 Guido Guenther <agx@sigxcpu.org> \*/  
Copyright (c) 2003 Hans-Frieder Vogt <hfvogt@arcor.de>, Based on SMBus 2.0 driver for AMD-8111 IO-Hub  
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Copyright (C) 2003 Hewlett-Packard Co \* David Mosberger-Tang <davidm@hpl.hp.com> \* \* 25-Sep-03 davidm Implement fsys\_rt\_sigprocmask(). \* 18-Feb-03 louisk Implement fsys\_gettimeofday(). \* 28-Feb-  
Copyright (C) 2003 Hewlett-Packard Co \* David Mosberger-Tang <davidm@hpl.hp.com> \*/#include <linux/init.h>#include <linux/string.h>  
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Copyright (C) 2003 Hewlett-Packard Co \* David Mosberger-Tang <davidm@hpl.hp.com> \* \* There are a number of reasons for patching instructions. Rather than duplicating code \* all over the place, we  
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Copyright (c) 2003 IBM Corp. \* Dave Engebretsen engebret@us.ibm.com \* Santiago Leon santil@us.ibm.com \*/  
Copyright (C) 2003 IBM Corp. \* \* Many thanks to the authors of pl2303 driver: all functions in this file \* are heavily based on pl2303 code, buffering code is a 1-to-1 copy.  
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Copyright (C) 2003 IBM. \*/#ifndef \_RPADLPAR\_IO\_H\_#define \_RPADLPAR\_IO\_H\_  
Copyright (C) 2003 IBM. \*/#include <linux/kobject.h>#include <linux/string.h>#include <linux/pci.h>#include <linux/pci\_hotplug.h>#include "rpaphp.h"#include "rpadlpar.h"#include "../pci.h"  
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Copyright (c) 2003, Intel Corporation \* \* Full lazy switching support, optimizations and more, by Nicolas Pitre  
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Copyright (C) 2003 Intel Co \* Suresh Siddha <suresh.b.siddha@intel.com> \* Fenghua Yu <fenghua.yu@intel.com> \* Arun Sharma <arun.sharma@intel.com> \* \* 12/07/98 S. Eranian added pt\_regs & switch\_st  
Copyright (c) 2003 International Business Machines, Corp. \* \* This file is part of the SCTP kernel implementation  
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Copyright (C) 2003 James Bottomley <jejb with parisc-linux.org>  
Copyright (c) 2003 James Morris <jmorris@intercode.com.au>  
Copyright (c) 2003 James Morris <jmorris@intercode.com.au> \* \* FIXME: deflate transforms will require up to a total of about 436k of kernel \* memory on i386 (390k for compression, the rest for deco  
Copyright (c) 2003 James Morris <jmorris@intercode.com.au> \* \* Todo:  
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Copyright (c) 2003 Jim Hague (jim.hague@acm.org) \* \* based on 2.4 driver:  
Copyright (C) 2003 Joerg Dorchain <joerg AT dorchain.net>  
Copyright (c) 2003 Joerg Heckenbach \* Many improvements by Bret Wallach <bwallac1@san.rr.com> \* Color fixes by Orion Sky Lawlor <olawlor@acm.org> (2/26/2000) \* OV7620 fixes by Charl P. Botha <cp  
Copyright (c) 2003 John Homppi (SCO, leave this notice here) \* \* derived from the Lego USB Tower driver 0.56:  
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 Copyright (C) 2003 John Williams <jwilliams@itee.uq.edu.au>  
 Copyright (C) 2003 Joshua Wise \*/  
 Copyright (c) 2003, Jouni Malinen <j@w1.fi>  
 Copyright (C) 2003 Jozsef Kadlecsik <kadlec@netfilter.org> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include  
 <linux/netfilter\_ipv4/ip\_tables.h>#include <linux/slab.h>  
 Copyright (C) 2003 Jozsef Kadlecsik <kadlec@netfilter.org> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include  
 <linux/netfilter\_ipv6/ip6\_tables.h>#include <linux/slab.h>  
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 Copyright (C) 2003 Krzysztof Halasa <khc@pm.waw.pl> \n"  
 Copyright (C) 2003 Krzysztof Halasa <khc@pm.waw.pl> \* \* Status:  
 Copyright (c) 2003 Kyle McMartin <kyle@debian.org> \*/  
 Copyright (c) 2003 Kyle McMartin <kyle@debian.org> \*/#include <crypto/internal/hash.h>#include <linux/kernel.h>#include  
 <linux/module.h>#include <linux/mm.h>#include <linux/init.h>#include <lin  
 Copyright (C) 2003 Ladislav Michl  
 Copyright (C) 2003 Ladislav Michl (ladis@linux-mips.org)  
 Copyright (C) 2003 Ladislav Michl (ladis@linux-mips.org) \*/#include <linux/export.h>  
 Copyright (C) 2003 Linda Xie <lxie@us.ibm.com>  
 Copyright (C) 2003 Linda Xie <lxie@us.ibm.com> \* \* All rights reserved  
 Copyright (C) 2003 Lineo uSolutions, Inc.  
 Copyright (C) 2003 Linus Torvalds \* \* Mon Mar 5, 2007: Davide Libenzi <davidel@xmailserver.org> \* Changed ->read() to return a siginfo strcture  
 instead of signal number. \* Fixed lockin  
 Copyright (C) 2003 Luis R. Rodriguez <mcgrof@ruslug.rutgers.edu>  
 Copyright (C) 2003 Luis R. Rodriguez <mcgrof@ruslug.rutgers.edu> \*/  
 Copyright (C) 2003 Maciej W. Rozycki  
 Copyright (C) 2003 Maciej W. Rozycki \*/#ifndef \_\_ASM\_MIPS\_DEC\_ECC\_H#define \_\_ASM\_MIPS\_DEC\_ECC\_H  
 Copyright (c) 2003 Maciej W. Rozycki \* \* include/asm-mips/time.h \* header file for the new style time.c file and time services.  
 Copyright (c) 2003 Manuel Estrada Sainz  
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 Copyright (c) 2003 Margit Schubert-While <margitsw@t-online.de> \* \* derived in part from w831785ts.c:  
 Copyright (c) 2003 Mark M. Hoffman <mhoffman@lightlink.com>  
 Copyright (C) 2003 Mark Studebaker \* \* Supports the following chips: \*  
 Copyright (C) 2003 Mark Studebaker \* \* Thanks to Leon Moonen, Steve Cliffe and Grant Coady for their help \* in testing and debugging this driver.  
 Copyright (C) 2003 Matjaz Breskvar <phoenix@bsemi.com>  
 Copyright (c) 2003 Matthew Wilcox <matthew@wil.cx> \* \* This file is where we call all the ethtool\_ops commands to get \* the information ethtool  
 needs.  
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 Copyright (C) 2003 Michael Hunold <michael@mihu.de>  
 Copyright (c) 2003 Michal Sojka, for high-speed iso transfers \*/  
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 Copyright (C) 2003 Mipsys - Benjamin Herrenschmidt \* \* UDMA support based on patches by Freescale (Bernard Kuhn, John Rigby), \* Domen Puncer  
 and Tim Yamin.

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Copyright (C) 2003 MontaVista Software Inc. * Author: Jun Sun, jsun@mvista.com or jsun@junsun.net * * 031022 - [jsun] add run-time configure and
partition setup
Copyright: (C) 2003 Monta Vista Software, Inc. * * Based on drivers/serial/8250.c by Russell King.
Copyright (C) 2003 Montavista Software, Inc * * Based on the code from the 2.4 kernel by * Dale Farnsworth <dfarnsworth@mvista.com> and Kent
Borg.
Copyright (C) 2003 MontaVista Software, Inc. * * Maintainer: Imre Kaloz <kaloz@openwrt.org>
Copyright: (C) 2003 Monta Vista Software, Inc. * * Note 1: This driver is made separate from the already too overloaded * 8250.c because it needs
some kirks of its own and that'll make it * easier
Copyright (C) 2003 Monta Vista Software, Inc. . Unified SMC91x driver by Nicolas Pitre .
Copyright (C) 2003 Monta Vista Software, Inc. * Unified SMC91x driver by Nicolas Pitre * * Arguments:
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Copyright (c) 2003 Neil Brown <neilb@cse.unsw.edu.au>
Copyright (c) 2003 Nils-Erik Mattsson, Joacim Haggmark, Magnus Erixzon * * Changes to meet Linux coding standards, to make it meet latest ccid3
draft * and to make it work as a loadable module in
Copyright (c) 2003 Nils-Erik Mattsson, Joacim Haggmark, Magnus Erixzon */#include <linux/types.h>
Copyright (c) 2003 Nils-Erik Mattsson, Joacim Haggmark, Magnus Erixzon */#include <linux/types.h>#include <linux/math64.h>#include "../dccb.h"
Copyright (C) 2003 Nokia Corporation * Author: Juha Yrjölä <juha.yrjola@nokia.com> *
Copyright (C) 2003 Nokia Corporation * Author: Juha Yrjölä <juha.yrjola@nokia.com> */#ifndef __MACH_OMAP1_LCD_DMA_H__#define
__MACH_OMAP1_LCD_DMA_H__
Copyright (C) 2003 Nokia Corporation * Written by Timo Teräs <ext-timo.teras@nokia.com> * * Added support for H2 & H3 Keypad
Copyright (C) 2003 Oliver Endriss
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Copyright (c) 2003 Open Source Development Lab */
Copyright (c) 2003 Open Source Development Lab * * The driver model core calls device_pm_add() when a device is registered. * This will initialize the
embedded device_pm_info object in the device
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Copyright (c) 2003 Patrick Mochel
Copyright (C) 2003 Paul Andreassen <paul@andreassen.com.au>
Copyright (C) 2003 Paul Mundt
Copyright (C) 2003 Paul Mundt */#ifndef __ASM_CPU_SH2_ADDRSPACE_H#define __ASM_CPU_SH2_ADDRSPACE_H
Copyright (C) 2003 Paul Mundt */#ifndef __ASM_CPU_SH2_CACHE_H#define __ASM_CPU_SH2_CACHE_H
Copyright (C) 2003 Paul Mundt */#ifndef __ASM_CPU_SH2_MMU_CONTEXT_H#define __ASM_CPU_SH2_MMU_CONTEXT_H
Copyright (C) 2003 Paul Mundt */#ifndef __ASM_SH_DREAMCAST_DMA_H#define __ASM_SH_DREAMCAST_DMA_H
Copyright (C) 2003 Paul Mundt */#ifndef __ASM_SH_FLAT_H#define __ASM_SH_FLAT_H
Copyright (C) 2003 Paul Mundt */#ifndef __ASM_SH_TLB_64_H#define __ASM_SH_TLB_64_H
Copyright (C) 2003 Paul Mundt */#ifndef DMA_REGISTER_H#define DMA_REGISTER_H
Copyright (C) 2003 Paul Mundt */#include <linux/sys.h>#include <linux/linkage.h>
Copyright (C) 2003 Paul Mundt <lethal@linux-sh.org> */
Copyright (C) 2003 Paul Mundt <lethal@linux-sh.org> * * Based on files with the following comments: *
Copyright (C) 2003 Paul Mundt <lethal@linux-sh.org> * * This file is part of the LinuxDC project (www.linuxdc.org)
Copyright (C) 2003 Paul Mundt * * Per-interrupt selective. IRLM=0 (Fixed priority) is not * supported being useless without a cascaded interrupt *
controller.
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Copyright (C) 2003 Pavel Machek (pavel@ucw.cz)
Copyright (C) 2003 Pavel Machek (pavel@ucw.cz) */
Copyright (C) 2003 Pavel Machek (pavel@ucw.cz) * * These routines maintain argument size conversion between 32bit and 64bit * ioctls.
Copyright (C) 2003 Peter Milne, D-TACQ Solutions Ltd * <Peter dot Milne at D hyphen TACQ dot com>
Copyright (C) 2003 Peter Milne, D-TACQ Solutions Ltd * <Peter dot Milne at D hyphen TACQ dot com> * * With acknowledgements to i2c-

```

```

algo-ibm_ocp.c by * Ian DaSilva, MontaVista So
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Copyright (C) 2003 PMC-Sierra, Inc., * written by Manish Lachwani *
Copyright (C) 2003 Ralf Baechle * 6/2004 pf */#ifndef __ASM_MACH_IP28_CPU_FEATURE_OVERRIDES_H#define
__ASM_MACH_IP28_CPU_FEATURE_OVERRIDES_H
Copyright (C) 2003 Ralf Baechle * * Handler for RM7000 extended interrupts. These are a non-standard * feature so we handle them separately from
standard interrupts.
Copyright (C) 2003 Ralf Baechle */#ifndef __ASM_ASMMACRO_H#define __ASM_ASMMACRO_H
Copyright (C) 2003 Ralf Baechle */#ifndef __ASM_MACH_GENERIC_CPU_FEATURE_OVERRIDES_H#define
__ASM_MACH_GENERIC_CPU_FEATURE_OVERRIDES_H
Copyright (C) 2003 Ralf Baechle */#ifndef __ASM_MACH_NETLOGIC_CPU_FEATURE_OVERRIDES_H#define
__ASM_MACH_NETLOGIC_CPU_FEATURE_OVERRIDES_H
Copyright (C) 2003 Ralf Baechle */#ifndef __ASM_PGTABLE_H#define __ASM_PGTABLE_H
Copyright (C) 2003 Ralf Baechle <ralf@linux-mips.org> *
Copyright (C) 2003 Ralf Baechle <ralf@linux-mips.org> */#ifndef __ASM_I8259_H#define __ASM_I8259_H
Copyright (C) 2003 Ralph Metzler <rjkm@metzlerbros.de> * * based on code: *
Copyright (C) 2003 Randolph Chung <tausq at debian . org>
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Copyright (C) 2003 Red Hat, Inc., James Morris <jmorris@redhat.com> */#include <linux/kernel.h>#include <linux/slab.h>#include
<linux/string.h>#include <linux/spinlock.h>#include <linux/rcupdate
Copyright (C) 2003 Red Hat, Inc., James Morris <jmorris@redhat.com> */#include <linux/types.h>#include <linux/stddef.h>#include
<linux/kernel.h>#include <linux/slab.h>#include <linux/fs.h>#include
Copyright (C) 2003 Red Hat Inc * * libata documentation is available via 'make {ps|pdf}docs', * as Documentation/driver-api/libata.rst
Copyright (C) 2003 Red Hat * * Some code was moved here from ide.c, see it for original copyrights.
Copyright (C) 2003 Richard Curnow
Copyright (C) 2003 Richard Curnow
Copyright (C) 2003 Richard Curnow */#include <linux/init.h>#include <linux/io.h>#include <asm/processor.h>#include <asm/cache.h>
Copyright (C) 2003 Richard Curnow */#include <linux/init.h>#include <linux/kernel.h>#include <linux/mm.h>#include <linux/log2.h>#include
<asm/mmu_context.h>#include <asm/processor.h>#include
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Copyright (C) 2003 Russell King. * * Generic SSP driver. This provides the generic core for simple * IO-based SSP applications.
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 <linux/uaccess.h>#include <linux/crc16.h>#include <linux/

```

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<linux/kernel_stat.h>#include <asm/io.h>#includ
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 various structure changes, and crypt  
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 <linux/mm.h>#include <linux/clk.h>#include <linux/irq.h>#include <lin  
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 Copyright (C) 2004 - 2006 rt2x00 SourceForge Project \* <http://rt2x00.serialmonkey.com> \* \* Module: eeprom\_93cx6 \* Abstract: EEPROM reader routines for 93cx6 chipsets. \* Supported chipsets: 93c46  
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 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Based on orig code from @author John Levon  
 <levon@movementarian.org>  
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 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Delay routines using pre computed loops\_per\_jiffy value.  
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 <psokolov@synopsys.com>  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \*/#ifndef \_\_ASMARC\_SETUP\_H#define \_\_ASMARC\_SETUP\_H  
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 Insn Scheduling improvements to csum core routines. \*  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* RajeshwarR: Dec 11, 2007 \* -- Added support for Inter Processor  
 Interrupts  
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 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Vineetg: Aug 2009 \* -"C" version of lowest level context switch asm  
 macro called by scheduler \* gcc doesn't genera  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: aug 2009 \* -Implemented CONFIG\_STACKTRACE APIs,  
 primarily save\_stack\_trace\_tsk( ) \* for displaying task'  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Vineetg: Aug 2009 \* -Moved core context switch macro out of  
 entry.S into this file. \* -This is the more "natural" h  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: Aug 2011 \* -Reintroduce duplicate PD fixup - some  
 customer chips still have the issue  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Vineetg: August 2010: From Android kernel work  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Vineetg: Dec 2007 \* -Check if we are running on Simulator or on  
 real hardware \* to skip certain things during b  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: Jan 2010 (Restarting of timer related syscalls)  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: June 2010 \* -\_\_clear\_user( ) called multiple times during  
 elf load was byte loop \* converted to do as  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: June 2011 \* -"/proc/meminfo | grep PageTables" kept on  
 increasing \* Recently added pgtable dtor was not g  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: March 2009 \* -Implemented task\_pt\_regs( )  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Vineetg: March 2009 (Supporting 2 levels of Interrupts) \* Stack  
 switching code can no longer reliably rely on the fac  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Vineetg: May 16th, 2008 \* - Current macro is now implemented as  
 "global register" r25  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: May 2011 \* -Folded PAGE\_PRESENT (used by VM) and  
 PAGE\_VALID (used by MMU) into 1. \* They are semantical  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: May 2011: for Non-aliasing VIPT D-cache following can be  
 NOPs \* -flush\_cache\_dup\_mm (fork) \* -likewise  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: May 2011 \* -Refactored get\_new\_mmu\_context( ) to only  
 handle live-mm. \* retiring-mm handled in other hook  
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 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: May 2011 \* -We had half-optimised memset/memcpy, got  
 better versions of those \* -Added memcmp, strchr, str  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Vineetg: Oct 2009 \* No need for ARC specific thread\_info allocator  
 (kmallo/free). This is \* anyways one page alloc  
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functions... * Add vports, etc */#incl
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functions... */#ifndef SCSI_TRANSPORT_FC_H
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Copyright (C) 2004-2007 Jean Delvare <jdelvare@suse.de> * Ported to Linux 2.6 by Gabriele Gorla <gorlik@yahoo.com> * and Jean Delvare
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Copyright (C) 2004 - 2007 Paul Mundt */#include <linux/init.h>#include <linux/platform_device.h>#include <linux/mtd/mtd.h>#include
<linux/mtd/partitions.h>#include <linux/mtd/physmap.h>#include
Copyright (C) 2004 - 2007 Paul Mundt */#include <linux/mm.h>#include <linux/init.h>#include <linux/dma-noncoherent.h>#include
<linux/module.h>#include <asm/cacheflush.h>#include <asm/addrspace
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<linux/interrupt.h>#include <linux/kernel_stat.h>#include <lin
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Copyright (C) 2004-2008 Atmel Corporation */#include <linux/blkdev.h>#include <linux/clk.h>#include <linux/debugfs.h>#include
<linux/device.h>#include <linux/dmaengine.h>#include <linux/dma-map
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 Copyright (C) 2004-2008 International Business Machines Corp. \* Author(s): Michael A. Halcrow <mhalcrow@us.ibm.com> \* Tyler Hicks  
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 Semiconductor. It measures \* two temperatures (its own and one e  
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 \_ASM\_C6X\_BUG\_H#define \_ASM\_C6X\_BUG\_H  
 Copyright (C) 2004, 2009, 2010, 2011 Texas Instruments Incorporated \* Author: Aurelien Jacquot (aurelien.jacquot@jaluna.com) \*/#ifndef  
 \_ASM\_C6X\_CMPXCHG\_H#define \_ASM\_C6X\_CMPXCHG\_H  
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 \_ASM\_C6X\_DELAY\_H#define \_ASM\_C6X\_DELAY\_H  
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 \_ASM\_C6X\_SWITCH\_TO\_H#define \_ASM\_C6X\_SWITCH\_TO\_H  
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 <linux/linkage.h>#include <linux/of\_fdt.h>#include <asm/asm  
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 <linux/mm.h>#include <linux/swap.h>#include <linux/modul  
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 <linux/module.h>#include <asm/checksum.h>#include <linux  
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 <linux/module.h>#include <linux/syscalls.h>#include <lin  
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 \_ASM\_C6X\_BITOPS\_H#define \_ASM\_C6X\_BITOPS\_H  
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 \_ASM\_C6X\_CACHEFLUSH\_H#define \_ASM\_C6X\_CACHEFLUSH\_H  
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 \_ASM\_C6X\_ELF\_H#define \_ASM\_C6X\_ELF\_H  
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 \_ASM\_C6X\_PGTABLE\_H#define \_ASM\_C6X\_PGTABLE\_H  
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 <linux/netdevice.h>#include <linux/usb.h>  
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<linux/device.h>#include <linux/module.h>#include <linux/slab.h>#in

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<linux/dmaengine.h>#include <linux/dma-mapping.h>#include <linux/pref

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Copyright (C) 2004 Advanced Micro Devices, Inc. \*/#include <linux/fb.h>#include <linux/delay.h>#include <asm/io.h>#include  
<asm/delay.h>#include <asm/msr.h>#include <linux/cs5535.h>  
Copyright (C) 2004 Advanced Micro Devices, Inc. \*/#include <linux/spinlock.h>#include <linux/fb.h>#include <linux/delay.h>#include  
<asm/io.h>#include <asm/div64.h>#include <asm/delay.h>  
Copyright (C) 2004 Advanced Micro Devices, Inc. \*/#include <linux/spinlock.h>#include <linux/fb.h>#include <linux/delay.h>#include  
<asm/io.h>#include <asm/div64.h>#include <asm/delay.h>#include  
Copyright (C) 2004 Alan Modra (amodra@au.ibm.com)), IBM Corp. \*/#include <asm/processor.h>#include <asm/ppc\_asm.h>#include  
<asm/unistd.h>#include <asm/vdso.h>  
Copyright (C) 2004 Alan Modra (amodra@au.ibm.com)), IBM Corp. \*/#include <asm/processor.h>#include <asm/ppc\_asm.h>#include  
<asm/unistd.h>#include <asm/vdso.h>#include <asm/ptrace.h> /\* XXX for  
Copyright (C) 2004 Albert Herranz  
Copyright (C) 2004 Albert Herranz \*/#include "ppc\_asm.h"  
Copyright (C) 2004 Albert Herranz \* LANDISK/sh4 supported by kogiidena \*/#include <linux/mm.h>#include <linux/kexec.h>#include  
<linux/delay.h>#include <linux/reboot.h>#include <linux/numa.h>#  
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 <asm/thread\_info.h>#include <asm/vfpmacros.h>#include <asm  
 Copyright (C) 2004 ARM Limited. \* Written by Deep Blue Solutions Limited. \*/#include <linux/types.h>#include <linux/cpu.h>#include  
 <linux/cpu\_pm.h>#include <linux/hardirq.h>#include <linux/ker  
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 <asm/ppc\_asm.h>#include <asm/vdso.h>#include <asm/asm-o  
 Copyright (C) 2004 Benjamin Herrenschmidt (benh@kernel.crashing.org), \* IBM Corp. \*/#include <asm/processor.h>#include  
 <asm/ppc\_asm.h>#include <asm/vdso.h>#include <asm/asm-of  
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 Copyright (C) 2004 by Ralf Baechle (ralf@linux-mips.org) \*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/interrupt.h>#include  
 <linux/pci.h>#include <linux/types.h>#include <as  
 Copyright (C) 2004 by Ralf Baechle (ralf@linux-mips.org) \* \* MIPS boards specific PCI support.  
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<linux/unistd.h>#include <linux/interrupt.h>#include <linux  
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<stddef.h>#include <init.h>#include <elf\_user.h>#incl  
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via sysfs  
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Version: 0.0.18 \* \* FEATURES currently supported:  
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Copyright (c) 2004 James Morris <jmorris@redhat.com>, \* Red Hat Inc. \*/#include <linux/rwsem.h>#include <linux/f2fs\_fs.h>#include  
<linux/security.h>#include <linux/posix\_acl\_xattr.h>#include "  
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Copyright (C) 2004 Jan M. Hochstein \* <hochstein@algo.informatik.tu-darmstadt.de> \*/#include <linux/device.h>#include <linux/kernel.h>#include  
<linux/module.h>#include <linux/usb.h>#include <li  
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Copyright (C) 2004 Jeff Lackey.  
 Copyright (C) 2004 Jens Axboe <axboe@kernel.dk> \* \* Helper functions for setting/querying io priorities of processes. The \* system calls closely  
 mimick getpriority/setpriority, see the man page fo  
 Copyright (c) 2004 Jocelyn Mayer  
 Copyright (C) 2004 Jochen Friedrich <jochen@scram.de> \* \* This code has basically these routines at the moment:  
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 Copyright (C) 2004 Jochen Friedrich \* \* Loosely based on drivers/pci/pci-sysfs.c and drivers/zorro/zorro-sysfs.c  
 Copyright (c) 2004 Joerg Albert <joerg.albert@gmx.de>  
 Copyright (C) 2004 John Steele Scott <toojays@toojays.net> \* \* TODO: Need a big cleanup here. Basically, we need to have different \*  
 cpufreq\_driver structures type of HW in  
 Copyright (c) 2004 Jon Smirl <jonsmirl@gmail.com>  
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 <linux/module.h>#include <linux/string.h>#include <linux/  
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 Copyright (C) 2004 Maciej W. Rozycki \*/#ifndef \_\_ASM\_CPU\_INFO\_H#define \_\_ASM\_CPU\_INFO\_H  
 Copyright (C) 2004 Maciej W. Rozycki \*/#ifndef \_\_ASM\_CPU\_TYPE\_H#define \_\_ASM\_CPU\_TYPE\_H  
 Copyright (C) 2004 Maciej W. Rozycki \*/#ifndef \_\_ASM\_MACH\_ATH25\_CPU\_FEATURE\_OVERRIDES\_H#define  
 \_\_ASM\_MACH\_ATH25\_CPU\_FEATURE\_OVERRIDES\_H  
 Copyright (C) 2004 Maciej W. Rozycki \*/#ifndef \_\_ASM\_MACH\_ATH79\_CPU\_FEATURE\_OVERRIDES\_H#define  
 \_\_ASM\_MACH\_ATH79\_CPU\_FEATURE\_OVERRIDES\_H  
 Copyright (C) 2004 Maciej W. Rozycki \*/#ifndef \_\_ASM\_MACH\_FALCON\_CPU\_FEATURE\_OVERRIDES\_H#define  
 \_\_ASM\_MACH\_FALCON\_CPU\_FEATURE\_OVERRIDES\_H  
 Copyright (C) 2004 Maciej W. Rozycki \*/#ifndef \_\_ASM\_MACH\_RC32434\_CPU\_FEATURE\_OVERRIDES\_H#define



```

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Copyright (C) 2004 Maciej W. Rozycki */#include <linux/delay.h>#include <linux/kernel.h>#include <linux/sched.h>#include
<linux/seq_file.h>#include <asm/bootinfo.h>#include <asm/cpu.h>#include
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Copyright (C) 2004 Nadia Yvette Chambers */
Copyright (C) 2004 Nadia Yvette Chambers */#include <linux/kallsyms.h>#include <linux/uaccess.h>#include <linux/module.h>#include
<linux/ftrace.h>#include <linux/kprobes.h>
Copyright (C) 2004 Nadia Yvette Chambers */#include <linux/module.h>#include <linux/kallsyms.h>#include <linux/uaccess.h>#include
<linux/ftrace.h>#include <linux/sched/rt.h>#include <linux/sche
Copyright (C) 2004 Nadia Yvette Chambers */#include <linux/ring_buffer.h>#include <generated/utsrelease.h>#include <linux/stacktrace.h>#include
<linux/writeback.h>#include <linux/kallsyms.h>#in
Copyright (C) 2004 Nadia Yvette Chambers */#include <linux/ring_buffer.h>#include <linux/debugfs.h>#include <linux/uaccess.h>#include
<linux/ftrace.h>#include <linux/slab.h>#include <linux/fs.h
Copyright (C) 2004 Nathan Lynch IBM Corporation. * * This driver creates a proc file at /proc/ppc64/lparcfg which contains * keyword - value pairs that
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 Copyright (C) 2004 Nokia Corporation \* Author: Imre Deak <imre.deak@nokia.com> \*/#ifndef \_\_LINUX\_OMAPFB\_H\_\_#define \_\_LINUX\_OMAPFB\_H\_\_  
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 <linux/interrupt.h>#include <linux/spinlock.h>#include  
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 Peter Ujfalusi <peter.ujfalusi@ti.com>  
 Copyright (C) 2004 Nokia Corporation by Imre Deak <imre.deak@nokia.com> \*/#include <linux/gpio.h>#include <linux/kernel.h>#include  
 <linux/platform\_device.h>#include <linux/delay.h>#include <linu  
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 Copyright (C) 2004 Nokia Corporation by Tony Lindgren <tony@atomide.com> \* \* H2 specific changes and cleanup  
 Copyright (C) 2004 Nokia Corporation by Tony Lindgren <tony@atomide.com> \*/#include <linux/gpio.h>#include <linux/kernel.h>#include  
 <linux/init.h>#include <linux/platform\_device.h>#include <linu  
 Copyright (C) 2004 Nokia Corporation \* \* Note: NS means "NAND Simulator". \* Note: Input means input TO flash chip, output means output FROM chip.  
 Copyright (C) 2004 Nokia Corporation \* Partial timer rewrite and additional dynamic tick timer support by \* Tony Lindgren <tony@atomide.com> and \*  
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 Copyright (C) 2004 Paul Mackerras <paulus@au.ibm.com>, IBM \*/  
 Copyright (C) 2004 Paul Mackerras <paulus@au.ibm.com>, IBM \*/#include <linux/kernel.h>#include <linux/kprobes.h>#include  
 <linux/ptrace.h>#include <linux/prefetch.h>#include <asm/sstep.h>#includ  
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 Copyright (C) 2004 Paul Mundt \*/#ifndef \_\_ASM\_CPU\_SH2A\_CACHE\_H#define \_\_ASM\_CPU\_SH2A\_CACHE\_H  
 Copyright (C) 2004 Paul Mundt \* \* SuperH SH4-202 MicroDev board support.  
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Copyright (c) 2004 Red Hat, Inc., James Morris <jmorris@redhat.com> */#include <linux/fs.h>#include <linux/slab.h>#include <linux/file.h>#include
<linux/xattr.h>#include <linux/mount.h>#include
Copyright (C) 2004 Red Hat, Inc., James Morris <jmorris@redhat.com> */#include <linux/init.h>#include <linux/types.h>#include
<linux/slab.h>#include <linux/stddef.h>#include <linux/kernel.h>#in
Copyright (C) 2004 Red Hat, Inc., James Morris <jmorris@redhat.com> */#include <linux/types.h>#include <linux/kernel.h>#include
<linux/netlink.h>#include <linux/rtnetlink.h>#include <linux/if.h>
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Copyright (c) 2004 Red Hat Inc.\n"); first = 0; }
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Copyright (C) 2004 Richard Curnow */
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Copyright (C) 2004 Richard Curnow */#ifndef __ASM_SH_BL_BIT_64_H#define __ASM_SH_BL_BIT_64_H
Copyright (C) 2004 Richard Curnow */#ifndef __ASM_SH_CACHE_INSNS_64_H#define __ASM_SH_CACHE_INSNS_64_H
Copyright (C) 2004 Richard Curnow */#ifndef __ASM_SH_SWITCH_TO_64_H#define __ASM_SH_SWITCH_TO_64_H
Copyright (C) 2004 Richard Curnow */#ifndef __ASM_SH_SWITCH_TO_H#define __ASM_SH_SWITCH_TO_H
Copyright (C) 2004 Richard Curnow */#ifndef __ASM_SH_TRAPS_64_H#define __ASM_SH_TRAPS_64_H
Copyright (C) 2004 Richard Curnow */#ifndef __ASSEMBLY__
Copyright (C) 2004 Richard Curnow */#include <linux/kallsyms.h>#include <linux/kernel.h>#include <linux/types.h>#include <linux/errno.h>#include
<asm/page.h>#include <asm/ptrace.h>#include <a
Copyright (C) 2004 Richard Curnow */#include <linux/kernel.h>#include <linux/rwsem.h>#include <linux/smp.h>#include <linux/interrupt.h>#include
<linux/init.h>#include <linux/errno.h>#include <
Copyright (C) 2004 Richard Curnow */#include <linux/rwsem.h>#include <linux/sched.h>#include <linux/mm.h>#include <linux/smp.h>#include
<linux/kernel.h>#include <linux/signal.h>#include <linu
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Copyright (c) 2004 Richard Purdie * * Based on code written by Sharp/Lineo for 2.4 kernels
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Copyright (C) 2004 Roelf Diedericks <roelfd@inet.co.za>
Copyright (c) 2004 Ron Lee (ron@debian.org) * rewritten for kernel 2.6 * * cPad display character device part is not included. It can be found at *
http://jan-steinhoff.de/linux/synaptics-usb.ht
Copyright (c) 2004 Rudolf Marek <r.marek@assembler.cz> * * Partly imported from i2c-vid.h of the lm_sensors project
Copyright (C) 2004 Russell King. */#ifndef __ASM_ARM_MACH_IRDA_H#define __ASM_ARM_MACH_IRDA_H
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 <linux/sysectl.h>#include <linux/spinlock.h>#include <linux/skbuff.h>#  
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 interface.  
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 requests to the WAHC \* (Wire Adater Host Contro  
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 UWB. This is stuff used \* across the stack that of  
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 information that describes the \* UWB neighborhood that the  
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 (struct usb\_hcd) for a \* Wireless USB Host Contro  
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 Wireless USB Host \* Controllers (struct wusbhc, emb  
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 Intel Wireless UWB \* Link 1480 device (both in th

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many as simultaneous * devices the Wireless USB Hos
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Copyright (C) 2005-2006 Micronas USA Inc.static int go7007_usb_vendor_request(struct go7007 *go, u16 request, u16 value, u16 index, void
*transfer_buffer, int length, int in){ struct go7007_usb *u
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 <linux/init.h>#include <linux/interrupt.h>#include <linux/io.h>  
 Copyright (C) 2005, 2007 Bartlomiej Zolnierkiewicz \*/  
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 <linux/slab.h>#include "musb\_core.h"  
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 <linux/spinlock.h>#include <linux/interrupt.h>  
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 Copyright (C) 2005-2009 Atmel Corporation \*/#include <linux/clk.h>#include <linux/delay.h>#include <linux/bitmap.h>#include  
 <linux/device.h>#include <linux/atmel\_pdc.h>#include <linux/gpio/cons  
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 <linux/interrupt.h>#include <linux/time.h>#include <linux/delay.h>#incl  
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 <linux/errno.h>#include <linux/io.h>#include <linux/er  
 Copyright (C) 2005-2009 Freescale Semiconductor, Inc. \*/#include <linux/export.h>#include <linux/module.h>#include <linux/types.h>#include  
 <linux/errno.h>#include <linux/delay.h>#include <linux  
 Copyright (C) 2005-2009 Freescale Semiconductor, Inc. \*/#include <linux/export.h>#include <linux/module.h>#include <linux/types.h>#include  
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 <linux/io.h>  
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 <linux/reset.h>#include <linux/platform\_device.h>#incl  
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 <media/davinci/vpfe\_types.h>  
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<linux/interrupt.h>#include <linux/timer.h>#include
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emu/double.h>int fcmpd(void *ft, void *fa, void *fb, int cmpop){ FP_DE
Copyright (C) 2005-2018 Andes Technology Corporation#include <asm/sfp-machine.h>#include <math-emu/soft-fp.h>#include <math-
emu/single.h>int fcmps(void *ft, void *fa, void *fb, int cmpop){ FP_DE
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<linux/smp.h>#include <asm/irq_regs.h>#include <asm/tim
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<linux/delay.h>#include <linux/input.h>#include <linux/in
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polldev.h>#include <linux/input/sparse-keymap.h>#incl
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```

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 often be a Host Bus Adapter to use the services o  
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 numer of classes without having to allocate storage  
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Copyright (C) 2005 Mips Technologies, Inc \*/#include <linux/cpu.h>#include <linux/cpuset.h>#include <linux/cpumask.h>#include  
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 <linux/kernel.h>#include <linux/init.h>#include <linux/slab.h>#include  
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 Copyright (C) 2005 Paul Mundt \* \* FRQCR parsing hacked out of arch/sh/kernel/time.c  
 Copyright (C) 2005 Paul Mundt \*/#include <linux/init.h>#include <linux/kernel.h>#include <asm/clock.h>#include <asm/freq.h>#include <asm/io.h>  
 Copyright (C) 2005 Paul Mundt \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/err.h>#include <linux/io.h>#include  
 <linux/clkdev.h>#include <asm/clock.h>#include <asm/freq.h>  
 Copyright (C) 2005 Paul Mundt \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/io.h>#include <asm/clock.h>#include  
 <asm/freq.h>#include <asm/processor.h>  
 Copyright (C) 2005 Paul Mundt \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/io.h>#include <linux/clkdev.h>#include  
 <asm/clock.h>#include <asm/freq.h>#include <asm/io.h>  
 Copyright (C) 2005 Paul Mundt \*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/superhyway.h>#include <linux/string.h>#include  
 <asm/addrspace.h>#include <asm/io.h>  
 Copyright (C) 2005 Paul Mundt \*/#include <linux/module.h>#include <linux/pci.h>#include <asm/machvec.h>#include <asm/io.h>  
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fmt

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 <linux/module.h>#include <linux/uaccess.h>  
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Copyright (C) 2006 - 2007 Akio Idehara */#include <linux/err.h>#include <linux/cache.h>#include <linux/cpumask.h>#include
<linux/delay.h>#include <linux/init.h>#include <linux/spinlock.h>#incl
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<linux/module.h>#include <linux/skbuff.h>#include <linu
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<linux/capability.h>#include <linux/skbuff.h>#include <lin
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<linux/types.h>#include <linux/kernel.h>#include <linux/in.h>#include
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<asm/clock.h>#include <asm/freq.h>
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 <linux/time.h>#include <linux/delay.h>#include <linux/  
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 <linux/input.h>#include <linux/platform\_device.h>#inc  
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 Copyright (C) 2006 ARM Ltd. \* written by Russell King, Deep Blue Solutions Ltd. \*/#include <linux/kernel.h>#include <linux/module.h>#include  
 <linux/i2c.h>#include <linux/i2c-algo-bit.h>#includ  
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 Copyright (C) 2006 Arnd Bergmann <arnd@arndb.de>  
 Copyright (C) 2006 Arnd Bergmann <arnd@arndb.de>, IBM Corp. \*/#if defined(CONFIG\_SERIAL\_8250\_CONSOLE) &&  
 defined(CONFIG\_MAGIC\_SYSRQ)#define SUPPORT\_SYSRQ#endif  
 Copyright (C) 2006 Arnd Bergmann <arnd@arndb.de>, IBM Corp. \*/#include <linux/console.h>#include <linux/module.h>#include  
 <linux/slab.h>#include <linux/delay.h>#include <linux/serial\_core.h>#in  
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code.
Copyright (C) 2006 Atsushi Nemoto <anemo@mba.ocn.ne.jp> */#include <linux/sched.h>#include <linux/sched/debug.h>#include
<linux/sched/task_stack.h>#include <linux/stacktrace.h>#include <linux/ex
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<linux/device.h>#include <linux/platform_device.h>#inclu
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busses.
Copyright (C) 2006 Bob Copeland (me@bobcopeland.com) * based on osf.c */
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<linux/fs.h>#include <linux/vfs.h>#include <linux/cred.h>
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 <linux/err.h>#include <linux/init.h>#include <linux/ker  
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 and by a test vector provided by Ken Buchanan at \*  
 Copyright (C) 2006 Hewlett-Packard Co \* Alex Williamson <alex.williamson@hp.com> \*/#include <linux/kernel.h>#include <linux/init.h>#include  
 <linux/module.h>#include <linux/string.h>  
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 Copyright (C) 2006 James Smart, Emulex Corporation \*/#include <linux/time.h>#include <linux/jiffies.h>#include <linux/security.h>#include  
 <linux/delay.h>#include <linux/slab.h>#include <linux  
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 <linux/serial\_sci.h>#include <linux/sh\_timer.  
 Copyright (C) 2006 Jamie Lenehan \*/#include <linux/platform\_device.h>#include <linux/init.h>#include <linux/serial.h>#include <linux/io.h>#include  
 <linux/sh\_timer.h>#include <linux/sh\_intc.h>  
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<linux/clk.h>#include <linux/clkdev.h>#include <linux/dma-con
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Copyright (C) 2006 Michael Hanselmann (linux-kernel@hansmi.ch) ** Clean room implementation based on the reverse engineered Mac OS X driver by
* Johannes Berg <johannes@sipsolutions.net>, document
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Copyright (c) 2006, Michael Wu <flamingice@sourmilk.net>
Copyright (c) 2006, Michael Wu <flamingice@sourmilk.net> * * Based on the islm (softmac prism54) driver, which is:

```

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Copyright (C) 2006 Mike Christie
Copyright (C) 2006 Mike Christie */#include <linux/slab.h>#include <linux/module.h>#include <scsi/scsi.h>#include <scsi/scsi_ah.h>#include
<scsi/scsi_dh.h>#include <scsi/scsi_device.h>
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Copyright (C) 2006 Nokia Corporation * Author: Imre Deak <imre.deak@nokia.com> */#include <linux/device.h>#include <linux/delay.h>#include
<linux/slab.h>#include <linux/workqueue.h>#include <li
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Copyright (C) 2006 Nokia Corporation */#include <linux/gpio.h>#include <linux/platform_device.h>
Copyright (C) 2006 Nokia Corporation */#include <linux/gpio.h>#include <linux/platform_device.h>#include <linux/platform_data/gpio-
omap.h>#include <linux/mfd/tps65010.h>
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Copyright (C) 2006 Nokia Corporation * Tony Lindgren <tony@atomide.com> */#include <linux/module.h>#include <linux/kernel.h>#include
<linux/errno.h>#include <linux/usb.h>#include <linux/platfor
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Copyright (C) 2006 Olof Johansson <olof@lixom.net> * * Dynamic DMA mapping support, pSeries-specific parts, both SMP and LPAR.
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Copyright (C) 2006 Paul Mackerras, IBM Corp. * * If we have CONFIG_VIRT_CPU_ACCOUNTING_NATIVE, we measure cpu time in * the same units as
the timebase. Otherwise we measure cpu time * in jiffies

```

```

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<linux/serial.h>#include <linux/serial_sci.h>#incl
Copyright (C) 2006 Paul Mundt */#include <linux/init.h>#include <linux/slab.h>#include <linux/module.h>#include <linux/interrupt.h>#include
<linux/platform_device.h>#include <asm/push-switch.h
Copyright (C) 2006 Paul Mundt */#include <linux/io.h>#include <linux/module.h>#include <linux/interrupt.h>#include
<linux/platform_device.h>#include <mach/highlander.h>#include <asm/push-switc
Copyright (C) 2006 Paul Mundt */#include <linux/linkage.h>#include <asm/page.h>
Copyright (C) 2006 Paul Mundt */#include <linux/platform_device.h>#include <linux/init.h>#include <linux/serial.h>#include <linux/io.h>#include
<linux/serial_sci.h>#include <linux/sh_dma.h>#i
Copyright (C) 2006 Paul Mundt */#include <linux/platform_device.h>#include <linux/init.h>#include <linux/serial.h>#include
<linux/serial_sci.h>#include <linux/uart_driver.h>#include <linux/sh_t
Copyright (C) 2006 Paul Mundt */#include <linux/platform_device.h>#include <linux/init.h>#include <linux/serial.h>#include
<linux/sh_timer.h>#include <linux/sh_intc.h>#include <linux/serial_sc
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Copyright (C) 2006 Rafael J. Wysocki <rjw@sisk.pl> */#define pr_fmt(x) "hibernate: " x#include <linux/cpu.h>#include <linux/kvm_host.h>#include
<linux/mm.h>#include <linux/pm.h>#include <linux/
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Copyright (C) 2006 Ralf Baechle <ralf@linux-mips.org> */#include <linux/console.h>#include <linux/init.h>#include <linux/interrupt.h>#include
<linux/mc146818rtc.h>#include <linux/param.h>#inclu
Copyright (C) 2006 Ralf Baechle <ralf@linux-mips.org> */#include <linux/dma-direct.h>#include <asm/ip32/crime.h>
Copyright (C) 2006 Ralf Baechle (ralf@linux-mips.org) */#include <linux/kernel.h>#include <linux/sched.h>#include <linux/cpumask.h>#include
<linux/interrupt.h>#include <linux/compiler.h>#include
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<linux/export.h>#include <linux/spinlock.h>#include <linux/debug_locks
Copyright (C) 2006 Red Hat, Inc., Ingo Molnar <mingo@redhat.com> */#include <linux/rwsem.h>#include <linux/mutex.h>#include

```

```

<linux/ww_mutex.h>#include <linux/sched.h>#include <linux/delay.h>#in
  Copyright (C) 2006 Red Hat, Inc., Ingo Molnar <mingo@redhat.com> */#include <linux/sched/task_stack.h>#include <linux/sched/debug.h>#include
<linux/sched.h>#include <linux/kernel.h>#include <lin
  Copyright (c) 2006 Red Hat, Inc., Ingo Molnar\n");
  Copyright (C) 2006, Red Hat, Inc., Ingo Molnar * * We check whether all boot CPUs have their TSC's synchronized, * print a warning if not and turn off
the TSC clock-source.
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http://gladman.plushost.co.uk/oldsite/cryptography_technology/index.php * See the o
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  Copyright (C) 2006 Rudolf Marek <r.marek@assembler.cz> * * Inspired from the w83785 and amd756 drivers.
  Copyright (C) 2006 Russ Anderson <rja@sgi.com> */#include <linux/types.h>#include <linux/init.h>#include <linux/sched.h>#include
<linux/interrupt.h>#include <linux/irq.h>#include <linux/kallsym
  Copyright (C) 2006 Russell King. */#ifndef ASMARM_THREAD_NOTIFY_H#define ASMARM_THREAD_NOTIFY_H
  Copyright (C) 2006 Rusty Russell IBM Corporation
  Copyright (C) 2006 Rusty Russell IBM Corporation * * Author: Michael S. Tsirkin <mst@redhat.com>
  Copyright (C) 2006 S1 Corp. * * Original driver for 2.6.10 pl2303 driver by * Greg Kroah-Hartman (greg@kroah.com) * Changes for 2.6.20 by Harald
Klein <hari@vt100.at>
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  Copyright (C) 2006 Savin Zlobec * * AT91SAM9 support:
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  Copyright (C) 2006 Secret Lab Technologies Ltd. * Grant Likely <grant.likely@secretlab.ca>
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  Copyright (C) 2006 Silicon Graphics, Inc., Christoph Lameter * * Page migration was first developed in the context of the memory hotplug * project. The
main authors of the migration code are: *
  Copyright (C) 2006 Silicon Motion Technology Corp. * Authors: Ge Wang, gewang@siliconmotion.com * Boyod boyod.yang@siliconmotion.com.cn
*
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boyod.yang@siliconmotion.com.cn *
  Copyright (C) 2006 * Simon Schulz (ark3116_driver <at> auctionant.de) * * ark3116 * - implements a driver for the arkmicro ark3116 chipset
(vendor=0x6547, * productid=0x0232) (used in a datac
  Copyright (c) 2006 Simtec Electronics */
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  Copyright (c) 2006 Simtec Electronics * Ben Dooks <ben@simtec.co.uk> * * Common code for SMDK2410 and SMDK2440 boards
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  Copyright (c) 2006 Simtec Electronics * Vincent Sanders <vince@simtec.co.uk> * Ben Dooks <ben@simtec.co.uk> * * Framebuffer driver for the Silicon
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  Copyright (C) 2006 STMicroelectronics Limited * Author: Carl Shaw <carl.shaw@st.com> * * Definitions for SH4 FPU operations

```

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Copyright (c) 2006 Tejun Heo <teheo@suse.de> */
Copyright (c) 2006 Tejun Heo <teheo@suse.de> */#include <linux/memblock.h> /* for max_pfn */#include <linux/acpi.h>#include <linux/dma-
direct.h>#include <linux/dma-noncoherent.h>#include <linux
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fixes by Amit Kucheria <amit.kucheria@verdurent.com>
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Copyright (C) 2006 Thomas Bogendoerfer */#include <linux/delay.h>#include <linux/init.h>#include <linux/interrupt.h>#include <linux/irq.h>#include
<linux/kernel.h>
Copyright (C) 2006 Thomas Bogendoerfer (tsbogend@alpha.franken.de) */
Copyright (C) 2006 Thomas Bogendoerfer (tsbogend@alpha.franken.de) */#ifndef __ASM_SNI_H#define __ASM_SNI_H
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Copyright (C) 2006 Torsten Ertbjerg Rasmussen <tr@newtec.dk> * - nvrn size determined from resource * - this ds1742 driver now supports ds1743.
*/
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Copyright (c) 2006 Tower Technologies * Based on ep93xx driver, bits from alim7101_wdt.c * * Authors: Ray Lehtiniemi <rayl@mail.com>, * Alessandro
Zummo <a.zummo@towertech.it>
Copyright (C) 2006 Tower Technologies * * based on ixdp425-setup.c:
Copyright (c) 2006 Trond Myklebust <Trond.Myklebust@netapp.com> */#include <linux/sysctl.h>#include <linux/nfs_fs.h>

```

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<linux/mtd/partitions.h>#include <linux/mtd/physmap.h>
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<linux/module.h>#include <linux/skbuff.h>#include <linux/netfilter/x_tabl

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<linux/module.h>#include <linux/skbuff.h>#include <net/tcp.h>#include <n
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<linux/log2.h>#include <linux/list.h>#include <linux/slab.h>#include <
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Copyright (C) 2007-2012 Siemens AG * * Written by:
Copyright (C) 2007-2012 ST-Ericsson AB * Author: Linus Walleij <linus.walleij@stericsson.com> * Author: Jonas Aaberg <jonas.aberg@stericsson.com>
*/#include <linux/clkdev.h>#include <linux/slab.h
Copyright (C) 2007-2012 ST-Ericsson AB * COH 901 571/3 - Used in DB3210 (U365 2.0) and DB3350 (U335 1.0) * Author: Linus Walleij
```

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 Copyright (C) 2007-2012 ST-Ericsson SA \* Core platform support, IRQ handling and device definitions. \* Author: Linus Walleij  
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 <linux/smp.h>#include <linux/stringify.h>  
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 Copyright (C) 2007 Alex Dubov <oakad@yahoo.com> \*/#define DRIVER\_NAME "ms\_block"#define pr\_fmt(fmt) DRIVER\_NAME ": " fmt  
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 Copyright (C) 2007 Andrew Sharp <andy.sharp@lsi.com> \*\* Real time clock driver for the Dallas 1511 chip, which also \* contains a watchdog timer.  
 There is a tiny amount of code that \* platform co  
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 Copyright (C) 2007 ARM Limited \*/#include <linux/cpu.h>#include <linux/err.h>#include <linux/init.h>#include <linux/smp.h>#include <linux/spinlock.h>#include <linux/log2.h>#include <linux/io.h>  
 Copyright (C) 2007 ARM Limited \*/#include <linux/init.h>#include <linux/highmem.h>#include <asm/cp15.h>#include <asm/cputype.h>#include <asm/cacheflush.h>  
 Copyright (C) 2007 Arnaldo Carvalho de Melo <acme@redhat.com> \*\* Based on code in the latency\_tracer, that is: \*  
 Copyright (c) 2007 Arnaldo Carvalho de Melo <acme@redhat.com> \*/#include <linux/moduleparam.h>#include "tfr.h"  
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 Copyright (C) 2007 Atmel Corporation \*/#ifndef \_LINUX\_I2C\_GPIO\_H\_#define \_LINUX\_I2C\_GPIO\_H\_  
 Copyright (C) 2007 Atmel Corporation \*/#include <linux/completion.h>#include <linux/debugfs.h>#include <linux/delay.h>#include <linux/gpio/consumer.h>#include <linux/i2c-algo-bit.h>#include <linux/i2c.h>  
 Copyright (C) 2007 Atmel Corporation \*/#include <linux/delay.h>#include <linux/kdebug.h>#include <linux/notifier.h>#include <linux/sched.h>#include <linux/sched/debug.h>#include <linux/hardirq.h>  
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 <linux/platform\_device.h>#include <linux/serial\_8250.h>  
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 Copyright (C) 2007 by Ralf Baechle \*/#ifndef \_\_ASM\_SGI\_SEEQ\_H#define \_\_ASM\_SGI\_SEEQ\_H  
 Copyright (C) 2007 by Ralf Baechle \*/#ifndef \_\_ASM\_SGI\_WD\_H#define \_\_ASM\_SGI\_WD\_H  
 Copyright (C) 2007 by Ralf Baechle \*/#ifndef \_\_ASM\_TOPOLOGY\_H#define \_\_ASM\_TOPOLOGY\_H  
 Copyright (C) 2007 by Ralf Baechle \*/#include <linux/clocksource.h>#include <linux/init.h>#include <linux/sched\_clock.h>  
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 Copyright (C) 2007 by Thomas Bogendoerfer \*/#include <linux/eisa.h>#include <linux/init.h>#include <linux/ioport.h>#include  
 <linux/console.h>#include <linux/screen\_info.h>#include <linux/platfo  
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 Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#ifndef \_\_WHCD\_H#define \_\_WHCD\_H  
 Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#ifndef \_\_WHCI\_WHCI\_HC\_H#define \_\_WHCI\_WHCI\_HC\_H  
 Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/delay.h>#include <linux/kernel.h>#include <linux/module.h>#include  
 <linux/pci.h>#include <linux/dma-mapping.h>#include <linux/s  
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 Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/dma-mapping.h>#include <linux/slab.h>#include  
 <linux/usb.h>  
 Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/export.h>#include "include/umc.h"  
 Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/export.h>#include <linux/slab.h>#include "include/umc.h"  
 Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/gfp.h>#include <linux/dma-mapping.h>  
 Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/gfp.h>#include <linux/dma-mapping.h>#include  
 <linux/usb.h>  
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 Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/sysfs.h>#include <linux/workqueue.h>#include  
 <linux/module.h>#include <linux/pci.h>#include "include/u  
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Copyright (c) 2007 Dave Airlie <airlied@linux.ie> \* \* DRM framebuffer helper functions  
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Thomas Gleixner suggestions.  
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(schwidefsky@de.ibm.com) \* Derived from "include/asm-i386/current.  
Copyright (C) 2007 David S. Miller (davem@davemloft.net) \*/#include <asm/asi.h>  
Copyright (C) 2007 David S. Miller <davem@davemloft.net> \*/#include <linux/kernel.h>#include <linux/export.h>#include <linux/init.h>  
Copyright (C) 2007 David S. Miller (davem@davemloft.net) \*/#include <linux/kernel.h>#include <linux/interrupt.h>#include <linux/slab.h>#include  
<linux/irq.h>  
Copyright (C) 2007 David S. Miller (davem@davemloft.net) \*/#include <linux/kernel.h>#include <linux/pci.h>#include <linux/slab.h>#include  
<linux/init.h>#include <linux/msi.h>#include <linux/exp  
Copyright (C) 2007 David S. Miller <davem@davemloft.net> \*/#include <linux/sched.h>#include <linux/device.h>#include <linux/cpu.h>#include  
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<linux/pfn.h>#include <linux/proc\_fs.h>#include <linux/strin  
Copyright (C) 2007 Eurek srl \* Author: Claudio Lanconelli <lanconelli.claudio@eptar.com> \* based on enc28j60.c written by David Anders for 2.4 kernel  
version \* \* \$Id: enc28j60.c,v 1.22 2007/12/20  
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bugs.  
Copyright (C) 2007 Florian Fainelli <florian@openwrt.org> \*/#include <linux/kernel.h>#include <linux/export.h>#include <linux/init.h>#include  
<linux/ctype.h>#include <linux/string.h>#include <l  
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<scottwood@freescale.com>  
Copyright (c) 2007 Freescale Semiconductor, Inc. \*/#include <linux/kernel.h>#include <linux/string.h>#include <linux/errno.h>#include  
<linux/unistd.h>#include <linux/interrupt.h>#include <linux  
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Copyright (C) 2007 FUJITA Tomonori <tomof@acm.org> \*/#include <linux/init.h>#include <linux/module.h>#include <linux/jiffies.h>#include  
<linux/err.h>#include <linux/slab.h>#include <linux/strin  
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<linux/export.h>#include <linux/slab.h>#include <linux/init.h>
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 Copyright (C) 2007 kogiidena \*/#include <linux/io.h>#include <linux/init.h>#include <linux/interrupt.h>#include <linux/platform\_device.h>#include  
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<linux/serial.h>#include <linux/serial_sci.h>#include <linux/sh_t
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<linux/module.h>#include <linux/init.h>

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 Copyright (C) 2007 Ralf Baechle <ralf@linux-mips.org> \*/#include <linux/init.h>#include <linux/interrupt.h>#include <linux/irq.h>#include <linux/sched\_clock.h>#include <asm/time.h>#include <asm  
 Copyright (C) 2007 Ralf Baechle (ralf@linux-mips.org) \*/#include <linux/init.h>#include <linux/platform\_device.h>#include <linux/serial\_8250.h>#include <linux/rtc/ds1685.h>  
 Copyright (C) 2007 Ralf Baechle (ralf@linux-mips.org) \*/#include <linux/init.h>#include <linux/serial\_8250.h>  
 Copyright(c) 2007, RealTEK Technology Inc. All Right Reserved. \* \* Module: Hal819xUsbDM.h (RTL8192 Header H File)  
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 Copyright (C) 2007, Red Hat, Inc., Ingo Molnar <mingo@redhat.com>  
 Copyright (C) 2007 Red Hat, Inc., Ingo Molnar <mingo@redhat.com> \* \* Interactivity improvements by Mike Galbraith  
 Copyright (C) 2007 Red Hat, Inc., Peter Zijlstra \* \* Code for /proc/lockdep and /proc/lockdep\_stats: \*  
 Copyright (C) 2007 Red Hat, Inc., Peter Zijlstra \* \* Contains functions related to writing back dirty pages at the \* address\_space level.  
 Copyright (C) 2007 Red Hat, Inc., Peter Zijlstra \*/#include "sched.h"  
 Copyright (C) 2007 Red Hat, Inc., Peter Zijlstra \* \* Move PELT related code from fair.c into this pelt.c file \* Author: Vincent Guittot <vincent.guittot@linaro.org>

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 Copyright (C) 2007 Sam Revitch <samr7@cs.washington.edu> \*/  
 Copyright (c) 2007 Samuel Ortiz <samuel@sortiz.org> \* which is in turn partly based on the wireless netlink code:  
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 Copyright (C) 2007 Texas Instruments, Inc. \* Lesly A M <x0080970@ti.com> \*/ #include <linux/power/smartreflex.h>  
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Copyright (C) 2007 Yoichi Yuasa <yuasa@linux-mips.org> \*/#ifndef \_ASM\_IRQ\_GT641XX\_H#define \_ASM\_IRQ\_GT641XX\_H

Copyright (C) 2007 Yoichi Yuasa <yuasa@linux-mips.org> \*/#include <linux/clockchips.h>#include <linux/init.h>#include <linux/interrupt.h>#include <linux/spinlock.h>#include <linux/irq.h>

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Copyright (C) 2007 Yoichi Yuasa <yuasa@linux-mips.org> \*/#include <linux/errno.h>#include <linux/init.h>#include <linux/ioport.h>#include <linux/platform\_device.h>#include <linux/serial\_8250.h>

Copyright (C) 2007 Yoichi Yuasa <yuasa@linux-mips.org> \*/#include <linux/errno.h>#include <linux/init.h>#include <linux/smp.h>#include <linux/ioport.h>#include <linux/platform\_device.h>

Copyright (C) 2007 Yoichi Yuasa <yuasa@linux-mips.org> \*/#include <linux/hardirq.h>#include <linux/init.h>#include <linux/irq.h>#include <linux/spinlock.h>#include <linux/types.h>

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\*/

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<linux/of_platform.h>#include <linux/kthread.h>#include <linux/i2c.h>#in
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<linux/kernel.h>#include <linux/ioport.h>#include
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<stdlib.h>#include <string.h>#include <stdarg.h>#include <sys/t
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Copyright (C) 2008 Atmel Corporation */#include <linux/moduleparam.h>#include <linux/export.h>#include <linux/debugfs.h>#include
<linux/fs.h>#include <linux/seq_file.h>#include <linux/slab.h>#
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Copyright (C) 2008 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/export.h>  
Copyright (C) 2008 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/random.h>#include <linux/slab.h>  
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Copyright (C) 2008 Cambridge Silicon Radio Ltd. \*/#include <linux/kthread.h>#include <linux/freezer.h>#include <linux/slab.h>#include  
<linux/delay.h>#include "uwb-internal.h"  
Copyright (C) 2008 Cambridge Silicon Radio Ltd. \*/#include <linux/slab.h>#include <linux/kernel.h>#include <linux/debugfs.h>#include  
<linux/seq\_file.h>#include <linux/export.h>  
Copyright (C) 2008 Cambridge Silicon Radio Ltd. \*/#include "wusbhc.h"  
Copyright (C) 2008 Cambridge Silicon Radio Ltd. \* \* Manage DRP Availability (the MAS available for DRP \* reservations). Thus: \*  
Copyright (C) 2008 Cambridge Silicon Radio Ltd. \* \* WUSB devices have to be paired (associated in WUSB lingo) so \* that they can connect to the  
system.  
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Copyright (C) 2008 Cavium Networks <support@caviumnetworks.com> \*/#ifndef \_\_ASM\_MIPS\_MACH\_CAVIUM\_OCTEON\_WAR\_H#define  
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Copyright (c) 2008, Christian Lamparter <chunkeey@web.de> \* \* Based on the islm (softmac prism54) driver, which is:  
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for flow control. TX empty IRQ is unusable, sin  
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```

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<linux/rtc.h>#include <linux/workqueue.h>
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Copyright (C) 2008 David S. Miller <davem@davemloft.net> */#include <linux/kernel.h>#include <linux/reboot.h>#include <linux/export.h>#include
<linux/pm.h>#include <linux/of.h>
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Copyright (C) 2008 Devin Heitmueller <dheitmueller@linuxtv.org>
Copyright (C) 2008 Dmitri Vorobiev */#include <linux/cpu.h>#include <linux/init.h>#include <linux/sched.h>#include <linux/ioport.h>#include
<linux/irq.h>#include <linux/of_fdt.h>#include <linux/
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Copyright (C) 2008 Dmitry Baryshkov * * Based on Sharp's NAND driver sharp_sl.c
Copyright (c) 2008 Dmitry Baryshkov */#ifndef CSB726_H#define CSB726_H
Copyright (c) 2008 Dmitry Baryshkov */#ifndef TOSA_BT_H#define TOSA_BT_H
Copyright (c) 2008 Dmitry Baryshkov */#include <linux/kernel.h>#include <linux/module.h>#include <linux/power_supply.h>#include
<linux/wm97xx.h>#include <linux/delay.h>#include <linux/spinlock.
Copyright (c) 2008 Dmitry Baryshkov * * T7L66 features: *
Copyright (c) 2008 Dmitry Eremin-Solenikov */#include <linux/kernel.h>#include <linux/init.h>#include <linux/io.h>#include
<linux/gpio/machine.h>#include <linux/platform_device.h>#include <linux/
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Copyright (C) 2008 Embedded Alley Solutions, Inc. */#include <linux/clk.h>#include <linux/delay.h>#include <linux/slab.h>#include
<linux/sched/task_stack.h>#include <linux/interrupt.h>#include
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<linux/kernel.h>#include <linux/leds.h>#include <linux/m
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drivers/media/video/omap/sensor\_ov6650.c  
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<linux/i2c.h>#include <linux/delay.h>#include <linux/log2.h>#incl  
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and camera devices. It should be used if the camera  
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register level documentation and suitable hardware for  
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which is  
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<linux/acpi.h>#include <linux/pci.h>#include <linux/pnp.h>  
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<linux/types.h>#include <linux/module.h>#include <lin  
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Josef Pavlik <josef@pavlik.it> Fixed diseqc\_msg, diseqc\_burst

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Kahraman Akdemir \*\* Added RFC4106 AES-GCM support f

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Copyright (c) 2008 Intel Corporation \* Author: Matthew Wilcox <willy@linux.intel.com> \*\* This file implements counting semaphores. \* A counting semaphore may be acquired 'n' times before sleeping

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Copyright (c) 2008 Intel Corporation \* Jesse Barnes <jesse.barnes@intel.com> \*\* Moorestown has an interesting PCI implementation:

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```

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depends wildly on alignment on many x86 processors. Th
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Copyright (c) 2008 Jonathan Cameron */#include <string.h>#include <stdlib.h>#include <stdio.h>#include <stdint.h>#include <dirent.h>#include
<errno.h>#include <ctype.h>#include "iio_utils.h"
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Copyright (c) 2008 Magnus Damm \*/#ifndef \_VIDEODMA\_DMA\_CONTIG\_H#define \_VIDEODMA\_DMA\_CONTIG\_H  
Copyright (C) 2008 Magnus Damm \*/#include <linux/clockdev.h>#include <linux/dma-mapping.h>#include <linux/init.h>#include <linux/platform\_device.h>#include <linux/interrupt.h>#include <linux/inpu  
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offsets.h>#include <asm/export.h>#include <asm/feature-fixup  
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<linux/platform_device.h>#include <linux/pci.h>#include <
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<linux/clk.h>#include <linux/etherdevice.h>#include <l
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Copyright (C) 2008 MaxLinear */static u16 MXL5005_RegisterInit(struct dvb_frontend *fe)
Copyright (C) 2008 Mercury IMC Ltd * Written by Mark Jackson <mpfj@mimc.co.uk> * * NOTE: Currently this driver only supports the bare minimum for
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<mach/sdk7780.h>
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<linux/platform_device.h>#include <linux/ata_platform.h>#include <asm/machvec.h>
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<linux/string.h>#include <linux/delay.h>#include <linux/mmc/host.h>#
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<linux/netdevice.h>#include <linux/etherdevice.h>#inclu
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<linux/llc.h>#include <linux/slab.h>#include <linu
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<net/mrp.h>#include "vlan.h"
Copyright (c) 2008 Patrick McHardy <kaber@trash.net> */#include <linux/types.h>#include <linux/if_vlan.h>#include <net/garp.h>#include "vlan.h"
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as10x_bus_adapter_t;struct as102_dev_t;
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<linux/platform\_device.h>#include <linux/regulator/driver.h>#include <linux/sla  
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<linux/err.h>#include <linux/platform\_device.h>#i  
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Updated pal\_cache\_flush() to be in sync with PAL v2.6. \* 00/03

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<linux/interrupt.h>#include <linux/init.h>#in
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<linux/interrupt.h>#include <linux/mc146818rtc.h>#include <linux/irq.h>
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#Positions Resistor Options (kOhm) * AD5258 1 64 1, 10, 50, 100 * AD
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 <asm/hw\_breakpoint.h>  
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 <linux/hw\_breakpoint.h>#include <linux/percpu.h>#include <lin  
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 <linux/perf\_event.h>#include <asm/processor.h>  
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 <linux/spinlock.h>#include "pcie-sh7786.h"  
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 \*/#include <linux/clk.h>#include <linux/delay.h>#include <  
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 \*/#include <linux/module.h>#include <linux/sched.h>#includ  
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 Copyright (C) 2009-2011, Frederic Weisbecker <fweisbec@gmail.com> \* \* Handle the callchains from the stream in an ad-hoc radix tree and then \* sort them in an rbtree.  
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using load-exclusive and \* store-exclusive for proce  
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"util/debug.h"#include "util/dso.h"#include <subcmd/page  
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```

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<linux/init.h>#include <linux/kernel.h>#include <linux/
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 <linux/of.h>#include <linux/of\_device.h>#include <linux  
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 <linux/platform\_device.h>#include <linux/spi/spi.h>  
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 <linux/pci.h>#include <linux/i2c.h>#include <linux/inter  
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mapping.h>#include <linux/raid/pq.h>#include <linux/async
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 offsets.h>#include <asm/regdef.h>#include <asm/asm.h>  
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 <loongson.h>#include <boot\_param.h>#include <workarounds.  
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<linux/clkdev.h>#include <linux/sh_clk.h>#include <asm/cloc
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<asm/suspend.h>#include <linux/uaccess.h>#include <asm/
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<linux/cpuidle.h>#include <linux/export.h>#include <asm
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<linux/serial_sci.h>#include <linux/sh_timer.h>#include <linux/sh_in
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__SPI_FSL_LIB_H__
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<soc/fsl/qe/qe.h>#include <linux/dma-mapping.h>#include <linux/f
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non-controllable regulators, as well as for allowing
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<linux/platform_device.h>#include <linux/reset-controller.h>#include <li
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Copyright (C) 2009 Paul Mundt * * Heavily based on the x86 and PowerPC implementations.
Copyright (C) 2009 Paul Mundt */#include <linux/init.h>#include <linux/platform_device.h>#include <linux/io.h>#include
<linux/mtd/physmap.h>#include <linux/regulator/fixd.h>#include <linux/re
Copyright (C) 2009 Paul Mundt */#include <linux/init.h>#include <linux/platform_device.h>#include <linux/sm501.h>#include <linux/sm501-
regs.h>#include <linux/fb.h>#include <linux/mtd/physmap.h
Copyright (C) 2009 Paul Mundt */#include <linux/kernel.h>#include <linux/init.h>#include <linux/io.h>#include <linux/irq.h>#include
<linux/perf_event.h>#include <asm/processor.h>

```



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Copyright (C) 2009 Paul Mundt */#include <linux/kernel.h>#include <linux/sched.h>#include <linux/perf_event.h>#include <linux/percpu.h>#include
<asm/unwinder.h>#include <asm/ptrace.h>
Copyright (C) 2009 Paul Mundt */#include <linux/platform_device.h>#include <linux/init.h>#include <linux/serial.h>#include
<linux/serial_sci.h>#include <linux/io.h>#include <linux/mm.h>#inclu
Copyright (C) 2009 Paul Mundt */#include <linux/platform_device.h>#include <linux/init.h>#include <linux/serial.h>#include
<linux/serial_sci.h>#include <linux/sh_eth.h>#include <linux/sh_timer
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single reset bit but can be written to zero so
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Copyright (C) 2009 Provigent Ltd. */#include <linux/acpi.h>#include <linux/clk-provider.h>#include <linux/clk.h>#include <linux/delay.h>#include
<linux/dmi.h>#include <linux/err.h>#include <li
Copyright (C) 2009 Provigent Ltd. */#include <linux/clk.h>#include <linux/delay.h>#include <linux/export.h>#include <linux/errno.h>#include
<linux/err.h>#include <linux/i2c.h>#include <linux/i
Copyright (C) 2009 Provigent Ltd. */#include <linux/delay.h>#include <linux/err.h>#include <linux/errno.h>#include <linux/export.h>#include
<linux/gpio/consumer.h>#include <linux/i2c.h>#includ
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* id and bind the device to the driver yourse
Copyright (C) 2009 Red Hat, Inc. * Author: Michael S. Tsirkin <mst@redhat.com> * * test virtio server in host kernel.
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Copyright (C) 2009 Red Hat, Inc., Ingo Molnar <mingo@redhat.com> * * Implements 3 trace clock variants, with differing scalability/precision *
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Copyright (C) 2009 Samsung Electronics \* Kim Kyuwon <q1.kim@samsung.com> \* \* Datasheet:  
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 Copyright (C) 2009 ST-Ericsson AB \* Handle board-bound regulators and board power not related \* to any devices. \* Author: Linus Walleij  
 <linus.walleij@stericsson.com> \*/#include <linux/device.h>  
 Copyright (C) 2009 ST-Ericsson. \* Author: Srinidhi Kasagar <srinidhi.kasagar@stericsson.com> \* \* This file is based on arm realview platform  
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 <stdarg.h>#include <sys/types.h>#include <sys/sta  
 Copyright (C) 2009 Steven Rostedt <rstedt@redhat.com> \*/#include <linux/ring\_buffer.h>#include <linux/completion.h>#include  
 <linux/kthread.h>#include <uapi/linux/sched/types.h>#include <linux/  
 Copyright (C) 2009 Steven Rostedt <rstedt@redhat.com> \*/#include <linux/stringify.h>#include <linux/kallsyms.h>#include  
 <linux/seq\_file.h>#include <linux/uaccess.h>#include <linux/trace.h>#i  
 Copyright (C) 2009, Steven Rostedt <rstedt@redhat.com> \*/#include <stdio.h>#include <stdlib.h>#include <string.h>#include <errno.h>  
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 <linux/kernel.h>#include <linux/init.h>#include <linux/  
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 OMAP-1509, OMAP-1510, and OMAP-1610 \* are differe  
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 Copyright (C) 2009 Theodore Kilgore <kilgota@auburn.edu> \* \* Based closely upon the file gspca/pac\_common.h  
 Copyright (C) 2009 Theodore Kilgore <kilgota@auburn.edu> \* \* Several previously unsupported cameras are owned and have been tested by \* Hans de Goede <hdegoede@redhat.com> and \* Thomas Kaiser <tho  
 Copyright (C) 2009 Theodore Kilgore <kilgota@auburn.edu> \* \* Support for the control settings for the CIF cameras is  
 Copyright (C) 2009 Theodore Kilgore \* \* Sportscam DV15 support and control settings are  
 Copyright (C) 2009 \* Theodore Ts'o (tytso@mit.edu) \* \* Track which blocks in the filesystem are metadata blocks that \* should never be used as data blocks by files or directories.  
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 Copyright (C) 2009 Thomas Chou <thomas@wytron.com.tw> \* \* All rights reserved  
 Copyright (C) 2009 Thomas Chou <thomas@wytron.com.tw> \* \* Based on arch/nios2/kernel/head.S  
 Copyright (C) 2009 Thomas Chou <thomas@wytron.com.tw> \*/#include <asm/page.h>#include <asm-generic/vmlinux.lds.h>#include  
 <asm/cache.h>#include <asm/thread\_info.h>  
 Copyright (C) 2009 Thomas Chou <thomas@wytron.com.tw> \* \* This is a collection of several routines from gzip-1.0.3 \* adapted for Linux.  
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 Copyright (C) 2009 T. Mertelj <tomaz.mertelj@guest.arnes.si> \* \* Based on max6650.c:  
 Copyright (C) 2009 TMT Services \* \* 2007-09-04: eric miao <eric.y.miao@gmail.com> \* rewrite to align with latest kernel  
 Copyright (C) 2009 TMT Services \* \* 2010-01-21 Hennie van der Merve <hvdmerwe@tmtservices.co.za>  
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 Copyright (c) 2009 Trond Myklebust <Trond.Myklebust@netapp.com> \*/#include <linux/kmod.h>#include <linux/module.h>#include  
 <linux/moduleparam.h>#include <linux/mount.h>#include <linux/namei.h>#  
 Copyright (c) 2009 Trond Myklebust <Trond.Myklebust@netapp.com> \* \* Resolves DNS hostnames into valid ip addresses  
 Copyright (C) 2009 Uwe Kleine-Koenig, Pengutronix \* \* Common code for i.MX1, i.MX21 and i.MX27

```

Copyright (C) 2009 Uwe Kleine-Koenig <u.kleine-koenig@pengutronix.de> * for Pengutronix */#include <linux/export.h>#include <linux/ftrace.h>
Copyright (C) 2009 Valentin Longchamp, EPFL Mobots group */
Copyright (C) 2009 Valentin Sidikov */#ifndef __ASM_SH_WATCHDOG_H#define __ASM_SH_WATCHDOG_H#endif __KERNEL__
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Copyright (C) 2009 VIA Technologies, Inc. * * based on existing coretemp.c, which is
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Copyright (C) 2009 Vivek Goyal <vgoyal@redhat.com> * Nauman Rafique <nauman@google.com> * * For policy-specific per-blkcg data:
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Copyright (C) 2009 William Hubbs * * This code is based on kobject-example.c, which came with linux 2.6.x.
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from MIPS.
Copyright (C) 2009 Wind River Systems, Inc. * Updated for supporting PPC405EX on Kilauea. * Tiejun Chen <tejun.chen@windriver.com> */#include
<stddef.h>#include "types.h"#include "string.h"
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Copyright (C) 2009 Wind River Systems, * written by Ralf Baechle <ralf@linux-mips.org> */#ifndef __ASM_COP2_H#define __ASM_COP2_H
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Copyright (C) 2009 Wind River Systems, * written by Ralf Baechle <ralf@linux-mips.org> */#include <linux/capability.h>#include
<linux/init.h>#include <linux/irqflags.h>#include <linux/notifier
Copyright (C) 2009 Wind River Systems, * written by Ralf Baechle <ralf@linux-mips.org> */#include <linux/init.h>#include <linux/irqflags.h>#include
<linux/notifier.h>#include <linux/prefetch.h
Copyright (C) 2009 Wind River Systems, * written by Ralf Baechle <ralf@linux-mips.org> */#include <linux/init.h>#include <linux/sched.h>#include
<linux/notifier.h>#include <linux/ptrace.h>
Copyright (C) 2009 Wind River Systems, * written by Ralf Baechle <ralf@linux-mips.org> */#include <linux/module.h>#include <linux/init.h>#include
<linux/slab.h>#include <linux/interrupt.h>#in
Copyright (C) 2009 Wind River Systems, * written by Ralf Baechle <ralf@linux-mips.org> */#include <linux/module.h>#include <linux/init.h>#include
<linux/slab.h>#include <linux/io.h>#include <
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Copyright (C) 2009 Wolfram Sang, Pengutronix * * The Maxim MAX7300/1 device is an I2C/SPI driven GPIO expander. There are * 28 GPIOs. 8 of them
can trigger an interrupt. See datasheet for more * d
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Copyright (c) 2009 Xilinx, Inc. * * Based on "ehci-ppc-of.c" by Valentine Barshak <vbarshak@ru.mvista.com> * and "ehci-ppc-soc.c" by Stefan Roesse
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 Copyright (C) 2009 Yoichi Yuasa <yuasa@linux-mips.org> \* \* based on code for MX31ADS,  
 Copyright (C) 2009 Zhang Le \*/#ifndef \_ASM\_BOOTINFO\_H#define \_ASM\_BOOTINFO\_H  
 Copyright (c) 2009 Zhang Le <r0bertz@gentoo.org> \*/#include <linux/errno.h>#include <asm/bootinfo.h>  
 Copyright (C) 2009 Zhang Le <r0bertz@gentoo.org> \* \* reference: /proc/cpuinfo, \* arch/mips/kernel/cpu-probe.c(cpu\_probe\_legacy), \*  
 arch/mips/kernel/proc.c(show\_cpuinfo), \* loongson2f user manual.  
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 Copyright (C) 2010-17 Digital Devices GmbH\n"); stat = ddb\_init\_ddbridge(); if (stat < 0) return stat; stat =  
 pci\_register\_driver(&ddb\_pci\_driver); if (stat < 0) ddb\_exit\_ddbridge(0, stat);  
 Copyright (c) 2010-2010 Analog Devices Inc. \*/  
 Copyright (c) 2010-2010 Analog Devices Inc. \*/#include <linux/types.h>#include <linux/mutex.h>#include <linux/device.h>#include  
 <linux/spi/spi.h>#include <linux/slab.h>#include <linux/sysfs.h>  
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 Copyright (C) 2010-2011 Hans de Goede <hdegoede@redhat.com> \* \* This module is adapted from the in kernel v4l1 cpia driver which is : \*  
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```

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Copyright (C) 2010-2011 Jaiganesh Narayanan <jnarayanan@atheros.com>
Copyright (c) 2010-2011, Jarod Wilson <jarod@redhat.com> ** Based on the original lirc_mceusb and lirc_mceusb2 drivers, by Dan * Conti, Martin
Blatter and Daniel Melander, the latter of which was
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Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> */
Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> ** Architecture specific procedures for creating, accessing and * interpreting the device
tree.
Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> * et al. */
Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> * et al. */#ifndef __ASM_OPENRISC_ELF_H#define __ASM_OPENRISC_ELF_H
Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> * et al. */#ifndef __ASM_OPENRISC_PTRACE_H#define __ASM_OPENRISC_PTRACE_H
Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> * et al. * * This file is part of OpenRISC 1000 Architectural Simulator.
Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> ** Here we handle the break vectors not used by the system call * mechanism, as well as
some general stack/register dumping * things.
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Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> ** Precise Delay Loops
Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> ** This file handles the architecture-dependent parts of initialization
Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> ** This file handles the architecture-dependent parts of process handling...
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Copyright (c) 2010 - 2011 Michal Simek <monstr@monstr.eu>
Copyright (C) 2010-2011 Mika Westerberg * * Explicit FIFO handling code was inspired by amba-pl022 driver.
Copyright (C) 2010-2011 Neil Brown
Copyright (C) 2010,2011 NetUP Inc.
Copyright (C) 2010-2011 Nicholas A. Bellinger <nab@kernel.org> * * Additional file for the target driver support.
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Copyright (C) 2010-2011 Pengutronix * Uwe Kleine-Koenig <u.kleine-koenig@pengutronix.de> */#include "../hardware.h"#include "devices-common.h"
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Copyright (c) 2010-2011 Picochip Ltd., Jamie Iles */#include <crypto/internal/ahash.h>#include <crypto/aes.h>#include <crypto/algapi.h>#include
<crypto/authenc.h>#include <crypto/internal/des.h>
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<hechangqing@semptian.com>
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Copyright (C) 2010 - 2011 Samsung Electronics Co., Ltd. * Contact: Sylwester Nawrocki, <s.nawrocki@samsung.com> * * Initial register configuration
based on a driver authored by * HeungJun Kim <riv
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Debski <k.debski@samsung.com>
Copyright (c) 2010-2011 Samsung Electronics Co., Ltd. * http://www.samsung.com/ * * Mixer register header file for Samsung Mixer driver
Copyright (c) 2010-2011 Samsung Electronics Co., Ltd. * http://www.samsung.com/ * * Video processor register header file for Samsung
Mixer driver
Copyright (C) 2010-2011 ST-Ericsson * * Implements u8500 semaphore handling for protocol 1, no interrupts.
Copyright (C) 2010-2011 ST Microelectronics
Copyright (C) 2010-2011 ST Microelectronics * /#ifndef _PLATFORM_DATA_DMA_DW_H#define _PLATFORM_DATA_DMA_DW_H
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to use the linux CEC framework: *
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<asm/thread_info.h>#include <asm/page.h>
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driver,
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Copyright (C) 2010-2012 Matthias Fuchs <matthias.fuchs@esd.eu>, esd gmbh */#include <linux/signal.h>#include <linux/slab.h>#include
<linux/module.h>#include <linux/netdevice.h>#include <linux/us

```



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Copyright (C) 2010 - 2012 Samsung Electronics Co., Ltd.\*/#define pr\_fmt(fmt) "%s:%d " fmt, \_\_func\_\_, \_\_LINE\_\_

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Copyright (C) 2010-2012 Stephane Grosjean <s.grosjean@peak-system.com> \* \* CAN driver for PEAK-System PCAN-PC Card \* Derived from the PCAN project file driver/src/pcan\_pccard.c

Copyright (C) 2010-2012 Stephane Grosjean <s.grosjean@peak-system.com> \* \* Many thanks to Klaus Hitschler <klaus.hitschler@gmx.de>

Copyright (C) 2010-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: July 10th 2012 \* -Decoupled the driver from arch/arc \* +Using platform\_get\_resource() for irq/membase (thx to bfin\_uart.c

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\_\_ARCH\_ARM\_MACH\_OMAP2\_OMAP\_HWMOD\_COMMON\_DATA\_H#define \_\_ARCH\_ARM\_MACH\_OMAP2\_OMAP\_HWMOD\_COMMON\_DATA\_H

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Copyright (C) 2010-2013 Felix Fietkau <nbd@openwrt.org> \*/#include <linux/netdevice.h>#include <linux/types.h>#include <linux/skbuff.h>#include <linux/debugfs.h>#include <linux/random.h>#includ

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 <linux/hrtimer.h>#include <linux/export.h>#include <linux/pm\_runtime.h>#i  
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 Copyright (c) 2010 Alexander Sverdlin <subaparts@yandex.ru> \* \* This driver support CS4271 codec being master or slave, working \* in control port  
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 Copyright (C) 2010 Alexey Charkov <alchark@gmail.com> \* \* Based on rtc-pxa.c  
 Copyright (C) 2010 Alexey Charkov <alchark@gmail.com> \* \* Based on skeletonfb.c and pxaafb.c  
 Copyright (C) 2010 Alexey Charkov <alchark@gmail.com> \* Moved from arch/arm/mach-vt8500/include/mach/debug-macro.S \* Minor changes for  
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 <linux/init.h>#include <asm/pci-bridge.h>#include <asm/ppc4  
 Copyright (c) 2010, Applied Micro Circuits Corporation \* Authors: Tirumala R Marri <tmarri@apm.com> \* Feng Kan <fkan@apm.com> \*/  
 Copyright (C) 2010, Applied Micro Circuits Corporation \* Victor Gallardo (vgallardo@apm.com) \* \* Based on arch/powerpc/platforms/44x/idle.c:  
 Copyright (C) 2010, ARM Ltd., Will Deacon <will.deacon@arm.com> \* \* Based on the previous xscale OProfile code.  
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 the x86 code.  
 Copyright (C) 2010 ARM Ltd. \* Written by Catalin Marinas <catalin.marinas@arm.com> \*/  
 Copyright (C) 2010 ARM Ltd. \* Written by Catalin Marinas <catalin.marinas@arm.com> \*/#include <linux/bug.h>#include <linux/smp.h>#include  
 <asm/outercache.h>  
 Copyright (C) 2010, Arnaldo Carvalho de Melo <acme@redhat.com> \*/#include <sys/types.h>#include <sys/time.h>#include <time.h>#include  
 <dirent.h>#include <errno.h>#include <unistd.h>#include "b  
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 Copyright (c) 2010 Arrow Electronics, Inc.  
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```

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Copyright (c) 2010 Broadcom Corporation */#include <linux/kernel.h>#include <linux/delay.h>#include <linux/bitops.h>
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<linux/spinlock.h>#include <linux/skbuff.h>#include <linux/netdevi
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Copyright (c) 2010 Cavium Networks, Inc. */#ifndef __ASM_MIPS_JUMP_LABEL_H#define __ASM_MIPS_JUMP_LABEL_H
Copyright (C) 2010 Cavium Networks, Inc. */#include <linux/dma-direct.h>#include <linux/memblock.h>#include <linux/swiotlb.h>#include
<linux/types.h>#include <linux/init.h>#include <linux/mm.h>
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Copyright (c) 2010 Cisco Systems, Inc. * * Portions based on tcm_loop_fabric_scsi.c and libfc/fc_fcp.c
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Copyright (C) 2010 CompuLab Ltd. \* \* 2010-13-07: Igor Grinberg <grinberg@compulab.co.il> \* initial version: pxa310 USB Host mode support  
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Copyright (C) 2010 Compulab Ltd. \* Author: Mike Rapoport <mike@compulab.co.il> \* \* Based on da903x  
Copyright (c) 2010, CompuLab, Ltd. \* Author: Mike Rapoport <mike@compulab.co.il> \* \* Based on NVIDIA PCIe driver  
Copyright (C) 2010 CompuLab Ltd. \* \* Igor Grinberg <grinberg@compulab.co.il>  
Copyright (C) 2010 CompuLab, Ltd. \* Konstantin Sinyuk <kostyas@compulab.co.il> \* \* Based on Marvell DB-MV88AP510-BP Development Board Setup  
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of Code 2010 \* program. Jeremy Fitzhardinge from Ci  
Copyright (c) 2010 Daniel Mack <daniel@caiaq.de>  
Copyright (C) 2010 Dan Williams <dcbw@redhat.com> \* \* Devices listed here usually provide a CDC ACM port on which normal modem \* AT commands  
and PPP can be used. But when that port is in-use by PP  
Copyright (c) 2010 David Chinner.  
Copyright (c) 2010 David H \* \* This driver receives TX data and passes it back as RX data, \* which is useful for (scripted) debugging of rc-core without \*  
having to use actual hardware.  
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<linux/ctype.h>#include <linux/delay.h>#include <linux/firmware.h>  
Copyright (C) 2010 Devin Heitmueller <dheitmueller@kernellabs.com> \*/#include <linux/kernel.h>#include <linux/errno.h>#include  
<linux/slab.h>#include <linux/mm.h>#include <linux/usb.h>  
Copyright (C) 2010 Devin Heitmueller <dheitmueller@kernellabs.com> \*/#include <linux/kernel.h>#include <linux/errno.h>#include  
<linux/slab.h>#include <linux/module.h>#include <linux/mm.h>#inclu  
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[http://github.com/projectgus/kernel\\_wm8505/blob/wm8505\\_2.6.29/](http://github.com/projectgus/kernel_wm8505/blob/wm8505_2.6.29/) \* drivers/video/wmt/r  
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```

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Copyright (c) 2010, Google Inc. * * Original authors: Code Aurora Forum
Copyright (C) 2010 Google, Inc. * Rewritten by David Rientjes * * The routines in this file are used to kill a process when * we're seriously out of
memory. This gets called from __alloc_pages
Copyright (C) 2010 Gra * * based on twl4030_bci_battery.c by TI
Copyright (c) 2010 Grant Erickson <marathon96@gmail.com> * * Also based on pwm-samsung.c
Copyright (c) 2010 Gregoire Baron <baronchon@n7mm.org> */
Copyright (C) 2010, Greg Ungerer (gerg@snapgear.com) */
Copyright (C) 2010 GUAN Xue-tao */#include <linux/kernel.h>#include <linux/errno.h>#include <linux/swap.h>#include <linux/init.h>#include
<linux/memblock.h>#include <linux/mman.h>#include <lin
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Copyright (C) 2010, Guennadi Liakhovetski <g.liakhovetski@gmx.de> */
Copyright (C) 2010, Guennadi Liakhovetski <g.liakhovetski@gmx.de> * * Based on Omnivision OV7670 Camera Driver
Copyright (C) 2010 Guennadi Liakhovetski <g.liakhovetski@gmx.de> */#ifndef CPU_DMA_REGISTER_H#define CPU_DMA_REGISTER_H
Copyright (C) 2010 Guennadi Liakhovetski <g.liakhovetski@gmx.de> */#ifndef SH_DMA_H#define SH_DMA_H
Copyright (C) 2010, Guennadi Liakhovetski <g.liakhovetski@gmx.de> */#ifndef SH_VOUE_H#define SH_VOUE_H
Copyright (C) 2010, Guennadi Liakhovetski <g.liakhovetski@gmx.de> */#include <linux/clock.h>#include <linux/delay.h>#include <linux/err.h>#include
<linux/hdmi.h>#include <linux/irq.h>#include <li
Copyright (C) 2010, Guennadi Liakhovetski <g.liakhovetski@gmx.de> * * Partially inspired by the IMX074 driver from the Android / MSM tree
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Copyright (C) 2010 Hans de Goede <hdegoede@redhat.com> * * Based on the usbvideo v4l1 konicawc driver which is: *
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Copyright (C) 2010 Henrik Rydberg <rydberg@euromail.se> * * Based on hdaps.c driver:
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Copyright (c) 2010 Herbert Xu <herbert@gondor.apana.org.au> */
Copyright (c) 2010 Herbert Xu <herbert@gondor.apana.org.au> * * The following concept of the memory management is used: *
Copyright (c) 2010 H Hartley Sweeten <hsweeten@visionengravers.com> * * This driver controls the pulse width modulated brightness control output, *
BRIGHT, on the Cirrus EP9307, EP9312, and EP9315
Copyright (C) 2010 Hitoshi Mitake <mitake@dcl.info.waseda.ac.jp> */#include <linux/init.h>#include <linux/irq.h>#include
<linux/regulator/fixd.h>#include <linux/regulator/machine.h>#include <l

```

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 Copyright (C) 2010 Igor M. Liplianin <liplianin@netup.ru> \*/  
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 Copyright (C) 2010 Intel Corporation <tony.luck@intel.com> \* \* This code is the generic layer to export data records from platform \* level persistent  
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 Copyright(C) 2010 Jarod Wilson <jarod@wilsonet.com> \* Portions based on the original lirc\_imon driver,  
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 substantially rewritten to support correct signal \* gaps an  
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 Copyright (C) 2010 Jean-Francois Moine (http://moinejf.free.fr) \* \*Notes: \* t613 + tas5130A \* \* Focus to light do not balance well as in win. \*  
 Quality in win is not good, but its kinda better.

```

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useful if you do not have access to a full IrDA stack
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source files. Please see the usb/serial files for * i
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Copyright (C) 2010 John Crispin <john@phrozen.org> */#ifndef _LANTIQ_H_#define _LANTIQ_H_
Copyright (C) 2010 John Crispin <john@phrozen.org> */#include <linux/io.h>#include <linux/export.h>#include <linux/init.h>#include
<linux/kernel.h>#include <linux/types.h>#include <linux/clk.h>
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SSE or not. It is not called directly instead this * file i
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allocation is done in buckets, which should match
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<linux/io.h>#include <linux/clockchips.h>#include <linux/clo
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__ASM_ARCH_NANOENGINE_H
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<linux/pci.h>

```



```

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almost re-written
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Copyright (C) 2010 Maurus Cuelenaere */
Copyright (C) 2010 - Maxim Levitsky * driver for Ricoh memstick readers */
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Copyright (C) 2010 Miguel Gaio <miguel.gaio@efixo.com> */
Copyright (c) 2010 Mika Laitio <lamikr@pilppa.org> * * This driver will read and write the value of 4 counters to w1_slave file in * sys filesystem. *
Inspired by the w1_therm and w1_ds2431 driver
Copyright (c) 2010 Mika Westerberg * * Based on s3c-ac97 ASoC driver by Jaswinder Singh.
Copyright (c) 2010 Mika Westerberg * * Based on snappercl15 machine driver by Ryan Mallon.
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Copyright (C) 2010 MIPS Technologies, Inc. * Author: Deng-Cheng Zhu * * This code is based on the implementation for ARM, which is in turn * based
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<mikecyr@linux.vnet.ibm.com>
Copyright (c) 2010 Nikolai Kondrashov */
Copyright (C) 2010 NISHIMOTO Hiroki */#include <linux/io.h>#include <linux/kernel.h>#include <linux/module.h>#include <linux/pinctrl/pinconf-

```

```

generic.h>#include <linux/regulator/driver.h>#includ
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<linux/pci.h>#include <linux/sh_intc.h>#include
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*/#include <linux/kernel.h>#include <linux/init.h>
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* * NOTE: This timer is not the same timer as the old O
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<linux/kernel.h>#include <linux/init.h>#include <linux/reboot.h>
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Copyright (C) 2010 OKI SEMICONDUCTOR Co., LTD. */#include <linux/module.h>#include <linux/kernel.h>#include <linux/slab.h>#include
<linux/pci.h>#include <linux/gpio/driver.h>#include <linux/int
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```

```

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Copyright (C) 2010 OMICRON electronics GmbH */#ifndef _PTP_PRIVATE_H_#define _PTP_PRIVATE_H_
Copyright (C) 2010 OMICRON electronics GmbH */#include <linux/capability.h>#include <linux/slab.h>
Copyright (C) 2010 OMICRON electronics GmbH */#include <linux/device.h>#include <linux/err.h>#include <linux/gpio.h>#include
<linux/init.h>#include <linux/interrupt.h>#include <linux/io.h>#include
Copyright (C) 2010 OMICRON electronics GmbH */#include <linux/device.h>#include <linux/export.h>#include <linux/file.h>#include <linux/posix-
clock.h>#include <linux/slab.h>#include <linux/sysca
Copyright (C) 2010 OMICRON electronics GmbH */#include <linux/errqueue.h>#include <linux/phy.h>#include <linux/ptp_classify.h>#include
<linux/skbuff.h>#include <linux/export.h>
Copyright (C) 2010 OMICRON electronics GmbH */#include <linux/idr.h>#include <linux/device.h>#include <linux/err.h>#include
<linux/init.h>#include <linux/kernel.h>#include <linux/module.h>#include
Copyright (C) 2010 OMICRON electronics GmbH */#include <linux/module.h>#include <linux/posix-clock.h>#include <linux/poll.h>#include
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<linux/gfp.h>#include <linux/skbuff.h>#include <linux/netfilte
Copyright (C) 2010, Paul Cercueil <paul@crapouillou.net> * JZ4740 SoC RTC driver */
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Copyright (C) 2010 Paul Mundt
Copyright (C) 2010 Paul Mundt * * Based on ohci-sh.c and ehci-atmel.c.
Copyright (C) 2010 Paul Mundt */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt
Copyright (C) 2010 Paul Mundt */#define pr_fmt(fmt) "PCI: " fmt
Copyright (C) 2010 Paul Mundt */#include <linux/bug.h>#include <linux/init.h>#include <linux/kernel.h>#include <linux/ioport.h>#include
<cpu/pfc.h>
Copyright (C) 2010 Paul Mundt */#include <linux/init.h>#include <linux/interrupt.h>#include <linux/gpio/driver.h>#include <linux/irq.h>#include
<linux/kernel.h>#include <linux/spinlock.h>#include
Copyright (C) 2010 Paul Mundt */#include <linux/init.h>#include <linux/io.h>#include <linux/bcd.h>#include <mach/fpga.h>#include <linux/sizes.h>
Copyright (C) 2010 Paul Mundt */#include <linux/init.h>#include <linux/kernel.h>#include <cpu/shx3.h>
Copyright (C) 2010 Paul Mundt */#include <linux/init.h>#include <linux/kernel.h>#include <linux/clock.h>#include <linux/io.h>#include
<linux/clkdev.h>#include <asm/clock.h>#include <asm/freq.h>
Copyright (C) 2010 Paul Mundt */#include <linux/init.h>#include <linux/kernel.h>#include <linux/string.h>#include <mach/fpga.h>
Copyright (C) 2010 Paul Mundt */#include <linux/init.h>#include <linux/platform_device.h>#include <linux/io.h>#include
<linux/regulator/fixd.h>#include <linux/regulator/machine.h>#include <li
Copyright (C) 2010 Paul Mundt */#include <linux/irq.h>#include <mach/fpga.h>#include <mach/irq.h>
Copyright (C) 2010 Paul Mundt * * Modelled after arch/powerpc/kernel/irq.c.
Copyright (c) 2010 Paul Thomas <pthomas8589@gmail.com> * * You need to have something like this in struct spi_board_info
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Copyright (C) 2010 Pengutronix, Marc Kleine-Budde <kernel@pengutronix.de> */#include "../hardware.h"#include "devices-common.h"
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Copyright (C) 2010 Pengutronix * Uwe Kleine-Koenig <u.kleine-koenig@pengutronix.de> */#include "devices/devices-common.h"
Copyright (C) 2010 Pengutronix * Uwe Kleine-Koenig <u.kleine-koenig@pengutronix.de> */#include "../hardware.h"#include "devices-common.h"
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Copyright (C) 2010 Pengutronix * Uwe Kleine-Koenig <u.kleine-koenig@pengutronix.de> */#include <linux/dma-mapping.h>#include <linux/sizes.h>
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Copyright (c) 2010 Red Hat Inc. * Author : Dave Airlie <airlied@redhat.com> ** ATPX support for both Intel/ATI
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<dh09.lee@samsung.com> */
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<jy0922.shim@samsung.com>
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 <linux/ioport.h>#include <linux/device.h>#include <linux/io.h>#include  
 Copyright (C) 2010 ST-Ericsson SA \* \* pl08x information required by platform code  
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 <linux/delay.h>  
 Copyright (C) 2010 Tanguy Bouzeloc <tanguy.bouzeloc@efixo.com> \*/  
 Copyright (C) 2010 Tejun Heo <tj@kernel.org> \* \* Chunks are allocated as a contiguous kernel memory using gfp \* allocation. This is to be  
 used on nommu architectures.  
 Copyright (C) 2010 Tejun Heo <tj@kernel.org> \* \* Chunks are mapped into vmalloc areas and populated page by page. \* This is the default  
 chunk allocator.  
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 <linux/init.h>#include <linux/kthread.h>#include <linux/ex  
 Copyright (C) 2010 Tejun Heo <tj@kernel.org> \* \* This is the generic async execution mechanism. Work items as are \* executed in process  
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 <linux/platform\_device.h>#include <linux/delay.h>#include <lin  
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Copyright (C) 2010 Texas Instruments Inc */#include <linux/kernel.h>#include <linux/init.h>#include <linux/module.h>#include
<linux/errno.h>#include <linux/fs.h>#include <linux/string.h>#include
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<linux/init.h>#include <linux/ctype.h>#include <linux/del
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<linux/module.h>#include <linux/errno.h>#include <linux/interrupt
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__ARCH_ARM_PLAT_OMAP_INCLUDE_PLAT_L3_3XXX_H#define __ARCH_ARM_PLAT_OMAP_INCLUDE_PLAT_L3_3XXX_H
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<linux/percpu.h>#include <linux/input.h>
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buffer setup from sysfs and starts a short capture
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| (r))#define SMIAPP_REG_MK_U16(r) ((SMIAPP_REG_16BI
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<asm/asm.h>
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driver began life based an an old version of th
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Copyright (c) 2011 Cambridge Silicon Radio Limited, a CSR plc group company. */#include <linux/bitops.h>#include <linux/log2.h>#include
<linux/hrtimer.h>struct sirfsoc_uart_param { const char *u

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<linux/io.h>#include <linux/irqchip.h>#include <linux/irqchip/
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Copyright (C) 2011 LAPIS Semiconductor Co., Ltd. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/kernel.h>#include
<linux/module.h>#include <linux/pci.h>#include <linux/delay.h>#in
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SUPPORT_SYSRQ#endif#include <linux/kernel.h>#include <linux/s
Copyright (C) 2011 LAPIS Semiconductor Co., Ltd. */#include <linux/gpio/driver.h>#include <linux/interrupt.h>#include <linux/irq.h>#include
<linux/kernel.h>#include <linux/module.h>#include <li

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<linux/of\_address.h>#include <linux/of\_platform.h>  
Copyright (C) 2011 Magnus Damm \*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/smp.h>#include <linux/spinlock.h>#include  
<linux/io.h>#include <linux/delay.h>#include <linux/  
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<linux/delay.h>#include <linux/clk.h>#include <linux/s  
Copyright (C) 2011 Marvell International Ltd. \*/#include <linux/module.h>#include <linux/init.h>#include <linux/platform\_device.h>#include  
<linux/slab.h>#include <linux/dma-mapping.h>#include <  
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Copyright (C) 2011 Mike Christie \*/#include <linux/slab.h>#include <linux/blk-mq.h>#include <linux/delay.h>#include <linux/scatterlist.h>#include  
<linux/bsg-lib.h>#include <linux/export.h>#i  
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Copyright (C) 2011 MIPS Technologies, Inc. \*/#include <linux/errno.h>#include <asm/asm.h>#include <asm/asm-offsets.h>#include  
<asm/export.h>#include <asm/regdef.h>  
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 Copyright (c) 2011, Novell Inc. \*/#include <linux/init.h>#include <linux/module.h>#include <linux/device.h>#include <linux/completion.h>#include  
 <linux/input.h>#include <linux/hid.h>#include <  
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 Astaro.  
 Copyright (C) 2011 Paul Gortmaker. \* \* This isn't a full driver; it just provides an alternate IRQ \* handler to deal with an errata. Everything else is just \*  
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 Copyright (C) 2011 Pengutronix \* Uwe Kleine-Koenig <u.kleine-koenig@pengutronix.de> \*/#include <linux/err.h>#include <linux/leds.h>#include  
 <linux/platform\_device.h>#include <linux/slab.h>  
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 Copyright (C) 2011 Rafael J. Wysocki <rjw@sisk.pl>, Renesas Electronics Corp. \*/#define pr\_fmt(fmt) "PM: " fmt

```

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<linux/export.h>#include <linux/slab.h>#include <linux/
Copyright (C) 2011 Rafael J. Wysocki <rjw@sisk.pl>, Renesas Electronics Corp. */#include <linux/kernel.h>#include <linux/pm_domain.h>#include
<linux/pm_qos.h>#include <linux/hrtimer.h>#include <
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Copyright (C) 2011 Red Hat, Inc., Frederic Weisbecker <fweisbec@redhat.com> * * Selftests for breakpoints (and more generally the do_debug() path)
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RENESAS_USB_FIFO_H
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<linux/slab.h>#include "common.h"#include "pipe.h"
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<linux/list.h>#include <linux/module.h>#include <linux/platform_
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Copyright (C) 2011 Richard Weinberger <richrd@nod.at> * Mostly copied from arch/x86/lib/delay.c */
Copyright (C) 2011 Richard Weinberger <richrd@nod.at> * * This vDSO turns all calls into a syscall so that UML can trap them.
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<linux/init.h>#include <linux/err.h>#include <linux/slab.h>#include <
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control and multiple chargers and to * monitor charging
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and control charger * during suspend-to-mem. * Charger
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PCI endpoint functions. This driver simply * r
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Copyright (C) 2011 Tejun Heo <tj@kernel.org> * * REQ_{PREFLUSH|FUA} requests are decomposed to sequences consisted of three * optional
steps - PREFLUSH, DATA and POSTFLUSH - according to the reque
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_ASM_C6X_CHECKSUM_H
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```

```

_ASM_C6X_UACCESS_H
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<linux/ctype.h>#include <linux/etherdevice.h>#include <asm/setup
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<linux/of_address.h>#include <linux/io.h>
Copyright (C) 2011 Texas Instruments Incorporated * Author: Mark Salter <msalter@redhat.com> */#include <linux/of.h>#include
<linux/of_address.h>#include <linux/io.h>#include <asm/soc.h>#include
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Copyright (C) 2011 Texas Instruments Incorporated - http://www.ti.com/ * * Modified from the original mach-omap/omap2/board-generic.c did by Paul
* to support the OMAP2+ device tree boards with an
Copyright (c) 2011 Texas Instruments Incorporated * * Some ideas are from old omap-sha1-md5.c driver.
Copyright (C) 2011 Texas Instruments, Inc. * Paul Walmsley */
Copyright (C) 2011 Texas Instruments, Inc. * Paul Walmsley */#ifndef __ARCH_ASM_MACH_OMAP2_PRMINST44XX_H#define
__ARCH_ASM_MACH_OMAP2_PRMINST44XX_H
Copyright (C) 2011 Texas Instruments, Inc. * Rajendra Nayak */
Copyright (C) 2011 Texas Instruments, Inc. * Rajendra Nayak <rnyak@ti.com> */
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Copyright (C) 2011 Texas Instruments, Inc. * Santosh Shilimkar <santosh.shilimkar@ti.com> */#ifndef
OMAP_ARCH_OMAP4_SAR_LAYOUT_H#define OMAP_ARCH_OMAP4_SAR_LAYOUT_H
Copyright (C) 2011 Texas Instruments, Inc. * Santosh Shilimkar <santosh.shilimkar@ti.com> */#ifndef OMAP_ARCH_WAKEUPGEN_H#define
OMAP_ARCH_WAKEUPGEN_H
Copyright (C) 2011 Texas Instruments, Inc. * Santosh Shilimkar <santosh.shilimkar@ti.com> * * OMAP4430 MPUSS mainly consists of dual Cortex-
A9 with per-CPU * Local timer and Watchdog, GIC, SCU, PL
Copyright (C) 2011 Texas Instruments, Inc. * Tero Kristo <t-kristo@ti.com> * * For historical purposes, the API used to configure the PRM * interrupt
handler refers to it as the "PRCM interrupt."
Copyright (C) 2011 Texas Instruments, Inc. * * This module exposes the interface to kernel space for specifying * per-device PM QoS dependencies. It
provides infrastructure for registration * of:
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Copyright (C) 2011 Thomas Chou <thomas@wytron.com.tw> * * Based on spi_s3c24xx.c, which is:
Copyright (C) 2011, Thomas Gleixner */#include <linux/io.h>#include <linux/irq.h>#include <linux/slab.h>#include <linux/export.h>#include
<linux/irqdomain.h>#include <linux/interrupt.h>#includ
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```

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Copyright (C) 2011 Tobias Klausner <tklausner@distanz.ch> ** Based on the code posted by Kazuyasu on the Altera Forum at:
Copyright (C) 2011 Tobias Waldvogel <tobias.waldvogel@gmail.com> */
Copyright (c) 2011, Tom Herbert <therbert@google.com> */#include <linux/types.h>#include <linux/kernel.h>#include <linux/jiffies.h>#include
<linux/dynamic_queue_limits.h>#include <linux/compiler
Copyright (c) 2011, Tom Herbert <therbert@google.com> ** This header file contains the definitions for dynamic queue limits (dql). * dql would be
used in conjunction with a producer/consumer type
Copyright (c) 2011 Tony Prisk <linux@prisktech.co.nz> ** This file is based on uhci-glib.c
Copyright (c) 2011 Unixphere */
Copyright (c) 2011 Unixphere ** This driver adds support for Bosch Sensortec's digital acceleration * sensors BMA150 and SMB380. * The SMB380 is
fully compatible with BMA150 and only differs in p
Copyright (c) 2011 Unixphere ** This driver provides the core support for a single RMI4-based device.
Copyright (c) 2011 Vista Silicon S.L. * Javier Martin <javier.martin@vista-silicon.com> */#include <linux/module.h>#include <linux/clk.h>#include
<linux/slab.h>#include <linux/interrupt.h>#incl
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Copyright (C) 2011 Weinmann Medical GmbH * Author: Nikolaus Voss <n.voss@weinmann.de> ** Evolved from original work by:
Copyright (c) 2011 Werner Almesberger <werner@almesberger.net> ** USB initialization is
Copyright (c) 2011 Wind River Systems, Inc. ** Author: Anders Wallin <anders.wallin@windriver.com>
Copyright (C) 2011 Wind River Systems ** The development page for this driver is located at * http://sourceforge.net/projects/acpi4asus/
Copyright (C) 2011 Wind River Systems, * written by Ralf Baechle <ralf@linux-mips.org> */#ifndef _ASM_REGDEF_H#define _ASM_REGDEF_H
Copyright (C) 2011 Wind River Systems, * written by Ralf Baechle (ralf@linux-mips.org) */#include <linux/bug.h>#include <linux/kernel.h>#include
<linux/mm.h>#include <linux/memblock.h>#include
Copyright (C) 2011 Wind River Systems, * written by Ralf Baechle <ralf@linux-mips.org> */#include <linux/compiler.h>#include <linux/elf-
randomize.h>#include <linux/errno.h>#include <linux/mm.h
Copyright (C) 2011 Wistron Co.Ltd * Joseph Lai <joseph_lai@wistron.com> and trimmed down by * Alan Cox <alan@linux.intel.com> in turn based on
bma023.c. * Device behaviour based on a misc driver po
Copyright (C) 2011 Wolfram Sang, Pengutronix e.K. */
Copyright (C) 2011 Wolfram Sang, Pengutronix e.K. */#ifndef __LINUX_STMP3XXX_RTC_WDT_H#define __LINUX_STMP3XXX_RTC_WDT_H
Copyright (C) 2011 Xilinx */
Copyright (C) 2011 Xilinx */#define UART_CR_OFFSET 0x00 /* Control Register [8:0] */#define UART_SR_OFFSET 0x2C /* Channel
Status [11:0] */#define UART_FIFO_OFFSET0x30 /* FIFO [15:0] or [7:0
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Vinod Koul <vinod.koul@intel.com> */#ifndef _PLATFORM_SST
Copyright (C) 2012-14 Renesas Solutions Corp. * Kuninori Morimoto <kuninori.morimoto.gx@renesas.com> ** This file is based on the
drivers/i2c/busses/i2c-sh7760.c
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Copyright: (C) 2012-2013 Linaro Limited \* \* Code to handle secondary CPU bringup and hotplug for the cluster power API.  
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used here.  
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Copyright: (C) 2012-2013 Linaro Limited \* \* This algorithm is described in more detail in \* Documentation/arm/vlocks.rst.  
Copyright (c) 2012-2013 Linaro Ltd. \* \* Author: Haojian Zhuang <haojian.zhuang@linaro.org>  
Copyright (C) 2012-2013 Linus Walleij \*/#include <linux/clk-provider.h>#include <linux/clkdev.h>#include <linux/err.h>#include <linux/io.h>#include  
<linux/platform\_data/clk-integrator.h>  
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 fmt, \_\_func\_\_, \_\_LINE\_\_  
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 Roger Quadros <rogerq@ti.com> \*/#include <linux/kernel.h>#  
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 Copyright (C) 2012-2013 Tieto Poland \*/  
 Copyright (C) 2012-2013 Uwe Kleine-Koenig for Pengutronix \*/#include <linux/kernel.h>#include <linux/io.h>#include <linux/spi/spi.h>#include  
 <linux/spi/spi\_bitbang.h>#include <linux/gpio.h>#inc  
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 Copyright (C) 2012-2013 Xilinx \* \* CPU idle support for Xilinx Zynq  
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 Copyright (c) 2012-2013 Xilinx \*/#include <linux/linkage.h>#include <linux/init.h>#include <asm/assembler.h>  
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<linux/of_platform.h>#include <linux/reboot.h>#include <lin
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Copyright (c) 2012-2016 Mentor Graphics Inc. */#include <linux/delay.h>#include <linux/fs.h>#include <linux/module.h>#include
<linux/of_platform.h>#include <linux/pinctrl/consumer.h>#include <l
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Copyright (c) 2012-2016 Synaptics Incorporated */#include <linux/input.h>#include <linux/input/mt.h>#include <linux/rmi.h>#include
"rmi_driver.h"#include "rmi_2d_sensor.h"
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Copyright (C) 2012-2017 Cavium Inc. */#include <linux/dma-mapping.h>#include <linux/gpio/consumer.h>#include <linux/interrupt.h>#include
<linux/mmc/mmc.h>#include <linux/mmc/slot-gpio.h>#includ
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<linux/iopoll.h>#include <linux/irq.h>#include <linux/module.h>#i
Copyright (c) 2012-2017 Mentor Graphics Inc. */#include <linux/delay.h>#include <linux/interrupt.h>#include <linux/module.h>#include
<linux/sched.h>#include <linux/slab.h>#include <linux/spinlo
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information useful to userland.
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interrupt remapping code to * the x86 interrupt man
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different interrupt * remapping drivers but with
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Copyright (c) 2012 Aeroflex Gaisler AB. * Author: Andreas Larsson <andreas@gaisler.com> */#include <linux/delay.h>#include <linux/dma-
mapping.h>#include <linux/fsl_devices.h>#include <linux/gpio
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Copyright (c) 2012, Alexander Holler. */#include <linux/device.h>#include <linux/platform_device.h>#include <linux/module.h>#include <linux/hid-
sensor-hub.h>#include <linux/iio/iio.h>#include <
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Copyright (C) 2012 Altera Corporation */#include <linux/delay.h>#include <linux/init.h>#include <linux/smp.h>#include <linux/io.h>#include
<linux/of.h>#include <linux/of_address.h>
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Copyright (C) 2012 Andrew Lunn */#include <linux/kernel.h>#include <linux/init.h>#include <linux/platform_device.h>#include
<linux/of_platform.h>#include <linux/module.h>#include <linux/reboot.
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Copyright (C) 2012 ARM Limited * Author: Will Deacon <will.deacon@arm.com> * * This code is based heavily on the ARMv7 perf event code.
Copyright (C) 2012 ARM Limited */#ifndef __ASM_VDSO_DATAPAGE_H#define __ASM_VDSO_DATAPAGE_H
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Copyright (C) 2012 ARM Ltd. */#ifndef CONFIG_COMPAT#define __ARCH_WANT_COMPAT_STAT64#define __ARCH_WANT_SYS_GETHOSTNAME#define
__ARCH_WANT_SYS_PAUSE#define __ARCH_WANT_SYS_GETPRGRP#define __ARCH_
Copyright (C) 2012 ARM Ltd. */#ifndef __ASM_ATOMIC_H#define __ASM_ATOMIC_H
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Copyright (C) 2012 ARM Ltd. */#ifndef __ASM_CACHEFLUSH_H#define __ASM_CACHEFLUSH_H
Copyright (C) 2012 ARM Ltd. */#ifndef __ASM_CACHE_H#define __ASM_CACHE_H
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Copyright (C) 2012 ARM Ltd. */#ifndef __ASM_COMPAT_H#define __ASM_COMPAT_H#define CONFIG_COMPAT
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Copyright (C) 2012 ARM Ltd. */#ifndef __ASM_FP_H#define __ASM_FP_H
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Copyright (C) 2012 ARM Ltd. /*#ifndef __ASSEMBLY__error "Only include this from assembly code"#endif
Copyright (C) 2012 ARM Ltd. /*#ifndef __CLKSOURCE_ARM_ARCH_TIMER_H#define __CLKSOURCE_ARM_ARCH_TIMER_H
Copyright (C) 2012 ARM Ltd. /*#include <linux/kernel.h>#include <linux/export.h>#include <linux/ftrace.h>#include <linux/kprobes.h>#include
<linux/sched.h>#include <linux/sched/debug.h>#include
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Copyright (C) 2012 Benjamin Herrenschmidt, IBM Corp. /*#include <linux/types.h>#include <linux/errno.h>#include <linux/kernel.h>#include
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 Copyright (c) 2012 Broadcom Corporation \*/#include <linux/debugfs.h>#include <linux/netdevice.h>#include <linux/module.h>#include  
 <linux/devcoredump.h>  
 Copyright (c) 2012 Broadcom Corporation \*/#include <linux/netdevice.h>  
 Copyright (c) 2012 Broadcom Corporation \*/#include <linux/slab.h>#include <linux/netdevice.h>#include <linux/etherdevice.h>#include  
 <linux/rtnetlink.h>#include <net/cfg80211.h>  
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 <linux/nfs4\_mount.h>#include <linux/nfs\_fs.h>#include "delegation.h"#include  
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<linux/interrupt.h>#include <linux/irq.h>#include <linux/ke  
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<linux/interrupt.h>#include <linux/irq.h>#include <linux/sl  
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<linux/iio/buffer.h>#include <linux/regmap.h>#in



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<asm/mach/arch.h>#include <asm/mach/map.h>  
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<linux/io.h>#include <linux/delay.h>#include <asm/sm  
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<sound/pcm.h>#include <sound/soc.h>#include <sound/jack.h>  
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mapping.h>#include <linux/platform\_device.h>#include "mmpf  
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<linux/ioport.h>#include <linux/uaccess.h>#inclu  
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<linux/kernel.h>#include <linux/ioport.h>#include <cpu/pfc.h>  
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_LINUX_BALLOON_COMPACTSION_H#include <linux/pagemap.h>#include <linux/page-flags.h
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<linux/export.h>#include <linux/balloon_compaction.h>
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__LINUX_OF_GRAPH_H
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<linux/interrupt.h>#include <linux/input.h>#include <li
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Copyright (C) 2012 Renesas Electronics Europe Ltd */#include <linux/platform_device.h>#include <linux/init.h>#include <linux/serial.h>#include
<linux/serial_sci.h>#include <linux/usb/r8a66597.h
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<linux/input/mt.h>#include <linux/interrupt.h>#incl
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Copyright (C) 2012 Russell King */#ifndef ARMADA_FB_H#define ARMADA_FB_H
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<linux/mpage.h>#include <linux/writeback.h>#include
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"f2fs.h"#include "node.h"#include "segment.h"
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<linux/bio.h>#include <linux/blkdev.h>#include <
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<linux/buffer_head.h>#include <linux/backing-dev.
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<linux/buffer_head.h>#include <linux/mpage.h>#in
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<linux/mpage.h>#include <linux/backing-dev.h>#in
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<linux/pagemap.h>#include <linux/sched.h>#includ
Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */#include <linux/fs.h>#include <linux/f2fs_fs.h>#include
<linux/sched/signal.h>#include <linux/unicode.h>
Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */#include <linux/fs.h>#include <linux/f2fs_fs.h>#include
<linux/stat.h>#include <linux/buffer_head.h>#inc
Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */#include <linux/fs.h>#include <linux/module.h>#include
<linux/backing-dev.h>#include <linux/init.h>#incl
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(Rotator) driver
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*/#define START_NID(nid) (((nid) / NAT_ENTR
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<linux/clk.h>#include <linux/io.h>#include <linux/clkdev.h>#include
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Copyright (C) 2012 Texas Instruments\*/#include <linux/module.h>#include <linux/delay.h>#include <linux/i2c.h>#include <linux/leds.h>#include



```

<linux/slab.h>#include <linux/platform_device.h>#in
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<linux/err.h>#include <linux/delay.h>#include <
    Copyright (C) 2012 Texas Instruments*/#include <linux/module.h>#include <linux/slab.h>#include <linux/i2c.h>#include <linux/leds.h>#include
<linux/backlight.h>#include <linux/err.h>#include <l
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ARCH_ARM_MACH_OMAP2_HDQ1W_H
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initdata for multi-OMAP kernels
    Copyright (C) 2012 Texas Instruments, Inc. * Paul Walmsley * * XXX This code should eventually be moved to a CM driver.
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    Copyright (C) 2012 Texas Instruments, Inc. * Written by Tony Lindgren <tony@atomide.com> * * Modified for clock framework by Paul Mundt
<paul.mundt@nokia.com>.
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    Copyright (C) 2012 Texas Instruments * Santosh Shilimkar <santosh.shilimkar@ti.com> * Sricharan <r.sricharan@ti.com> */#ifndef
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    Copyright (C) 2012 Tomasz Figa <tomasz.figa@gmail.com> */#define pr_fmt(fmt) "%s:%d " fmt, __func__, __LINE__
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<linux/compiler.h>#include <linux/errno.h>#include <linux/s
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<linux/bio.h>#include <linux/blkdev.h>#include <linux/kmemle
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Copyright (C) 2013-2014 Jens Axboe */#include <linux/kernel.h>#include <linux/module.h>
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<linux/smp.h>#include <linux/cpu.h>
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<linux/module.h>
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Copyright (c) 2013-2014 Takashi Sakamoto */#include "./fireworks.h"
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<linux/miscdevice.h>#include <linux/atomic.h>#include <linu

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<linux/spinlock.h>#include <linux/module.h>#include <linux/
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<linux/vmalloc.h>#include <linux/lz4.h>#include <crypto/in
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__builtin_ctz, and therefore they can't be remov
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<linux/of\_platform.h>#include <linux/irqchip.h>#incl  
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Copyright (C) 2013 Imagination Technologies Ltd. \*/#include <linux/device.h>#include <linux/fs.h>#include <linux/err.h>#include <linux/wait.h>#include <linux/sched.h>#include <linux/interrupt.h

Copyright (C) 2013 Imagination Technologies Ltd. \*/#include <linux/device.h>#include <linux/fs.h>#include <linux/err.h>#include <linux/wait.h>#include <linux/sched.h>#include <linux/smp.h>

Copyright (C) 2013 Imagination Technologies Ltd. \*/#include <linux/errno.h>#include <linux/sched.h>#include <linux/sched/debug.h>#include <linux/sched/task.h>#include <linux/sched/task\_stack.h>

Copyright (C) 2013 Imagination Technologies Ltd. \*/#include <linux/kernel.h>#include <linux/device.h>#include <linux/fs.h>#include <linux/slab.h>#include <linux/export.h>

Copyright (C) 2013 Imagination Technologies Ltd. \*/#include <linux/kernel.h>#include <linux/fs.h>#include <linux/syscalls.h>#include <linux/moduleloader.h>#include <linux/atomic.h>#include <lin

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codenamed: Knights Ferry, and * the Intel pro
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<linux/ctype.h>#include <linux/errno.h>#includ
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Copyright (C) 2013 Linaro Limited. * Author: Sandeepa Prabhu <sandeepa.prabhu@linaro.org> */#include <linux/kasan.h>#include
<linux/kernel.h>#include <linux/kprobes.h>#include <linux/extable.h>
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Copyright (C) 2013 Linaro Limited. * Written by: Yi Li (yi.li@linaro.org) * * based on arch/ia64/include/asm/dmi.h
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Copyright (C) 2013 Magnus Damm */#include <linux/cpu.h>#include <linux/delay.h>#include <linux/init.h>#include <linux/io.h>#include
<linux/smp.h>#include <asm/cacheflush.h>#include <asm/smp_p
Copyright (C) 2013 Magnus Damm */#include <linux/cpu_pm.h>#include <linux/delay.h>#include <linux/init.h>#include <linux/io.h>#include
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<linux/kernel.h>#include <linux/module.h>#include <linux/slab.h>#incl
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Copyright (c) 2013 Michael Stapelberg <michael@stapelberg.de> */#include <linux/kernel.h>#include <linux/string.h>#include
<linux/ctype.h>#include <linux/errno.h>#include <linux/unistd.h>#inclu
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Copyright (C) 2013, Microsoft, Inc. * Author : K. Y. Srinivasan <kys@microsoft.com> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt
Copyright (c) 2013 * Minchan Kim <minchan@kernel.org> */#include <linux/types.h>#include <linux/mutex.h>#include <linux/slab.h>#include
<linux/buffer_head.h>#include <linux/sched.h>#include <
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Copyright (C) 2013 Pali Roh */#ifndef OMAP_ARCH_OMAP_SECURE_H#define OMAP_ARCH_OMAP_SECURE_H

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Copyright (C) 2013 Pengutronix \* Uwe Kleine-Koenig <u.kleine-koenig@pengutronix.de> \*/#include <linux/io.h>#include <linux/clock-  
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Copyright (C) 2013 Renesas Solutions Corp. \*/#include <linux/module.h>#include <linux/init.h>#include <linux/errno.h>#include  
<linux/kernel.h>#include <linux/interrupt.h>#include <linux/i2c.h>  
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 <linux/gpio/machine.h>#include <linux/init.h>#include <linux/kernel.h  
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 <linux/io.h>#include <linux/irqchip.h>#include  
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"nfs4session.h"#include "callback.h"#include "pnfs.h  
Copyright (c) 2013 Trond Myklebust <Trond.Myklebust@netapp.com> \*/#undef TRACE\_SYSTEM#define TRACE\_SYSTEM nfs  
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<linux/module.h>#include <linux/of.h>#include <linux/mutex.h>#include  
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drivers/devfreq/exynos/exynos\_ppmu.c  
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<linux/slab.h> #include <linux/pr.h>  
Copyright (c) 2014-2016 Christoph Hellwig. \*/ #include <linux/sunrpc/svc.h> #include <linux/blkdev.h> #include <linux/nfs4.h> #include  
<linux/nfs\_fs.h> #include <linux/nfs\_xdr.h> #include <linux/pr.h  
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Copyright (c) 2014-2016, Intel Corporation. */#ifndef LINUX_ISHTP_CL_BUS_H#define LINUX_ISHTP_CL_BUS_H
Copyright (c) 2014-2016, Intel Corporation. */#include "test/nfit_test.h"#include <linux/blkdev.h>#include <pmem.h>#include <nd.h>
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µPD78F0730 8-bit Single-Chip Microcontroller * USB-
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<linux/uprobes.h>#include <asm/cacheflush.h>
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<linux/kernel.h>#include <linux/serial_core.h>#include <
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<linux/kernel.h>#include <linux/mm.h>#include <linu
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<linux/io.h>#include <linux/pci.h>
Copyright (C) 2014 Akihiro Tsukada <tskd08@gmail.com> */#include <linux/dma-mapping.h>#include <linux/kernel.h>#include <linux/pci.h>
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<linux/nfs.h>#include <linux/nfs3.h>#include <linux/nfs4.h>#include
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Copyright (C) 2014 ARM Ltd. */#ifndef __ASM_PT_DUMP_H#define __ASM_PT_DUMP_H
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Copyright (C) 2014 ARM Ltd. */#include <asm/arch_timer.h>#include <asm/cache.h>#include <asm/cpu.h>#include <asm/cputype.h>#include
<asm/cpufeature.h>#include <asm/fpsimd.h>

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struct brcmf_mp_device *settings);#elsestatic void brcmf_of_p
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 <linux/sched.h>#include <linux/sunrpc/addr.h>  
 Copyright (c) 2014 Christoph Hellwig. \*/#include "xfs.h"#include "xfs\_shared.h"#include "xfs\_format.h"#include "xfs\_log\_format.h"#include  
 "xfs\_trans\_resv.h"#include "xfs\_mount.h"#include "xfs\_  
 Copyright (C) 2014 Christoph Hellwig \* \* SCSI queueing library. \* Initial versions: Eric Youngdale (eric@andante.org). \* Based upon  
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 <linux/vmcache.h>#include <asm/pgtable.h>  
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__ASM_MACH_MIPS_KERNEL_ENTRY_INIT_H
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Copyright (C) 2014 Imagination Technologies Ltd. */#ifndef __UAPI_ASM_INST_H#define __UAPI_ASM_INST_H
Copyright (C) 2014, Imagination Technologies Ltd. */#include <linux/bitops.h>#include <linux/bug.h>#include <linux/compiler.h>#include
<linux/context_tracking.h>#include <linux/cpu_pm.h>#includ
Copyright (C) 2014, Imagination Technologies Ltd. */#include <linux/cache.h>#include <linux/context_tracking.h>#include <linux/irqflags.h>#include
<linux/sched.h>#include <linux/mm.h>#include <
Copyright (C) 2014 Imagination Technologies Ltd. */#include <linux/errno.h>#include <asm/asm.h>#include <asm/asmmacro.h>#include
<asm/irqflags.h>#include <asm/mipsregs.h>#include <asm/regdef.h>
Copyright (C) 2014 Imagination Technologies Ltd. */#include <linux/errno.h>#include <asm/asm.h>#include <asm/asm-offsets.h>#include
<asm/export.h>#include <asm/regdef.h>
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 Copyright (C) 2014 Intel Corporation; author Matt Fleming \* \* Support for invoking 32-bit EFI runtime services from a 64-bit \* kernel.  
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 Copyright (C) 2014 Intel Corporation \* \* DRM universal plane helper functions  
 Copyright (C) 2014 Intel Corporation \*/#ifndef \_DMA\_DW\_H#define \_DMA\_DW\_H  
 Copyright (c) 2014, Intel Corporation. \* \* IIO driver for AL3320A (7-bit I2C slave address 0x1C).  
 Copyright (c) 2014, Intel Corporation. \* \* IIO driver for KMX61 (7-bit I2C slave address 0x0E or 0x0F).  
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Copyright (C) 2014 Lad, Prabhakar <prabhakar.csengg@gmail.com> * * TODO : add support for VBI & HBI data service * add static buffer allocation
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<linux/of_device.h>#include <linux/of_address.h>#inc
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Copyright (C) 2014 Linaro Ltd. * Author: Rob Herring <robh@kernel.org> */#include <linux/kernel.h>#include <linux/console.h>#include
<linux/init.h>#include <linux/serial_core.h>
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<linux/platform\_device.h>#include <linux/of.h>#include <linux/of\_address.h  
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<linux/mfd/syscon.h>#include <linux/module.h>#include <linux/notifi  
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<linux/kernel.h>#include <linux/module.h>#include
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KBUILD_MODNAME ":" fmt
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<linux/module.h>#include <linux/of_irq.h>#include <
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<asm/mach/arch.h>#include <linux/of.h>#include <li
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0x44  
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<poll.h>#include <stdlib.h>#include <unistd.h>  
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<linux/phy.h>#include <linux/of.h>  
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<linux/of.h>#include <linux/of\_platform.h>#include <linux/plat  
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contiguous.h>#include <linux/init.h>#include <linux/genalloc.h>#incl  
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<linux/delay.h>#include <linux/module.h>#include <linux/i2c.h>#  
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<linux/io.h>#include <linux/kernel.h>#include <linux/modu
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core.h>#include <linux/fpga/fpga-mgr.h>#inclu
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<linux/module.h>#include <linux/of_device.h>
Copyright (C) 2015-2016 Altera Corporation */#include <linux/clk.h>#include <linux/device.h>#include <linux/delay.h>#include <linux/fpga/fpga-
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<linux/ctype.h>#include <linux/delay.h>#include <linux/err

```

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<linux/module.h>#include "nvmet.h"
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"nvmet.h"
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<linux/miscdevice.h>#include <linux/module.h>#include <linux
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<linux/module.h>#include <linux/slab.h>#include <linux/sta
Copyright (c) 2015-2016 HGST, a Western Digital Company. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/init.h>#include <linux/slab.h>#include <rdma/mr_poo
Copyright (c) 2015-2016 HGST, a Western Digital Company. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/random.h>#include <linux/rculist.h>#include <linux/
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<linux/rculist.h>
Copyright (c) 2015-2016 HGST, a Western Digital Company. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/scatterlist.h>#include
<linux/blk-mq.h>#include <linux/nvme.h>#include <linux
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<linux/iopoll.h>#include <linux/module.h>#include
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<linux/idr.h>#include <linux/sched.h>#include <linux/slab.h>#include
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__DTS_IOMMU_PORT_MT8173_H
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<linux/device.h>#include <linux/err.h>#include <linux
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<linux/clk.h>#include <linux/component.h>#include <lin
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 Copyright (C) 2015, 2017-2018 \* Author: Matt Ranostay <matt.ranostay@konsulko.com> \* \* TODO: interrupt mode, and signal strength reporting  
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Copyright (C) 2015-2017 Pengutronix, Uwe Kleine-K \*/#include <linux/kernel.h>#include <linux/device.h>#include <linux/module.h>#include  
<linux/slab.h>#include <linux/sysfs.h>  
Copyright (C) 2015-2017 Pengutronix, Uwe Kleine-K \*/#include <linux/kernel.h>#include <linux/kthread.h>#include <linux/siox.h>  
Copyright (c) 2015-2017 QLogic Corporation  
Copyright (c) 2015-2017 Qualcomm Atheros, Inc.  
Copyright (c) 2015,2017 Qualcomm Atheros, Inc.  
Copyright (c) 2015,2017 Qualcomm Atheros, Inc. \*/#ifndef \_WOW\_H\_#define \_WOW\_H\_  
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Copyright (C) 2015-2017 Renesas Electronics Corporation \*/  
Copyright (C) 2015-2017 Renesas Electronics Corporation \* \* This is based on the phy-rcar-gen2 driver:  
Copyright (C) 2015-2017 Russell King. \*/#include <linux/interrupt.h>#include <linux/io.h>#include <linux/module.h>#include  
<linux/platform\_device.h>#include <linux/sched.h>#include <linux/slab.  
Copyright (C) 2015-2017 Sebastian Reichel <sre@kernel.org> \*/  
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Copyright (c) 2015-2017 Takashi Sakamoto <o-takashi@sakamocchi.jp> \*/  
Copyright (c) 2015-2017 Takashi Sakamoto <o-takashi@sakamocchi.jp> \*/#include "motu.h"  
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Copyright (C) 2015, 2018 \* Author: Matt Ranostay <matt.ranostay@konsulko.com> \* \* Datasheets:  
Copyright (C) 2015, 2018 \* Author: Matt Ranostay <matt.ranostay@konsulko.com> \* \* TODO: enable pulse length controls via device tree properties  
Copyright (C) 2015, 2018 \* Author: Matt Ranostay <matt.ranostay@konsulko.com> \* \* TODO: gesture + proximity calib offsets  
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Copyright (C) 2015-2018 Linaro Ltd. \*/#ifndef QC\_MSM\_CAMSS\_CSID\_H#define QC\_MSM\_CAMSS\_CSID\_H  
Copyright (C) 2015-2018 Linaro Ltd. \*/#ifndef QC\_MSM\_CAMSS\_H#define QC\_MSM\_CAMSS\_H  
Copyright (C) 2015-2018 Linaro Ltd. \*/#ifndef QC\_MSM\_CAMSS\_ISPIF\_H#define QC\_MSM\_CAMSS\_ISPIF\_H

Copyright (C) 2015-2018 Linaro Ltd. \*/#ifndef QC\_MSM\_CAMSS\_VFE\_H#define QC\_MSM\_CAMSS\_VFE\_H  
Copyright (C) 2015-2018 Linaro Ltd. \*/#ifndef QC\_MSM\_CAMSS\_VIDEO\_H#define QC\_MSM\_CAMSS\_VIDEO\_H  
Copyright (C) 2015-2018 Linaro Ltd. \*/#include <linux/clk.h>#include <linux/completion.h>#include <linux/interrupt.h>#include <linux/io.h>#include  
<linux/iopoll.h>#include <linux/kernel.h>#inc  
Copyright (C) 2015-2018 Linaro Ltd. \*/#include <linux/clk.h>#include <linux/completion.h>#include <linux/interrupt.h>#include <linux/io.h>#include  
<linux/kernel.h>#include <linux/of.h>#include  
Copyright (C) 2015-2018 Linaro Ltd. \*/#include <linux/clk.h>#include <linux/completion.h>#include <linux/interrupt.h>#include  
<linux/iommu.h>#include <linux/mutex.h>#include <linux/of.h>#inclu  
Copyright (C) 2015-2018 Linaro Ltd. \*/#include <linux/clk.h>#include <linux/media-bus-format.h>#include <linux/media.h>#include  
<linux/module.h>#include <linux/platform\_device.h>#include <linux  
Copyright (C) 2015-2018 Linaro Ltd. \*/#include <linux/slab.h>#include <media/media-entity.h>#include <media/v4l2-dev.h>#include <media/v4l2-  
device.h>#include <media/v4l2-ioctls.h>#include <media  
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Garzik and Anton Vorontsov  
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Copyright (C) 2015 Aleksa Sarai <cyphar@cyphar.com> \*/  
Copyright (C) 2015 Alexander Sverdlin \* \* The driver uses polling to get the conversion status. According to EP93xx \* datasheets, reading ADCResult  
register starts the conversion, but user is also  
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Copyright (c) 2015 Andrew Lutomirski * * Based on asm and ptrace code by many authors. The code here originated * in ptrace.c and signal.c.
Copyright (C) 2015 Andrew Lutomirski <luto@kernel.org> */
Copyright (c) 2015 Andrew Lutomirski * * On AMD CPUs, SYSRET can return with a valid SS descriptor with with * the hidden attributes set to an
unusable state. Make sure the kernel * doesn't let t
Copyright (c) 2015 Andrew Lutomirski * * These are little helpers that make it easier to switch bitness on * the fly.
Copyright (c) 2015 Andrew Lutomirski * * This makes sure that sa_restorer == NULL keeps working on 32-bit * configurations. Modern glibc doesn't use
it under any circumstances, * so it's easy to
Copyright (C) 2015 Andrey Smetanin <asmetanin@virtuozzo.com> * * Authors:
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Copyright (C) 2015 Annapurna Labs Ltd. */
Copyright (C) 2015 Anshuman Khandual, IBM Corporation.
Copyright (C) 2015 Anshuman Khandual, IBM Corporation. */#define GPR_1 1#define GPR_2 2#define GPR_3 3#define GPR_4 4
Copyright (C) 2015 Anshuman Khandual, IBM Corporation. */#define TAR_1 10#define TAR_2 20#define TAR_3 30#define TAR_4 40#define
TAR_5 50
Copyright (C) 2015 Anshuman Khandual, IBM Corporation. */#define VEC_MAX 128#define VSX_MAX 32#define VMX_MAX 32
Copyright (C) 2015 Anshuman Khandual, IBM Corporation. */#include <inttypes.h>#include <unistd.h>#include <stdlib.h>#include <string.h>#include
<malloc.h>#include <errno.h>#include <time.h>#i
Copyright (C) 2015 Anshuman Khandual, IBM Corporation. */#include "ptrace.h"#include "ptrace-gpr.h"#include "reg.h"
Copyright (C) 2015 Anshuman Khandual, IBM Corporation. */#include "ptrace.h"#include "ptrace-gpr.h"#include "tm.h"
Copyright (C) 2015 Anshuman Khandual, IBM Corporation. */#include "ptrace.h"#include "ptrace-tar.h"
Copyright (C) 2015 Anshuman Khandual, IBM Corporation. */#include "ptrace.h"#include "ptrace-vsx.h"
Copyright (C) 2015 Anshuman Khandual, IBM Corporation. */#include "ptrace.h"#include "tm.h"
Copyright (C) 2015 Anshuman Khandual, IBM Corporation. */#include "ptrace.h"#include "tm.h"#include "ptrace-tar.h"
Copyright (C) 2015 Anshuman Khandual, IBM Corporation. */#include "ptrace.h"#include "tm.h"#include "ptrace-vsx.h"
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<linux/interrupt.h>#include <linux/io.h>#include
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Copyright (C) 2015 ARM Limited * * Author: Will Deacon <will.deacon@arm.com>
Copyright (C) 2015 ARM Limited */#include <linux/perf_event.h>#include <linux/uaccess.h>
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Copyright (C) 2015 ARM Ltd. * Author: Lorenzo Pieralisi <lorenzo.pieralisi@arm.com> */#include <linux/acpi.h>#include <linux/irq.h>#include
<linux/irqdomain.h>#include <linux/of.h>
Copyright (C) 2015 - ARM Ltd * Author: Marc Zyngier <marc.zyngier@arm.com> */
Copyright (C) 2015 - ARM Ltd * Author: Marc Zyngier <marc.zyngier@arm.com> */#include <linux/jump_label.h>
Copyright (C) 2015 ARM Ltd. * Author: Marc Zyngier <marc.zyngier@arm.com> */#include <linux/kvm_host.h>#include <kvm/arm_vgic.h>#include
<linux/uaccess.h>#include <asm/kvm_mmu.h>#include <asm/c
Copyright (C) 2015 ARM Ltd. */#ifndef __ASM_ARCH_GICV3_H#define __ASM_ARCH_GICV3_H
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by Boris BREZILLON.  
Copyright (C) 2015 Atmel Corporation, \* Nicolas Ferre <nicolas.ferre@atmel.com> \* \* Evolved from driver at91-poweroff.c.  
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making sure the structure is aligned and packed.  
Copyright (C) 2015 Azael Avalos <coproscefalo@gmail.com> \* \* Thanks to Matthew Garrett for background info on ACPI innards which \* normal people  
aren't meant to understand :-)  
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Copyright (c) 2015 Benjamin Tissoires \*/  
Copyright (C) 2015 Bert Vermeulen <bert@biot.com> \* \* This file was based on the patches for Linux 2.6.27.39 published by \* MikroTik for their  
RouterBoard 4xx series devices.  
Copyright (c) 2015 BMW Car IT GmbH  
Copyright (c) 2015 BMW Car IT GmbH \*/#include <stdio.h>#include <unistd.h>#include <stdlib.h>#include <signal.h>#include <linux/bpf.h>#include  
<bpf/bpf.h>#include "bpf\_load.h"  
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Copyright (C) 2015 Boris Barbulovski <bbarbulovski@gmail.com> \*/  
Copyright (C) 2015 Boris Barbulovski <bbarbulovski@gmail.com>.\n\n"Bug reports and feature request can also be entered at  
<http://bugzilla.kernel.org/>";  
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Copyright (C) 2015 Broadcom Corporation \* Author: Florian Fainelli <f.fainelli@gmail.com> \*/#include <linux/kernel.h>#include <linux/io.h>#include  
<linux/spinlock.h>#include <linux/reset/bcm63xx  
Copyright(c) 2015 Bryan O \*/#ifndef \_IMR\_H#define \_IMR\_H  
Copyright(c) 2015 Bryan O \* \* IMR registers define an isolated region of memory that can \* be masked to prohibit certain system agents from accessing  
memory. \* When a device behind a masked port p  
Copyright(c) 2015 Bryan O \* \* IMR self test. The purpose of this module is to run a set of tests on the \* IMR API to validate it's sanity. We check for  
overlapping, reserved \* addresses and setup/  
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Copyright (C) 2015 Dan Streetman, IBM Corp \* \* See 842.h for details of the 842 compressed format.  
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Copyright (c) 2015 David Ahern <dsa@cumulusnetworks.com> \*/  
Copyright (c) 2015 David Ahern <dsa@cumulusnetworks.com> \* \* Based on dummy, team and ipvlan drivers  
Copyright (c) 2015 David Ahern <dsa@cumulusnetworks.com> \*/#ifndef \_NET\_L3MDEV\_H\_#define \_NET\_L3MDEV\_H\_  
Copyright (C) 2015 Davidlohr Bueso. \*/  
Copyright (C) 2015 Davidlohr Bueso. \* \* Block a bunch of threads and let parallel waker threads wakeup an \* equal amount of them. The program  
output reflects the avg latency \* for each individual  
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"Hisilicon Network Subsystem"
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_CRYPTO_AKCIPHER_H# include <linux/crypto.h>
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_CRYPTO_AKCIPHER_INT_H# include <crypto/akcipher.h> #include <crypt
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<linux/types.h>
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<linux/export.h> #include <linux/err.h> #include <linux/fips.h> #include
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<linux/regmap.h>#include <linux/iio/iio.h>#include "inv_mpu_iio.h"
Copyright (c) 2015, Intel Corporation. */#include <linux/debugfs.h> /* debugfs_create_u32() */#include <linux/mm_types.h>
/* mm_struct, vma, etc... */#include <linux/pkeys.h>
Copyright (c) 2015, Intel Corporation. */#include <linux/kernel.h>#include <linux/module.h>#include <linux/init.h>#include <linux/acpi.h>#include
<linux/thermal.h>#include "int340x_thermal_zone
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 <linux/mm.h>#include <linux/slab.h>#include <li  
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 <linux/tee\_drv.h>#include "tee\_private.h"  
 Copyright (c) 2015, Linaro Limited \*/#include <linux/device.h>#include <linux/slab.h>#include <linux/uaccess.h>#include "optee\_private.h"  
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Copyright (C) 2015 Linaro Ltd. \*/#ifndef \_\_QCOM\_SCM\_H#define \_\_QCOM\_SCM\_H  
Copyright (C) 2015 Linaro Ltd. \*/#include <linux/bitops.h>#include <linux/device.h>#include <linux/errno.h>#include <linux/gpio/driver.h>#include  
<linux/irqchip/chained\_irq.h>#include <linux/in  
Copyright (C) 2015 Linaro Ltd. \*/#include <linux/platform\_device.h>#include <linux/init.h>#include <linux/cpumask.h>#include  
<linux/export.h>#include <linux/dma-direct.h>#include <linux/dma-map  
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<linux/irqchip.h>#include <linux/irqchip/arm-gic.  
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<linux/regmap.h>#include <linux/mfd/syscon.h>  
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<linux/ethtool.h>#include <linux/phy.h>#include <linux/microchipphy.h>  
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<linux/iopoll.h>#include <linux/mfd/syscon.h>#i
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<stdbool.h>#include <string.h>#include <time.h>#include
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<linux/string.h>#include <sound/core.h>#include <s  
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<linux/slab.h>#include <linux/err.h>#include <linux/init.h>#inc  
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 <linux/regulator/consumer.h>#include <linux/spi/spi.h>  
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 <linux/cpu.h>#include <linux/sort.h>  
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 <linux/mm.h>#include <linux/filter.h>#include <linux/perf\_e  
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 direction.h>#include <linux/dma-mapping.h>#include <linux  
 Copyright (c) 2016-2017 Hisilicon Limited.#include <linux/crypto.h>#include <linux/dma-mapping.h>#include <linux/dmapool.h>#include  
 <linux/module.h>#include <linux/mutex.h>#include <linux/slab.h  
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 Copyright (c) 2016-2017 HiSilicon Technologies Co., Ltd. \*/#include <linux/kernel.h>#include <linux/mfd/syscon.h>#include <linux/module.h>#include  
 <linux/of\_device.h>#include <linux/platform\_dev  
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 <linux/tty.h>#include <linux/tty\_driver.h>#include <linux/poll.h>  
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Copyright (C) 2016, 2018 * Author: Matt Ranostay <matt.ranostay@konsulko.com> * * TODO: bias voltage + polarity control, and multiple chip support
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Copyright (c) 2016-2018 Cavium Inc. */#ifndef _FCOE_FW_FUNCS_H#define _FCOE_FW_FUNCS_H#include "drv_scsi_fw_funcs.h"#include
"qedf_hsi.h"#include <linux/qed/qed_if.h>
Copyright (c) 2016-2018 Cavium Inc. */#ifndef _QEDFC_H#define _QEDFC_H
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Copyright (c) 2016-2018 Cavium Inc. */#ifndef _QEDF_HSI#define _QEDF_HSI
Copyright (c) 2016-2018 Cavium Inc. */#ifndef _SCSI_FW_FUNCS_H#define _SCSI_FW_FUNCS_H#include <linux/qed/common_hsi.h>#include
<linux/qed/storage_common.h>#include <linux/qed/fcoe_common.h>
Copyright (c) 2016-2018 Cavium Inc. */#include "drv_fcoe_fw_funcs.h"#include "drv_scsi_fw_funcs.h"
Copyright (c) 2016-2018 Cavium Inc. */#include "drv_scsi_fw_funcs.h"
Copyright (c) 2016-2018 Cavium Inc. */#include <linux/if_ether.h>#include <linux/if_vlan.h>#include "qedf.h"
Copyright (c) 2016-2018 Cavium Inc. */#include <linux/init.h>#include <linux/kernel.h>#include <linux/module.h>#include <linux/pci.h>#include
<linux/device.h>#include <linux/highmem.h>#include
Copyright (c) 2016-2018 Cavium Inc. */#include <linux/spinlock.h>#include <linux/vmalloc.h>#include "qedf.h"#include <scsi/scsi_tcq.h>
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<linux/iomap.h>#include <linux/backing-dev.h>#include <linux/uid.h>
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<linux/iomap.h>#include <linux/pagemap.h>#include <linux/uid.h>#i
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<linux/kernel.h>#include <linux/of.h>#include <lin  
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<media/v4l2-async.h>#include <media/v4l2-event.h>#include  
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<linux/init.h>#include <linux/delay.h>#include <linux  
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\_DT\_BINDINGS\_AMLOGIC\_MESON\_GXBB\_RESET\_H#define \_DT\_BINDINGS\_AMLOGIC\_MESON\_GXBB\_RESET\_H  
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<linux/mfd/syscon.h>#include "meson-aoclk.h"#include "gxbb-aoclk  
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<linux/mmc/mmc.h>#include <linux/module.h>#include <linux/of.h>#inc
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<linux/virtio_config.h>#include <linux/module.h>#include "blk-
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<errno.h>#include <string.h>#include <assert.h>#i  
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<asm/unistd.h>#include <fcntl.h>#include <unistd.h>#incl  
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<unistd.h>#include <assert.h>#include <sys/wait.h>#  
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<assert.h>#include <sched.h>#include <stdlib.h>#include <t  
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Copyright (c) 2016 Facebook \*/#include <linux/bpf.h>#include <linux/btf.h>#include <linux/jhash.h>#include <linux/filter.h>#include  
<linux/rculist\_nulls.h>#include <linux/random.h>#include <ua  
Copyright (c) 2016 Facebook \*/#include <linux/bpf.h>#include <linux/if\_link.h>#include <assert.h>#include <errno.h>#include <signal.h>#include  
<stdio.h>#include <stdlib.h>#include <string.h>  
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<linux/perf\_event.h>#include <linux/elf.h>#include <l  
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<linux/bpf\_perf\_event.h>#include <linux/filter.h>#include <  
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Copyright (c) 2016 Facebook \*/#include "percpu\_freelist.h"  
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<string.h>#include <linux/perf\_event.h>#include <errno.  
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<linux/mtd/partitions.h>#include <linux/etherdevice.h>#include  
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Copyright (C) 2016 Google, Inc \* \* This driver uses the cros-ec interface to communicate with the Chrome OS \* EC about sensors data. Data access is  
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Copyright (c) 2016 HGST, a Western Digital Company. \*/#ifndef \_\_RDMA\_MR\_POOL\_H#define \_\_RDMA\_MR\_POOL\_H 1  
Copyright (c) 2016 HGST, a Western Digital Company. \*/#ifndef \_\_RDMA\_RW\_H#define \_\_RDMA\_RW\_H  
Copyright (c) 2016 HGST, a Western Digital Company. \*/#include <linux/moduleparam.h>#include <linux/slab.h>#include <linux/pci-  
p2pdma.h>#include <rdma/mr\_pool.h>#include <rdma/rw.h>  
Copyright (c) 2016 HGST, a Western Digital Company. \*/#include <rdma/ib\_verbs.h>#include <rdma/mr\_pool.h>  
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 Copyright (C) 2016 IBM Corp. \*/#include <linux/bitops.h>#include <linux/init.h>#include <linux/io.h>#include <linux/kernel.h>#include  
 <linux/mfd/syscon.h>#include <linux/mutex.h>#include <linux  
 Copyright (C) 2016 IBM Corp. \*/#include <linux/bitops.h>#include <linux/init.h>#include <linux/io.h>#include <linux/kernel.h>#include  
 <linux/mutex.h>#include <linux/of.h>#include <linux/platfo  
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 Jung Bauermann <bauerma@linux.vnet.ibm.com>.  
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 arch/arm/kernel/bios32.c.  
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 <linux/kernel.h>#include <linux/signal.h>#include <linux  
 Copyright (C) 2016, Imagination Technologies Ltd. \*/#include <linux/compiler.h>#include <linux/errno.h>#include <linux/signal.h>#include  
 <linux/sched/signal.h>#include <linux/uaccess.h>  
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_CRYPTO_SCOMP_INT_H#include <linux/crypto.h>
Copyright (c) 2016, Intel Corporation * Author: Giovanni Cabiddu <giovanni.cabiddu@intel.com> */#include <linux/errno.h>#include
<linux/kernel.h>#include <linux/module.h>#include <linux/seq_file
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Copyright (C) 2016, Intel Corporation * Author: Rafael J. Wysocki <rafael.j.wysocki@intel.com> */#include "sched.h"
Copyright (c) 2016 Intel Corporation * Author: Ross Zwisler <ross.zwisler@linux.intel.com> * Author: Matthew Wilcox <matthew.r.wilcox@intel.com>
*/#include <linux/radix-tree.h>#include <linux/sla
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_CRYPTO_ECDH_
Copyright (c) 2016, Intel Corporation * Authors: Salvatore Benedetto <salvatore.benedetto@intel.com> */#ifndef _CRYPTO_KPP_INT_H#define
_CRYPTO_KPP_INT_H#include <crypto/kpp.h>#include <crypto/a
Copyright (c) 2016, Intel Corporation * Authors: Salvatore Benedetto <salvatore.benedetto@intel.com> */#include <linux/errno.h>#include
<linux/kernel.h>#include <linux/module.h>#include <linux/s
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<linux/export.h>#include <linux/err.h>#include <linux/str
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_CRYPTO_ACOMP_H#define _CRYPTO_ACOMP_H#inclu
Copyright (c) 2016, Intel Corporation * Authors: Weigang Li <weigang.li@intel.com> * Giovanni Cabiddu <giovanni.cabiddu@intel.com> */#ifndef
_CRYPTO_ACOMP_INT_H#define _CRYPTO_ACOMP_INT_
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*/#include <linux/errno.h>#include <linux/kernel.h>#in
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Copyright (c) 2016, Intel Corporation. * * Heartbeat STM source will send repetitive messages over STM devices to a * trace host.
Copyright (c) 2016, Intel Corporation. * * IIO driver for ADS1015 ADC 7-bit I2C slave address:
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<linux/platform_device.h>#include <linux/spi/pxa2xx_spi.h>
Copyright (c) 2016, Intel Corporation. */#include <linux/device.h>#include <linux/mod_devicetable.h>#include <linux/i2c.h>#include

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<linux/module.h>#include <linux/acpi.h>#include <linux/regmap.
Copyright (c) 2016, Intel Corporation. */#include "test/nfit_test.h"#include <linux/mm.h>#include ".././../drivers/dax/dax-private.h"
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<linux/netdevice.h>#include <linux/export.h>#include <asm/unaligned
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mq.h>#include <linux/elevator.h>#include <linux/bio.
Copyright (C) 2016 Jens Axboe */#include <linux/kernel.h>#include <linux/module.h>#include <linux/blk-mq.h>
Copyright (C) 2016 Jens Axboe */#include <linux/kernel.h>#include <linux/rculist.h>#include <linux/blk-mq.h>
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Copyright (c) 2016 Joachim Eastwood <manabian@gmail.com> * * Based on the imx ocotp driver,
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Copyright (c) 2016 Joel Holdsworth * * This driver adds support to the FPGA manager for configuring the SRAM of * Lattice iCE40 FPGAs through slave
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<errno.h>#include <signal.h>#include <stdio.h>#incl
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Copyright (c) 2016, Linaro Limited \* Author: Linus Walleij <linus.walleij@linaro.org> \*/#include <linux/export.h>#include <linux/io.h>#include  
<linux/of.h>#include <linux/of\_address.h>#include  
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Copyright (C) 2016 Linaro Ltd. \* Author: Linus Walleij <linus.walleij@linaro.org> \* Loosely based on the previous BH1780 ALS misc driver  
Copyright (C) 2016 Linaro Ltd. \* Author: Linus Walleij <linus.walleij@linaro.org> \* \* This hardware was found in the very first Nexus One handset from  
Google/HTC \* and an early endavour into mobil  
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Copyright (C) 2016 Linaro Ltd \* Author: Sumit Semwal <sumit.semwal@linaro.org> \* \* From internet archives, the panel for Nexus 7 2nd Gen, 2013  
model is a \* JDI model LT070ME05000, and its data she  
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Copyright (C) 2016, Linaro Ltd - Daniel Lezcano <daniel.lezcano@linaro.org>#define pr\_fmt(fmt) "irq\_timings: " fmt  
Copyright (C) 2016 Linaro Ltd. \*/#include <linux/clock-provider.h>#include <linux/device.h>#include <linux/kernel.h>#include  
<linux/of\_address.h>#include <linux/of\_device.h>#include <linux/platf  
Copyright (C) 2016 Linaro Ltd \*/#include <linux/module.h>#include <linux/ulpi/driver.h>#include <linux/ulpi/regs.h>#include <linux/clock.h>#include  
<linux/regulator/consumer.h>#include <linux/of\_<br>Copyright (C) 2016 Linaro Ltd \*/#include <linux/module.h>#include <linux/ulpi/driver.h>#include <linux/ulpi/regs.h>#include  
<linux/phy/phy.h>#include <linux/pinctrl/consumer.h>#include <linux/p<br>Copyright (C) 2016 Linaro, Ltd. \* Stephen Boyd <stephen.boyd@linaro.org> \*/  
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must use device\_property\_read\_u8\_array().  
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Copyright (c) 2016 Mentor Graphics Inc. */#include <linux/delay.h>#include <linux/irq.h>#include <linux/module.h>#include
<linux/platform_device.h>#include <linux/slab.h>#include <linux/spinloc
Copyright (c) 2016 Mentor Graphics Inc. */#include <linux/module.h>#include "imx-media.h"
Copyright (c) 2016 Mentor Graphics Inc. */#include <linux/of_platform.h>#include <media/v4l2-ctrls.h>#include <media/v4l2-device.h>#include
<media/v4l2-fwnode.h>#include <media/v4l2-subdev.h>#i
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<stdio.h>#include <stdlib.h>#include <string.h>  
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 <linux/module.h>#include <linux/of\_device.h>#inclu  
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 <linux/slab.h>#include <linux/raid/md\_p.h>#include <linux  
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Copyright (c) 2016 Tom Haynes <loghyr@primarydata.com> \*/#include <linux/sunrpc/svc.h>#include <linux/nfs4.h>  
Copyright (c) 2016 Tom Haynes <loghyr@primarydata.com> \*\* The following implements a super-simple flex-file server \* where the NFSv4.1 mds is  
also the ds. And the storage is \* the same. I.e., wri  
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<linux/errno.h>#include <linux/gpio/driver.h>#include <linux/io.h  
Copyright (C) 2016 William Breathitt Gray \*/#include <linux/bitops.h>#include <linux/device.h>#include <linux/errno.h>#include  
<linux/gpio/driver.h>#include <linux/iio/iio.h>#include <linux/iio  
Copyright (C) 2016 William Breathitt Gray \*/#include <linux/device.h>#include <linux/dmi.h>#include <linux/errno.h>#include <linux/io.h>#include  
<linux/ioport.h>#include <linux/isa.h>#include  
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 Copyright (c) 2017-2018 Covalent IO, Inc. [http://covalent.io#include <stddef.h>#include <string.h>#include <linux/bpf.h>#include  
 <linux/if\\_ether.h>#include <linux/if\\_packet.h>#include <linux/ip.>#include <sys/ioctl.h>#include <sys/select.h>#include <netinet/in.h>#incl  
 Copyright \(c\) 2017-2018 Covalent IO, Inc. \[http://covalent.io#include <stdio.h>#include <stdlib.h>#include <sys/socket.h>#include  
 <sys/ioctl.h>#include <sys/select.h>#include <netinet/in.h>#incl  
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 \\\_\\\_EROFS\\\_INTERNAL\\\_H#define \\\_\\\_EROFS\\\_INTERNAL\\\_H  
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 \\\_\\\_EROFS\\\_XATTR\\\_H#define \\\_\\\_EROFS\\\_XATTR\\\_H  
 Copyright \\(C\\) 2017-2018 HUawei, Inc. \\\* <http://www.huawei.com/> \\\* Created by Gao Xiang <gaoxiang25@huawei.com> \\\*/#include "internal.h"  
 Copyright \\(C\\) 2017-2018 HUawei, Inc. \\\* <http://www.huawei.com/> \\\* Created by Gao Xiang <gaoxiang25@huawei.com> \\\*/#include  
 "internal.h"#include <linux/prefetch.h>  
 Copyright \\(C\\) 2017-2018 HUawei, Inc. \\\* <http://www.huawei.com/> \\\* Created by Gao Xiang <gaoxiang25@huawei.com> \\\*/#include  
 <linux/module.h>#include <linux/buffer\\\_head.h>#include <linux/  
 Copyright \\(C\\) 2017-2018 HUawei, Inc. \\\* <http://www.huawei.com/> \\\* Created by Gao Xiang <gaoxiang25@huawei.com> \\\*/#include  
 <linux/security.h>#include "xattr.h"  
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<linux/falloc.h>#include <linux/file.h>#incl  
Copyright (c) 2017-2019 Borislav Petkov, SUSE Labs. \*/#include <linux/mm.h>#include <linux/gfp.h>#include <linux/ras.h>#include  
<linux/kernel.h>#include <linux/workqueue.h>  
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<errno.h>#include <signal.h>#include <stdio.h>#include  
Copyright (c) 2017 Chao Yu <chao@kernel.org> \*/#include <linux/compiler.h>#include <linux/proc\_fs.h>#include <linux/f2fs\_fs.h>#include  
<linux/seq\_file.h>#include <linux/unicode.h>  
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<errno.h>#include <signal.h>#include <stdio.h>#include <st
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Copyright (C) 2017 Dell Inc. All Rights Reserved
Copyright (c) 2017 Dell Inc. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt
Copyright (C) 2017 Dell, Inc. */#ifndef _UAPI_LINUX_WMI_H#define _UAPI_LINUX_WMI_H
Copyright (C) 2017 Dennis Zhou <dennisszhou@gmail.com> ** The percpu allocator handles both static and dynamic areas. Percpu * areas
are allocated in chunks which are divided into units. There
Copyright (C) 2017 Dennis Zhou <dennisz@fb.com> ** Prints statistics about the percpu allocator and backing chunks.
Copyright (C) 2017 DENX Software Engineering ** Anatolij Gustschin <agust@denx.de>
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Copyright (c) 2017 Dialog Semiconductor ** Author: Adam Thomson <Adam.Thomson.Opensource@diasemi.com>
Copyright (c) 2017 Diego Elio Petten
Copyright (C) 2017, EETS GmbH, Felix Brack <fb@ltec.ch> ** This driver implements a lcd device for the ORISE OTM3225A display * controller. The
control interface to the display is SPI and the disp
Copyright (C) 2017 Endless Mobile, Inc. */
Copyright (C) 2017 Endless Mobile, Inc. * Authors: David Yang <yangxiaohua@everest-semi.com>, * Daniel Drake <drake@endlessm.com> *
Copyright (c) 2017, EPAM Systems */#include <linux/device.h>#include <linux/dma-buf.h>#include <linux/genalloc.h>#include <linux/slab.h>#include
<linux/tee_drv.h>#include "optee_private.h"#inc
Copyright (C) 2017 Eric Anholt */
Copyright (C) 2017 Eric Nelson */#include <linux/module.h>#include <linux/i2c.h>#include <linux/slab.h>#include <linux/rtc.h>#include
<linux/init.h>#include <linux/err.h>#include <linux/errno.h>
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Copyright (c) 2017 Eva Rachel Retuya <eraretuya@gmail.com> */
Copyright (c) 2017 Eva Rachel Retuya <eraretuya@gmail.com> ** 7-bit I2C slave address: 0x1D (ALT ADDRESS pin tied to VDDIO) or * 0x53 (ALT
ADDRESS pin grounded)
Copyright (c) 2017 Eva Rachel Retuya <eraretuya@gmail.com> ** Datasheet: http://www.analog.com/media/en/technical-documentation/data-
sheets/ADXL345.pdf
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Copyright (C) 2017 Facebook */
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Copyright (C) 2017 Facebook// Author: Roman Gushchin <guro@fb.com>
Copyright (c) 2017 Facebook */#ifndef __MAP_IN_MAP_H__#define __MAP_IN_MAP_H__
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Copyright (c) 2017 Facebook */#include <linux/bpf.h>#include <linux/slab.h>#include <linux/vmalloc.h>#include <linux/etherdevice.h>#include
<linux/filter.h>#include <linux/sched/signal.h>#incl
Copyright (c) 2017 Facebook */#include <linux/slab.h>#include <linux/bpf.h>
Copyright (c) 2017 Facebook */#include <stddef.h>#include <linux/bpf.h>#include <linux/pkt_cls.h>#include "bpf_helpers.h"
Copyright (c) 2017 Facebook#include <stddef.h>#include <stdbool.h>#include <string.h>#include <linux/pkt_cls.h>#include <linux/bpf.h>#include
<linux/in.h>#include <linux/if_ether.h>#include <
Copyright (c) 2017 Facebook */#include <stddef.h>#include <string.h>#include <linux/bpf.h>#include <linux/if_ether.h>#include
<linux/if_packet.h>#include <linux/ip.h>#include <linux/ipv6.h>#i
Copyright (c) 2017 Facebook */#include <stddef.h>#include <string.h>#include <linux/bpf.h>#include <linux/pkt_cls.h>#include "bpf_helpers.h"
Copyright (c) 2017 Facebook */#include <stdio.h>#include <unistd.h>#include <fcntl.h>#include <stdlib.h>#include <signal.h>#include
<linux/bpf.h>#include <string.h>#include <linux/perf_event.h>
Copyright (c) 2017 Facebook */#include <sys/resource.h>#include <sys/socket.h>#include <arpa/inet.h>#include <stdint.h>#include
<assert.h>#include <errno.h>#include <stdlib.h>#include <stdio.h>

```



```

Copyright (c) 2017 Facebook */#include "test_progs.h"#include "cgroup_helpers.h"#include "bpf_rlimit.h"#include <argp.h>#include <string.h>
Copyright (c) 2017 Facebook */#include <uapi/linux/bpf.h>#include "bpf_helpers.h"
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Copyright (C) 2017 Florian Eckert <fe@dev.tdt.de> */
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Copyright (C) 2017 Free Electrons * Maxime Ripard <maxime.ripard@free-electrons.com> */
Copyright (C) 2017 Free Electrons, * Quentin Schulz <quentin.schulz@free-electrons.com> * * The Sama5d2 SoC has two audio PLLs (PMC
and PAD) that shares the same parent * (FRAC). FRAC can
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Copyright (c) 2017, Fuzhou Rockchip Electronics Co., Ltd * * David Wu <david.wu@rock-chips.com>
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Copyright (C) 2017 Glider bvba */
Copyright (C) 2017 Glider bvba * * Based on clk-rcar-gen2.c
Copyright (C) 2017 Glider bvba * * Based on r8a7795-cpg-mssr.c
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__DT_BINDINGS_CLOCK_R8A77995_CPG_MSSR_H_
Copyright (C) 2017 Glider bvba */#ifndef __DT_BINDINGS_POWER_R8A77995_SYSC_H_#define __DT_BINDINGS_POWER_R8A77995_SYSC_H_
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Copyright (c) 2017 Google, Inc. * * Based on it87_wdt.c by Oliver Schuster * gpio-it8761e.c by Denis Turishev * gpio-stmpe.c by
Rabin Vincent
Copyright (C) 2017 Google, Inc. * Matthew Garrett <mjg59@google.com> * Thiebaud Weksteen <tweek@google.com> */#include
<linux/efi.h>#include <linux/tpm_eventlog.h>#include <asm/efi.h>
Copyright (C) 2017 Google, Inc. * Thiebaud Weksteen <tweek@google.com> */
Copyright (c) 2017 Google Inc * * VOUT_MODE is not supported by the device. The driver fakes VOUT linear16 * mode with exponent value -8 as direct
mode with m=256/b=0/R=0.
Copyright (c) 2017 Hans de Goede <hdegoede@redhat.com> */
Copyright (C) 2017 Hans de Goede <hdegoede@redhat.com> */
Copyright (C) 2017 Hans de Goede <hdegoede@redhat.com> * * Based on various non upstream patches to support the CHT Whiskey Cove PMIC:
Copyright (C) 2017 Hans de Goede <hdegoede@redhat.com> * * Loosely based on android x86 kernel code which is: *
Copyright (C) 2017 Hans de Goede <hdegoede@redhat.com> * * Note the quirks in this file are shared with fbdev/efib and as such * must not depend
on other drm code.
Copyright (C) 2017 Hans de Goede <hdegoede@redhat.com> * * Some Intel Cherry Trail based device which ship with Windows 10, have * this weird
INT33FE ACPI device with a CRS table with 4 I2cSerialBu
Copyright (C) 2017 Hans P. Moller <hmoller@uc.cl> */
Copyright (C) 2017 Hari Bathini, IBM Corporation */
Copyright (C) 2017 Hauke Mehrkens <hauke@hauke-m.de> */
Copyright (C) 2017 Hauke Mehrkens <hauke@hauke-m.de> * Based on EP93xx wdt driver */
Copyright (C) 2017 Hauke Mehrkens <hauke@hauke-m.de> */#ifndef _DT_BINDINGS_MIPS_LANTIQ_RCU_GPHY_H#define
__DT_BINDINGS_MIPS_LANTIQ_RCU_GPHY_H
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Copyright (c) 2017 HiSilicon Technologies Co., Ltd. \* \* Simple HiSilicon phase clock implementation.  
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Copyright (C) 2017 Horms Solutions, Simon Horman  
Copyright (c) 2017 Huawei Corporation. ";static struct hnae3\_client client;  
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Copyright (C) 2017 Impinj, Inc. \* \* Author: Andrey Smirnov <andrew.smirnov@gmail.com>  
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Copyright (C) 2017 Intel Corp \* Author: Jaikrishna Nemallapudi <jaikrishna.nemallapudi@intel.com> \* Author: Subhransu S. Prusty  
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Copyright (c) 2017, Intel Corporation. \* Author: Andi Kleen \*/#include <linux/list.h>#include <linux/zalloc.h>#include <stdlib.h>#include  
<sys/mman.h>#include <sys/stat.h>#include <fcntl.h>#i  
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<samu.onkalo@intel.com> * Jouni Högander <j
Copyright (C) 2017 Intel Corporation/* Functions to access TPS68470 power management chip. */
Copyright (C) 2017 Intel Corporation */#ifndef _LINUX_FPGA_MGR_H#define _LINUX_FPGA_MGR_H
Copyright (C) 2017, Intel Corporation */#include <linux/clk-provider.h>#include <linux/io.h>#include <linux/slab.h>#include "stratix10-clk.h"#include
"clk.h"
Copyright (c) 2017, Intel Corporation. */#include <linux/device.h>#include <linux/hid-sensor-hub.h>#include <linux/iio/buffer.h>#include
<linux/iio/iio.h>#include <linux/iio/triggered_buffer.h>
Copyright (C) 2017 Intel Corporation */#include <linux/fpga/fpga-bridge.h>#include <linux/fpga/fpga-mgr.h>#include <linux/fpga/fpga-
region.h>#include <linux/idr.h>#include <linux/kernel.h>#incl
Copyright (C) 2017 Intel Corporation */#include <linux/fpga/fpga-bridge.h>#include <linux/idr.h>#include <linux/kernel.h>#include
<linux/module.h>#include <linux/of_platform.h>#include <linux/s
Copyright (C) 2017, Intel Corporation */#include <linux/slab.h>#include <linux/clk-provider.h>#include <linux/io.h>
Copyright (C) 2017, Intel Corporation */#include <linux/slab.h>#include <linux/clk-provider.h>#include <linux/of_device.h>#include
<linux/of_address.h>#include <linux/platform_device.h>
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Copyright (C) 2017 Jacopo Mondì <jacopo *
Copyright (C) 2017 James.Bottomley@HansenPartnership.com */#include <linux/slab.h>#include "tpm-dev.h"
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Copyright (C) 2017 Jernej Skrabec <jernej.skrabec@siol.net> * * Coefficients are taken from BSP driver, which is:
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Copyright (c) 2017 Jesper Dangaard Brouer, Red Hat Inc. * * Example howto extract XDP RX-queue info
Copyright (c) 2017 Jesper Dangaard Brouer, Red Hat Inc. */#ifndef __LINUX_NET_XDP_H__#define __LINUX_NET_XDP_H__
Copyright (c) 2017 Jesper Dangaard Brouer, Red Hat Inc. */#include <linux/bpf.h>#include <linux/filter.h>#include <linux/types.h>#include
<linux/mm.h>#include <linux/netdevice.h>#include <linux
Copyright(c) 2017 Jesper Dangaard Brouer, Red Hat, Inc. */#include <uapi/linux/if_ether.h>#include <uapi/linux/if_packet.h>#include
<uapi/linux/if_vlan.h>#include <uapi/linux/ip.h>#include <uapi
Copyright(c) 2017 Jesper Dangaard Brouer, Red Hat, Inc. */static const char * __doc__ = "XDP monitor tool, based on tracepoints\n"
Copyright(c) 2017 Jesper Dangaard Brouer, Red Hat, Inc. */static const char * __doc__ = " XDP redirect with a CPU-map type
\"BPF_MAP_TYPE_CPUMAP\"";
Copyright (c) 2017 Jesper Dangaard Brouer, Red Hat Inc. */static const char * __doc__ = " XDP RX-queue info extract example\n\n" "Monitor how
many packets per sec (pps) are received\n" "per NIC RX
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Copyright (C) 2017 Josh Poimboeuf <jpoimboe@redhat.com> \*/#ifndef \_ORC\_LOOKUP\_H#define \_ORC\_LOOKUP\_H  
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Copyright (C) 2017 Linaro Ltd. \*/#ifndef \_\_HFI\_H\_\_#define \_\_HFI\_H\_\_  
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Copyright (C) 2017 Linaro Ltd. \*/#ifndef \_\_VENUS\_FIRMWARE\_H\_\_#define \_\_VENUS\_FIRMWARE\_H\_\_  
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Copyright (C) 2017 Linaro Ltd. \*/#ifndef \_\_VENUS\_HFI\_VENUS\_IO\_H\_\_#define \_\_VENUS\_HFI\_VENUS\_IO\_H\_\_  
Copyright (C) 2017 Linaro Ltd. \*/#ifndef \_\_VENUS\_VDEC\_H\_\_#define \_\_VENUS\_VDEC\_H\_\_  
Copyright (C) 2017 Linaro Ltd. \*/#ifndef \_\_VENUS\_VENC\_H\_\_#define \_\_VENUS\_VENC\_H\_\_  
Copyright (C) 2017 Linaro Ltd. \*/#include <linux/clock.h>#include <linux/init.h>#include <linux/ioctl.h>#include <linux/list.h>#include  
<linux/module.h>#include <linux/of\_device.h>#include <linu  
Copyright (C) 2017 Linaro Ltd. \*/#include <linux/clock.h>#include <linux/iopoll.h>#include <linux/list.h>#include <linux/mutex.h>#include  
<linux/pm\_runtime.h>#include <linux/slab.h>#include <med  
Copyright (C) 2017 Linaro Ltd. \*/#include <linux/clock.h>#include <linux/module.h>#include <linux/mod\_devicetable.h>#include  
<linux/platform\_device.h>#include <linux/pm\_runtime.h>#include <linux/  
Copyright (C) 2017 Linaro Ltd. \*/#include <linux/errno.h>#include <linux/hash.h>  
Copyright (c) 2017, Linaro Ltd. \*/#include <linux/firmware.h>#include <linux/module.h>#include <linux/notifier.h>#include <linux/slab.h>#include  
<linux/interrupt.h>#include <linux/io.h>#includ

Copyright (C) 2017 Linaro Ltd. \*/#include <linux/hash.h>#include <linux/list.h>#include <linux/slab.h>#include <media/videobuf2-v4l2.h>  
Copyright (C) 2017 Linaro Ltd. \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/debugfs.h>#include <linux/device.h>#include  
<linux/platform\_device.h>#include <linux/qrtr.h>#  
Copyright (C) 2017 Linaro Ltd. \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/device.h>#include <linux/qrtr.h>#include  
<linux/net.h>#include <linux/completion.h>#include <  
Copyright (C) 2017 Linaro Ltd. \*/#include <linux/slab.h>#include <linux/mutex.h>#include <linux/list.h>#include <linux/completion.h>#include  
<linux/platform\_device.h>#include <linux/videodev2.h  
Copyright (C) 2017 Linaro Ltd. \*/#include <linux/slab.h>#include <linux/uaccess.h>#include <linux/module.h>#include <linux/kernel.h>#include  
<linux/errno.h>#include <linux/string.h>#include <l  
Copyright (C) 2017 Linaro Ltd. \*/#include <linux/types.h>#include <media/v4l2-ctrls.h>  
Copyright (C) 2017 Linaro, Ltd., Rob Herring <robh@kernel.org> \* \* Based on hci\_ldisc.c: \*  
Copyright (C) 2017 Linus Walleij \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/platform\_device.h>#include  
<linux/gpio/consumer.h>#include <linux/of.h>#include <linux/of\_de  
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Copyright (C) 2017 Linus Walleij <linus.walleij@linaro.org> \* \* Based on a rewrite of arch/arm/mach-gemini/timer.c:  
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Copyright (C) 2017 Linus Walleij <linus.walleij@linaro.org> \* \* Based on the out-of-tree OpenWRT patch for Cortina Gemini:  
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Janos Laube <janos.dev@gmail.com>  
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Copyright (C) 2017 Linus Walleij <linus.walleij@linaro.org> \* Parts of this file were based on sources as follows: \*  
Copyright (C) 2017 Linus Walleij <linus.walleij@linaro.org> \* \* This is a group-only pin controller.  
Copyright (C) 2017 Linus Walleij <linus.walleij@linaro.org> \* \* This is a one-off touchkey controller based on the Cypress Semiconductor \* CY8C214  
MCU with some firmware in its internal 8KB flash.  
Copyright (C) 2017 Linus Walleij <linus.walleij@linaro.org> \* \* This SoC has an elaborate flash control register, so we need to \* detect and set it up  
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__DTS_IOMMU_PORT_MT2712_H
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<media/v4l2-mc.h>#include <media/v4l2-subdev.h>#include
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Copyright (c) 2017 Microsemi Corporation */#include <asm/machine.h>#include <asm/prom.h>
Copyright (c) 2017 Microsemi Corporation */#include <linux/bitops.h>#include <linux/irq.h>#include <linux/of_address.h>#include
<linux/of_irq.h>#include <linux/irqchip.h>#include <linux/irqchip
Copyright (c) 2017 Microsemi Corporation */#include <linux/delay.h>#include <linux/io.h>#include <linux/notifier.h>#include
<linux/mfd/syscon.h>#include <linux/of_address.h>#include <linux/of_d
Copyright (c) 2017 Microsemi Corporation */#include <linux/etherdevice.h>#include <linux/ethtool.h>#include <linux/if_bridge.h>#include
<linux/if_ether.h>#include <linux/if_vlan.h>#include <lin
Copyright (c) 2017 Microsemi Corporation */#include <linux/interrupt.h>#include <linux/module.h>#include <linux/of_net.h>#include
<linux/netdevice.h>#include <linux/of_mdio.h>#include <linux/of
Copyright (c) 2017 Microsemi Corporation */#include <linux/io.h>#include <linux/kernel.h>#include <linux/platform_device.h>
Copyright (c) 2017 Microsemi Corporation */#include "ocelot.h"#include <soc/mscc/ocelot_hsio.h>
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XArray.
Copyright (C) 2017, Microsoft Corporation. * * Author(s): Long Li <longli@microsoft.com>
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<net/sock.h>#include <net/af_vsock.h>
Copyright (C) 2017 MIPS Tech, LLC */#ifndef __ASM_SERIAL_H#define __ASM_SERIAL_H
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Copyright (c) 2017 Oscar Campos  
Copyright (c) 2017 Pablo M. Bermudo Garay <pablombg@gmail.com> \* \* This code is based on net/netfilter/nft\_fib\_inet.c, written by \* Florian Westphal  
<fw@strlen.de>.  
Copyright (c) 2017 Pablo Neira Ayuso <pablo@netfilter.org> \*/  
Copyright (C) 2017 Paolo Valente <paolo.valente@linaro.org> \* \* Bfq is a proportional-share I/O scheduler, with some extra \* low-latency capabilities.  
Bfq also supports full hierarchical \* schedul  
Copyright (c) 2017 Paul Cercueil <paul@crapouillou.net>  
Copyright (C) 2017 Pengutronix, Jan Luebbe <kernel@pengutronix.de> \*/  
Copyright (C) 2017 Pengutronix, Juergen Borleis <jbe@pengutronix.de> \*/#include <linux/dsa/lan9303.h>#include <linux/etherdevice.h>#include  
<linux/list.h>#include <linux/slab.h>  
Copyright (C) 2017 Pengutronix, Juergen Borleis <kernel@pengutronix.de> \*/#include <linux/kernel.h>#include <linux/module.h>#include  
<linux/gpio/consumer.h>#include <linux/regmap.h>#include <lin  
Copyright (C) 2017 Pengutronix, Juergen Borleis <kernel@pengutronix.de> \*/#include <linux/kernel.h>#include <linux/module.h>#include  
<linux/i2c.h>#include <linux/of.h>  
Copyright (C) 2017 Pengutronix, Juergen Borleis <kernel@pengutronix.de> \* \* Partially based on a patch from  
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<linux/interrupt.h>#include <linux/module.h>#include <linux/of\_grap  
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Copyright (c) 2017-present, Facebook, Inc. \*/#include <linux/crypto.h>#include <linux/init.h>#include <linux/interrupt.h>#include  
<linux/mm.h>#include <linux/module.h>#include <linux/net.h>#in  
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<linux/compiler.h>#include <subcmd/parse-options.h>#include "debug  
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<linux/module.h>#include <uapi/linux/kvm\_para.h>#inclu  
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DEFINITION FOR AUDIO DEVICES Release 3.0.  
Copyright (c) 2017 Sagi Grimberg. \*/#include <linux/blk-mq.h>#include <linux/blk-mq-rdma.h>#include <rdma/ib\_verbs.h>  
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Copyright (C) 2017 Samsung Electronics Co.Ltd \* Author:  
Copyright (c) 2017 Samsung Electronics Co., Ltd. \* Author: Marek Szymowski <m.szymowski@samsung.com> \* \* Common Clock Framework support  
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<linux/iopoll.h>#include <linux/mdio.h>#include <linux/mfd/syscon.h>#i
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<linux/mfd/mt6323/registers.h>#include <linux/mfd/mt6397/core.h>#include <linux/m
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<linux/mod_devicetable.h>#include <linux/init.h>#include <linux/device.h>#incl
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and can use the SBI to setup * events.
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Copyright (C) 2017 SiFive */#ifndef __UAPI_ASM_HWCAP_H#define __UAPI_ASM_HWCAP_H
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<linux/errno.h>#include <linux/gpio/driver.h>#include <linux/inte  
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<linux/syscalls.h>#include <linux/compat.h>#include <linux/refcount.  
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Copyright (C) 2018-2019 Intel Corporation \*/#ifndef \_\_NET\_WIRELESS\_CORE\_H#define \_\_NET\_WIRELESS\_CORE\_H#include <linux/list.h>#include  
<linux/netdevice.h>#include <linux/rbtree.h>#include <linux  
Copyright (C) 2018 - 2019 Intel Corporation \*/#ifndef \_\_PMSR\_H#define \_\_PMSR\_H#include <net/cfg80211.h>#include "core.h"#include  
"nl80211.h"#include "rdev-ops.h"  
Copyright (C) 2018-2019 Intel Corporation \*/#include <linux/export.h>#include <linux/bitops.h>#include <linux/etherdevice.h>#include  
<linux/slab.h>#include <linux/ieee80211.h>#include <net/cfg8  
Copyright (C) 2018-2019 Intel Corporation \*/#include <linux/kernel.h>#include <linux/slab.h>#include <linux/module.h>#include  
<linux/netdevice.h>#include <linux/wireless.h>#include <linux/nl802  
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<signal.h>#include <inttypes.h>  
Copyright (C) 2018-2019 Jens Axboe \*/#include <stdio.h>#include <fcntl.h>#include <string.h>#include <stdlib.h>#include <unistd.h>#include  
<assert.h>#include <errno.h>#include <inttypes.h>#i  
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Copyright (c) 2018-2019, Vladimir Oltean <olteanv@gmail.com> \*/#ifndef \_SJA1105\_STATIC\_CONFIG\_H#define \_SJA1105\_STATIC\_CONFIG\_H  
Copyright (c) 2018-2019, Vladimir Oltean <olteanv@gmail.com> \*/#include <linux/packing.h>#include <linux/module.h>#include  
<linux/bitops.h>#include <linux/errno.h>#include <linux/types.h>  
Copyright (c) 2018-2019, Vladimir Oltean <olteanv@gmail.com> \*/#include <linux/packing.h>#include "sja1105.h"  
Copyright (c) 2018-2019, Vladimir Oltean <olteanv@gmail.com> \*/#include <linux/spi/spi.h>#include <linux/packing.h>#include "sja1105.h"  
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Copyright (c) 2018-2019, Vladimir Oltean <olteanv@gmail.com> \*/#include "sja1105\_static\_config.h"#include <linux/crc32.h>#include  
<linux/slab.h>#include <linux/string.h>#include <linux/errno.h>  
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<linux/platform\_device.h>#include <linux/reset-controller.h>#include <  
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<linux/serial\_core.h>#include <asm/sbi.h>

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and cache topology. \* Due to the relative p  
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Copyright (C) 2018 \* Author: Jeff LaBundy <jeff@labundy.com> \* \* These devices require firmware exported from a PC-based configuration tool \* made  
available by the vendor. Firmware files may be pu  
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<linux/of.h>#include <linux/platform_device.h>#include <li
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Copyright (c) 2018 Cavium, Inc. */#include <linux/bcd.h>#include <linux/delay.h>#include <linux/i2c.h>#include <linux/module.h>#include
<linux/mutex.h>#include <linux/nvmem-provider.h>#include
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<linux/irqchip.h>#include <linux/irqdomain
Copyright (C) 2018 Christoph Hellwig. * * DMA operations that map physical memory directly without using an IOMMU.
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<linux/pagemap.h>#include <linux/pagevec.h>
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Copyright (c) 2018 Covalent IO, Inc. http://covalent.io#define SOCKMAP#define TEST_MAP_TYPE BPF_MAP_TYPE SOCKMAP
Copyright (c) 2018 Covalent IO, Inc. http://covalent.io */#include <uapi/linux/btf.h>#include <linux/kernel.h>#include <linux/types.h>#include
<linux/slab.h>#include <linux/bpf.h>#include <linu
Copyright (c) 2018 Covalent IO, Inc. http://covalent.io#undef SOCKMAP#define TEST_MAP_TYPE BPF_MAP_TYPE SOCKHASH
Copyright (c) 2018, Craig Tatlor. */
Copyright (c) 2018 Crane Merchandising Systems. All rights reserved
Copyright (C) 2018 Cryptera A/S * * Author: Cyrille Pitchen <cyrille.pitchen@atmel.com> * Author: Piotr Bugalski <bugalski.piotr@gmail.com>
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 Copyright (c) 2018 Davide Caratti, Red Hat inc.  
 Copyright (C) 2018 David Lechner <david@lechnology.com> \*/  
 Copyright (C) 2018 David Lechner <david@lechnology.com> \*\* Based on arch/arm/mach-davinci/clock.c  
 Copyright (C) 2018 Davidlohr Bueso. \*\* Benchmark the various operations allowed for epoll\_ctl(2). \* The idea is to concurrently stress a single epoll instance  
 Copyright (C) 2018 Davidlohr Bueso. \*\* This program benchmarks concurrent epoll\_wait(2) monitoring multiple \* file descriptors under one or two load balancing models. The first, \* and default, is  
 Copyright (c) 2018, DENX Software Engineering GmbH \* Author: Parthiban Nallathambi <pn@denx.de> \*\* TODO: Proximity  
 Copyright (C) 2018 Digital Devices GmbH  
 Copyright (c) 2018 Dmitry Safonov, Arista Networks \*\* MAP\_POPULATE | MAP\_PRIVATE should COW VMA pages.  
 Copyright (c) 2018 Dmitry V. Levin <ldv@altlinux.org> \* All rights reserved  
 Copyright (c) 2018, Eideticom Inc. \*/  
 Copyright (C) 2018 embedded brains GmbH \*/#include "libfdt\_env.h"  
 Copyright (C) 2018 EPAM Systems Inc. \*\* Author: Oleksandr Andrushchenko <oleksandr\_andrushchenko@epam.com>  
 Copyright (c) 2018, Ericsson AB \* All rights reserved  
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 Copyright (c) 2018 Facebook  
 Copyright (c) 2018 Facebook  
 Copyright (C) 2018 Facebook  
 Copyright (C) 2018 Facebook// Author: Yonghong Song <yhs@fb.com>  
 Copyright (c) 2018 Facebook \*\* BPF program to automatically reflect TOS option from received syn packet  
 Copyright (c) 2018 Facebook#ifndef \_UAPI\_LINUX\_BTFS\_H\_#define \_UAPI\_LINUX\_BTFS\_H\_  
 Copyright (c) 2018 Facebook, Inc. \* Author: Johannes Weiner <hannes@cmpxchg.org> \*\* Polling support by Suren Baghdasaryan <surenb@google.com>  
 Copyright (c) 2018 Facebook#include <linux/bpf.h>#include "bpf\_helpers.h"  
 Copyright (c) 2018 Facebook \*/#include <linux/bpf.h>#include <linux/err.h>#include <linux/sock\_diag.h>#include <net/sock\_reuseport.h>  
 Copyright (c) 2018 Facebook#include <stddef.h>#include <linux/bpf.h>#include <linux/types.h>#include "bpf\_helpers.h"  
 Copyright (c) 2018 Facebook#include <uapi/linux/bpf.h>#include "bpf\_helpers.h"  
 Copyright (C) 2018 Felix Fietkau <nbd@nbd.name> \*/#include "mt76.h"  
 Copyright (C) 2018 Florian Fainelli <f.fainelli@gmail.com> \*/  
 Copyright (C) 2018 Free Software Foundation, Inc. \*/  
 Copyright (c) 2018 Fuzhou Rockchip Electronics Co., Ltd \*/  
 Copyright (C) 2018 Gao Xiang <gaoxiang25@huawei.com> \*/#ifndef \_\_EROFS\_FS\_TAGPTR\_H#define \_\_EROFS\_FS\_TAGPTR\_H  
 Copyright (C) 2018 Gateworks Corporation \*/  
 Copyright (C) 2018 Gateworks Corporation \*/#include <linux/delay.h>#include <linux/hdmi.h>#include <linux/i2c.h>#include <linux/init.h>#include <linux/interrupt.h>#include <linux/kernel.h>#inc  
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 Copyright (C) 2018 Google, Inc. \*/  
 Copyright (C) 2018 Google, Inc. \*\* Author: Hans Verkuil <hans.verkuil@cisco.com> \* Author: Sakari Ailus <sakari.ailus@linux.intel.com>  
 Copyright (C) 2018 Google, Inc. \*\* Author: schuffelen@google.com  
 Copyright (C) 2018 Google, Inc. \*/#ifndef \_\_APEX\_H#define \_\_APEX\_H  
 Copyright (C) 2018 Google, Inc.#ifndef \_\_GASKET\_CONSTANTS\_H#define \_\_GASKET\_CONSTANTS\_H  
 Copyright (C) 2018 Google, Inc. \*/#ifndef \_\_GASKET\_CORE\_H#define \_\_GASKET\_CORE\_H  
 Copyright (C) 2018 Google, Inc. \*/#ifndef \_\_GASKET\_H#define \_\_GASKET\_H  
 Copyright (C) 2018 Google, Inc. \*/#ifndef \_\_GASKET\_INTERRUPT\_H#define \_\_GASKET\_INTERRUPT\_H  
 Copyright (C) 2018 Google, Inc.#ifndef \_\_GASKET\_IOCTL\_H#define \_\_GASKET\_IOCTL\_H  
 Copyright (C) 2018 Google, Inc.#include "gasket.h"#include "gasket\_ioctl.h"#include "gasket\_constants.h"#include "gasket\_core.h"#include "gasket\_interrupt.h"#include "gasket\_page\_table.h"#inclu  
 Copyright (C) 2018 Google, Inc.#include "gasket\_sysfs.h"  
 Copyright (c) 2018 Google, Inc. \*\* When CPU, memory and IO are contended, tasks experience delays that \* reduce throughput and introduce latencies into the workload. Memory \* and IO contention, i  
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Copyright (C) 2018, Google LLC. */
Copyright (C) 2018, Google LLC. * * Adaptions for s390x:
Copyright (C) 2018, Google LLC. * * Header file that describes API to the sparsebit library. * This library provides a memory efficient means of storing *
the settings of bits indexed via a uint64
Copyright (C) 2018, Google LLC. * * IA32_TSC_ADJUST test
Copyright (C) 2018, Google LLC. */#ifndef SELFTEST_KVM_UTIL_H#define SELFTEST_KVM_UTIL_H
Copyright (C) 2018, Google LLC. * * This is a regression test for the bug fixed by the following commit:
Copyright (C) 2018, Google LLC. * * Verifies expected behavior of x86 KVM_CAP_SYNC_REGS functionality, * including requesting an invalid register set,
updates to/from values * in kvm_run.s.regs wh
Copyright (c) 2018 Guenter Roeck */
Copyright (c) 2018 Guillaume Dou * * Implementation inspired by existing x86 platform drivers, in special * asus/eepc/fujitsu-laptop, thanks to their
authors.
Copyright (C) 2018 Hangzhou C-SKY Microsystems co.,ltd.
Copyright (C) 2018 Hangzhou C-SKY Microsystems co.,ltd.#include <linux/kernel.h>#include <linux/module.h>#include <linux/init.h>#include
<linux/delay.h>
Copyright (c) 2018 Hanno Zulla <kontakt@hanno.de> */
Copyright (C) 2018 Hans de Goede <hdegoede@redhat.com> * * Based on various non upstream patches for ISP support:
Copyright (C) 2018 Helge Deller <deller@gmx.de> *
Copyright (C) 2018 Himanshu Jha <himanshujha199640@gmail.com> * * 7-Bit I2C slave address is:
Copyright (C) 2018 Himanshu Jha <himanshujha199640@gmail.com> * * Datasheet:
Copyright (C) 2018 Himanshu Jha <himanshujha199640@gmail.com> */#include <linux/acpi.h>#include <linux/module.h>#include
<linux/of.h>#include <linux/regmap.h>#include <linux/spi/spi.h>
Copyright (c) 2018 Hisilicon Limited. */
Copyright (c) 2018 HiSilicon Technologies Co., Ltd. */
Copyright (C) 2018 HUAWEI, Inc. * http://www.huawei.com/ * Created by Gao Xiang <gaoxiang25@huawei.com> */#ifndef
__EROFS_FS_ZDATA_H#define __EROFS_FS_ZDATA_H
Copyright (C) 2018 HUAWEI, Inc. * http://www.huawei.com/ * Created by Gao Xiang <gaoxiang25@huawei.com> */#ifndef
__EROFS_FS_ZPVEC_H#define __EROFS_FS_ZPVEC_H
Copyright (C) 2018 HUAWEI, Inc. * http://www.huawei.com/ * Created by Gao Xiang <gaoxiang25@huawei.com> */#include
"internal.h"#include <linux/pagevec.h>
Copyright (C) 2018 HUAWEI, Inc. * http://www.huawei.com/ * Created by Gao Xiang <gaoxiang25@huawei.com> */#include "zdata.h"#include
"compress.h"#include <linux/prefetch.h>
Copyright (c) 2018 IBM Corp.
Copyright (C) 2018 IBM Corp. All rights reserved
Copyright (C) 2018 IBM Corporation */
Copyright (C) 2018 IBM Corporation. */#include <limits.h>#include <linux/kernel.h>#include <sys/mman.h>#include <sys/types.h>#include
<sys/stat.h>#include <sys/time.h>#include <sys/resource.h>
Copyright (C) 2018 IBM Corporation */#include <linux/efi.h>#include <linux/module.h>#include <linux/ima.h>
Copyright (C) 2018 IBM Corporation. */#include "ptrace.h"#include "child.h"
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Copyright (C) 2018 Intel Corporation \* \* Author: Lu Baolu <baolu.lu@linux.intel.com>  
Copyright (C) 2018 Intel Corporation \* Author: Rafael J. Wysocki <rafael.j.wysocki@intel.com> \* \* The idea of this governor is based on the observation  
that on many systems \* timer events are two  
Copyright (C) 2018 Intel Corporation \* Author: Rafael J. Wysocki <rafael.j.wysocki@intel.com> \* \* This driver is based on Section 9.18 of the ACPI 6.2  
specification revision.  
Copyright (C) 2018 Intel Corporation \* \* Author: Reinette Chatre <reinette.chatre@intel.com>  
Copyright (C) 2018, Intel Corporation \* Authors: Andy Shevchenko <andriy.shevchenko@linux.intel.com> \* Mika Westerberg  
<mika.westerberg@linux.intel.com> \*/  
Copyright(c) 2018 Intel Corporation. \* \* Author(s): Björn Töpel <bjorn.topel@intel.com> \* Magnus Karlsson <magnus.karlsson@intel.com>  
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Copyright (C) 2018, Intel Corporation \* Copied from reset-sunxi.c \*/  
Copyright (C) 2018 Intel Corporation \*/#ifndef \_\_NET\_WIRELESS\_NL80211\_H#define \_\_NET\_WIRELESS\_NL80211\_H  
Copyright (C) 2018 Intel Corporation \*/#include <linux/completion.h>#include <linux/fpga/fpga-mgr.h>#include <linux/firmware/intel/stratix10-svc-  
client.h>#include <linux/module.h>#include <linux  
Copyright (C) 2018 Intel Corporation \*/#include <linux/slab.h>#include <linux/kernel.h>#include <linux/if\_arp.h>#include  
<linux/netdevice.h>#include <linux/rtnetlink.h>#include <net/mac80211.h>  
Copyright (C) 2018 Intel Corporation \*/#include <linux/types.h>#include <net/cfg80211.h>#include "ieee80211\_i.h"#include "sta\_info.h"#include  
"driver-ops.h"  
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header files.  
Copyright (C) 2018 Intel Corporation \* \* Portions of this file are derived from the ipw3945 project, as well \* as portions of the ieee80211 subsystem  
header files.  
Copyright (C) 2018 Intel Corporation \* \* Specification for this interface is available at: \*  
Copyright (C) 2018 Intel Corporation \* \* Transmit and frame generation functions.  
Copyright (C) 2018, Intel, Inc. \* \* Author : Yi Sun <yi.y.sun@intel.com>  
Copyright(c) 2018 Ira Weiny, Intel Corporation \*/  
Copyright (C) 2018 IVI Foundation, Inc. \*/  
Copyright (C) 2018 IVI Foundation, Inc. \* \* This file holds USB constants defined by the USB Device Class \* and USB488 Subclass Definitions for Test  
and Measurement devices \* published by the USB-  
Copyright (C) 2018 Jacob Feder \* \* Authors: Jacob Feder <jacobsfeder@gmail.com>  
Copyright (C) 2018 Jacopo Mondi <jacopo  
Copyright (C) 2018 Jacopo Mondi <jacopo \*/  
Copyright (C) 2018 Jacopo Mondi <jacopo \*  
Copyright (C) 2018 Jacopo Mondi <jacopo \* \* Based on Renesas R-Car M3-W System Controller  
Copyright (C) 2018 Jacopo Mondi <jacopo \*/#ifndef \_\_DT\_BINDINGS\_CLOCK\_R8A77965\_CPG\_MSSR\_H\_\_define  
\_\_DT\_BINDINGS\_CLOCK\_R8A77965\_CPG\_MSSR\_H\_\_  
Copyright (C) 2018, Jacopo Mondi <jacopo@jmondi.org> \*  
Copyright (C) 2018 Jacopo Mondi <jacopo@jmondi.org> \* \* Based on mt9v032 driver  
Copyright (c) 2018 James.Bottomley@HansenPartnership.com \* \* CFB is a stream cipher mode which is layered on to a block \* encryption scheme. It  
works very much like a one time pad where \* the pad  
Copyright (C) 2018 Jan Breuer <jan.breuer@jaybee.cz> \* \* Improve driver for i2c detect from i2c-tools to detect i2c devices on the bus.  
Copyright (c) 2018 Jernej Skrabec <jernej.skrabec@siol.net>  
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Copyright (C) 2018 Jerome Brunet, Baylibre SAS <jbrunet@baylibre.com> */
Copyright (c) 2018 Jesper Dangaard Brouer, Red Hat Inc.
Copyright (c) 2018 Jesper Dangaard Brouer, Red Hat Inc. * * Example howto transfer info from XDP to SKB, e.g. skb->mark
Copyright (c) 2018 Jesper Dangaard Brouer, Red Hat Inc. */static const char *__doc__ = "Libbpf test program for loading BPF ELF object files";
Copyright(c) 2018 Jesper Dangaard Brouer. * * XDP/TC VLAN manipulation example
Copyright (c) 2018 Jim Wilson (jimw@sifive.com) */
Copyright (C) 2018 Joe Lawrence <joe.lawrence@redhat.com>
Copyright (C) 2018 Joel Fernandes (Google) <joel@joelfernandes.org> */
Copyright (C) 2018 Johan Hovold <johan@kernel.org> */
Copyright (C) 2018 Johannes Thumshirn <jth@kernel.org> */#include <linux/io.h>#include <linux/kernel.h>#include <linux/mcb.h>#include
<linux/module.h>#include <linux/watchdog.h>
Copyright (c) 2018 Johannes Thumshirn, SUSE Linux GmbH */
Copyright (c) 2018 Johannes Thumshirn, SUSE Linux GmbH * * This is entirely based on drivers/nvme/host/trace.h
Copyright (C) 2018 John Crispin <john@phrozen.org> */
Copyright (c) 2018 Jonathan Marek <jonathan@marek.ca> * * Based on msm_pwm_vibrator.c from downstream Android sources:
Copyright (C) 2018 Josef Bacik */#include <linux/kernel.h>#include <linux/blk_types.h>#include <linux/backing-dev.h>#include
<linux/module.h>#include <linux/timer.h>#include <linux/memcontrol.h
Copyright (c) 2018 Juergen Gross */
Copyright (c) 2018 Julian Sax <jsbc@gmx.de> *
Copyright (C) 2018 Kent Gustavsson <kent@minoris.se> */#include <linux/clk.h>#include <linux/delay.h>#include <linux/err.h>#include
<linux/iio/iio.h>#include <linux/module.h>#include <linux/reg
Copyright (C) 2018 Kirill Marinushkin <kmarinushkin@birdec.com>
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Copyright (c) 2018 Kontron Electronics GmbH * * Author: Frieder Schrempf <frieder.schrempf@kontron.de>
Copyright (C) 2018 Kontron Electronics GmbH * * Transition to SPI MEM interface:
Copyright (C) 2018 KVASER AB, Sweden. All rights reserved
Copyright (C) 2018 Laurent Pinchart <laurent.pinchart@ideasonboard.com> * * Based on work from Jyri Sarha <jsarha@ti.com>
Copyright (C) 2018 Laurent Pinchart <laurent.pinchart@ideasonboard.com> */#ifndef __RCAR_DU_OF_H__#define __RCAR_DU_OF_H__
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Copyright (c) 2018, Linaro Ltd. * Author: Manivannan Sadhasivam <manivannan.sadhasivam@linaro.org> */
Copyright (C) 2018 Linaro Ltd. * * Author: Stanimir Varbanov <stanimir.varbanov@linaro.org>
Copyright (C) 2018 Linaro Ltd * * Based on OV5640 Sensor Driver
Copyright (c) 2018 Linaro Ltd. */#ifndef DT_BINDINGS_POWER_OWL_S900_POWERGATE_H#define
DT_BINDINGS_POWER_OWL_S900_POWERGATE_H
Copyright (C) 2018 Linaro Ltd.#ifndef __VENUS_HFI_PARSER_H__#define __VENUS_HFI_PARSER_H__
Copyright(c) 2018 Linus Torvalds. All rights reserved
Copyright (C) 2018 Linus Walleij <linus.walleij@linaro.org> * Parts of this file were based on the MCDE driver by Marcus Lorentzon
Copyright (C) 2018 Linus Walleij * * This module binds to the "arm,vexpress-muxfpga" device on the * Versatile Express configuration bus and sets up
which CLCD instance * gets muxed out on the DVI
Copyright (C) 2018 Linus Wallej <linus.walleij@linaro.org> * Includes portions of code from the firmware uploader by:
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```

```

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Copyright (C) 2018 Lorenzo Bianconi <lorenzo.bianconi83@gmail.com> */#include <linux/kernel.h>#include <linux/firmware.h>
Copyright (C) 2018 Lorenzo Bianconi <lorenzo.bianconi83@gmail.com> */#include <linux/kernel.h>#include <linux/firmware.h>#include
<linux/module.h>
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Copyright (c) 2018, Mahesh Bandewar <maheshb@google.com> */
Copyright (c) 2018 Manivannan Sadhasivam */
Copyright (c) 2018 Manivannan Sadhasivam * * RDA8810PL has two independent timers: OSTIMER (56 bit) and HWTIMER (64 bit). * Each timer
provides optional interrupt support. In this driver, OSTIMER i
Copyright (C) 2018 Marcus Folkesson <marcus.folkesson@gmail.com>
Copyright (C) 2018 Marcus Folkesson <marcus.folkesson@gmail.com> */
Copyright (C) 2018 Marcus Folkesson <marcus.folkesson@gmail.com> */#include <linux/bitops.h>#include <linux/iio/iio.h>#include
<linux/iio/sysfs.h>#include <linux/init.h>#include <linux/module.h>
Copyright (C) 2018 Marek Behun <marek.behun@nic.cz> */
Copyright (C) 2018 Marek Vasut <marex@denx.de> */#include <linux/delay.h>#include <linux/ethtool.h>#include <linux/kernel.h>#include
<linux/mii.h>#include <linux/module.h>#include <linux/phy.h>
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Copyright (c) 2018 MediaTek Inc. * Author: Chunhui Dai <chunhui.dai@mediatek.com> */
Copyright (c) 2018 MediaTek Inc. * Author: Houlong Wei <houlong.wei@mediatek.com> *
Copyright (c) 2018 MediaTek Inc. * Author: Jie Qiu <jie.qiu@mediatek.com> */
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Copyright (C) 2018 MediaTek Inc. * Author: Sean Wang <sean.wang@mediatek.com> * Zhiyong Tao <zhiyong.tao@mediatek.com> *
Hongzhou.Yang <hongzhou.yang@mediatek.com> */
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<linux/of.h>#include <linux/of\_device.h>#inclu  
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<linux/sched.h>#include <linux/slab.h>#include <video/imx-ipu-v3.h>  
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Copyright (c) 2018 Politecnico di Torino#define MAP\_TYPE BPF\_MAP\_TYPE\_QUEUE  
Copyright (c) 2018 Politecnico di Torino#define MAP\_TYPE BPF\_MAP\_TYPE\_STACK  
Copyright (c) 2018 Politecnico di Torino \*/#include <linux/bpf.h>#include <linux/list.h>#include <linux/slab.h>#include <linux/capability.h>#include  
"percpu\_freelist.h"  
Copyright (c) 2018 Politecnico di Torino#include <stddef.h>#include <string.h>#include <linux/bpf.h>#include <linux/if\_ether.h>#include  
<linux/ip.h>#include <linux/pkt\_cls.h>#include "bpf\_helpe  
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with an index size of 2^64. A sparsebit arr  
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Copyright (C) 2018 Red Hat, Inc. \*/#ifndef \_\_SS\_SIDTAB\_H\_\_define \_\_SS\_SIDTAB\_H\_\_  
Copyright (C) 2018, Red Hat, Inc. \*/#include "kvm\_util.h"  
Copyright (C) 2018, Red Hat, Inc. \*/#include "kvm\_util.h"#include "../kvm\_util\_internal.h"  
Copyright (C) 2018 Red Hat, Inc. \*/#include <linux/errno.h>#include <linux/kernel.h>#include <linux/slab.h>#include <linux/sched.h>#include  
<linux/spinlock.h>#include <asm/barrier.h>#include "  
Copyright (C) 2018, Red Hat, Inc. \* \* Tests for Enlightened VMCS, including nested guest state.  
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sending \* a "Read Medium Error" sense.

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Copyright (C) 2018 Renesas Electronics Corp. \*/#ifndef \_\_DT\_BINDINGS\_CLOCK\_R8A774C0\_CPG\_MSSR\_H\_\_#define

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\_\_DT\_BINDINGS\_POWER\_R8A7744\_SYSC\_H\_\_

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Copyright (C) 2018 Stefan Wahren <stefan.wahren@i2se.com> \*/#include <linux/device.h>#include <linux/err.h>#include <linux/hwmon.h>#include  
<linux/module.h>#include <linux/platform\_device.h>#in  
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<linux/clk.h>#include <linux/delay.h>#include <linux/io.h>#inc  
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Copyright (C) 2018 Western Digital Corporation \*/#include "ufs\_bsg.h"  
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Copyright (C) 2018 William Breathitt Gray \*/#ifndef \_COUNTER\_ENUM\_H#define \_COUNTER\_ENUM\_H  
Copyright (C) 2018 William Breathitt Gray \*/#ifndef \_COUNTER\_H#define \_COUNTER\_H  
Copyright (C) 2018 William Breathitt Gray \*/#include <linux/counter.h>#include <linux/device.h>#include <linux/err.h>#include  
<linux/export.h>#include <linux/fs.h>#include <linux/gfp.h>#include  
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Copyright (C) 2018 Xue Liu <liuxuenetmail@gmail.com> \*/#include <linux/kernel.h>#include <linux/module.h>#include  
<linux/gpio/consumer.h>#include <linux/spi/spi.h>#include <linux/workqueue.h>#i  
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<linux/platform_device.h>#include <linux/reset-controller.h>#include
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Copyright (c) 2019 Bootlin */
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Copyright (C) 2019 Christian Hewitt <christianshewitt@gmail.com>
Copyright (c) 2019 Christian Mauderer <oss@c-mauderer.de>
Copyright (C) 2019 Christoph Hellwig */#ifndef LINUX_IO_URING_H#define LINUX_IO_URING_H
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Copyright (c) 2019 Cloudflare
Copyright (C) 2019 Collabora Ltd. */
Copyright (c) 2019 David Ahern <dsahern@gmail.com>. All rights reserved
Copyright (C) 2019 Dell Inc * by Narendra K <Narendra.K@dell.com> * * System firmware advertises the address of the RC12 Table via * an EFI
Configuration Table entry. This code retrieves the RC12
Copyright (C) 2019, Dell Inc * * The PCIe Link Bandwidth Notification provides a way to notify the * operating system when the link width or data rate
changes. This * capability is required for a
Copyright (C) 2019 Dialog Semiconductor
Copyright (C) 2019 Dialog Semiconductor * * Author: Eric Jeong <eric.jeong.opensource@diasemi.com>
Copyright (c) 2019 Facebook
Copyright (c) 2019 Facebook
Copyright (c) 2019 Facebook */
Copyright (C) 2019 Facebook
Copyright (c) 2019 Facebook#define STACK_MAX_LEN 100
Copyright (c) 2019 Facebook#define STACK_MAX_LEN 180
Copyright (c) 2019 Facebook#define STACK_MAX_LEN 50
Copyright (c) 2019 Facebook#define STACK_MAX_LEN 600/* clang will not unroll the loop 600 times. * Instead it will unroll it to the amount it deemed *
appropriate, but the loop will still execute
Copyright (c) 2019 Facebook#define STACK_MAX_LEN 600#define NO_UNROLL/* clang will not unroll at all. * Total program size is around 2k insns */
Copyright (c) 2019 Facebook#ifndef __ASM_GOTO_WORKAROUND_H#define __ASM_GOTO_WORKAROUND_H
Copyright (c) 2019 Facebook#ifndef __BPF_SK_STORAGE_H#define __BPF_SK_STORAGE_H
Copyright (c) 2019 Facebook */#ifndef __LIBBPF_HASHMAP_H#define __LIBBPF_HASHMAP_H
Copyright (c) 2019 Facebook Inc *
Copyright (c) 2019 Facebook#include <features.h>
Copyright (c) 2019 Facebook#include <linux/bpf.h>#include "bpf_helpers.h"#define ATTR __always_inline#include "test_jhash.h"
Copyright (c) 2019 Facebook#include <linux/bpf.h>#include "bpf_helpers.h"#define ATTR __attribute__((noinline))#include "test_jhash.h"
Copyright (c) 2019 Facebook#include <linux/bpf.h>#include <linux/version.h>#include "bpf_helpers.h"
Copyright (c) 2019 Facebook#include <linux/compiler.h>#include <linux/err.h>
Copyright (c) 2019 Facebook#include <linux/rculist.h>#include <linux/list.h>#include <linux/hash.h>#include <linux/types.h>#include
<linux/spinlock.h>#include <linux/bpf.h>#include <net/bpf_sk_
Copyright (c) 2019 Facebook#include <linux/sched.h>#include <linux/ptrace.h>#include <stdint.h>#include <stddef.h>#include <stdbool.h>#include
<linux/bpf.h>#include "bpf_helpers.h"
Copyright (c) 2019 Facebook */#include <stdbool.h>
Copyright (c) 2019 Facebook#include <stddef.h>#include <string.h>#include <linux/bpf.h>#include <linux/if_ether.h>#include
<linux/if_packet.h>#include <linux/ip.h>#include <linux/ipv6.h>#inclu
Copyright (c) 2019 Facebook */#include <stdint.h>#include <stdlib.h>#include <stdio.h>#include <errno.h>#include <linux/err.h>#include
"hashmap.h"
Copyright (c) 2019 Facebook */#include <stdio.h>#include <errno.h>#include <linux/err.h>#include "hashmap.h"
Copyright (c) 2019 Facebook#include <test_progs.h>static int libbpf_debug_print(enum libbpf_print_level level, const char *format,
va_list args){ if (level != LIBBPF_DEBUG) { vprintf(fo
Copyright (C) 2019 Facebook * * One challenge of controlling IO resources is the lack of trivially * observable cost metric. This is distinguished from CPU
and memory where * wallclock time and t
Copyright (c) 2019 Facebook *//* ----- START-EXPECTED-OUTPUT ----- */enum e1 {A = 0, B = 1,
Copyright (c) 2019 Facebook *//* ----- START-EXPECTED-OUTPUT ----- */struct packed_trailing_space { int a; short b; } __attribute__((packed));
Copyright (c) 2019 Facebook *//* ----- START-EXPECTED-OUTPUT ----- */struct padded_implicitly { int a; long int b; char c;
Copyright (c) 2019 Facebook *//* ----- START-EXPECTED-OUTPUT ----- */struct s1 {};
Copyright (c) 2019 Facebook *//* ----- START-EXPECTED-OUTPUT ----- */struct S { int S; int U;

```

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Copyright (c) 2019 Facebook **/ * ----- START-EXPECTED-OUTPUT ----- */typedef int arr_t[2];
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Copyright (C) 2019 Glider bvba */
Copyright (c) 2019 Google Inc. */
Copyright (C) 2019 Google, Inc.
Copyright (c) 2019, Google Inc. * Author - Rajat Jain * * This code instantiates platform devices for intel_pmc_core driver, only * on supported platforms
that may not have the ACPI devices in the
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Copyright (C) 2019 Google, Inc. *Tomasz Figa <tfiga@chromium.org> */
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Copyright (c) 2019 Google LLC * * Based on aesni-intel_glue.c by:
Copyright (C) 2019 Google LLC <ebiggers@google.com>
Copyright (C) 2019, Google LLC. * * Test for KVM_CAP_MAX_VCPU and KVM_CAP_MAX_VCPU_ID.
Copyright (C) 2019, Google LLC. * * This test verifies the integrity of calling the ioctl KVM_SET_NESTED_STATE.
Copyright (c) 2019 Google LLC * * Updated RFC4106 AES-GCM testing. * Authors: Aidan O'Mahony (aidan.o.mahony@intel.com) * Adrian
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Copyright (C) 2019 Hangzhou C-SKY Microsystems co.,ltd.// Mapping of DWARF debug register numbers into register names.
Copyright (C) 2019 Helge Deller <deller@gmx.de> */
Copyright (C) 2019 Helge Deller <deller@gmx.de> * * Based on arch/arm64/kernel/jump_label.c
Copyright (c) 2019 Hisilicon Limited.
Copyright (c) 2019 HiSilicon Limited.#ifndef HISI_ACC_QM_H#define HISI_ACC_QM_H
Copyright (c) 2019 HiSilicon Limited.#ifndef HISI_ACC_SGL_H#define HISI_ACC_SGL_H
Copyright (c) 2019 HiSilicon Limited.#ifndef HISI_ZIP_H#define HISI_ZIP_H
Copyright (c) 2019 HiSilicon Limited.#include <asm/page.h>#include <linux/bitmap.h>#include <linux/debugfs.h>#include <linux/dma-
mapping.h>#include <linux/io.h>#include <linux/irqreturn.h>#incl
Copyright (c) 2019 HiSilicon Limited.#include <crypto/internal/acompress.h>#include <linux/bitfield.h>#include <linux/dma-mapping.h>#include
<linux/scatterlist.h>#include "zip.h"
Copyright (c) 2019 HiSilicon Limited.#include <linux/acpi.h>#include <linux/aer.h>#include <linux/bitops.h>#include <linux/debugfs.h>#include
<linux/init.h>#include <linux/io.h>#include <linux/
Copyright (c) 2019 HiSilicon Limited.#include <linux/dma-mapping.h>#include <linux/module.h>#include "./sgl.h"
Copyright (C) 2019 HUAWEI, Inc. * http://www.huawei.com/ * Created by Gao Xiang <gaoxiang25@huawei.com> */#ifndef
__EROFS_FS_COMPRESS_H#define __EROFS_FS_COMPRESS_H
Copyright (C) 2019 HUAWEI, Inc. * http://www.huawei.com/ * Created by Gao Xiang <gaoxiang25@huawei.com> */#include
"compress.h"#include <linux/module.h>#include <linux/lz4.h>
Copyright (C) 2019 IBM Corp.
Copyright (C) 2019 IBM Corp.#include <linux/bitops.h>#include <linux/init.h>#include <linux/io.h>#include <linux/kernel.h>#include
<linux/mfd/syscon.h>#include <linux/mutex.h>#include <linux/of
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Copyright (C) 2019 Intel Corporation * * Author: Lu Baolu <baolu.lu@linux.intel.com>
Copyright (C) 2019 Intel Corporation * Author: Luis Carlos Cobo <luisca@cozybit.com> */
Copyright (C) 2019 Intel Corporation * Author: Luis Carlos Cobo <luisca@cozybit.com> */#include <linux/gfp.h>#include <linux/kernel.h>#include
<linux/random.h>#include <linux/rculist.h>
Copyright (C) 2019, Intel Corporation * Author: Mika Westerberg <mika.westerberg@linux.intel.com> */
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Copyright (C) 2019 Intel Corporation. * * Author: Zhang Rui <rui.zhang@intel.com>
Copyright (C) 2019 Intel Corporation * * Contact Information:
Copyright (c) 2019, Intel Corporation. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt
Copyright (c) 2019, Intel Corporation. * * Heterogeneous Memory Attributes Table (HMAT) representation
Copyright (C) 2019 Intel Corporation */#include <linux/export.h>#include <net/mac80211.h>#include "ieee80211_i.h"#include "driver-ops.h"
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granted, provided that the above * copyrig
Copyright (C) 2019 Intel Corporation * * Portions of this file are derived from the ipw3945 project, as well * as portions of the ieee80211 subsystem
header files.
Copyright(c) 2019 Intel Corporation * * This module allows virtio devices to be used over a vhost-user socket.
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Copyright (C) 2019 Jeff Kletsky * * Author: Jeff Kletsky <git-commits@allycomm.com>
Copyright (c) 2019 Jeff Layton * * I/O and data path helper functionality.
Copyright (c) 2019, Jeffrey Hugo */
Copyright (C) 2019 Jens Axboe
Copyright (C) 2019 Jeroen Hofstee <jhofstee@victtronenergy.com>
Copyright (C) 2019, Jiaxun Yang <jiaxun.yang@flygoat.com> * Loongson-1 platform IRQ support */
Copyright (C) 2019 Kai-Heng Feng <kai.heng.feng@canonical.com> */
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Copyright (C) 2019 Laurent Pinchart <laurent.pinchart@ideasonboard.com> */
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Copyright (c) 2019 Linaro, Ltd. <ard.biesheuvel@linaro.org> * * Heavily based on:
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Copyright (c) 2019 Linaro Ltd. * Author: Manivannan Sadhasivam <manivannan.sadhasivam@linaro.org> */
Copyright (c) 2019, Linaro Ltd */#include <dt-bindings/power/qcom-aoss-qmp.h>#include <linux/clk-provider.h>#include <linux/interrupt.h>#include
<linux/io.h>#include <linux/mailbox_client.h>#in
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Copyright (C) 2019 Linus Walleij <linus.walleij@linaro.org>
Copyright (C) 2019 Linus Walleij <linus.walleij@linaro.org> * * Based on arch/arm/mach-ixp4xx/common.c
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./Documentation/core-api/kernel-api.rst for more details.
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Copyright (c) 2019 MediaTek Inc. * Author: Yong Liang <yong.liang@mediatek.com> */
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Copyright (C) 2019 Mellanox Technologies, Ltd. * * This was inspired by Brendan Higgins' ipmi-bmc-bt-i2c driver.
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<linux/bcd.h>#include <linux/rtc.h>#include <linux/module.h>#inc
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Copyright (C) 2019 Paul Cercueil <paul@crapouillou.net> */#ifndef DRIVERS_CLK_INGENIC_PM_H#define DRIVERS_CLK_INGENIC_PM_H
Copyright (c) 2019 Paulo Alcantara <palcantara@suse.de> */#include <linux/init.h>#include <linux/fs.h>#include <linux/types.h>#include
<linux/ctype.h>#include <linux/string.h>#include <linux/ro
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Copyright (C) 2019 Pengutronix, Marco Felsch <kernel@pengutronix.de> * * Base on AT42QT1070 driver by:
Copyright (C) 2019 Pengutronix, Michael Tretter <kernel@pengutronix.de> * * Allegro DVT video encoder driver
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Copyright (C) 2019, Red Hat Inc, Arnaldo Carvalho de Melo <acme@redhat.com>#ifndef __PERF_EVSWITCH_H#define __PERF_EVSWITCH_H 1
Copyright (C) 2019 Red Hat, Inc. * * Author(s): Pankaj Gupta <pagupta@redhat.com>
Copyright (C) 2019 Red Hat, Inc. */#include "kvm_util.h"
Copyright (C) 2019, Red Hat, Inc. * * Test expected behavior of the KVM_CAP_SYNC_REGS functionality.
Copyright (C) 2019, Red Hat, Inc. * * Verify that nothing bad happens if a KVM user exits with open * file descriptors while executing a nested guest.
Copyright (C) 2019 Renesas Electronics Corp. */
Copyright (C) 2019 Renesas Electronics Corp. * * Based on clk-rcar-gen3.c
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RENESAS_USB_DRIVER_H#define RENESAS_USB_DRIVER_H
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RENESAS_USB_MOD_H#define RENESAS_USB_MOD_H
Copyright (C) 2019 Renesas Electronics Corporation * Kuninori Morimoto <kuninori.morimoto.gx@renesas.com> */#include <linux/clk.h>#include
<linux/err.h>#include <linux/gpio.h>#include <linux/io.
Copyright (C) 2019 Renesas Electronics Corporation * Kuninori Morimoto <kuninori.morimoto.gx@renesas.com> */#include <linux/delay.h>#include
<linux/dma-mapping.h>#include <linux/io.h>#include <l
Copyright (C) 2019 Renesas Electronics Corporation * Kuninori Morimoto <kuninori.morimoto.gx@renesas.com> */#include <linux/delay.h>#include
<linux/io.h>#include <linux/scatterlist.h>#include "c
Copyright (C) 2019 Renesas Electronics Corporation * Kuninori Morimoto <kuninori.morimoto.gx@renesas.com> */#include <linux/interrupt.h>
Copyright (c) 2019 Robert Eshleman <bobbyeshleman@gmail.com> * * Datasheet: https://datasheets.maximintegrated.com/en/ds/MAX44009.pdf
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<linux/i2c.h>#include <linux/interrupt.h>#include <linux/irq.h>  
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<linux/interrupt.h>#include <linux/mfd/stmfx.h>#include <lin  
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Copyright (c) 2019, Vladimir Oltean <olteanv@gmail.com> \*/#include <linux/if\_vlan.h>#include <linux/dsa/sja1105.h>#include

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lib.h>#include <linux/idr.h>#include <net/tcp.h>#include <s
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 <linux/socket.h>#include <linux/in.h>#include <linux/kernel.h>#include  
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Copyright: Copyright (c) 1996 John Shifflett, GeoLog Consulting */staticunsigned char __maybe_unused calc_sync_xfer(unsigned int period,
unsigned int offset)
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"stdio.h"#include "page.h"#include "ops.h"

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"stdio.h"#include "page.h"#include "ops.h"#include "i
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"page.h"#include "ops.h"
Copyright (C) Paul Mackerras 1997. */#include <stdio.h>#include <stdlib.h>#include <unistd.h>#include <fcntl.h>#include <string.h>#include
"rs6000.h"
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<linux/in.h>#include <linux/kernel.h>#include <linux/ti  
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<linux/smp.h>#include <linux/interrupt.h>#include <linux/kernel_st
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<linux/capability.h>#include <linux/errno.h>#include <linux/types.h>#inclu
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copyright: failed return -ENODEV;}
copyright + flash_ver images, 0 for failure.static u32 chk_cfg(u8 *cfg, u32 length, u32 *flash_ver){ u16 *pw = (u16 *)cfg - 1; u32 sz = 0; u32
len = length;
copyright follow:
copyright follows: * -----
copyright follows: *
Copyright follows: *
copyright */ for (i = EEPROM_COPYRIGHT;
copyright for the ov511 driver is: *
copyright; for (;) while (*++c != '$') if (*c == '\\0') exit(0); while (*++c != '$')
putc(*c, stderr); putc('\\n', stderr); }
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Copyright IBM Corporation 2007 */#ifndef JITDUMP_H#define JITDUMP_H

```



```

Copyright IBM Corporation 2007 */#include <sys/types.h>#include <sys/stat.h> /* for mkdir() */#include <stdio.h>#include <errno.h>#include
<string.h>#include <stdlib.h>#include <stdint.h>#inc
Copyright IBM Corporation, 2007 * Josh Boyer <jwboyer@linux.vnet.ibm.com> * * Based on ebony wrapper:
Copyright IBM Corporation, 2008
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Copyright IBM Corporation, 2008 * * Author: Dipankar Sarma <dipankar@in.ibm.com> * Paul E. McKenney <paulmck@linux.ibm.com> Hierarchical
algorithm
Copyright IBM Corporation, 2008 * * Author: Ingo Molnar <mingo@elte.hu> * Paul E. McKenney <paulmck@linux.ibm.com>
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McKenney <paulmck@linux.ibm.com> Hierarchical versio
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Copyright IBM Corporation, 2011 * * Author: Paul E. McKenney <paulmck@linux.ibm.com>
Copyright IBM Corporation, 2012
Copyright IBM Corporation, 2012 */#include <linux/types.h>
Copyright IBM Corporation, 2013.
Copyright IBM Corporation, 2013
Copyright IBM Corporation, 2013 * Author Aneesh Kumar K.V <aneesh.kumar@linux.vnet.ibm.com> */
Copyright IBM Corporation, 2014 * * Author: Paul E. McKenney <paulmck@linux.ibm.com>
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Copyright IBM Corporation, 2018
Copyright IBM Corporation, 2018 * Author(s): Nayna Jain <nayna@linux.ibm.com> */
Copyright IBM Corporation, 2018 * Authors Suraj Jitindar Singh <sjitindarsingh@gmail.com> * Paul Mackerras <paulus@ozlabs.org> * * Description:
KVM functions specific to running nested KVM-HV g
Copyright IBM Corporation, 2019 * * Author: Paul E. McKenney <paulmck@linux.ibm.com>
copyright); i copy[i] = (char)(get_u16(&sensor0->copyright[i]) >> 8); copy[i] = '\0';
COPYRIGHT | IEC958_AES0_CON_EMPHASIS_5015; ctrl->ch_status[1] = IEC958_AES1_CON_DIGDIGCONV_ID; ctrl->ch_status[2]
= 0x00; ctrl->ch_status[3] = IEC958_AES3_CON_FS_44100 | IEC958_AES3
COPYRIGHT | IEC958_AES0_CON_EMPHASIS_NONE; cs[1] = IEC958_AES1_CON_GENERAL; cs[2] = IEC958_AES2_CON_SOURCE_UNSPEC |
IEC958_AES2_CON_CHANNEL_UNSPEC; cs[3] = IEC958_AES3_CON_CLOCK_1000PPM | fs;
COPYRIGHT | IEC958_AES0_CON_EMPHASIS_NONE; cs[1] = IEC958_AES1_CON_GENERAL; cs[2] = IEC958_AES2_CON_SOURCE_UNSPEC |
IEC958_AES2_CON_CHANNEL_UNSPEC; cs[3] = IEC958_AES3_CON_CLOCK_1000PPM | IEC958_AES
COPYRIGHT | IEC958_AES0_CON_EMPHASIS; ucontrol->value.iec958.status[1] =
IEC958_AES1_CON_ORIGINAL | IEC958_AES1_CON_CATEGORY; ucontrol->value.iec958.status[3] = IEC958_AES3_C
COPYRIGHT | IEC958_AES0_CON_EMPHASIS; ucontrol->value.iec958.status[1] = IEC958_AES1_CON_ORIGINAL |
IEC958_AES1_CON_CATEGORY; ucontrol->value.iec958.status[3] = IEC958_AES3_CON
COPYRIGHT | IEC958_AES0_CON_EMPHASIS; value->value.iec958.status[1] = IEC958_AES1_CON_CATEGORY |
IEC958_AES1_CON_ORIGINAL; return 0;}
COPYRIGHT (IEC958_AES1_CON_ADC_COPYRIGHT_ID#define IEC958_AES1_CON_ADC_COPYRIGHT_OTHER
(IEC958_AES1_CON_ADC_COPYRIGHT_ID[0x60]#define IEC958_AES1_CON_SOLIDMEM_MASK0x0f#define IEC958_AES1_CON_SOLID
COPYRIGHT) \ | ((IEC958_AES1_CON_MIXER \ | IEC958_AES1_CON_ORIGINAL) << 8) \ | (0x10 <<
16) \ | ((IEC958_AES3_CON_FS_48000) << 24))
copyright IN
copyright ;-) */#include <linux/etherdevice.h>
copyright info and historical * changelogs. */#include <linux/capability.h>#include <linux/clocksource.h>#include <linux/workqueue.h>#include
<linux/hrtimer.h>#include <linux/jiffies.h>#includ
copyright information. * -----
copyright information. * -----
copyright information. */
copyright information
copyright information */
Copyright Information:
copyright information. * 03-16-15 02.00.10 Updated for MPI v2.6. * Added MPI2_SATA_PT_REQ_PT_FLAGS_FPDMA. * -----

```

```

copyright information. * 03-16-15 02.00.17 Updated for MPI v2.6. * Added MPI26_SCSIIO_IOFLAGS_ESCAPE_PASSTHROUGH. *
Added MPI2_SEP_REQ_SLOTSTATUS_DEV_OFF a
copyright information. * 03-16-15 02.00.26 Updated for MPI v2.6. * Added MPI2_EVENT_ACTIVE_CABLE_EXCEPTION and *
MPI26_EVENT_DATA_ACTIVE_CABLE_EXCEPT. * Added
copyright information. * 08-25-16 02.00.14 Added new values for the Flags field of Toolbox Clean * Tool Request Message. * 07-22-18
02.00.15 Added defines for new TOOLBOX_PC
copyright information. * Added MPI2_BIOSPAGE1_OPTIONS_ADVANCED_CONFIG. * Added AdapterOrderAux fields to BIOS
Page 3. * 03-16-15 02.00.31 Updated for MPI v
copyright information. * * a header file containing define's for the USB device supported by the * various drivers.
copyright information. * * a header file containing prototypes and types for internal use of the dvb-usb-lib
copyright information and source see the end of this file.static u8 mac_reader[] = { 0x06, 0x00, 0x00, 0xea, 0x04, 0x00, 0x00, 0xea, 0x03, 0x00, 0x00,
0xea, 0x02, 0x00, 0x00, 0xea, 0x01, 0x00, 0x00
copyright* information as follows:
copyright information. * Bumped MPI2_HEADER_VERSION_UNIT. * 03-16-15 02.00.37 Bumped MPI2_HEADER_VERSION_UNIT. *
Added Scratchpad registers to *
copyright information */#define FC_LOG_PREFIX "flexcop_usb"#include "flexcop-usb.h"#include "flexcop-common.h"
copyright information. Do not change the version information unless an improvement has been made.Merely removing my name, as Compex has done in
the past, does not count as an improvement.
copyright information */#ifndef __FLEXCOP_COMMON_H__#define __FLEXCOP_COMMON_H__
copyright information */#ifndef __FLEXCOP_H__#define __FLEXCOP_H__
copyright information */#ifndef __FLEXCOP_REG_H__#define __FLEXCOP_REG_H__
copyright information */#ifndef __FLEXCOP_USB_H_INCLUDED__#define __FLEXCOP_USB_H_INCLUDED__
copyright information */#include "flexcop.h"
copyright information */#include <media/tuner.h>#include "flexcop.h"#include "mt312.h"#include "stv0299.h"#include "s5h1420.h"#include
"itd1000.h"#include "cx24113.h"#include "cx24123.h"#inc
copyright information of those functions. *
copyright information. pr_info("NetWinder Floating Point Emulator V0.97 (" NWFPE_BITS " precision)\n");
copyright information. * * the headerfile, all dvb-usb-drivers have to include.
copyright information. * * This file contains functions for (de-)initializing an I2C adapter.
copyright information. * * This file contains functions for downloading the firmware to Cypress FX 1 and 2 based devices.
copyright information. * * This file contains functions for downloading the firmware to Cypress FX 1 * and 2 based devices.
copyright information. * * This file contains functions for initializing and handling the * linux-dvb API.
copyright information. * * This file contains functions for initializing the input-device and for handling remote-control-queries.
copyright information *//* This file is automatically generated, do not edit things here. */#ifndef __FLEXCOP_IBI_VALUE_INCLUDED__#define
__FLEXCOP_IBI_VALUE_INCLUDED__
copyright information. * * This file keeps functions for initializing and handling the * BULK and ISOC USB data transfers in a generic way. * Can be used
for DVB-only and also, that's the plan, fo
copyright information. * * This file keeps functions for initializing and handling the * USB and URB stuff.
Copyright: Intel Corp 2005 * Jonathan Cameron <jic23@cam.ac.uk> 2009 */
Copyright Intel Corp. 2018#include <linux/init.h>#include <linux/module.h>#include <linux/moduleparam.h>#include <linux/nd.h>#include
"pmem.h"#include "pfn.h"#include "nd.h"#include "nd-core.h
Copyright: Intel Corp. * * Modified 2009: Jonathan Cameron <jic23@cam.ac.uk>
Copyright Intel Corporation (C) 2014-2016. All Rights Reserved
Copyright Intel Corporation (C) 2017. All Rights Reserved
Copyright Intel Corporation (C) 2017. * * Based on the i2c-axxia.c driver.
Copyright © International Business Machines Corp., 2006
Copyright © International Business Machines Corp., 2006-2008 * * DESCRIPTION * This test excercises the futex syscall op codes needed for
requeuing * priority inheritance aware POSIX co
Copyright © International Business Machines Corp., 2006-2008 * * DESCRIPTION * This test excercises the futex_wait_requeue_pi() signal handling
both * before and after the requeue. The f
Copyright © International Business Machines Corp., 2009
Copyright © International Business Machines Corp., 2009 * * DESCRIPTION * 1. Block a thread using FUTEX_WAIT * 2. Attempt to use
FUTEX_CMP_QUEUE_PI on the futex from 1. * 3. The
Copyright © International Business Machines Corp., 2009 * * DESCRIPTION * Block on a futex and wait for timeout.
Copyright © International Business Machines Corp., 2009 * * DESCRIPTION * GCC atomic builtin wrappers * http://gcc.gnu.org/onlinedocs/gcc-
4.1.0/gcc/Atomic-Builtins.html
Copyright © International Business Machines Corp., 2009 * * DESCRIPTION * Glibc independent futex library for testing kernel functionality.
Copyright © International Business Machines Corp., 2009 * * DESCRIPTION * Test if FUTEX_WAIT op returns -EWOULDBLOCK if the futex value
differs * from the expected one.
copyright I put the copyright notice from the * frle-0.22 device driver. * Some code is based on the nicstar driver by M. Welsh. * *
Author: Rui Prior (rprior@inescn.pt) * P

```

```

copyright is: *
Copyright is a null terminated ASCII string containing the JR3 * copyright notice. */
copyright); ixgbev_f_wq = create_singlethread_workqueue(ixgbev_driver_name); if (!ixgbev_f_wq) { pr_err("%s: Failed to create
workqueue\n", ixgbev_driver_name); return -ENOMEM; }
Copyright: J */
Copyright: Jamal Hadi Salim (2002-13) */
copyright Jamal Hadi Salim (2002-4) */
copyright Jamal Hadi Salim (2015)*/
copyright Jamal Hadi Salim (2016)*/
Copyright: jfclere@sinix.net */
copyright Joerg Dorchain (dorchain@mpi-sb.mpg.de) *
Copyright Joern Engel <joern@lazybastard.org># Inspired by Linus Torvalds# Original idea maybe from Keith Owens# s390 port and big speedup
by Arnd Bergmann <arnd@bergmann-dalldorf.de># Mips port b
Copyright Jonathan Naylor G4KLX (g4klx@g4klx.demon.co.uk)
Copyright Jonathan Naylor G4KLX (g4klx@g4klx.demon.co.uk) */#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include
<linux/in.h>#include <linux/kernel.h>#include <linux/
Copyright Jonathan Naylor G4KLX (g4klx@g4klx.demon.co.uk) */#include <linux/module.h>#include <linux/proc_fs.h>#include
<linux/kernel.h>#include <linux/interrupt.h>#include <linux/fs.h>#include
Copyright Keith Owens <kaos@ocs.com.au>. GPL.
Copyright KOSAKI Motohiro <kosaki.motohiro@jp.fujitsu.com> * * DESCRIPTION * Internally, Futex has two handling mode, anon and file. The
private file * mapping is special. At first it be
Copyright KOSAKI Motohiro <kosaki.motohiro@jp.fujitsu.com> * * DESCRIPTION * Wait on uninitialized heap. It should be zero and FUTEX_WAIT
should * return immediately. This test is intent
copyright label on that
Copyright Larry Finger <Larry.Finger@lwfinger.net> * * A huge thanks goes to Sara V. Nari who forgives me when I'm * sitting in front of my laptop at
evening, week-end, night...
Copyright LG Electronics Inc., 2014 * Written by:
Copyright Linas Vepstas 2005, 2006
Copyright Linas Vepstas 2005, 2006 */
Copyright Linas Vepstas <linas@austin.ibm.com> 2004 */
Copyright Linas Vepstas <linas@austin.ibm.com> 2007 * * All rights reserved
Copyright Linas Vepstas <linas@linas.org> 2004, 2005 * * All rights reserved
Copyright line includes x86 and tile ones. */
Copyright Linus Torvalds and others. *
copyright Linus Torvalds and others. * Heavily rewritten. */
Copyright Management Information (UDF 2.50 3.3.4.5.1.2)struct DVDCopyrightImpUse { __le16 headerChecksum; uint8_t
CGMSInfo; uint8_t dataType; uint8_t protectionSystemInfo[4];} __packed;
Copyright: Marvell International Ltd. */
Copyright: Marvell Semiconductors Inc. */
Copyright Matthew Wilcox for Intel Corp, 2010
Copyright Matthew Wilcox for Intel Corp, 2010 * * This file contains definitions taken from the * USB Mass Storage Class Specification Overview
Copyright Matt Mackall <mpm@selenic.com>, 2003, 2004, 2005 *
copyright MaxLinear
Copyright message. * 0.0.17 * Add iec958 file in proc file system to show status of SPDIF in. * 0.0.18 * Implement support for Line-in capture on
SB Live 24bit. * * This code was initial
Copyright message. * 0.0.17 * Implement Mic and Line in Capture. * 0.0.18 * Add support for mute control on SB Live 24bit (cards w/ SPI DAC) *
* This code was initially based on code fr
Copyright message. * 0.0.19 * Added I2C and SPI registers. Filled in interrupt enable. * 0.0.20 * Added GPIO info for SB Live 24bit. * 0.0.21 *
Implement support for Line-in capture o
Copyright message. * 0.0.19 * Finally fix support for SB Live 24 bit. SB0410 and SB0413. * The output codec needs resetting, otherwise all output is
muted. * 0.0.20 * Merge "pci_disabl
copyright message * below) provided to Red Hat by VIA Networking Technologies, Inc. for * addition to the Linux kernel. * * The code has been merged
into one source file, cleaned up to follow * L
copyright message in any source redistribution in whole or part. *
copyright messages: *
Copyright Micro Memory Inc 2001-2002. All rights reserved
Copyright Micro Memory INC 2001. All rights reserved
copyright) * Modifications: 2006 (c) MontaVista Software, Inc. * * Modified by: Mark A. Greer <mgreer@mvista.com>
COPYRIGHT);MODULE_LICENSE("GPL");
COPYRIGHT);MODULE_LICENSE("GPL");MODULE_ALIAS_MISCDEV(TUN_MINOR);
COPYRIGHT);MODULE_LICENSE("GPL");MODULE_FIRMWARE("ipw2200-ibss.fw");#ifdef CONFIG_IPW2200_MONITORMODULE_FIRMWARE("ipw2200-

```

```

sniffer.fw");#endifMODULE_FIRMWARE("ipw2200-bss.fw");
COPYRIGHT);MODULE_LICENSE("GPL
v2");MODULE_VERSION(DRV_VERSION);MODULE_FIRMWARE(FIRMWARE_D101M);MODULE_FIRMWARE(FIRMWARE_D101S);MODULE_FIRMWARE(FIRMWAR
E_D102E);
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Copyright: MontaVista Software Inc. * * Code common to all PXA machines.
Copyright: MontaVista Software Inc. * * Code specific to PXA21x/25x/26x variants.
Copyright: MontaVista Software Inc. * * Code specific to PXA27x aka Bulverde.
Copyright: MontaVista Software, Inc. */#ifndef __ASM_ARCH_DMA_H#define __ASM_ARCH_DMA_H
Copyright: MontaVista Software Inc. */#ifndef __ASM_MACH_IRQS_H#define __ASM_MACH_IRQS_H
Copyright: MontaVista Software Inc. */#include <linux/bitops.h>#include <linux/init.h>#include <linux/module.h>#include <linux/interrupt.h>#include
<linux/syscore_ops.h>#include <linux/io.h>#i
Copyright: MontaVista Software Inc. */#include <linux/clkdev.h>#include <linux/gpio.h>#include <linux/gpio/gpio-reg.h>#include
<linux/gpio/machine.h>#include <linux/module.h>#include <linux/ker
Copyright: MontaVista Software Inc. */#include <linux/gpio/consumer.h>#include <linux/module.h>#include <linux/init.h>#include
<linux/interrupt.h>#include <linux/kernel.h>#include <linux/errno.
Copyright: MontaVista Software Inc. */#include <linux/gpio.h>#include <linux/gpio/gpio-reg.h>#include <linux/gpio/machine.h>#include
<linux/init.h>#include <linux/platform_device.h>#include <li
Copyright: MontaVista Software Inc. */#include <linux/gpio.h>#include <linux/init.h>#include <linux/platform_device.h>#include
<linux/syscore_ops.h>#include <linux/interrupt.h>#include <linux/s
Copyright: MontaVista Software Inc. */#include <linux/module.h>#include <linux/clk.h>#include <linux/err.h>#include <linux/gpio/driver.h>#include
<linux/gpio-pxa.h>#include <linux/init.h>#incl
Copyright: MontaVista Software Inc. * * Originally based upon linux/drivers/pcmcia/sa1100_neponset.c
Copyright: MontaVista Software, Inc. * * Splitting done by: Marek Vasut <marek.vasut@gmail.com> * If something doesn't work and it worked before
splitting, e-mail me, * dont bother Nicolas please ;-
Copyright Motorola 1984 (
copyright; } mpeg_format_info; } format_info;
COPYRIGHT); new new |= ((ucontrol->value.iec958.status[3] & IEC958_AES3_CON_FS) << 24); if ((new &
IEC958_AES0_CON_EMPHASIS) == IEC958_AES0_CON_EMPHASIS_5015) val |= 1<<3;
copyright notes. */
copyright notes. */#include <api/fs/fs.h>#include <errno.h>#include <inttypes.h>#include <poll.h>#include "cpumap.h"#include
"util/mmap.h"#include "thread_map.h"#include "target.h"#include "
copyright not found\n");#endif /* Validate checksum */ for (i = s = 0; i < EEPROM_CHECKSUM; i++) s += e[i]; s &= 0xFF; if (s !=
e[EEPROM_CHECKSUM]) { printk(KERN_ERR DEV_LABEL "(itf %d): EEPR
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Copyright NXP *
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copyright of original i386 version is: *
copyright of skeleton.c was as follows: * * skeleton.c Written 1993 by Donald Becker.
copyright * of their respective owners. */
copyright of the respective owners.*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/module.h>#include <linux/string.h>#include
<linux/slab.h>#include <linux/delay.h>#inc
copyright of who own it
Copyright: Phytect Messtechnik GmbH * e-Mail: armlinux@phytec.de * * based on Intel Mainstone Board
Copyright Pierre Ossman\n");
copyright preserved * below. * Steve Whitehouse: Some bug fixes, cleaning up some code to make it *
compatible with my routing layer. *
copyright ;- ) * * PS wx handler mostly stolen from hostap, copyright who
Copyright RDA Microelectronics Company Limited
Copyright Realtek corp. * (available in drivers/staging/rtd1817se directory of Linux 3.14) * - other GPL, unpublished (until now), Linux driver code,
Copyright Red Hat 2003
Copyright Red Hat, 2009
Copyright Red Hat Inc 2010.
Copyright Red Hat Inc 2010. * Author : Dave Airlie <airlied@redhat.com> */

```

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Cornelia Huck <cohuck@redhat.com>
Copyright RedHat, Inc. * * libata documentation is available via 'make {ps|pdf}docs', * as Documentation/driver-api/libata.rst
Copyright RedHat, Inc. * * This driver probably works with non-Apple versions of the * Broadcom chipset...
COPYRIGHT; regmap_write(ak4104->regmap, AK4104_REG_CHN_STATUS(0), val);
copyright ReturnCode - One of the ERR_CAPTURE_...
COPYRIGHT: return dvd_read_copyright(cdi, s, cg);
COPYRIGHT); } return pci_register_driver();
copyright); return pci_register_driver(); module_init(atl2_init_module);
COPYRIGHT: return "SET_COPYRIGHT"; case CX2341X_ENC_SET_EVENT_NOTIFICATION:
COPYRIGHT: return "SET_COPYRIGHT"; case CX2341X_ENC_SET_EVENT_NOTIFICATION:
Copyright Roman Zippel, 1997. All rights reserved
Copyright Rusty Russell IBM Corporation 2007. #ifndef __KERNEL__ #include <stdint.h> #endif #include <linux/types.h> #include <linux/virtio_types.h>
Copyright Samuel Mendoza-Jonas, IBM Corporation 2018.
Copyright Samuel Mendoza-Jonas, IBM Corporation 2018. */
Copyright Sarah Sharp for Intel Corp, 2010 */
COPYRIGHT)) sbits if (val & AC_DIG1_LEVEL) sbits |= (IEC958_AES1_CON_ORIGINAL << 8); sbits |= val &
(0x7f << 8); } return sbits;
copyright Sebastian Carlier
Copyright © Sebastian Cla * Jan Engelhardt, 2007-2010 * * based on ipt_ROUTE.c from Cédric de Launois * <delaunois@info.ucl.be>
copyright; see LICENSE.FlashPoint for details.
copyright: %s\n", copy);}
COPYRIGHT); snd_emu10k1x_ptr_write(chip, SPCS1, 0, chip->spdif_bits[1] = SPCS_CLKACCY_1000PPM |
SPCS_SAMPLERATE_48 | SPCS_CHANNELNUM_LEFT | SPCS_SOURCENUM_UNSPEC |
COPYRIGHT); snd_emu10k1x_ptr_write(chip, SPCS2, 0, chip->spdif_bits[2] = SPCS_CLKACCY_1000PPM |
SPCS_SAMPLERATE_48 | SPCS_CHANNELNUM_LEFT | SPCS_SOURCENUM_UNSPEC |
COPYRIGHT) snd_iprintf(buffer, " Non-Copyright"); if (digi1 & AC_DIG1_NONAUDIO) snd_iprintf(buffer, " Non-Audio"); if (digi1 &
AC_DIG1_PROFESSIONAL) snd_iprintf(buffer, " Pro"); if (digi1 & A
COPYRIGHT) { snd_iprintf(buffer, "permitted\n"); } else { snd_iprintf(buffer, "protected\n"); }
snd_iprintf(buffer, "Emphasis: "); if ((status[0] & IEC958_AES0_CON_EMPHASIS) != IEC958_AE
Copyright : %s\n", (status snd_iprintf(buffer, "Emphasis : %s\n", emphasis[(status & SPCS_EMPHASISMASK) >> 3]);
snd_iprintf(buffer, "Mode : %i\n", (status
COPYRIGHT | SPCS_NOTAUDIODATA; snd_emu10k1_ptr_write(emu, SPCS0 + i, 0, bits); }
Copyright (ST) 2011 Vipin Kumar (vipin.kumar@st.com) */
Copyright (ST) 2012 Rajeev Kumar (rajeevkumar.linux@gmail.com)
Copyright (ST) 2012 Rajeev Kumar (rajeevkumar.linux@gmail.com) */
Copyright (ST) 2012 Rajeev Kumar (rajeevkumar.linux@gmail.com) */
Copyright (ST) 2012 Vipin Kumar (vipin.kumar@st.com) */
copyright) */static inline int in_8(const volatile unsigned char{int ret;
Copyright status#define AC97_ALC650_PRE 0x0038 /* Preemphasis status */#define AC97_ALC650_PRE_SHIFT 3#define
AC97_ALC650_MODE 0x00C0 /* Preemphasis status */#define
Copyright status#define AC97_SC_PRE 0x0008 /* Preemphasis status */#define AC97_SC_CC_MASK 0x07f0 /* Category Code mask
*/#define AC97_SC_CC_SHIFT 4#define AC97_SC_L 0x0800 /* Generation Level
copyright status * of the DEC code. */
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permitted provided that the following conditions *
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Copyright ST-Ericsson 2012. * * Author: Arun Murthy <arun.murthy@stericsson.com>
Copyright STMicroelectronics, 2007. */
copyright string __le32 int_fw_offset; /* internal firmware image offset */ __le32 int_fw_len; /* internal firmware image length */ __le32
ext_fw_offset; /* external firmware image offset */ __le32
copyright(struct cdrom_device_info struct packet_command *cg){ int ret; u_char buf[8]; const struct cdrom_device_ops
*cdo = cdi->ops;
copyright(struct comedi_device{ struct jr3_block __iomem *block = dev->mmio; struct jr3_sensor __iomem *sensor0 = &block[0].sensor;

```

```

Copyright; struct regid      appIdent; struct timestamprecordingDateAndTime;   struct regid      impIdent; uint8_t
impUse[64];   __le32      predecessorVolDescSeqLocation;   __le16      flags;uint8_t      reserve
Copyright SUSE Linux Products GmbH 2009 * * Authors: Alexander Graf <agraf@suse.de>
Copyright SUSE Linux Products GmbH 2010
Copyright SUSE Linux Products GmbH 2010 * * Authors: Alexander Graf <agraf@suse.de>
COPYRIGHT;   switch (bits      case 0x10: diga->status[0] |= IEC958_AES0_CON_EMPHASIS_NONE; break;   case 0x00: diga-
>status[0] |= IEC958_AES0_CON_EMPHASIS_5015; break;   }   if (!(bits & 0x80))   diga-
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copyright text*/
copyright text 2001jul05 change message format to improve OVERRUN case
copyright text 2001jul05 change message format to improve OVERRUN case
copyright text 2001jul05 change message format to improve OVERRUN case*/
copyright = 'The kernel development communityauthor = 'The kernel development community'
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Copyright Tomi Manninen OH2BNS (oh2bns@sral.fi) */#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include
<linux/in.h>#include <linux/kernel.h>#include <linux/timer.h>
Copyright Tomi Manninen OH2BNS (oh2bns@sral.fi) */#include <linux/types.h>#include <linux/slab.h>#include <linux/socket.h>#include
<linux/timer.h>#include <net/ax25.h>#include <linux/skbuff.h>
Copyright Torsten Duwe <duwe@informatik.uni-erlangen.de> 1993
copyright = True
copyright { __u8 type;
COPYRIGHT; ucontrol->value.iec958.status IEC958_AES1_CON_ORIGINAL; return 0;}
COPYRIGHT; ucontrol->value.iec958.status IEC958_AES1_CON_ORIGINAL; ucontrol->value.iec958.status[3] =
IEC958_AES3_CON_FS; return 0;}
Copyright unasserted) * AN = 0 (Audio data) * P = 0 (Consumer) */ def_bits = SPCS_CLKACCY_1000PPM |
SPCS_SAMPLERATE_48 | SPCS_CHANNELNUM_LEFT | SPCS
Copyright unasserted) * AN = 0 (Audio data) * P = 0 (Consumer) */ emu->spdif_bits[0] = emu->spdif_bits[1] =
emu->spdif_bits[2] = SPCS_CLKACCY_1000PPM
Copyright unasserted) * AN = 0 (Audio data) * P = 0 (Consumer) */ snd_emu10k1x_ptr_write(chip, SPCS0, 0,
chip->spdif_bits[0] = SPCS
Copyright */ val = (cfg->iec60958_cfg->status
COPYRIGHT)) val if (sbits & (IEC958_AES1_CON_ORIGINAL << 8)) val |= AC_DIG1_LEVEL; val |= sbits &
(IEC958_AES1_CON_CATEGORY << 8); } return val;
COPYRIGHT)) val val |= ((new >> 8) & 0xff) << 4;// category + original switch ((new >> 24) & 0xff) { case
IEC958_AES3_CON_FS_44100: val |= 0<<12; break; case IEC958_AES3_CON_FS_48000: val
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copyright was as follows: * * skeleton.c Written 1993 by Donald Becker.
copyright was changed, and the change * is retroactive to all "true" BSD software (ie everything * from UCB as opposed to other peoples code that just
carried
Copyright who own it
Copyright who own it */#include "ieee80211.h"
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Copyright: Wolfson Microelectronics. PLC. */
copyright * * WPA code stolen from the ipw2200 driver.
Copyright Yaegashi Takeshi, 2001 * Adrian McMenamin, 2008 - 2009 */
(C) ordering is still observed by the pmu driver. */ if (!rb->aux_overwrite) { aux_tail = READ_ONCE(rb->user_page->aux_tail);
handle->wakeup = rb->aux_wakeup + rb->aux_watermark; if (aux_head
(C) or pretend (B,A) that the write * was successfully written on the peer. */ __RQ_NET_OK,
(C) Peter T. Breuer (ptb@ot.uc3m.es) 2003 * * bitmap_create - sets up the bitmap structure * bitmap_destroy - destroys the bitmap structure
(C) programmable clock divider controlled by <Select * (D) constant div-by-3 clock divider * (E) programmable clock divider bypass controlled by
<Switch> * (F) constant div-by-3 clock mux co
(C) protects the consumer from speculatively loading the data before * the producer pointer actually has been read. If we do not have this * barrier,

```

```

some architectures could load old data as specul
(C) pxor GHASH_MUL %xmm8, %xmm13, %xmm9, %xmm10, %xmm11, %xmm5, %xmm6 # final GHASH computation movdqa
SHUF_MASK(%rip), %xmm10 PSHUFB_XMM %xmm10, %xmm8
(C) Roman Zippel (assigned DJD, 1999) *
(C) Rusty Russell, IBM 2002 * * Generalized futexes, futex requeueing, misc fixes by Ingo Molnar
(C) * 'R * * Write * ' ' = Other Information * ' ' = Must not be written (D), value ignored when written (R,A,F) * 'W' = Can be written
(C) SBE Inc., used by permission. */
(C) SGI 2006, Christoph Lameter * Cleaned up and restructured to ease the addition of alternative * implementations of SLAB allocators.
(C) SK" ;#define LINT_USE(x)#else# define LINT_USE(x) (x)=(x)#endif# define MS2BCLK(x) ((x)*12500L)
(C) SK" ;#endif
(C) SK" ;#endif
(C) State timer broadcast controlstatic void lpic_timer_state_broadcast(struct acpi_processor *pr, struct acpi_processor_cx *cx,
int broadcast){ int state = cx - pr->power.st
(C) ST-Ericsson SA 2013 */
(C) ST-Ericsson SA 2013 */#include <drm/drm_simple_kms_helper.h>
(C) ST-Ericsson SA 2013 */#include <linux/clock.h>#include <linux/delay.h>#include <linux/dma-buf.h>
(C) Steve Whitehouse 1999 derived from the * ipv4 netfilter header file which is * (C)1998 Rusty Russell -- This code is GPL. */
(C) * STORE * smp_wmb() (B) smp_mb() (D) * STORE ->data_head STORE ->data_tail * }
(C) * STORE * smp_wmb() (B) smp_mb() (D) * STORE ->data_head STORE ->data_tail * }
(C) * STORE * smp_wmb() (B) smp_mb() (D) * STORE ->producer STORE ->consumer * }
(C) Texas Instruments. */
(C) Texas Instruments, Inc.
(C) Texas Instruments, Inc. http://www.ti.com/
(C) The elitist m68k Users(TM) * * based on the existing parport_amiga and lp_mfc
(c) there is clear notice in each modified Data File or * in the Software as well as in the documentation associated with the Data * File(s) or Software that
the data or software has been modified.
(C) using AArch64 CRC instructions *
(C) using ARM CRC, NEON and Crypto Extensions instructions *
(C) UV_Meta_Plane * (D) UV_UBWC_Plane * * Y_Meta_Plane consists of meta information to decode compressed * tile data in
Y_UBWC_Plane. * Y_UBWC_Plane consists of Y data in compressed macro-ti
(C) UV_Meta_Top_Field_Plane * (D) UV_UBWC_Top_Field_Plane * (E) Y_Meta_Bottom_Field_Plane * (F) Y_UBWC_Bottom_Field_Plane
* (G) UV_Meta_Bottom_Field_Plane * (H) UV_UBWC_Bottom_Field_Plane
(C) Waldorf Electronics, Germany * Written by Andreas Busse * * NOTE: most of the structure definitions here are endian dependent. * If you want to
use this driver on big endian machines, the data
(C) Xilinx Inc. * * This driver has originally been pushed by Xilinx using a Zynq-branding. This * still shows in the naming of this file, the kconfig symbols
and some symbols * in the code.
(C) YOKOTA Hiroshi <yokota (at) netlab. is. tsukuba. ac. jp>
Linux kernel crash utility 7.0.3 unknown:undefined
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need
for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
(C)# B. (c)
(C)!b go :more /./!{ N s\n# // b more
(C) __btowc (C)# else
(C) btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef _LIBC# define MEMPCPY(D, S, N) __mempcpy (D, S,
N)# else# if HAVE_MEMPCPY# define MEMPCPY(D
(C) (((C)
(C) ((C >=
C-'0' : ((C >= 'A' && C <= 'F') ? \ C-'A'+10 : ((C >= 'a' && C <= 'f') ? \ C-'a'+10 :
-1)))
(C) (((C)#define CC3(OP) (((OP)>>8)&0x3)#define SBIT(OP) ((OP)&0xF)#define MMR(OP) ((OP)&0x7F)#define MMRX(OP)
((((OP)>>4)&0xF)+16)#define MMRYP(OP) (((
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_PATH_WITH_DIR(P) \ (strchr (P, '/') != NULL ||
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)#endif
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])#endif
(C) ((C) ==# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define FILESYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) == DIRECTORY_SEPARATOR)#endif
(C) ((C) ==#endif
(C) (C)# ifdef _LIBC# define STRLEN(S) __wcslen (S)# define STRCAT(D, S) __wscat (D, S)# define MEMPCPY(D, S, N) __wmempcpy (D, S,
N)# else# define STRLEN(S) wcslen (S)# defin

```

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```
(C) ea      OBITOP (O_BIST, 1, 1, ea &= ~m;      if (!C) ea |= m);      /* bist */      OBITOP (O_BSTZ, 1, 1, ea &= ~m;      if (Z) ea |= m);      /* bstz */      OBITOP (O_BISTZ, 1, 1, ea &= ~m;      if (!Z) ea |=
```

```
(C) is the return code for exiting with status C. - M32C_MAKE_STOPPED (S) is the return code for stopping on signal S. */#define M32C_MAKE_STOPPED() (0)#define M32C_MAKE_HIT_BREAK() (1)
```

```
(C) is the return code for exiting with status C. - RL78_MAKE_STOPPED (S) is the return code for stopping on signal S. */#define RL78_MAKE_STOPPED() (1)#define RL78_MAKE_HIT_BREAK() (2)
```

```
(C) is the return code for exiting with status C. - RX_MAKE_STOPPED (S) is the return code for stopping on signal S. */#define RX_MAKE_STOPPED() (1)#define RX_MAKE_HIT_BREAK() (2)
```

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copyright} \gdef \gdef^^ab{\guillemetleft} \gdef^^ac{\not$} \gdef^^ad{\-\} \gdef^^ae{\registeredymbol} \gdef^^af{\=} %
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'/^\.ifndef/,$$p' \ < $(top_srcdir)/import/extra/snippet/warn-on
Copyright Header\n"); fprintf (file, _(" copyright: %.*s\n"), (int)(rec_len - sizeof (struct vms_emh_common)), (char *)rec +
sizeof (struct vms_emh_common)
copyright header of the files in the given list.
copyright headers. This script is only# fixing the first one it finds, so we need to finish the update# by hand.MULTIPLE_COPYRIGHT_HEADERS = (
"gdb/doc/gdb.texinfo", "gdb/doc/refcard.tex",
Copyright' in line: return True lineno += 1 if lineno > 50:
copyright is a brief statement that the program is
copyright Jiri Gaisler 1995\n", sis_version); printf(" Bug-reports to jgais@wd.estec.esa.nl\n\n"); while (stat < argc) { if (argv[stat][0] == '-') {
if (strcmp(argv[stat], "-v") == 0) {
copyright lib/alloca.c lib/alloca.in.h lib/config.charset lib/fnmatch.c lib/fnmatch.in.h lib/fnmatch_loop.c lib/inttypes.in.h lib/localcharset.c
lib/localcharset.h lib/mbrtowc.c
copyright line is 72.# Set UPDATE_COPYRIGHT_MAX_LINE_LENGTH to use a different length.# 5. Set UPDATE_COPYRIGHT HOLDER if the
copyright holder is other# than "Free Software Foundation,
copyright message after the interpreter is installed when it is any sane interpreter. */ if (!quiet && !current_interp_named_p (INTERP_MI1)) {
/* Print all the junk at the top, with
copyright message before the interpreter is installed. That way it isn't encapsulated in MI output. */ if (!quiet && strcmp (interpreter_p,
INTERP_MI1) == 0) { /* Print all the junk
copyright\n");
COPYRIGHT\n"); printf_filtered ("HOLDERS AND/OR OTHER PARTIES PROVIDE THE PROGRAM \"AS IS\" WITHOUT WARRANTY\n"); printf_filtered ("OF
ANY KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING, BUT NOT LIM
copyright script for that. "" # We want to use year intervals in the copyright notices, and # all years should be collapsed to one single year
interval, # even if there are "holes" in
copyright script prints # a well defined warning when it did not find the copyright notice. # For each of those, do a sanity check and see if they may
in fact # have one. For the files th
Copyright/s/ -e '/Starting program: /s/[A-z]:./|'|' \ -e '/main (/s/=0x[0-9a-f][0-9a-f]*/=XYZ/g' \ > ${base}.tst if diff --binary -u
${base}.out ${base}.tst ; then
Copyright sign { ISFUNC, rl_insert }, /* Feminine ordinal indicator */ { ISFUNC, rl_insert }, /* Left pointing double angle quotation mark */ {
ISFUNC, rl_insert }, /* Not sign */ { ISFUNC, rl
copyright string. len = obj_som_copyright_hdr (abfd)->header_id.length - 4; obj_som_file_hdr (abfd)->aux_header_size += len;
current_offset += len; if (bfd_bwrite ((void *) obj
copyright target rule# in maint.mk from gnuilib's maintainer-makefile module.
copyright year intervals in a reformatted FSF copyright statement# are expanded instead.# If UPDATE_COPYRIGHT_USE_INTERVALS=2, convert
a sequence with gaps# to the minimal containing
copyright year intervals. (See "Environment variables"# below.)
copyright year is 2 or 4 digits, and years are separated by# commas or dashes. Whitespace may appear after commas.
copyright year list to include the current year.
copyright years...
copyright years (such as 90, 1991, 1992-2007, 2008) in a# reformatted FSF copyright statement is collapsed to a single# interval (such as 1990-
2008). If unset or set to 0, all existing
(C) rd c = res; }
(C) rd c = res; } else
(C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 1/0 - SUBF3 Alias: i, i3*/#define
S_CLASS_INSN(name, opcode, level) \

```

```

(C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) dst = Register 0-11 (r) Instr: 2/0 - ADDF3, MPYF3 Alias: i,
i3*/#define SC_CLASS_INSN(name, opcode,
(C) src1 = Register 0-11 (g), Indirect 0,1,IR0,IR1 (J), C4x T2: Indirect (O) Instr: 1/0 - CMPF3 Alias: i, i3*/#define S2_CLASS_INSN(name, opcode,
level) \ { name, opcode|0x20000000, 0
(C)<<srca>;", 8 }
(C)" takes one of the following forms or is omitted# entirely:
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C) tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if defined
__WIN32 || defined __WIN32__ || defined __E
linux-misc 2.0 opensuse:usbip/2.0-26.3.1/ppc64le
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belf CFLAGS=\$SAVE\_CFLAGS fi ;:\*-\*so  
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Copyright (C) 2012 Milan Broz <gmazyland@gmail.com>  
Copyright (C) 2012 Milan Broz <gmazyland@gmail.com> \* \* Run this (for LUKS as root), \* e.g. ./crypt\_dict test.img /usr/share/john/password.lst 4  
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Linux Unified Key Setup 2.2.2 fedora:cryptsetup/2.2.2-1.fc30/armv7hl  
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Linux Unified Key Setup 2.3.0 alpine:cryptsetup-dev/2.3.0-r0/armhf  
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Linux Unified Key Setup 2.3.1 opensuse:cryptsetup/2.3.1-1.2/s390x  
 (C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test yes != "\$lt\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
 belf CFLAGS=\$SAVE\_CFLAGS fi ;\*-\*so  
 (C)/!b go :more /\.!{ N s|\n# || b more  
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 COPYRIGHT (C) 1986 Gary S. Brown. You may use this program, or \* code or tables extracted from it, as desired without restriction. \* \* First, the  
 polynomial itself and its table of feedback term  
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 Copyright (C) 1995-2003, 2005-2006, 2008-2013 Free Software Foundation,  
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 Copyright (C) 1996-2001, 2003-2015 Free Software Foundation, Inc.# Written by Gordon Matzigkeit, 1996  
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 linux-yocto v5.4 yocto:linux-yocto/v5.4  
 (C) 10#define AC97\_GP\_DRSS\_78 0x0400 /\* LR 7+8 \*/  
 (C) \* 11101: sent, \* recv\_ack (B) or implicit "ack" (A), \* still waiting for the barrier ack. \* master\_bio may already be completed  
 and invalidated. \* 11100: write acked  
 (C) 1986-1988 V4.0I 03/16/88  
 (C) 1986-1989 V5.0C2/14/89  
 (C) 1986-1989 V6.0A7/28/89  
 (C) 1986-1990 1800-V1.07/28/89", 5, 50, 6, 0x1fcc  
 (C) 1986-1990 1800-V2.07/28/89", 5, 50, 6, 0x1fcc  
 (C) 1986-1990 1800-V2.07/28/89", 72, 50, 6, 0x1fa2  
 (C) 1986-1990 1800-V2.0", 73, 43, 6, 0x1fa2  
 (C) 1986-1990 V6.0105/31/90  
 (C) 1986-1990 V6.0209/18/90  
 (C) 1986-1990 V7.009/18/90  
 (C) 1991 1800-V2.0.", 72, 39, 6, 0x1fa3  
 (C) 1991, 1992 by Linus Torvalds \*/  
 (C) 1991 Linus Torvalds - minix filesystem \*/  
 (C) 1991 Linus Torvalds - minix filesystem \* 1992, 1993, 1994 Eric Youngdale Modified for ISO 9660 filesystem. \* 1994 Eberhard Mönkeberg -  
 multi session handling. \* 1995 Mark Dob

(C) 1991 Linus Torvalds - minix filesystem \* \* affs directory handling functions  
(C) 1991 Linus Torvalds - minix filesystem \* \* affs regular file handling primitives  
(C) 1991 Linus Torvalds - minix filesystem \*/#include <linux/sched.h>#include <linux/cred.h>#include <linux/gfp.h>#include "affs.h"  
(C) 1991 Linus Torvalds - minix filesystem \* \* Steve Beynon : Missing last directory entries fixed \* (stephen@askone.demon.co.uk) : 21st  
June 1996  
(C) 1992, 1993, 1994 Eric Youngdale Modified for ISO 9660 filesystem. \*  
(C) 1992,1993 Brad McLean. See aha1740.c \* for more info \*  
(C) 1992, 1993 Eric Youngdale \* \* Rock Ridge Extensions to iso9660  
(C) 1992, 1993 Linus Torvalds, (C) 1997 Ingo Molnar \* \* IRQ/IPI changes taken from work by Thomas Radke \* <tomsoft@informatik.tu-chemnitz.de>  
(C) 1992, 1993 Linus Torvalds, (C) 1997 Ingo Molnar \* \* moved some of the old arch/i386/kernel/irq.h to here. VY  
(C) 1992 Eric Youngdale Modified for ISO 9660 filesystem. \*  
(C) 1992 Eric Youngdale Modified for ISO9660 filesystem. \*  
(C) 1992 V3.00.004/02/92", 5, 44, 6, 0  
(C) 1992 V8.00.004/02/92 \* \* (The cards which do \*NOT\* work are all 8-bit cards -- although some of \* them have a 16-bit form-factor, the upper 8-  
bits are used only for IRQs \* and are \*NOT\* used f  
(C) 1993 by Hamish Macdonald \*  
(C) 1993 by Hartmut Schirmer  
(C) 1993, Hamish Macdonald, see a2091.c for more info \*  
(C) 1993, Hamish Macdonald, see a3000.c for more info \*  
(C) 1993, Hamish Macdonald, see mvme147.c for more info \*  
(C) 1993, Ralf Baechle, see gvp11.c for more info  
(C) 1993 Ray Burr - Amiga FFS filesystem. \* \* Please send bug reports to: hjw@zvw.de  
(C) 1993 Ray Burr - Modified for Amiga FFS filesystem. \*  
(C) 1993 V3.203/12/93", 5, 44, 7, 0{"IBM F1 P2 BIOS v1.0011/09/92", 5, 28, 7, 0x1ff3 },{"IBM F1 P2 BIOS v1.0104/29/93",  
5, 28, 7, 0 },{"Future Domain Corp. V1.0008/18/93", 5, 33, 7  
(C) 1994, 1995, 1996,1997 Russell King  
(C) 1994, 1995, 1996\* Normunds Saumanis (normunds@fi.ibm.com)  
(C) 1994-2001 Russell King  
(C) 1994-2001 Russell King \* \* This file contains the ARM-specific time handling details:  
(C) 1994 Linus Torvalds \*/  
(C) 1994\* Normunds Saumanis (normunds@rx.tech.swh.lv)  
(C) 1994 V3.5", 5, 44, 7, 0{"FUTURE DOMAIN CORP. V3.6008/18/93", 5, 34, 7, 0 },{"FUTURE DOMAIN CORP. V3.6108/18/93",  
5, 34, 7, 0 },};#define SIGNATURE\_COUNT ARRAY\_SIZE(signatures)  
(C) 1995-2001 Russell King v0.04\n";  
(C) 1995-2003 Linux DECnet Project Team\n";  
(C) 1995 by Andreas Busse (andy@waldorf-gmbh.de) \* \* A driver for the Mac onboard Sonic ethernet chip.  
(C) 1995 by Andreas Busse (andy@waldorf-gmbh.de) \* \* A driver for the onboard Sonic ethernet controller on Mips Jazz \* systems (Acer Pica-61, Mips  
Magnum 4000, Olivetti M700 and \* perhaps others,  
(C) 1995 by Andreas Busse (andy@waldorf-gmbh.de) \* \* A driver for the onboard Sonic ethernet controller on the XT2000.  
(C) 1995 by Andreas Busse (andy@waldorf-gmbh.de) \* \* Core code included by system sonic drivers  
(C) 1995 Linus Torvalds \* \* Alpha division..  
(C) 1996,1998 by Thomas Bogendoerfer (tsbogend@alpha.franken.de) \* \* This driver is based on work from Andreas Busse, but most of \* the code is  
rewritten.  
(C) 1996-1998 by Thomas Sailer, HB9JNX/AE4WA \*/  
(C) 1996-1998 by Thomas Sailer, HB9JNX/AE4WA \*/#ifndef \_HDLCDRV\_H#define \_HDLCDRV\_H  
(C) 1996-2000 Thomas Sailer, HB9JNX/AE4WA\n""baycom\_par: version 0.9\n";  
(C) 1996-2000 Thomas Sailer, HB9JNX/AE4WA\n""baycom\_ser\_fdx: version 0.10\n";  
(C) 1996-2000 Thomas Sailer, HB9JNX/AE4WA\n""baycom\_ser\_hdx: version 0.10\n";  
(C) 1996-2000 Thomas Sailer HB9JNX/AE4WA\n");printf(KERN\_INFO "hdldrv: version 0.8\n"); return 0;}  
(C) 1996 by Thomas Bogendoerfer (tsbogend@bigbug.franken.de) \* \* This driver is based on work from Andreas Busse, but most of \* the code is  
rewritten.  
(C) 1996 Gordon Chaffee \* \* Joliet: Microsoft's Unicode extensions to iso9660  
(C) 1996 Miguel de Icaza  
(C) 1996 P. Braam and M. Callahan  
(C) 1996 Peter Braam  
(C) 1996 Peter Braam \*/  
(C) 1996, Peter J. Braam  
(C) 1996 Philip Blundell \* \* 10-04-96 RMK Various cleanups & reduced register usage. \* 08-04-98 RMK Shifts re-ordered  
(C) 1997-1998 by Thomas Sailer, HB9JNX/AE4WA \*/  
(C) 1997-1998 Caldera, Inc.

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(C) 1997 Carnegie Mellon University \* \* Carnegie Mellon encourages users of this code to contribute improvements \* to the Coda project. Contact Peter Braam <coda@cs.cmu.edu>.  
(C) 1997 Carnegie Mellon University \* \* Carnegie Mellon encourages users to contribute improvements to \* the Coda project. Contact Peter Braam (coda@cs.cmu.edu).  
(C) 1997 Carnegie Mellon University \* \* Carnegie Mellon University encourages users of this software to \* contribute improvements to the Coda project.  
(C) 1997 Linus Torvalds  
(C) 1997 M. Kirkwood \* Converted to new API by Alan Cox <alan@lxorguk.ukuu.org.uk> \* Various bugfixes and enhancements by Russell Kroll <rkroll@exploits.org> \* \* Converted to the radio-isa f  
(C) 1997 M. Kirkwood \* Converted to new API by Alan Cox <alan@lxorguk.ukuu.org.uk> \* Various bugfixes and enhancements by Russell Kroll <rkroll@exploits.org> \* \* Converted to the radio-isa framewo  
(C) 1997 Thomas Schoebel-Theuer, \* with heavy changes by Linus Torvalds \*/  
(C) 1998-1999 Dave Boynton  
(C) 1998,1999 Kenneth Albanowski, D. Jeff Dionne\n");  
(C) 1998-2000 Thomas Sailer, HB9JNX/AE4WA\n""baycom\_epp: version 0.7\n";  
(C) 1998-2001 Ben Fennema  
(C) 1998-2001 Ben Fennema \* \* HISTORY  
(C) 1998-2004 Ben Fennema  
(C) 1998-2004 Ben Fennema \* \* HISTORY  
(C) 1998 Alan Cox \* \* Debugging Andreas Ehliar, Michael Schmitz  
(C) 1998 Ben Pfaff \*  
(C) 1998 Christian Schmidt (chr.schmidt@tu-bs.de)  
(C) 1998 Christian Schmidt <schmidt@digadd.de>  
(C) 1998 Dave Boynton  
(C) 1998 James Banks  
(C) 1998 Krzysztof G. Baranowski  
(C) 1998 Michael Tesch (tesch@cs.wisc.edu) \*  
(C) 1998 Red Hat  
(C) 1998 Red Hat \* TCP NFS related read + write fixes  
(C) 1998 Stephen Tweedie \*/#include <linux/mm.h>#include <linux/gfp.h>#include <linux/kernel\_stat.h>#include <linux/swap.h>#include <linux/swapops.h>#include <linux/init.h>#include <linux/page  
(C) 1998 Thomas Bogendoerfer \* \* This driver is based on tgafb.c  
(C) 1998 Thomas Bogendoerfer (tsbogend@alpha.franken.de)  
(C) 1998 Tom Lees <tom@lpsg.demon.co.uk> \* Minor reorganizations by David Hinds <dahinds@users.sourceforge.net>  
(C) 1999, 2000, 05, 06 Ralf Baechle (ralf@linux-mips.org)  
(C) 1999-2000 Kurt Garloff \* \* Oliver Neukum <oliver@neukum.name> \* Ali Akcaagac <aliakc@web.de> \* Jamie Lenehan <lenehan@twibble.org>  
(C) 1999,2000 Nortel Networks.\n"  
(C) 1999, 2000 Silicon Graphics, Inc. \*/#include <linux/bcd.h>#include <linux/clockchips.h>#include <linux/init.h>#include <linux/kernel.h>#include <linux/sched.h>#include <linux/sched\_clock.h>  
(C) 1999-2000 Stelias Computing Inc \* \* HISTORY  
(C) 1999-2000 Yon Uriarte <yon@astaro.de> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/in.h>#include <linux/module.h>#include <linux/skbuff.h>#include <linux/ip.h>  
(C) 1999-2000 Yon Uriarte <yon@astaro.de> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include <linux/skbuff.h>#include <linux/in.h>#include <linux/ip.h>  
(C) 1999-2001 Ben Fennema  
(C) 1999-2001 Ben Fennema \* \* HISTORY  
(C) 1999-2001 by Helge Deller <deller@gmx.de> \* \* based on parport\_pc.c by \* Grant Guenther <grant@torque.net> \* Phil Blundell <philb@gnu.org> \* Tim Waugh <tim@cyberelk.demon  
(C) 1999-2001 by Helge Deller <deller@gmx.de> \* \* based on parport\_pc.c by \* Grant Guenther <grant@torque.net> \* Phil Blundell <Philip.Blundell@pobox.com> \* Tim Waugh <tim@cybe  
(C) 1999-2001 James Morris <jmorros@intercode.com.au> \*/  
(C) 1999-2001 Marc Boucher <marc@mbsi.ca>  
(C) 1999-2001 Marc Boucher <marc@mbsi.ca> \*  
(C) 1999-2001 Michal Ludvig <michal@logix.cz> \*/  
(C) 1999-2001 Paul  
(C) 1999-2001 Torben Mathiasen  
(C) 1999-2003 Nemosoft Unv.  
(C) 1999-2004 Ben Fennema  
(C) 1999-2004 Max Krasnyansky <maxk@qualcomm.com>"  
(C) 1999-2004 Nemosoft Unv.  
(C) 1999-2008 Helge Deller <deller@gmx.de>

(C) 1999 Andrea Arcangeli <andrea@suse.de>  
(C) 1999 Andrea Arcangeli <andrea@suse.de>  
(C) 1999 Andrea Arcangeli <andrea@suse.de>  
(C) 1999 Andrea Arcangeli <andrea@suse.de> (dynamic inode allocation) \*/#include <linux/export.h>#include <linux/fs.h>#include  
<linux/mm.h>#include <linux/backing-dev.h>#include <linux/hash.h>#  
(C) 1999 Andrea Arcangeli, SuSE GmbH, andrea@suse.de \* Gerhard Wichert, Siemens AG, Gerhard.Wichert@pdb.siemens.de \*  
(C) 1999 Benjamin Reed \* \* TODO list is at the wiki: \*  
(C) 1999 by Joshua M. Thompson (funaho@jurai.org) \* Based on via-cuda.c by Paul Mackerras. \* \* 1999-07-01 (jmt) - First implementation for new  
driver architecture.  
(C) 1999 by Joshua M. Thompson (funaho@jurai.org) \* \* Redistribution and use in source and binary forms, with or without \* modification, are  
permitted provided that the following conditions \* are  
(C) 1999 Chris Kemp and Tim Watterton  
(C) 1999 Dave Airlie, University of Limerick, Ireland <airlied@linux.ie> \*/  
(C) 1999 Dave Airlie, University of Limerick, Ireland <airlied@linux.ie> \* \* Rewrite of larges part of the code in order to stabilize TCP stuff. \* Fix  
behaviour when socket buffer is full.  
(C) 1999 David A. Hinds \*/  
(C) 1999 David A. Hinds  
(C) 1999 David A. Hinds \*/  
(C) 1999 David Airlie, airlied@linux.ie \* [07-SEP-99] Bugfixes \* \* [06-Jan-2002] Russell King <rmk@arm.linux.org.uk> \* Converted to new serial core  
(C) 1999 David Huggins-Daines <dhd@debian.org> \*  
(C) 1999 Helge Deller <deller@gmx.de>  
(C) 1999 Mihai Spatar  
(C) 1999 Nicolas Pitre <nico@fluxnic.net> \* \* Reorganised to be machine independent.  
(C) 1999 Rt-Control, Inc\n");  
(C) 1999 Rusty Russell (rusty@rustcorp.com.au). \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt  
(C) 1999 Stelias Computing Inc \* \* HISTORY  
(C) 1999 Thomas Bogendoerfer (tsbogend@alpha.franken.de) \* \* 11/26/2000 -- disabled the existing code because it didn't work for \* me in 2.4.  
Replaced with a significantly more primitive version  
(C) 1999 Thomas Bogendoerfer (tsbogend@alpha.franken.de) \* \* based on code from Oliver Jowett <oliver@jowett.manawatu.gen.nz>  
(C) 1999 Trond Myklebust <trond.myklebust@fys.uio.no> \*  
(C) 1999 Ulf Carlsson (ulfc@thepuffingruop.com) \* \* This driver is based on sgicons.c and cons\_newport.  
(C) (1 << (((C) > 2)  
(C) 1:M \* ... -->|(A)|--+ | +-----+ +----+ +-->| 1 | +----+ \* ... -->| | +-->|(D) 1:3 |-----+ +----+ \* pll1.N -->| N |  
+-----\*  
(C) 2000,2001,2002,2003,2004 Omnikey AG \*  
(C) 2000-2001 by Harald Welte <laforge@gnumonks.org>  
(C) 2000,2001 by Harald Welte <laforge@netfilter.org> \* \* Hop Limit matching module  
(C) 2000-2001 Felix Domke (tmbinc@gmx.net), Gillem (htoa@gmx.net) \*  
(C) 2000-2001 Svenning Soerensen <svenning@post5.tele.dk>  
(C) 2000-2002 by Harald Welte <laforge@gnumonks.org> \* based on RR's ip\_contrack\_ftp.c  
(C) 2000-2002 Harald Welte <laforge@netfilter.org>  
(C) 2000-2002 James Morris <jmorris@intercode.com.au>  
(C) 2000-2004 by Harald Welte <laforge@netfilter.org> \*/  
(C) 2000-2005 by Harald Welte <laforge@gnumonks.org> \* \* Development of this code funded by Astaro AG (http://www.astaro.com/)  
(C) 2000,2005 by Harald Welte <laforge@netfilter.org> \* \* Hop Limit modification target for ip6tables \* Maciej Soltysiak <solt@dns.toxicfilms.tv>  
(C) 2000 Aleph One Ltd (Tak-Shing Chan) \* EtherM integration re-engineered by Russell King. \* \* Changelog:  
(C) 2000 Andrea Arcangeli <andrea@suse.de> SuSE \* Queue request tables / lock, selectable elevator, Jens Axboe <axboe@suse.de> \* kernel-doc  
documentation started by NeilBrown <neilb@cse.unsw.edu.au>  
(C) 2000 by Harald Welte <laforge@gnumonks.org>  
(C) 2000 by Harald Welte <laforge@netfilter.org>  
(C) 2000 by Matthew G. Marsh <mgm@paktronix.com> \* \* See RFC2474 for a description of the DSCP field within the IP Header.  
(C) 2000 by Matthew G. Marsh <mgm@paktronix.com> \* This software is distributed under GNU GPL v2, 1991 \* \* See RFC2474 for a description of the  
DSCP field within the IP Header.  
(C) 2000 by R.E.Wolff@BitWizard.nl \* Copied snippets from zatm.c by Werner Almesberger, EPFL LRC/ICA  
(C) 2000 Carnegie Mellon University \* \* Carnegie Mellon encourages users of this code to contribute improvements \* to the Coda project  
http://www.coda.cs.cmu.edu/ <coda@cs.cmu.edu>.  
(C) 2000 ChyGwyn Limited - http://www.chygwyn.com/ \* \* Author: Steven Whitehouse <steve@chygwyn.com>  
(C) 2000 David Brown <usb-storage@davidb.org> \* \* This driver was developed with information provided in FREECOM's USB \* Programmers Reference  
Guide. For further information contact Freecom \* (ht  
(C) 2000 IBM Corp. by Fritz Elfert (felfert@millenux.com)");  
(C) 2000 In-System Design, Inc. (support@in-system.com) \* \* The ISD200 ASIC does not natively support ATA devices. The chip \* does implement an

```

interface, the ATA Command Block (ATACB) which provi
(C) 2000 Marc Boucher <marc@mbsi.ca> *
(C) 2000 Milan Pikula <www@fornax.sk> * * sir_ir - Device driver for use with SIR (serial infra red) * mode of IrDA on many notebooks.
(C) 2000 Nicolas Pitre <nico@fluxnic.net> */#include <linux/module.h>#include <linux/types.h>#include <linux/ioport.h>#include
<linux/kernel.h>#include <linux/init.h>#include <linux/errno.h>#i
(C) 2000 Nicolas Pitre <nico@fluxnic.net> * * This code is GPL
(C) 2000 Nicolas Pitre <nico@fluxnic.net> * * This code is GPL
(C) 2000 Red Hat. GPL */
(C) 2000 Red Hat. GPL */
(C) 2000 Red Hat. GPL */
(C) 2000 Red Hat. GPL * * 10/10/2000 Nicolas Pitre <nico@fluxnic.net> * - completely revamped method functions so they are aware and *
independent of the flash geometry (buswidth, interleave,
(C) 2000 Red Hat. GPL See JEDEC (http://www.jedec.org/) standard JESD21C (section 3.5) for the standard this probe goes back to.
(C) 2000 Stelias Computing Inc * * HISTORY
(C) 2000 -- This code is GPL.
(C) 2000 YAEGASHI Takeshi
(C) 2001, 2002, 2003, 2004 Rusty Russell * * This code is licenced under the GPL.
(C) 2001,2002,2003 Benjamin Herrenschmidt (benh@kernel.crashing.org)
(C) 2001,2002,2006 by Jan-Benedict Glaw <jbglaw@lug-owl.de> * * This driver is a modified version of Erik Mouw's example proc * interface, so: thank
you, Erik! He can be reached via email at * <J.
(C) 2001-2002 Andras Kis-Szabo <kisza@sch.bme.hu> */
(C) 2001-2002 Andras Kis-Szabo <kisza@sch.bme.hu> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/skbuff.h>#include <linux/ip.h>#include <linux/ipv6.h>#inc
(C) 2001-2002 Andras Kis-Szabo <kisza@sch.bme.hu> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/skbuff.h>#include <linux/ipv6.h>#include <linux/types.h>#
(C) 2001-2002 Bj * * Developed with the assistance of:
(C) 2001, 2002 by: * Alan Cox <alan@redhat.com> * Thomas Hood * Brian Gerst <bgerst@didntduck.org> *
(C) 2001-2002 IBM Deutschland Entwicklung GmbH");
(C) 2001-2002 Maciej Soltysiak <solt@dns.toxicfilms.tv> */
(C) 2001-2002 Magnus Boden <mb@ozaba.mine.nu>
(C) 2001-2002 Magnus Boden <mb@ozaba.mine.nu> */
(C) 2001-2002 Patrick McHardy <kaber@trash.net> */
(C) 2001-2003 Bart De Schuymer <bdschuym@pandora.be> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt
(C) 2001-2003 Red Hat, Inc. * GPL'd */
(C) 2001-2004 Dave Jones.
(C) 2001 - 2005 Tensilica Inc. */
(C) 2001,2006 IBM Corporation");
(C) 2001 - 2007 Tensilica Inc. * Kevin Chea <kchea@yahoo.com> * Marc Gauthier <marc@linux-xtensa.org> * Chris Zankel <chris@zankel.net> *
(C) 2001 - 2013 Tensilica Inc. */
(C) 2001 Andreas Gruenbacher, <a.gruenbacher@computer.org>*/
(C) 2001 Andreas Gruenbacher, <a.gruenbacher@computer.org> */#ifndef __F2FS_XATTR_H__#define __F2FS_XATTR_H__
(C) 2001 Brad Hards
(C) 2001 by Jay Schulist <jschlst@samba.org>,
(C) 2001 by Jay Schulist <jschlst@samba.org>
(C) 2001 * by Karsten Merker <merker@linuxtag.org> * and Maciej W. Rozycki <macro@ds2.pg.gda.pl> * Later on, I had access to the device's
documentation (referenced below). */
(C) 2001 Clemson University and The University of Chicago
(C) 2001 Clemson University and The University of Chicago * * Changes by Acxiom Corporation to add proc file handler for pvfs2 client
(C) 2001 Clemson University and The University of Chicago * * Changes by Acxiom Corporation to add protocol version to kernel
(C) 2001 Clemson University and The University of Chicago * * See COPYING in top-level directory.
(C) 2001 Dave Jones, Arjan van de ven.
(C) 2001 Dimitromanolakis Apostolos <apdim@grecian.net> * * Based in the radio Maestro PCI driver. Actually it uses the same chip * for radio but
different pci controller.
(C) 2001 IBM Corp. by Fritz Elfert (felbert@millenux.com");MODULE_DESCRIPTION("Linux for S/390 IUCV lowlevel driver");
(C) 2001 IBM Corporation by Fritz Elfert (felbert@millenux.com");MODULE_DESCRIPTION ("Linux for S/390 IUCV network driver");
(C) 2001 IBM Deutschland Entwicklung GmbH by Carsten Otte and "
(C) 2001 Johannes Erdfelt <jerdfelt@valinux.com>
(C) 2001 Marc Boucher (marc@mbsi.ca).
(C) 2001 Marc Boucher (marc@mbsi.ca). */
(C) 2001 Martin Josefsson <gandalf@wlug.westbo.se> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/skbuff.h>#include <linux/netfilter.h>#include <net/netfil

```

(C) 2001 NeilBrown <neilb@cse.unsw.edu.au> \* \* This driver for the Micro Memory PCI Memory Module with Battery Backup  
(C) 2001 Red Hat, Inc. \* \* Author: Arjan Van De Ven <arjanv@redhat.com> \* Loosly based on i82365.c from the pcmcia-cs package  
(C) 2001 Red Hat, Inc. \* \* GPL'd  
(C) 2001 San Mehat <netwerk@valinux.com>  
(C) 2002 - 2003 Dominik Brodowski <linux@brodo.de>  
(C) 2002 - 2003 Dominik Brodowski <linux@brodo.de> \* \* Based upon reverse engineered information, and on Intel documentation \* for chipsets ICH2-M and ICH3-M.  
(C) 2002 - 2003 Dominik Brodowski <linux@brodo.de> \* \* BIG FAT DISCLAIMER: Work in progress code. Possibly \*dangerous\*  
(C) 2002 - 2003 Dominik Brodowski <linux@brodo.de> \*/#ifndef LINUX\_CPUFREQ\_H#define LINUX\_CPUFREQ\_H  
(C) 2002 - 2003 Dominik Brodowski <linux@brodo.de> \* \* Library for common functions for Intel SpeedStep v.1 and v.2 support  
(C) 2002-2003 Intel Corp \* Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com>. \*  
(C) 2002, 2003 Marc Zyngier <maz@wild-wind.fr.eu.org> \*/  
(C) 2002-2003 Nadia Yvette Chambers, IBM  
(C) 2002 - 2004 Dominik Brodowski <linux@brodo.de> \*/  
(C) 2002-2004 Ingo Molnar, Red Hat \* \* pid-structures are backing objects for tasks sharing a given ID to chain \* against. There is very little to them aside from hashing them and \* parking tasks  
(C) 2002-2004 Netfilter Core Team <coreteam@netfilter.org>  
(C) 2002-2004 Netfilter Core Team <coreteam@netfilter.org> \*/  
(C) 2002-2004 Netfilter Core Team <coreteam@netfilter.org> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include <linux/skbuff.h>#include <linux/slab.h>#include <linux/i  
(C) 2002-2004 Netfilter Core Team <coreteam@netfilter.org> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include <linux/types.h>#include <linux/udp.h>#include <linux/skb  
(C) 2002-2005 BUFFALO INC.\n");}  
(C) 2002-2005 by Harald Welte <laforge@gnumonks.org>  
(C) 2002-2005 Netfilter Core Team <coreteam@netfilter.org> \*/  
(C) 2002-2006 by Harald Welte <laforge@gnumonks.org>  
(C) 2002-2006 Netfilter Core Team <coreteam@netfilter.org>  
(C) 2002-2006 Netfilter Core Team <coreteam@netfilter.org> \*/  
(C) 2002-2006 Netfilter Core Team <coreteam@netfilter.org> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include <linux/netfilter/x\_tables.h>#include <net/netfilter/nf\_na  
(C) 2002-2013 Jozsef Kadlec <kadlec@netfilter.org>  
(C) 2002 Alan Stern <stern@rowland.org> \*/  
(C) 2002 Alan Stern <stern@rowland.org> \* \* Initial work:  
(C) 2002 Andreas Gruenbacher, <a.gruenbacher@computer.org>\*/  
(C) 2002 Arjan van de Ven <arjanv@redhat.com>  
(C) 2002 Atom Create Engineering Co., Ltd.  
(C) 2002 Atom Create Engineering Co., Ltd. \*/#include <linux/module.h>#include <linux/init.h>#include <linux/kdev\_t.h>#include <linux/cdev.h>#include <linux/fs.h>#include <asm/io.h>#include <l  
(C) 2002 \* by Adam Belay <ambx1@neo.rr.com> \*/  
(C) 2002 by Brian J. Murrell <netfilter@interlinx.bc.ca> \* based on a copy of HW's ip\_nat\_irc.c as well as other modules  
(C) 2002 by Brian J. Murrell <netfilter@interlinx.bc.ca> \* based on HW's ip\_contrack\_irc.c as well as other modules  
(C) 2002 by gaoyonghong  
(C) 2002 by Harald Welte <laforge@gnumonks.org>  
(C) 2002 by Harald Welte <laforge@gnumonks.org> \* \* This software is distributed under GNU GPL v2, 1991  
(C) 2002 by Harald Welte <laforge@netfilter.org>  
(C) 2002 by Harald Welte <laforge@netfilter.org>\*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/in.h>#include <linux/module.h>#include <linux/skbuff.h>#include <linux/ip.h>#include  
(C) 2002 by Harald Welte <laforge@netfilter.org> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include <linux/skbuff.h>#include <linux/ip.h>#include <linux/ipv6.h>#incl  
(C) 2002 David Woodhouse <dwmw2@infradead.org>  
(C) 2002 Harald Welte <laforge@gnumonks.org>  
(C) 2002 Harald Welte <laforge@gnumonks.org> \* \* This software is distributed under GNU GPL v2, 1991  
(C) 2002 Harald Welte <laforge@gnumonks.org> \* This software is distributed under GNU GPL v2, 1991 \* \* See RFC2474 for a description of the DSCP field within the IP Header.  
(C) 2002 Hirokazu Takahashi <taka@valinux.co.jp> \*/  
(C) 2002 Hiroshi Miura <miura@da-cha.org> \* All Rights Reserved  
(C) 2002 Louis Zhuang <louis.zhuang@intel.com>. \* 2007 Alexander Eichner \* 2008 Pekka Paalanen <pq@iki.fi> \*/  
(C) 2002 Martin Devera (devik@cdi.cz). \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include <linux/bitops.h>#include <linux/skbuff.h>#include <linux/math64.h>#include  
(C) 2002 Nadia Yvette Chambers, IBM  
(C) 2002 Oliver Neukum \* \* Original author: The Zapman <zapman@interlan.net> \* Inspired by, and much credit goes to Michael Rothwell \*

<rothwell@interlan.net> for the test equipment, h  
 (C) 2002 Padraig Brady. <padraig@antefacto.com> \* \* Based upon datasheets & sample CPUs kindly provided by VIA.  
 (C) 2002 Pascal Dameme <netinet@freesurf.fr>  
 (C) 2002 Red Hat  
 (C) 2002 Samuel Chessman  
 (C) 2002 Tora T. Engstad \* All Rights Reserved  
 (C) 2002 Zwane Mwaikambo <zwane@commfireservices.com>  
 (C) 2003, 2004, 2006 Andriy Skulysh  
 (C) 2003,2004 Aurelien Alleaume <slts@free.fr>  
 (C) 2003-2004 by Harald Welte <laforge@netfilter.org>  
 (C) 2003-2004 by Harald Welte <laforge@netfilter.org> \* based on ideas of Fabio Olive Leite <olive@unixforge.org> \* \* Development of this code  
 funded by SuSE Linux AG, <http://www.suse.com/>  
 (C) 2003 - 2004 David Woodhouse  
 (C) 2003 - 2004 Dominik Brodowski \*/  
 (C) 2003 - 2004 Dominik Brodowski \*/  
 (C) 2003 - 2004 Dominik Brodowski <linux@dominikbrodowski.de> \* \* Based on code found in \* linux/arch/i386/kernel/cpu/cpufreq/speedstep-  
 centrino.c \* and originally developed by Jeremy Fitzharding  
 (C) 2003-2004 Paul Clements, SteelEye Technology: \* - persistent bitmap code \*/  
 (C) 2003, 2004 Paul Mundt  
 (C) 2003,2004 USAGI/WIDE Project <<http://www.linux-ipv6.org>>  
 (C) 2003-2005 Netfilter Core Team <coreteam@netfilter.org> \*/  
 (C) 2003-2006 Netfilter Core Team <coreteam@netfilter.org>  
 (C) 2003 - 2008 Dominik Brodowski \*/  
 (C) 2003 - 2010 Dominik Brodowski \*/  
 (C) 2003 - 2010 Dominik Brodowski \* \* This file contains definitions \_only\_ needed by the PCMCIA core modules. \* It must not be included by PCMCIA  
 socket drivers or by PCMCIA device \* drivers.  
 (C) 2003 Andreas Gruenbacher <agruen@suse.de> \*/#ifndef \_\_LINUX\_NFSACL\_H#define \_\_LINUX\_NFSACL\_H  
 (C) 2003 Andreas Gruenbacher <agruen@suse.de> \*/#ifndef \_\_UAPI\_\_LINUX\_NFSACL\_H#define \_\_UAPI\_\_LINUX\_NFSACL\_H  
 (C) 2003 Ardis Technologies <roman@ardistech.com>  
 (C) 2003 Ardis Technologies <roman@ardistech.com> \*/  
 (C) 2003 Ardis Technologies <roman@ardistech.com> \*  
 (C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Export hfs data via xattr  
 (C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Format of structures on disk \* Information taken from Apple Technote #1150 (HFS Plus  
 Volume Format)  
 (C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handle basic btree node operations  
 (C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handle individual btree records  
 (C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handle opening/closing btree  
 (C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handler routines for unicode strings  
 (C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handling of allocation file  
 (C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handling of catalog records  
 (C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handling of directories  
 (C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handling of Extents both in catalog and extents overflow trees  
 (C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Handling of HFS wrappers around HFS+ volumes  
 (C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Inode handling routines  
 (C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Option parsing  
 (C) 2003 Ardis Technologies <roman@ardistech.com> \* \* Search routines for btrees  
 (C) 2003 Aurelien Alleaume <slts@free.fr>  
 (C) 2003 Bruno Ducrot  
 (C) 2003 by Patrick Mchardy <kaber@trash.net>  
 (C) 2003 by Sampsa Ranta <sampsa@netsonic.fi> \*/  
 (C) 2003 Christoph Jungegger <disdos@traum404.de> \* \* Some of this code is borrowed from orinoco\_plx.c  
 (C) 2003 Dave Jones. \* \* AMD-specific information  
 (C) 2003 Dave Jones on behalf of SuSE Labs  
 (C) 2003 Dave Jones on behalf of SuSE Labs. \* \* Based upon datasheets & sample CPUs kindly provided by AMD.  
 (C) 2003 Dave Jones. \* \* VIA-specific information  
 (C) 2003 Herbert Valerio Riedel <hvr@gnu.org>  
 (C) 2003 Hiroshi Miura <miura@da-cha.org> \*/  
 (C) 2003 IBM Corporation by Martin Schwidefsky (schwidefsky@de.ibm.com)");MODULE\_DESCRIPTION ("Linux for S/390 IUCV special message driver");  
 (C) 2003 John Whitney (john.whitney@timesys.com)  
 (C) 2003 Jozsef Kadlec <kadlec@netfilter.org>  
 (C) 2003 Jun Nakajima <jun.nakajima@intel.com>



(C) 2003 Linux Networx (<http://lnxi.com>)  
(C) 2003 Luis R. Rodriguez <mcgrof@ruslug.rutgers.edu>  
(C) 2003 Luis R. Rodriguez <mcgrof@ruslug.rutgers.edu> \*/  
(C) 2003 Marc Zyngier <maz@wild-wind.fr.eu.org> \*/  
(C) 2003 Marc Zyngier <maz@wild-wind.fr.eu.org> \*\* Ivan Kokshaysky <ink@jurassic.park.msu.ru> :  
(C) 2003 Marc Zyngier <maz@wild-wind.fr.eu.org> \* modified for SNI usage by Thomas Bogendoerfer \*/  
(C) 2003 Red Hat, Inc.  
(C) 2003 Simtec Electronics\*/  
(C) 2003 Venkatesh Pallipadi <venkatesh.pallipadi@intel.com>.  
(C) 2003 Venkatesh Pallipadi <venkatesh.pallipadi@intel.com>. \* Jun Nakajima <jun.nakajima@intel.com>  
(C) 2003 Venkatesh Pallipadi <venkatesh.pallipadi@intel.com>. \* Jun Nakajima <jun.nakajima@intel.com> \*/  
(C) 2003 Yasuyuki Kozakai @USAGI <yasuyuki.kozakai@toshiba.co.jp> \*/  
(C) 2003 zecke@handhelds.org \* \* based on arch/arm/kernel/apm.c \* factor out the information needed by architectures to provide \* apm status  
(C) 2004,2005 Benjamin Herrenschmidt, IBM Corp. \* \* NAPI and NETPOLL support  
(C) 2004-2006 Luc Saillard (luc@saillard.org)  
(C) 2004-2006 Luc Saillard (luc@saillard.org)  
(C) 2004-2006 Luc Saillard (luc@saillard.org)  
(C) 2004-2006 Sebastian Witt <se.witt@gmx.net> \* \* Based upon reverse engineered information  
(C) 2004-2009 Dominik Brodowski <linux@dominikbrodowski.de>  
(C) 2004-2009 Dominik Brodowski <linux@dominikbrodowski.de> \*/  
(C) 2004 Bruno Ducrot <ducrot@poupinou.org> \* \* Based on code found in \* linux/arch/i386/kernel/cpu/cpufreq/powernow-k8.c \* and originally developed by Paul Devriendt  
(C) 2004 by Bart De Schuymer <bdschuym@pandora.be> \*  
(C) 2004 by Eric Lemoine (eric.lemoine@gmail.com) \*  
(C) 2004 Dan Brown <dan\_brown@ieee.org>  
(C) 2004 David Bronaugh <dbronaugh>  
(C) 2004 Dominik Brodowski <linux@brodo.de>  
(C) 2004 Dominik Brodowski <linux@dominikbrodowski.de> \* \* Based on code found in \* linux/include/asm-i386/ist.h and linux/arch/i386/kernel/setup.c \* and originally developed by Andy Grover <andr  
(C) 2004 Dominik Brodowski \* \* To use:  
(C) 2004 Hiroshi Miura <miura@da-cha.org>  
(C) 2004 IBM Corporation by Xenia Tkatschow (xenia@us.ibm.com)\n"  
(C) 2004 Intracom, S.A.  
(C) 2004 jornada 720 kbd driver by Alex Lange <chicken@handhelds.org> \*/#include <linux/device.h>#include <linux/errno.h>#include <linux/interrupt.h>#include <linux/input.h>#include <linux/ker  
(C) 2004 Kaveh Lember <kaveh@smartlink.ee> \* \* Author: David Woodhouse <dwmw2@infradead.org> \* Additional Diskonchip 2000 and Millennium support by Dan Brown <dan\_brown@ieee.org> \* Diskonchip Mill  
(C) 2004 Linux Networx (<http://lnxi.com>)  
(C) 2004 Margit Schubert-While <margitsw@t-online.de> \*/  
(C) 2004 Nadia Yvette Chambers, Oracle  
(C) 2004 Nadia Yvette Chambers, Oracle \*/#include "sched.h"  
(C) 2004 NTT DATA Intellilink Co. <http://www.intellilink.co.jp/>  
(C) 2004 Paul Serice - The new inode scheme requires switching \* from iget() to iget5\_locked() which means \* the NFS export operations have to be h  
(C) 2004 Pavel Machek <pavel@ucw.cz> \* Based upon datasheets & sample CPUs kindly provided by AMD. \* \* Valuable input gratefully received from Dave Jones, Pavel Machek, \* Dominik Brodowski, Jac  
(C) 2004 Red Hat \* \* Marcelo Tosatti <marcelo@conectiva.com.br> : SMP fixes  
(C) 2004 Rusty Russell <rusty@rustcorp.com.au> IBM Corporation \* based on a copy of RR's ip\_nat\_ftp.c \*/  
(C) 2004 Tom Rini (trini@mvista.com)  
(C) 2004 Zou Nan hai <nanhai.zou@intel.com>. \*/  
(C) 2005-2006 Harald Welte <laforge@gnumonks.org>  
(C) 2005-2006 Harald Welte <laforge@gnumonks.org> \* - add support for poll() \* - driver cleanup \* - add waitqueues \* - adhere to linux kernel coding style and policies \* - support 2.6.13 "ne  
(C) 2005, 2006 Linux Networx (<http://lnxi.com>)  
(C) 2005-2006 MontaVista Software, Inc. \* Vitaly Bordug <vbordug@ru.mvista.com> \*/  
(C) 2005-2006 Netfilter Core Team <coreteam@netfilter.org>  
(C) 2005-2006 Red Hat Inc \* Alan Cox <alan@lxorguk.ukuu.org.uk> \* \* based upon cs5535.c from AMD <Jens.Altmann@amd.com> as cleaned up and \* made readable and Linux style by Wolfgang Zuleger <w  
(C) 2005-2006 Red Hat Inc \* Alan Cox <alan@lxorguk.ukuu.org.uk> \* \* The MPIIX is different enough to the PIIX4 and friends that we give it \* a separate driver. The old ide/pci code handles thi  
(C) 2005, 2006 Red Hat Inc. \* \* Author: David Woodhouse <dwmw2@infradead.org> \* Tom Sylla <tom.sylla@amd.com>

(C) 2005-2006 Red Hat Inc \* \* Based on pata-sil680. Errata information is taken from data sheets \* and the amd74xx.c driver by Vojtech Pavlik. Nvidia SATA devices are \* claimed by sata-nv.c.

(C) 2005-2006 Red Hat Inc \* \* Documentation \* Most chipset documentation available under NDA only

(C) 2005-2007 Linux Networx (<http://lnxi.com>)

(C) 2005-2012 by Pablo Neira Ayuso <pablo@netfilter.org> \* \* Initial connection tracking via netlink development funded and \* generally made possible by Network Robots, Inc. ([www.networkrobots.com](http://www.networkrobots.com))

(C) 2005-2012 Patrick McHardy <kaber@trash.net> \*/

(C) 2005-2017 by Pablo Neira Ayuso <pablo@netfilter.org> \* \* Initial netfilter messages via netlink development funded and \* generally made possible by Network Robots, Inc. ([www.networkrobots.com](http://www.networkrobots.com))

(C) 2005 Benedikt Spranger <b.spranger@linutronix.de> \*/

(C) 2005 by Christian Hentschel <chentschel@arnet.com.ar> \* based on RR's ip\_contrack\_ftp.c and other modules.

(C) 2005 by Christian Hentschel <chentschel@arnet.com.ar> \* based on RR's ip\_nat\_ftp.c and other modules.

(C) 2005 by Harald Welte <laforge@netfilter.org>

(C) 2005 by Harald Welte <laforge@netfilter.org> \*/

(C) 2005 Chuck Lever <cel@netapp.com> \* \* IPv6 support contributed by Gilles Quillard, Bull Open Source, 2005. \* <gilles.quillard@bull.net>

(C) 2005 EADS Astrium

(C) 2005 Finn Thain \* \* Converted to DMA API, added zero-copy buffer handling, and \* (from the mac68k project) introduced dhd's support for 16-bit cards.

(C) 2005 Finn Thain \* \* Converted to DMA API, and (from the mac68k project) introduced \* dhd's support for 16-bit cards.

(C) 2005 Finn Thain \* \* Converted to DMA API, converted to unified driver model, made it work as \* a module again, and from the mac68k project, introduced more 32-bit cards \* and dhd's support for

(C) 2005 Frank Rowand ([frowand@mvista.com](mailto:frowand@mvista.com))

(C) 2005 Frank Rowand ([frowand@mvista.com](mailto:frowand@mvista.com)) \* PPC32 support restored by Vitaly Wool <vwool@ru.mvista.com> and \* Sergei Shtylyov <sshtylyov@ru.mvista.com>

(C) 2005 Georges Toth <g.toth@e-biz.lu>

(C) 2005 Harald Welte <laforge@gnumonks.org>

(C) 2005 Harald Welte <laforge@netfilter.org> \* \* This software is distributed under GNU GPL v2, 1991

(C) 2005 John Williams \* \* MMU code derived from arch/ppc/kernel/head\_4xx.S:

(C) 2005 Michel Xhaard [mxhaard@magic.fr](mailto:mxhaard@magic.fr) \* \* V4L2 by Jean-Francois Moine <<http://moinejf.free.fr>>

(C) 2005 Nokia Corporation \*/

(C) 2005 Pablo Neira Ayuso <pablo@eurodev.net> \*/

(C) 2005 Patrick McHardy <kaber@trash.net>

(C) 2005 Red Hat

(C) 2005 Red Hat <[alan@lxorguk.ukuu.org.uk](mailto:alan@lxorguk.ukuu.org.uk)> \* \* This is a fairly generic MWDMA controller. It has some limitations \* as it requires timing reloads on PIO/DMA transitions but it is otherwise

(C) 2005 Red Hat (<http://www.redhat.com>)

(C) 2005 Red Hat Inc

(C) 2005 Red Hat Inc \* Alan Cox <[alan@lxorguk.ukuu.org.uk](mailto:alan@lxorguk.ukuu.org.uk)>

(C) 2005 Red Hat Inc \* Alan Cox <[alan@lxorguk.ukuu.org.uk](mailto:alan@lxorguk.ukuu.org.uk)> \* \* based upon

(C) 2005 Red Hat Inc \* \* based in part upon \* linux/drivers/ide/pci/alim15x3.c Version 0.17 2003/01/02

(C) 2005 Red Hat Inc \* \* Based on \* linux/drivers/ide/pci/opti621.c Version 0.7Sept 10, 2002

(C) 2005 Red Hat Inc \* \* based upon

(C) 2005 Red Hat Inc \* \* based upon cs5530.c by Mark Lord.

(C) 2005 Red Hat \* \* Some parts based on ata\_piix.c by Jeff Garzik and others.

(C) 2005 Thomas Gleixner <[tglx@linutronix.de](mailto:tglx@linutronix.de)> \* \* Simplifications of the original code by \* Oleg Nesterov <[oleg@tv-sign.ru](mailto:oleg@tv-sign.ru)>

(C) 2005 Thomas Winischhofer.; static const char bootlogo[] = "(o\_//\\ V\_/\_";

(C) 2005 USAGI/WIDE Project <<http://www.linux-ipv6.org>> \*/

(C) 2006-2007 MontaVista Software\n");

(C) 2006-2007 Shaohua Li <[shaohua.li@intel.com](mailto:shaohua.li@intel.com)> \* \* This code is licenced under the GPL.

(C) 2006-2007 Venkatesh Pallipadi <[venkatesh.pallipadi@intel.com](mailto:venkatesh.pallipadi@intel.com)> \* Shaohua Li <[shaohua.li@intel.com](mailto:shaohua.li@intel.com)> \* Adam Belay <[abelay@novell.com](mailto:abelay@novell.com)> \* \* This code is licenced under t

(C) 2006-2008 by Openmoko, Inc. \* All rights reserved

(C) 2006-2008 by Openmoko, Inc. \* Author: Balaji Rao <[balajirao@openmoko.org](mailto:balajirao@openmoko.org)> \* All rights reserved

(C) 2006-2008 by Openmoko, Inc. \* Author: Harald Welte <[laforge@openmoko.org](mailto:laforge@openmoko.org)> \* Balaji Rao <[balajirao@openmoko.org](mailto:balajirao@openmoko.org)> \* All rights reserved

(C) 2006-2008 Harald Welte <[laforge@gnumonks.org](mailto:laforge@gnumonks.org)> \*

(C) 2006,2008 Red Hat, Inc., James Morris <[jmorris@redhat.com](mailto:jmorris@redhat.com)> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include <linux/security.h>#include <linux/skbuff.h>#include

(C) 2006,2008 Red Hat, Inc., James Morris <[jmorris@redhat.com](mailto:jmorris@redhat.com)> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include <linux/skbuff.h>#include <linux/netfilter/x\_tables.h>

(C) 2006-2008 Takashi Iwai <[tiwai@suse.de](mailto:tiwai@suse.de)> \*/

(C) 2006-2009 VIA Technology, Inc.

```

(C) 2006-2010 Patrick McHardy <kaber@trash.net> */
(C) 2006-2012 */#include <linux/kernel.h>#include <linux/init.h>#include <linux/ipv6.h>#include <linux/netfilter.h>#include
<linux/netfilter_ipv6.h>#include <linux/export.h>#include <net/addrcl
(C) 2006-2012 */#include <linux/kernel.h>#include <linux/netfilter.h>#include <linux/netfilter_ipv4.h>#include <linux/ip.h>#include
<linux/skbuff.h>#include <linux/gfp.h>#include <linux/export
(C) 2006-2012 Patrick McHardy <kaber@trash.net>
(C) 2006-2012 Patrick McHardy <kaber@trash.net> */
(C) 2006-2012 Patrick McHardy <kaber@trash.net> ** Based on the old ipv4-only ipt_ULOG.c:
(C) 2006-2012 Patrick McHardy <kaber@trash.net> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt
(C) 2006-2012 Patrick McHardy <kaber@trash.net> ** Limitations:
(C) 2006-2012 Patrick McHardy <kaber@trash.net> ** TODO: - NAT to a unique tuple, not to TCP source port *      (needs netfilter tuple
reservation)
(C) 2006 by Patrick McHardy <kaber@trash.net>
(C) 2006 IBM Corp */
(C) 2006 kogiidena
(C) 2006 Michael Ellerman, IBM Corp */
(C) 2006 MontaVista Software, Inc. *      Vitaly Bordug <vbordug@ru.mvista.com> */
(C) 2006 Montavista Software\n");
(C) 2006 Patrick McHardy <kaber@trash.net> */#include <linux/kernel.h>#include <linux/module.h>#include <linux/moduleparam.h>#include
<linux/textsearch.h>#include <linux/skbuff.h>#include <linu
(C) 2006 Red Hat
(C) 2006 Red Hat <alan@lxorguk.ukuu.org.uk> * *      Some parts based on ata_piix.c by Jeff Garzik and others.
(C) 2006 Red Hat Inc */
(C) 2006 Red Hat Inc *      Alan Cox * * Based heavily on * linux/drivers/ide/pci/cy82c693.c      Version 0.40      Sep. 10, 2002
(C) 2006 Red Hat Inc * *      The Opti DMA controllers are related to the older PIO PCI controllers *      and indeed the VLB ones. The main differences are
that the timing * numbers are now based off PCI c
(C) 2006 Simtec Electronics
(C) 2006 Thomas Gleixner <tglx@linutronix.de>
(C) 2006 Thomas Maier <balagi@justmail.de> *
(C) 2006 Tim Small
(C) 2007, 2008, 2011, 2012 Patrick McHardy <kaber@trash.net> */
(C) 2007,2008 Dmitry Baryshkov * * Based on:
(C) 2007,2008 Jochen Friedrich <jochen@scram.de> */
(C) 2007-2008, MontaVista Software, Inc.
(C) 2007, 2008 Patrick McHardy <kaber@trash.net> */
(C) 2007,2009,2010 Bartlomiej Zolnierkiewicz * * Based in part on linux/drivers/ide/pci/pdc202xx_old.c
(C) 2007,2009 Bartlomiej Zolnierkiewicz * * Based upon linux/drivers/ide/pci/sis5513.c
(C) 2007-2010 by Jan Engelhardt <jengelh@medozas.de> * * Extracted from xt_TEE.c
(C) 2007,2011 Bartlomiej Zolnierkiewicz * * Based in part on drivers/ide/pci/aec62xx.c
(C) 2007-2012 Patrick McHardy <kaber@trash.net> */#include <linux/module.h>#include <linux/gfp.h>#include <linux/types.h>#include
<linux/skbuff.h>#include <linux/tcp.h>#include <linux/udp.h>#i
(C) 2007 aCarLab (India) Pvt. Ltd. (http://acarlab.com)
(C) 2007 Alexander Shishkin <virtuoso@slind.org>
(C) 2007 Bartlomiej Zolnierkiewicz * * based upon
(C) 2007 by Hans J. Koch <hjk@hansjkoch.de>
(C) 2007 by Hans J. Koch <hjk@hansjkoch.de> * * based on code written by John Morris <john.morris@spirentcom.com>
(C) 2007 by Holger Schurig <hs4233@mail.mn-solutions.de>
(C) 2007 by Patrick McHardy <kaber@trash.net> * * Based on the old ipv4-only ip_queue.c:
(C) 2007 by Sebastian Cla
(C) 2007 Dmitry Adamushko <dmitry.adamushko@gmail.com> * * Group scheduling enhancements by Srivatsa Vaddagiri
(C) 2007 EADS Astrium * * This driver is based on the lm75 and other lm_sensors/hwmon drivers
(C) 2007 Hans J. Koch <hjk@hansjkoch.de>
(C) 2007 jetzbroadband (http://jetzbroadband.com)
(C) 2007 Kees Lemmens (adapted for kernel 2.6.20)
(C) 2007 Laszlo Attila Toth <panther@balabit.hu> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/kernel.h>#include
<linux/module.h>#include <linux/skbuff.h>#include <linux/netdevice
(C) 2007 Linux Networx (http://lnxi.com)
(C) 2007 Martin K. Petersen <mkp@mkp.net>
(C) 2007 Michel Benoit * * Based on rtc-at91rm9200.c by Rick Bronson
(C) 2007 Mike Galbraith <efault@gmx.de> * * Various enhancements by Dmitry Adamushko.
(C) 2007 Montavista Software, Inc. * Vitaly Bordug <vib@kernel.crashing.org> * * Converted to of_platform_device. Renamed to i2c-cpm.c.

```

```

(C) 2007 Patrick McHardy <kaber@trash.net> */#include <linux/module.h>#include <linux/skbuff.h>#include <linux/gen_stats.h>
(C) 2007 Patrick McHardy <kaber@trash.net> */#include <linux/module.h>#include <linux/skbuff.h>#include <linux/gen_stats.h>#include
<linux/jhash.h>#include <linux/rtnetlink.h>#include <linux/ra
(C) 2007 Pavel Emelyanov <xemul@openvz.org>, OpenVZ, SWsoft Inc.
(C) 2007 Red Hat Inc * * Based upon * linux/drivers/ide/pci/cmd640.c Version 1.02 Sep 01, 1996
(C) 2007 Red Hat, Inc.\n");
(C) 2007 Red Hat Inc * * Note: The controller like many controllers has shared timings for * PIO and DMA. We thus flip to the DMA timings in dma_start
and flip back * in the dma_stop function. Thu
(C) 2007 Rodolfo Giometti\n");
(C) 2007 sgi, Christoph Lameter
(C) 2007 SGI, Christoph Lameter
(C) 2007 SGI, Christoph Lameter */#include <linux/kobject.h>
(C) 2007 sgi. Christoph Lameter. * * Virtual memory maps allow VM primitives pfn_to_page, page_to_pfn, * virt_to_page, page_address() to be
implemented as a base offset * calculation without memor
(C) 2007 SoftwareBitMaker
(C) 2007 Sukadev Bhattiprolu <sukadev@us.ibm.com>, IBM * Many thanks to Oleg Nesterov for comments and help *
(C) 2007 United Security Providers
(C) 2007 Venkatesh Pallipadi <venkatesh.pallipadi@intel.com> * Shaohua Li <shaohua.li@intel.com> * Adam Belay <abelay@novell.com>
* * This code is licenced under the GPL.
(C) 2007 www.douglaskthompson.com
(C) 2007 www.softwarebitmaker.com
(C) 2008, 2009, 2010, 2011 by Marc Kleine-Budde <kernel@pengutronix.de> */
(C) 2008, 2009 by Marc Kleine-Budde <kernel@pengutronix.de> * * Bosch C_CAN controller is compliant to CAN protocol version 2.0 part A and B. *
Bosch C_CAN user manual can be obtained from:
(C) 2008-2009 Pablo Neira Ayuso <pablo@netfilter.org> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/skbuff.h>#include <linux/jhash.h>#include <linux/ip.h>
(C) 2008-2013 Christoph Lameter <cl@linux.com> * * Optimized manipulation for memory allocated through the per cpu * allocator or for addresses of
per cpu variables.
(C) 2008 Anatolij Gustschin <agust@denx.de> * DENX Software Engineering */
(C) 2008 Korolev Alexey <akorolev@infradead.org>
(C) 2008 Krzysztof Piotr Oledzki <ole@ans.pl> */
(C) 2008 Manuel Traut <manut@linutronix.de> *
(C) 2008 Vasilij Leonenko <vasilij.leonenko@gmail.com> */#include <linux/module.h>#include <linux/types.h>#include <linux/kernel.h>#include
<linux/init.h>#include <linux/errno.h>#include <linux
(C) 2008 Vasilij Leonenko <vasilij.leonenko@gmail.com> * Many thanks to Roman Borisov for initial enabling * * TODO:
(C) 2009-2010 Bartlomiej Zolnierkiewicz
(C) 2009-2010 Bartlomiej Zolnierkiewicz * * Based on
(C) 2009-2010 Bartlomiej Zolnierkiewicz * * Some parts based on ata_piix.c by Jeff Garzik and others.
(C) 2009 Alexander Clouter <alex@digriz.org.uk> */
(C) 2009 Alexander Clouter <alex@digriz.org.uk>
(C) 2009 Bartlomiej Zolnierkiewicz * * Documentation:
(C) 2009 Daniel Mack <daniel@caiaq.de> ). *
(C) 2009 Google Inc. John(Jung-Ik) Lee <jilee@google.com> * * Per Atp867 data sheet rev 1.2, Acard. * Based in part on early ide code from * 2003-
2004 by Eric Uhrhane, Google, Inc.
(C) 2009 - Peter Feuerer peter (a) piie.net * http://piie.net * 2009 Borislav Petkov bp (a) alien8.de * * Inspired by and
many thanks to:
(C) 2009 Samsung Electronics * Author: Michal Nazarewicz (mina86@mina86.com) */
(C) 2009 Sascha Hauer, Pengutronix
(C) 2009 Siemens AG */#include <linux/fb.h>#include <linux/delay.h>#include <linux/init.h>#include <linux/interrupt.h>#include
<linux/module.h>#include <linux/pci.h>#include <linux/slab.h>#if
(C) 2009 Uwe Kleine-Koenig, Pengutronix */
(C) 2009 Valentin Sidikov <v.sidikov@gmail.com>
(C) 2010-2011 Red Hat, Inc.
(C) 2010-2011 Red Hat, Inc.*/
(C) 2010-2011 Thomas Graf <tgraf@redhat.com>
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. */
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. * * Based on Len Brown's <lenb@kernel.org> turbostat tool.
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. * * Based on SandyBridge monitor. Implements the new package C-states * (PC8,
PC9, PC10) coming with a specific Haswell (family 0
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. * * Based on the idea from Michael Matz <matz@suse.de>
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. * * Ideas taken over from the perf userspace tool (included in the Linus * kernel

```

git repo): subcommand builtins and param parsin

```
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. * * Miscellaneous helpers which do not fit or are worth * to put into separate headers
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. * * Output format inspired by Len Brown's <lenb@kernel.org> turbostat tool.
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. * * PCI initialization based on example code from:
(C) 2010,2011 Thomas Renninger <trenn@suse.de>, Novell Inc. * * ToDo: Needs to be done more properly for AMD/Intel specifics
(C) 2010 Bartlomiej Zolnierkiewicz * * based upon
(C) 2010 Eric Dumazet */
(C) 2010 Luotao Fu <l.fu@pengutronix.de> * All rights reserved
(C) 2010 Michael J. Cree * * Somewhat based on the Sparc code, and to a lesser extent the PowerPC and
(C) 2010 Pablo Neira Ayuso <pablo@netfilter.org> */
(C) 2010 Red Hat, Inc. * * Author: Michael S. Tsirkin <mst@redhat.com>
(C) 2010 Red Hat Inc * Author: Michael S. Tsirkin <mst@redhat.com> * * This software is distributed under GNU GPL v2, 1991
(C) 2010 Thomas Renninger <trenn@suse.de> */
(C) 2011 Bartlomiej Zolnierkiewicz * * Based in part on linux/drivers/ide/pci/sl82c105.c * SL82C105/Winbond 553 IDE driver
(C) 2011 Bartlomiej Zolnierkiewicz * * Documentation:
(C) 2011 DENX Software Engineering, Anatolij Gustschin <agust@denx.de> */
(C) 2011 DENX Software Engineering, Anatolij Gustschin <agust@denx.de> * * FIXME: this driver is used on a device-tree probed platform: it * should be defined as a bit-banged SPI device and probed
(C) 2011 Hans de Goede <hdegoede@redhat.com>
(C) 2011 Hans de Goede <hdegoede@redhat.com> */
(C) 2011 Intra2net AG <http://www.intra2net.com> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt
(C) 2011 Intra2net AG <http://www.intra2net.com> */#include <linux/init.h>#include <linux/module.h>#include <linux/kernel.h>#include <linux/skbuff.h>#include <linux/atomic.h>#include <linux/ref
(C) 2011 Linux Foundation, Christoph Lameter */
(C) 2011 Linux Foundation, Christoph Lameter * * Compile with: *
(C) 2011 Omnibond Systems * * Changes by Acxiom Corporation to implement generic service_operation()
(C) 2011 Pablo Neira Ayuso <pablo@netfilter.org>
(C) 2011 Patrick McHardy <kaber@trash.net> */
(C) 2011 Patrick McHardy <kaber@trash.net> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/in.h>#include <linux/ip.h>#include <net/ip.h>#include <linux/module.h>#include <linux/skb
(C) 2011 The Chromium OS Authors */
(C) 2011 The Chromium OS Authors * * In the file "/sys/module/dm_verity/parameters/prefetch_cluster" you can set * default prefetch value. Data are read in "prefetch_cluster" chunks from the * has
(C) 2011 Thomas Renninger <trenn@novell.com> Novell Inc. */
(C) 2011 Thomas Renninger <trenn@suse.de>, Novell Inc. */
(C) 2012-2013 by Pablo Neira Ayuso <pablo@netfilter.org> * * This software has been sponsored by Sophos Astaro <http://www.sophos.com>
(C) 2012-2014 by sysmocom - s.f.m.c. GmbH
(C) 2012 by Hans Schillstrom <hans.schillstrom@ericsson.com>
(C) 2012 by Lemonage GmbH * Author: Lars Poeschel <poeschel@lemonage.de> * All rights reserved
(C) 2012 by Pablo Neira Ayuso <pablo@netfilter.org>
(C) 2012 by Pablo Neira Ayuso <pablo@netfilter.org> */
(C) 2012 by Vyatta Inc. <http://www.vyatta.com> */
(C) 2012 by Vyatta Inc. <http://www.vyatta.com> */#include <linux/init.h>#include <linux/module.h>#include <linux/kernel.h>#include <linux/rculist.h>#include <linux/rculist_nulls.h>#include <li
(C) 2012 Christoph Lameter <cl@linux.com> */#include <linux/slab.h>
(C) 2012 Google, Inc. * Author: Kent Overstreet <koverstreet@google.com> * * This implements a refcount with similar semantics to atomic_t - atomic_inc(), * atomic_dec_and_test() - but percpu.
(C) 2012 Michel Lespinasse <>walken@google.com>
(C) 2012 MontaVista Software, LLC <source@mvista.com> * * Based upon * linux/drivers/ide/pci/cmd64x.c Version 1.30 Sept 10, 2002
(C) 2012 Pablo Neira Ayuso <pablo@netfilter.org> * * This software has been sponsored by Vyatta Inc. <http://www.vyatta.com>
(C) 2012 Sasha Levin <levinsasha928@gmail.com> */
(C) 2012 Wolfram Sang, Pengutronix */
(C) 2013 Astaro GmbH */
(C) 2013 Daniel Borkmann <dborkman@redhat.com> */
(C) 2013 James Chapman <jchapman@katalix.com> */
(C) 2013 Viresh Kumar <viresh.kumar@linaro.org> * * Oct 2005 - Ashok Raj <ashok.raj@intel.com> * Added handling for CPU hotplug * Feb 2006 - Jacob Shin <jacob.shin@amd.com> * Fix handling for C
(C) 2014-2015 Pratik Patel <pratikp@codeaurora.org> * * Serious refactoring, code cleanup and upgrading to the Coresight upstream * framework by Mathieu Poirier
(C) 2014 by Pablo Neira Ayuso <pablo@netfilter.org> */
```

```

(C) 2014 by Pablo Neira Ayuso <pablo@netfilter.org> * * Based on code from ebt_log from: *
(C) 2014 Sittisak <sittisaks@hotmail.com> */
(C) 2015-2016 Chunyan Zhang <zhang.chunyan@linaro.org> */#include <asm/local.h>#include <linux/acpi.h>#include <linux/amba/bus.h>#include
<linux/bitmap.h>#include <linux/clk.h>#include <linux/c
(C) 2015-2016 Mathieu Poirier <mathieu.poirier@linaro.org> * * Guaranteed timing and support for various packet type coming from the * generic STM
API by Chunyan Zhang
(C) 2015-2018 Kevin Darbyshire-Bryant <kevin@darbyshire-bryant.me.uk>
(C) 2015 Borislav Petkov <bp@alien8.de> * * This driver allows to upgrade microcode on x86 processors.
(C) 2015 by Il Han <corone.il.han@gmail.com> */
(C) 2015 Pengutronix, Alexander Aring <aar@pengutronix.de>
(C) 2015 Pengutronix, Alexander Aring <aar@pengutronix.de> */
(C) 2015 Pengutronix, Alexander Aring <aar@pengutronix.de> * * Authors:
(C) 2015 Red Hat GmbH * Author: Florian Westphal <fw@strlen.de> */
(C) 2016 Arnaldo Carvalho de Melo <acme@redhat.com> */
(C) 2016 by Pablo Neira Ayuso <pablo@netfilter.org> */
(C) 2016 by Pablo Neira Ayuso <pablo@netfilter.org> * * Author: Harald Welte <hwelte@sismocom.de> * Pablo Neira Ayuso
<pablo@netfilter.org> * Andreas Schultz <aschultz@travelping.com>
(C) 2016 Pengutronix, Alexander Aring <aar@pengutronix.de> */
(C) 2017-2018 Synopsys, Inc. (www.synopsys.com)
(C) 2017 Anju T Sudhakar, IBM Corporation.
(C) 2017 Helge Deller <deller@gmx.de> */
(C) 2017 Hemant K Shaw, IBM Corporation. */
(C) 2017 Hemant K Shaw, IBM Corporation. */#include <linux/kernel.h>#include <linux/platform_device.h>#include <linux/of.h>#include
<linux/of_address.h>#include <linux/of_platform.h>#include <
(C) 2017 Hemant K Shaw, IBM Corporation. */#include <linux/perf_event.h>#include <linux/slab.h>#include <asm/opal.h>#include <asm/imc-
pmu.h>#include <asm/cputhreads.h>#include <asm/smp.h>#incl
(C) 2017 Linus Walleij <linus.walleij@linaro.org> */#ifndef _GEMINI_ETHERNET_H#define _GEMINI_ETHERNET_H
(C) 2017 Red Hat GmbH * Author: Florian Westphal <fw@strlen.de> * * split from xt_connlimit.c:
(C) 2018 Mojatau, Alexander Aring <aring@mojatau.com>
(C) 2018 Uladzislau Rezki (Sony) <urezki@gmail.com> */#include <linux/init.h>#include <linux/kernel.h>#include <linux/module.h>#include
<linux/vmalloc.h>#include <linux/random.h>#include <linux
(C) * 2 - 1) : ((C)
(C) Adam Belay. */#include <linux/module.h>#include <linux/pci.h>#include <linux/pnp.h>#include <linux/string.h>#include
<linux/kernel.h>#include <linux/serial_core.h>#include <linux/bitops.h>
(C) Alex Holden " "alex@linuxhacker.org> 1998.\n", VERSION);
(C) and server (S): * * Setup of client connection:
(C) Architecture Software * Developer's Manual, Volume 3: System Prgramming Guide, * revision #12 in Table B-1: MSRs in the Pentium 4 and *
Intel Xeon Processors, on page B-4 and B-5. */ fsb_
(C) * A(W1) * C(C) * * Which would create W1->C->W1 dependencies, even though there is no * actual deadlock
possible. There are two solutions, using a * read-recursive acquire on the wor
(C) Balbir Singh, IBM Corp. 2006
(C) Balbir Singh, IBM Corp. 2006 */
(C) Benjamin Reed,
(C) bits in a HPT * entry can be set by hardware at any time up until * the HPTE is invalidated and the TLB invalidation * sequence
has completed. This means that when * removing a HPTE,
(C) __BUG_ON((unsigned long)(C))
(C) by the respective authors, and licensed under the GPL
(C) by Vojtech Pavlik */
(C) (((C) << 11)
(C) Carnegie Mellon University
(C) CC Computer Consultants GmbH, 2007 */
(C) CC Computer Consultants GmbH, 2007 * * based on ...
(C) CC Computer Consultants GmbH, 2007 */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/in.h>#include <linux/in6.h>#include
<linux/ip.h>#include <linux/ipv6.h>#include <linux/jhash.
(C) CC Computer Consultants GmbH, 2008 */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/skbuff.h>#include <linux/ip.h>#include <linux/ipv6.h>#include <linux
(C)(C) Leaguerme (Shenzhen) MicroElectronics Co., Ltd. * * see Documentation/media/dvb-drivers/dvb-usb.rst for more information
(C) Copyright 1991-2000 Linus Torvalds * * We have a per-user structure to keep track of how many * processes, files etc the user has claimed, in order
to be * able to have per-user limits for sys
(c)Copyright 1992 Hewlett-Packard Co. *
(C) Copyright 1994 - 1997 Marco van Wieringen */

```

(C) Copyright 1994 Linus Torvalds  
(C) Copyright 1994 Linus Torvalds \*/  
(C) Copyright 1995 1996, 2014 Linus Torvalds \*  
(C) Copyright 1995 1996 Linus Torvalds  
(C) Copyright 1995 1996 Linus Torvalds \*/  
(C) Copyright 1995 1996 Linus Torvalds \* Hacked for ARM by Phil Blundell <philb@gnu.org> \* Hacked to allow all architectures to build, and various  
cleanups \* by Russell King  
(C) Copyright 1995 1996 Linus Torvalds \* \* Hacked for ARM by Phil Blundell <philb@gnu.org> \* Hacked to allow all architectures to build, and various  
cleanups \* by Russell King  
(C) Copyright 1995 1996 Linus Torvalds \*/#include <linux/vmalloc.h>#include <linux/mm.h>#include <linux/sched.h>#include <linux/io.h>#include  
<linux/export.h>#include <asm/cacheflush.h>#include  
(C) Copyright 1995 - 1997 Marco van Wieringen - ELM Consultancy B.V. \* \* Plugged two leaks. 1) It didn't return acct\_file into the free\_filps if \* the file  
happened to be read-only. 2) If the acc  
(C) Copyright 1995-1999 Tekram Technology Co., Ltd. \* \* Kurt Garloff <garloff@suse.de>  
(C) Copyright 1995-2003 by Geert Uytterhoeven <geert@linux-m68k.org> \* \* Fixes and tips by:  
(c) Copyright 1995 Alan Cox <alan@lxorguk.ukuu.org.uk> \*/  
(c) Copyright 1995 Alan Cox <alan@lxorguk.ukuu.org.uk> \*  
(c) Copyright 1995 Alan Cox <alan@lxorguk.ukuu.org.uk> \* \* 14-Dec-2001 Matt Domsch <Matt\_Domsch@dell.com> \* Added nowayout  
module option to override CONFIG\_WATCHDOG\_NOWAYOUT  
(c) Copyright 1995 Alan Cox <alan@lxorguk.ukuu.org.uk> \* \* 14-Dec-2001 Matt Domsch <Matt\_Domsch@dell.com> \* Added nowayout  
module option to override CONFIG\_WATCHDOG\_NOWAYOUT \* Added t  
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 <linux/io.h>#include <asm/pci-bridge.h>  
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 (C) Copyright 2004 Linus Torvalds \*/#include <linux/pci.h>#include <linux/mm.h>#include <linux/export.h>#include <asm/io.h>#include <asm/pci-

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bridge.h>#include <asm/isa-bridge.h>
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SOURCEFILE_NAME "hpi6205.c"#include "hpi_internal.h"#include "hpi_version.
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<linux/mount.h>#include <linux/fs.h>#include <linux/nsproxy.h>#include <u  
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Management Controller) via interrupts. \* Unlike IPMI i  
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(C) Copyright TOSHIBA CORPORATION 2000-2001, 2004-2007 */#include <linux/kernel.h>#include <linux/interrupt.h>#include <linux/irq.h>#include
<asm/tx9/pci.h>#include <asm/tx9/tx4927pcic.h>
(C) Copyright TOSHIBA CORPORATION 2004-2005 * All Rights Reserved
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(C) Copyright TOSHIBA CORPORATION 2004-2006 * * This is a very generic AC97 sound machine driver for boards which * have (AC97) audio at ACLC
(e.g. RBTX49XX boards).
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(C) Copyright TOSHIBA CORPORATION 2005-2007 */#include <linux/rtc.h>#include <linux/platform_device.h>#include <linux/interrupt.h>#include
<linux/module.h>#include <linux/io.h>#include <linux/g
(C) Copyright TOSHIBA CORPORATION 2007 */#ifndef __TXX9_NDFMC_H#define __TXX9_NDFMC_H
(C) Copyright Yggdrasil Computing, Inc. 2000 * (usb_device_id matching changes by Adam J. Richter)
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(C) David Brownell */
(C) David Hinds.
(C) David Hinds and the Linux wireless
(C) David S. Miller. */
(C) DENX Software Engineering. * * Author: Yuri Tikhonov <yur@emcraft.com>
(C) (diagram), 9.2 * Upon reception of the SHUTDOWN COMPLETE chunk the endpoint will verify * that it is in SHUTDOWN-ACK-SENT state, if it is not
the chunk should be * discarded. If the endpoint is
(C) " DRIVER_AUTHOR "\n";
(C) DVB-S USB2.0");
(C) IBM Corporation, 2011. * Author: Suzuki Poulouse <suzuki@in.ibm.com> * * - Based on ppc64 code - reloc_64.S
(C) in bits ( MOVQ_R64_XMM %r12, %xmm1
(C) in bits ( vmovq %r12, %xmm1 vpslldq $8, %xmm15, %xmm15 # xmm15 = len(A)|| 0x0000000000000000
(C) INESC 1998 */
(C) INESC 1998 *
(C) INESC 1999 */
(C) * in woken_wake_function() such that either we see the wait condition * being true or the store to wq_entry->flags in woken_wake_function()
* follows ours in the coherence order. */ smp_s
(C) IP_VS_DBG_ADDR((C)->af, ntohs((C)->cport), \ IP_VS_DBG_ADDR((C)->af, &((C)->vaddr)), \
ntohs((C)->vport), \ IP_VS_DBG_ADDR((C)->daf, &((C)->daddr)), \ ntohs((C)->dport), \
(C) Jay Lan, SGI, 2006
(C) Jean Tourrilhes.
(C) Jens Axboe <jens.axboe@oracle.com> 2008 */
(C) Jiri Kosina\n");out:
(C) Len Brown <len.brown@intel.com>\n");}
(C) Linus Torvalds * * 'traps.c' handles hardware exceptions after we have saved some state in * 'linux/arch/arm/lib/traps.S'. Mostly a debugging aid,

```

```

but will probably * kill the offending pr
(C) Linux Foundation 2008-2013 * Unified interface for all slab allocators */
(C) Nadia Yvette Chambers, April 2004 */#include <linux/list.h>#include <linux/init.h>#include <linux/mm.h>#include <linux/seq_file.h>#include
<linux/syset.h>#include <linux/highmem.h>#include
(C) Neil Horman <nhorman@tuxdriver.com> */
(C) * Notes: To disconnect the current associated BSS * Command Mode */struct disconnect_parm { u32 rsvd;
(C) * Notes: To enter USB suspend mode * Command Mode */struct usb_suspend_parm { u32 action; /* 1: sleep, 0:resume */
copyright ;-) */
COPYRIGHT (0 << 2)
COPYRIGHT) ? 0 : AC97_CXR_COPYRGT; v |= new & IEC958_AES0_NONAUDIO ? AC97_CXR_SPDIF_AC3 : AC97_CXR_SPDIF_PCM;
change |= snd_ac97_update_bits_nolock(ac97, AC97_CXR_AUDIO_MISC, AC97_CXR
copyright (0 = copy inhibited) * D1: 0 = digital audio / 1 = non-digital audio * * C6 upper channel status (digital serial data description)
(CS4237B,CS4238B) * D7-D6: sample freque
COPYRIGHT 0#define DVD_CPM_COPYRIGHTED 1
COPYRIGHT (0)#define EEPROM_COPYRIGHT_LEN (44)#define EEPROM_CHECKSUM (62)#define EEPROM_CHECKSUM_REV (63)#define
EEPROM_MAC (64)#define EEPROM_MAC_REV (70)#define EEPROM_SERIAL (112)#define
COPYRIGHT 0x00000004 /
COPYRIGHT 0x01#define DVD_STRUCT_DISCKEY 0x02#define DVD_STRUCT_BCA 0x03#define DVD_STRUCT_MANUFACT 0x04
COPYRIGHT = 0x10, AUDIO_STATUS_NONAUDIO = 0x20, AUDIO_STATUS_PROFESSIONAL = 0x40, AUDIO_STATUS_LEVEL =
0x80};
COPYRIGHT (0x1 << 2)
COPYRIGHT 0xd4#define CX2341X_ENC_SET_EVENT_NOTIFICATION 0xd5#define CX2341X_ENC_SET_NUM_VSYNC_LINES
0xd6#define CX2341X_ENC_SET_PLACEHOLDER 0xd7#define CX2341X_ENC_MUTE_VIDEO 0xd9#define CX
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Copyright 10/16/2005 Tilman Kranz <tilde@tk-sls.de> * Creative Audio MIDI, for the CA0106 Driver * Version: 0.0.1 * * Changelog:
COPYRIGHT (1 << 2)#define HDMI_I2S_LINEAR_PCM (0 << 1)#define HDMI_I2S_NO_LINEAR_PCM (1 << 1)#define
HDMI_I2S_CONSUMER_FORMAT (0)#define HDMI_I2S_PROF_FORMAT (1)#define HDMI_I2S_CH_ST_0_CLR
COPYRIGHT (1<<2) /*#define IEC958_AES0_CON_EMPHASIS (7<<3) /* mask - emphasis */#define IEC958_AES0_CON_EMPHASIS_NONE
(0<<3) /* none emphasis */#define IEC958_AES0_CON_EMPHASIS_5015 (1<<3) /* 50/15u
COPYRIGHT (1<<4)#define AC_DIG1_NONAUDIO (1<<5)#define AC_DIG1_PROFESSIONAL (1<<6)#define AC_DIG1_LEVEL
(1<<7)
COPYRIGHT (1 << 5)#define m_AUDIO_STATUS_ADDITION (3 << 2)#define m_AUDIO_STATUS_CLK_ACCURACY (2 << 0)#define
v_AUDIO_STATUS_NLPCM(n) ((n & 1) << 7)#define AUDIO_N_H 0x3f#define AUDIO_N_M 0
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*/
copyright 1989 Regents of the University of California.\n");#endif#endif CONFIG_SLIP_SMART printk(KERN_INFO "SLIP linefill/keepalive
option.\n");#endif
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gcc's inlining heuristics.
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return double(temp)\*0.582-58.16; \* else \* return double(temp)\*  
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 <linux/errno.h>#include <linux/reboot.h>#include <linux/slab.h>#include  
 Copyright 2007 David Gibson <dwg@au1.ibm.com>, IBM Corporation. \*/#include <linux/kernel.h>#include <linux/of.h>#include <linux/init.h>#include  
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<linux/module.h>#include <linux/mm.h>#include <linux/types.h>#include
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<linux/device.h>#include <linux/slab.h>#include <l
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<asm/reg.h>#include <asm/cputable.h>
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<linux/platform\_device.h>#include <linux/bitops.h>  
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<asm/reg.h>#include <asm/cputable.h>  
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<linux/percpu.h>#include <linux/uaccess.h>#include <li  
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\_ASM\_GENERIC\_ATOMIC64\_H#include <linux/types.h>  
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<linux/spinlock.h>#include <linux/init.h>#include <linux/export.h>#inc  
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mapping.h>#include <linux/input.h>#include <linux/io.h>#inc
Copyright 2010-2011 Calxeda, Inc. */#include <linux/module.h>#include <linux/mod_devicetable.h>#include <linux/kernel.h>#include
<linux/circ_buf.h>#include <linux/interrupt.h>#include <linux/et
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"efx.h"#include "nic.h"#include "io.h"#include "mcdi.h"#inc

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<linux/kernel.h>#include <linux/mm.h>#include <li
  Copyright 2010 Freescale Semiconductor, Inc. */#include <linux/kernel.h>#include <linux/sched.h>#include <linux/perf_event.h>#include
<linux/percpu.h>#include <linux/hardirq.h>#include <asm/reg
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<linux/virtio.h>#include <linux/virtio\_config.h>#include <linux/virtio  
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Copyright 2011 Calxeda, Inc. \*/#include <linux/io.h>#include <asm/proc-fns.h>#include <linux/reboot.h>  
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"string.h"#include "stdio.h"#include "page.h"#include "ops.h  
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Copyright 2011 IBM Corporation. */#include <linux/types.h>#include <linux/kernel.h>#include <linux/irq.h>#include <linux/smp.h>#include
<linux/interrupt.h>#include <linux/cpu.h>#include <linux
Copyright 2011 IBM Corporation. */#include <linux/types.h>#include <linux/threads.h>#include <linux/kernel.h>#include <linux/irq.h>#include
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 <linux/delay.h>#include <linux/errno.h>#include <linux/fb  
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<linux/regmap.h>#include <linux/slab.h>#include <linux/cdev.h>#inc  
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<linux/slab.h>#include <linux/sysfs.h>#include <linux/  
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<linux/export.h>#include <linux/kernel.h>#include <linux/kref.h>#in  
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<linux/debugfs.h>#include <linux/list.h>#include <lin
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<linux/usb/hcd.h>#include <linux/greybus.h>  
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 Copyright 2015 Carnegie Mellon University \* \* Expose entries from QEMU's firmware configuration (fw\_cfg) device in \* sysfs (read-only, under  
 "/sys/firmware/qemu\_fw\_cfg/...").  
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 checking its FPU registers, at some \* p  
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 The check\_fpu function in asm only checks \* t  
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 checking its VMX registers, at some \* p

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The check\_vmx function in asm only checks \* t  
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preemption happened so this test just \* uses many thr  
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Copyright 2015 IBM Corporation \* Author: Mahesh Salgaonkar <mahesh@linux.vnet.ibm.com> \*/  
Copyright 2015 IBM Corporation. \*/#include <asm/ppc\_asm.h>#include <asm/export.h>#include <asm/ppc-opcode.h>  
Copyright 2015 \*/#include <linux/delay.h>#include <linux/io.h>#include <linux/leds.h>#include <linux/module.h>#include <linux/of.h>#include  
<linux/platform\_device.h>#include <linux/spinlock.h>  
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Copyright © 2015 Intel Corporation. \* \* Authors: David Woodhouse <dwmw2@infradead.org>  
Copyright 2015 Intel Deutschland GmbH \*/#include <net/mac80211.h>#include "ieee80211\_i.h"#include "trace.h"#include "driver-ops.h"  
Copyright 2015 Joachim Eastwood <manabian@gmail.com> \*/  
Copyright 2015 Joachim Eastwood <manabian@gmail.com> \* \* UNSUPPORTED hardware features:  
Copyright 2015 Jonas Gorski <jogo@openwrt.org> \*/#include <linux/io.h>#include <linux/leds.h>#include <linux/module.h>#include  
<linux/of.h>#include <linux/platform\_device.h>#include <linux/spin  
Copyright 2015 Karol Herbst  
Copyright 2015 Karol Herbst <nouveau@karolherbst.de>  
Copyright © 2015 Keith Packard <keithp@keithp.com> \*/  
Copyright 2015, Laurent Dufour, IBM Corp. \* \* Test the kernel's signal returning code to check reclaim is done if the \* sigreturn() is called while in a  
transaction (suspended since active is \* al  
Copyright 2015 LG Electronics Inc.  
Copyright 2015 Linaro. \*/#include <linux/sched.h>#include <linux/device.h>#include <linux/dmaengine.h>#include <linux/dma-mapping.h>#include  
<linux/dmapool.h>#include <linux/init.h>#include <l  
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Copyright 2015 Linaro Ltd.  
Copyright 2015 Linaro Ltd. \*/  
Copyright 2015 Linaro Ltd. \*/#include <errno.h>#include <fcntl.h>#include <stdio.h>#include <string.h>#include <stdlib.h>#include  
<stdint.h>#include <poll.h>#include <sys/types.h>#include <t  
Copyright 2015 Linaro Ltd. \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/slab.h>#include <linux/sizes.h>#include  
<linux/cdev.h>#include <linux/fs.h>#include <linux/idr.h>  
Copyright 2015 Linaro Ltd. \*/#include <linux/kernel.h>#include <linux/module.h>#include <sound/soc.h>#include <sound/pcm\_params.h>  
Copyright 2015 Linaro Ltd. \*/#undef TRACE\_SYSTEM#define TRACE\_SYSTEM greybus

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Copyright 2015 Mentor Graphics Corporation. \*/  
Copyright 2015 Mentor Graphics Corporation. \* \* vdsomunge - Host program which produces a shared object \* architecturally specified to be usable by  
both soft- and hard-float \* programs.  
Copyright 2015, Michael Ellerman, IBM Corp. \*/  
Copyright 2015, Michael Ellerman, IBM Corp. \* \* This test simply tests that certain syscalls are implemented. It doesn't \* actually exercise their logic in  
any way.  
Copyright 2015, Michael Neuling, IBM Corp. \* \* Edited: Rashmica Gupta, Nov 2015  
Copyright 2015, Michael Neuling, IBM Corp. \* Original: Michael Neuling 19/7/2013 \* Edited: Rashmica Gupta 01/12/2015 \* \* Do some transactions, see  
if the tar is corrupted. \* If the transaction is  
Copyright 2015, Michael Neuling, IBM Corp. \* \* Original: Michael Neuling 3/4/2014 \* Modified: Rashmica Gupta 8/12/2015  
Copyright 2015, Michael Neuling, IBM Corp. \* \* Original: Michael Neuling 4/12/2013 \* Edited: Rashmica Gupta 4/12/2015  
Copyright 2015, Michael Neuling, IBM Corp. \* \* Test the kernel's signal delievery code to ensure that we don't \* trelaim twice in the kernel signal delivery  
code. This can happen \* if we trigger  
Copyright 2015, Michael Neuling, IBM Corp. \* \* Test the kernel's signal return code to ensure that it doesn't \* crash when both the transactional and  
suspend MSR bits are set in \* the signal conte  
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Copyright 2015 Regents of the University of California  
Copyright 2015 Regents of the University of California, Berkeley \* \* See asm-generic/syscall.h for descriptions of what we must do here.  
Copyright 2015 Robert Jarzmik <robert.jarzmik@free.fr> \*/  
Copyright 2015, Sam Bobroff, IBM Corp. \* \* Test the kernel's system call code to ensure that a system call \* made from within an active HTM transaction  
is aborted with the \* correct failure code.  
Copyright 2015 Samuel Pitoiset  
Copyright 2015 Samuel Pitosiet  
Copyright 2015 Simon Arlott \* \* Derived from bcm63138\_nand.c:  
Copyright 2015 Simon Arlott \* \* This is based on the BCM7038 (which supports SMP) but with a single \* enable register instead of separate  
mask/set/clear registers.  
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Copyright 2016-17 IBM Corp. \*/#include <asm/ppc-opcode.h>#include <asm/reg.h>  
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Copyright 2016,2017 IBM Corporation. \*/  
Copyright 2016,2017 IBM Corporation. \*/#ifndef \_ASM\_POWERPC\_XIVE\_H#define \_ASM\_POWERPC\_XIVE\_H  
Copyright 2016,2017 IBM Corporation. \*/#ifndef \_ASM\_POWERPC\_XIVE\_REGS\_H#define \_ASM\_POWERPC\_XIVE\_REGS\_H  
Copyright 2016,2017 IBM Corporation. \*/#ifndef \_\_XIVE\_INTERNAL\_H#define \_\_XIVE\_INTERNAL\_H  
Copyright 2016-2017 Intel Deutschland GmbH  
Copyright 2016-2017 NXP \*/  
Copyright 2016-2017 NXP \*/#include <linux/init.h>#include <linux/module.h>#include <linux/platform\_device.h>#include  
<linux/etherdevice.h>#include <linux/of\_net.h>#include <linux/interrupt.h>#  
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Copyright 2016 Alexey Kardashevskiy, IBM Corporation <aik@au1.ibm.com> \*/  
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Copyright © 2016 Andrew Lunn <andrew@lunn.ch> \*/#include <linux/device.h>#include <linux/module.h>#include <linux/mtd/mtd.h>#include  
<linux/mtd/partitions.h>#include <linux/mutex.h>#include <li  
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Copyright 2016 Broadcom \*/  
Copyright 2016 Broadcom \*/#ifndef DRIVERS\_PCI\_ECAM\_H#define DRIVERS\_PCI\_ECAM\_H  
Copyright 2016 Broadcom \*/#include <linux/clk.h>#include <linux/platform\_device.h>#include <linux/device.h>#include <linux/of\_mdio.h>#include  
<linux/module.h>#include <linux/phy.h>#include <li  
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Copyright 2016 Chen-Yu Tsai <wens@csie.org> \* \* Based on the Allwinner SDK driver, released under the GPL.  
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Copyright © 2016 Collabora Ltd
Copyright 2016 Crestez Dan Leonard <leonard.crestez@intel.com> * * SI1132 (7-bit I2C slave address 0x60) * SI1141/2/3 (7-bit I2C slave address
0x5a) * SI1145/6/6 (7-bit I2C slave address 0x60)
Copyright 2016, Cyril Bur, IBM Corp. */
Copyright 2016, Cyril Bur, IBM Corp. * * Sending one self a signal should always get delivered.
Copyright 2016, Cyril Bur, IBM Corp. * * Syscalls can be performed provided the transactions are suspended. * The exec() class of syscall is unique as a
new process is loaded.
Copyright 2016, Cyril Bur, IBM Corp. * * Test the kernel's signal frame code.
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Copyright 2016 Ezequiel Garcia <ezequiel@vanguardiasur.com.ar> * * Based on LED IDE-Disk Activity Trigger
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upstreaming attempt by:
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Copyright 2016 Freescale Semiconductor, Inc. * * Author: Zhao Qiang <B45475@freescale.com>
Copyright 2016 Freescale Semiconductor, Inc. * * There is no Shared Descriptor for PKC so that the Job Descriptor must carry * all the desired key
parameters, input and output pointers.
copyright 2016 google inc.
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Copyright 2016 Google Inc. */
Copyright 2016 Google Inc. */#include <linux/kernel.h>#include <linux/module.h>#include <linux/slab.h>#include <linux/sizes.h>#include
<linux/uaccess.h>#include <linux/greybus.h>
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Copyright 2016 HUAWEI TECHNOLOGIES CO., LTD. */
Copyright 2016 HUAWEI TECHNOLOGIES CO., LTD. */
Copyright © 2016 IBM Corporation
Copyright 2016 IBM Corporation. */#include <linux/types.h>#include <linux/kernel.h>#include <linux/irq.h>#include <linux/smp.h>#include
<linux/interrupt.h>#include <linux/cpu.h>#include <linux
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Boirie <gregor.boirie@parrot.com>
Copyright 2016 Karol Herbst
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Copyright 2016 Linaro Ltd. */
Copyright 2016 Linaro Ltd. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt
Copyright 2016 Linaro Ltd. */#include <linux/kernel.h>#include <linux/module.h>#include <linux/pm_runtime.h>#include <sound/soc.h>#include
<sound/pcm_params.h>#include <uapi/linux/input.h>
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Matt Evans
Copyright 2016 Naveen N. Rao <naveen.n.rao@linux.vnet.ibm.com> *           IBM Corporation */#ifndef _BPF_JIT64_H#define _BPF_JIT64_H
Copyright 2016 Naveen N. Rao <naveen.n.rao@linux.vnet.ibm.com> *           IBM Corporation */#include <linux/kprobes.h>#include
<linux/ptrace.h>#include <linux/hardirq.h>#include <linux/preempt.h>#inc
Copyright 2016 Netflix, Inc.
Copyright 2016 Netflix, Inc. */#include <stdio.h>#include <stdlib.h>#include <unistd.h>#include <errno.h>#include <signal.h>#include
<string.h>#include <assert.h>#include <linux/perf_event.h>
Copyright 2016 Noralf Tr
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Copyright 2016 Nuvoton Technology Corp. * Author: John Hsu <KCHSU0@nuvoton.com> */
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Copyright 2016 NXP */#ifndef _FSL_DPNI_CMD_H#define _FSL_DPNI_CMD_H
Copyright 2016 NXP */#include <linux/kernel.h>#include <linux/errno.h>#include <linux/fsl/mc.h>#include "dpni.h"#include "dpni-cmd.h"
Copyright 2016 NXP Semiconductor * * Derived from mpc85xx_edac.c * Author: Dave Jiang <djiang@mvista.com>
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Copyright 2016 Pengutronix, <kernel@pengutronix.de> */
Copyright 2016 Peter Meerwald-Stadler <pmeerw@pmeerw.net> * * IIO driver for VEML6070 (7-bit I2C slave addresses 0x38 and 0x39)
Copyright 2016 Qtechnology A/S * 2016 Ricardo Ribalda <ricardo.ribalda@gmail.com> */#ifndef __LINUX_PLATFORM_DATA_AD5761_H__#define
__LINUX_PLATFORM_DATA_AD5761_H__
Copyright 2016 Qtechnology A/S * 2016 Ricardo Ribalda <ricardo.ribalda@gmail.com> */#include <linux/kernel.h>#include <linux/module.h>#include
<linux/spi/spi.h>#include <linux/bitops.h>#include
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Copyright 2016, Rashmica Gupta, IBM Corp. *
Copyright 2016, Rashmica Gupta, IBM Corp. * * This traverses the kernel pagetables and dumps the * information about the used sections of memory to
* /sys/kernel/debug/kernel_pagetables.
Copyright 2016, Rashmica Gupta, IBM Corp. * * This traverses the kernel virtual memory and dumps the pages that are in * the hash pagetable, along
with their flags to * /sys/kernel/debug/kernel_ha
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<linux/moduleparam.h>#include <linux/init.h>#include <linux/delay.h>#include
Copyright 2016 Realtek Semiconductor Corp. * Author: Oder Chiou <oder_chiou@realtek.com> */
Copyright 2016 Red Hat Inc.
Copyright 2016 Red Hat, Inc. and/or its affiliates. */#include <linux/kvm_host.h>#include <linux/debugfs.h>#include "lapic.h"
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<jcsing.lee@samsung.com>
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Copyright 2017-2018 NXP \* Dong Aisheng <aisheng.dong@nxp.com> \* \* Implementation of the SCU based Power Domains  
Copyright 2017-2018 NXP \* \* Header file containing the public API for the System Controller (SC) \* Power Management (PM) function. This includes  
functions for power state \* control, clock control,  
Copyright 2017-2018 NXP \*/#ifndef \_DPSECI\_H\_#define \_DPSECI\_H\_  
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Copyright 2017 Advanced Micro Devices, Inc.  
Copyright 2017 Advanced Micro Devices, Inc. \* \* This file is modified from rt288 machine driver  
Copyright © 2017 Amazon.com, Inc. or its affiliates. \* \* Author: David Woodhouse <dwmw2@infradead.org>  
Copyright 2017 Analog Devices Inc. \* Author: Lars-Peter Clausen <lars@metafoo.de> \*/  
Copyright 2017 Analog Devices Inc. \*/#include <linux/bitfield.h>#include <linux/clk.h>#include <linux/delay.h>#include <linux/device.h>#include  
<linux/err.h>#include <linux/gpio/consumer.h>#in  
Copyright 2017, Anju T, IBM Corp. \*/  
Copyright 2017, Anshuman Khandual, IBM Corp. \* \* Works on architectures which support 128TB virtual \* address range and beyond.  
Copyright 2017 ARM Ltd. \*  
Copyright 2017 ATMEL  
Copyright 2017 \* \* Author: Dong Aisheng <aisheng.dong@nxp.com>  
Copyright 2017 \* Author: Dong Aisheng <aisheng.dong@nxp.com> \* \* File containing client-side RPC functions for the MISC service. These \* function  
are ported to clients that communicate to the SC.  
Copyright 2017 Benjamin Herrenschmidt, IBM Corporation. \*/  
Copyright 2017 Benjamin Herrenschmidt, IBM Corporation \*/  
Copyright 2017 Ben Whitten <ben.whitten@gmail.com>  
Copyright © 2017 Broadcom  
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Copyright 2017 Broadcom. All Rights Reserved  
Copyright 2017 by Helge Deller <deller@gmx.de> \* \* possible future enhancements:  
Copyright: 2017 Cadence Design Systems, Inc. \* \* Author: Boris Brezillon <boris.brezillon@bootlin.com>  
Copyright 2017 Cadence Design Systems Inc. \*/#include <linux/memblock.h>#include <linux/stddef.h>#include <linux/kernel.h>#include  
<linux/init.h>#include <linux/errno.h>#include <linux/reboot.h  
Copyright 2017 Canonical Ltd. \*/  
Copyright 2017 Canonical Ltd. \* \* TODO \* If a task uses change\_hat it currently does not return to the old \* cred or task context but instead creates a  
new one. Ideally the task \* should return  
Copyright 2017 Cirrus Logic \*/  
Copyright 2017 Cirrus Logic, Inc. \* \* Author: Brian Austin <brian.austin@cirrus.com>  
Copyright 2017 Cirrus Logic, Inc. \* \* Author: Li Xu <li.xu@cirrus.com>  
Copyright 2017 Cirrus Logic, Inc. \* \* Authors: Li Xu <li.xu@cirrus.com>  
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```

Copyright 2017 Collabora Ltd. */
Copyright 2017 (c) Samsung Electronics Software, Inc. * * Based on the Exynos PRNG driver drivers/crypto/exynos-rng by * Krzysztof Kozłowski
<krzk@kernel.org>
Copyright 2017 CS Systemes d *
Copyright 2017 Daniel Axtens, IBM Corporation. */
Copyright 2017, Daniel Axtens, IBM Corporation. * [based on crc32c-vpmsum_glue.c] */
Copyright 2017 David Lechner <david@lechnology.com> */
Copyright 2017 David Lechner <david@lechnology.com> * * Some code copied from mipi-dbi.c
Copyright 2017 * Dong Aisheng <aisheng.dong@nxp.com> */
Copyright 2017 Free Electrons * * Author: Boris Brezillon <boris.brezillon@free-electrons.com>
Copyright 2017 - Free Electrons * * Authors:
Copyright 2017 Google Inc.
Copyright 2017 Google Inc. */
Copyright 2017 Google, Inc. */
Copyright 2017 Google Inc * * Provides a simple driver to control the ASPEED LPC snoop interface which * allows the BMC to listen on and save the data
written by * the host to an arbitrary LPC I/O
Copyright 2017 Google, Inc * * This driver uses the memory mapper cross-ec interface to communicate * with the Chrome OS EC about accelerometer
data or older commands. * Accelerometer access is pre
Copyright 2017 Google LLC * * Based on Intel IPU4 driver.
Copyright 2017, Gustavo Romero, Breno Leitao, Cyril Bur, IBM Corp. * * Force FP, VEC and VSX unavailable exception during transaction in all * possible
scenarios regarding the MSR.FP and MSR.VEC st
Copyright 2017, Gustavo Romero, IBM Corp. * * Check if thread endianness is flipped inadvertently to BE on trap * caught in TM whilst MSR.FP and
MSR.VEC are zero (i.e. just after * load_fp and loa
Copyright 2017 Hannes Reinecke, SUSE Linux GmbH <hare@suse.com> * * Based on the original DAC960 driver,
Copyright 2017 Hannes Reinecke, SUSE Linux GmbH <hare@suse.com> * * Based on the original DAC960 driver, which has
Copyright 2017 * * Header file containing the public API for the System Controller (SC) * Miscellaneous (MISC) function.
Copyright 2017 * * Header file containing the public System Controller Interface (SCI) * definitions.
Copyright 2017 * * Header file containing types used across multiple service APIs.
Copyright 2017 IBM Corp.
Copyright 2017 IBM Corp. */
Copyright 2017 IBM Corp.#ifndef _ASM_PNV_OCXL_H#define _ASM_PNV_OCXL_H
Copyright 2017 IBM Corp.#ifndef _CHECKER_#define CREATE_TRACE_POINTS#include "trace.h"
Copyright 2017 IBM Corp.#ifndef _MISC_OCXL_H#define _MISC_OCXL_H
Copyright 2017 IBM Corp.#ifndef _OCXL_CONFIG_H#define _OCXL_CONFIG_H
Copyright 2017 IBM Corp.#ifndef _OCXL_INTERNAL_H#define _OCXL_INTERNAL_H
Copyright 2017 IBM Corp.#ifndef _UAPI_MISC_OCXL_H#define _UAPI_MISC_OCXL_H
Copyright 2017 IBM Corp.#include <asm/pnv-ocxl.h>#include <asm/opal.h>#include <asm/xive.h>#include <misc/ocxl-config.h>#include "pci.h"
Copyright 2017 IBM Corp.#include <linux/fs.h>#include <linux/poll.h>#include <linux/sched/signal.h>#include <linux/eventfd.h>#include
<linux/uaccess.h>#include <uapi/misc/ocxl.h>#include <asm/r
Copyright 2017 IBM Corp.#include <linux/interrupt.h>#include <asm/pnv-ocxl.h>#include "ocxl_internal.h"#include "trace.h"
Copyright 2017 IBM Corp.#include <linux/module.h>#include <linux/pci.h>#include <asm/mmu.h>#include "ocxl_internal.h"
Copyright 2017 IBM Corp.#include <linux/pci.h>#include <asm/pnv-ocxl.h>#include <misc/ocxl-config.h>#include "ocxl_internal.h"
Copyright 2017 IBM Corp.#include <linux/sched/mm.h>#include <linux/mutex.h>#include <linux/mm_types.h>#include
<linux/mmu_context.h>#include <asm/copro.h>#include <asm/pnv-ocxl.h>#include <misc
Copyright 2017 IBM Corp.#include <linux/sched/mm.h>#include "trace.h"#include "ocxl_internal.h"
Copyright 2017 IBM Corp.#include <linux/sysfs.h>#include "ocxl_internal.h"
Copyright 2017 IBM Corp.#include "ocxl_internal.h"
Copyright 2017 IBM Corporation
Copyright 2017 IBM Corporation */
Copyright 2017, IBM Corporation * cpufeatures is the new way to discover CPU features with /cpus/features * devicetree. This supersedes PVR based
discovery ("cputable"), and older * device tree
Copyright 2017, IBM Corporation. * * This uses code from arch/sparc/kernel/nmi.c and kernel/watchdog.c
Copyright 2017 IBM Corp.#undef TRACE_SYSTEM#define TRACE_SYSTEM ocxl
Copyright 2017 Icenowy Zheng <icenowy@aosc.io> */
Copyright 2017 Icenowy Zheng <icenowy@aosc.io> *
Copyright 2017 Icenowy Zheng <icenowy@aosc.xyz> */
Copyright 2017 Impinj, Inc * Author: Andrey Smirnov <andrew.smirnov@gmail.com> * * Based on the code of analogus driver: *
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```

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Copyright 2017 Intel Deutschland GmbH \*/  
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Copyright 2017 Karol Herbst  
Copyright © 2017 Keith Packard <keithp@keithp.com> \*/  
Copyright © 2017 Keith Packard <keithp@keithp.com> \*/#include <linux/file.h>#include <linux/uaccess.h>  
Copyright 2017 Linaro Ltd.  
Copyright 2017 Linaro Ltd. \*/  
Copyright 2017 Linaro Ltd. \* \* Author: Baoyou Xie <baoyou.xie@linaro.org>  
Copyright 2017 Manivannan Sadhasivam <manivannanece23@gmail.com> \* \* IIO driver for VL6180 (7-bit I2C slave address 0x29)  
Copyright 2017 Maxime Roussin-B \* expanded by Silvan Murer <silvan.murer@gmail.com> \*/  
Copyright 2017, Michael Ellerman, IBM Corporation. \*/  
Copyright 2017, Michael Ellerman, IBM Corporation. \*/#ifndef \_LINUX\_SET\_MEMORY\_H#define \_LINUX\_SET\_MEMORY\_H  
Copyright 2017, Michael Neuling, IBM Corp. \* Original: Breno Leitao <brenohl@br.ibm.com> & \* Gustavo Bueno Romero  
<gromero@br.ibm.com> \* Edited: Michael Neuling \* \* Force VMX unavailabl  
Copyright 2017, Nicholas Piggin, IBM Corporation \*/  
Copyright 2017 Noralf Tr \* \* The driver supports:  
Copyright 2017 Nuvoton Technology Crop.  
Copyright 2017 Nuvoton Technology Crop. \* \* Author: David Lin <ctlin0@nuvoton.com> \* Co-author: John Hsu <kchsu0@nuvoton.com> \* Co-author:  
Seven Li <wtli@nuvoton.com>  
Copyright 2017 NXP  
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Copyright 2017 NXP \* \* Based on WM8523 ALSA SoC Audio driver written by Mark Brown  
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Copyright 2017 Peter Meerwald-Stadler <pmeerw@pmeerw.net>  
Copyright 2017 Peter Meerwald-Stadler <pmeerw@pmeerw.net> \* \* 7-bit I2C slave address 0x4c  
Copyright 2017 Peter Meerwald-Stadler <pmeerw@pmeerw.net> \* \* Datasheet: <https://www.idt.com/document/dst/zopt2201-datasheet> \* 7-bit I2C  
slave addresses 0x53 (default) or 0x52 (programmed)  
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Copyright 2017 Red Hat \* Parts ported from amdgpu (fence wait code).  
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Copyright 2017 Tempo Semiconductor, Inc.// Author: Steven Eckhoff <steven.eckhoff.opensource@gmail.com>  
Copyright 2017 Texas Instruments \* \* Author: Milo Kim <milo.kim@ti.com>  
Copyright 2017 Texas Instruments, Inc. \*/#ifndef \_\_DT\_BINDINGS\_CLK\_AM3\_H#define \_\_DT\_BINDINGS\_CLK\_AM3\_H  
Copyright 2017 Texas Instruments, Inc. \*/#ifndef \_\_DT\_BINDINGS\_CLK\_AM4\_H#define \_\_DT\_BINDINGS\_CLK\_AM4\_H  
Copyright 2017 Texas Instruments, Inc. \*/#ifndef \_\_DT\_BINDINGS\_CLK\_DM814\_H#define \_\_DT\_BINDINGS\_CLK\_DM814\_H  
Copyright 2017 Texas Instruments, Inc. \*/#ifndef \_\_DT\_BINDINGS\_CLK\_DM816\_H#define \_\_DT\_BINDINGS\_CLK\_DM816\_H  
Copyright 2017 Texas Instruments, Inc. \*/#ifndef \_\_DT\_BINDINGS\_CLK\_DRA7\_H#define \_\_DT\_BINDINGS\_CLK\_DRA7\_H  
Copyright 2017 Texas Instruments, Inc. \*/#ifndef \_\_DT\_BINDINGS\_CLK\_OMAP4\_H#define \_\_DT\_BINDINGS\_CLK\_OMAP4\_H  
Copyright 2017 Texas Instruments, Inc. \*/#ifndef \_\_DT\_BINDINGS\_CLK\_OMAP5\_H#define \_\_DT\_BINDINGS\_CLK\_OMAP5\_H  
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Copyright © 2018-2019 Intel Corporation \* \* Autogenerated file by GPU Top : <https://github.com/rib/gputop> \* DO NOT EDIT manually!  
Copyright 2018-2019 NXP. \*/  
Copyright 2018-2019 NXP \*/  
Copyright 2018-2019 NXP \* \* Based on caamalg.c crypto API driver.  
Copyright 2018-2019 NXP \*/#ifndef DPAA2\_ETH\_DEBUGFS\_H#define DPAA2\_ETH\_DEBUGFS\_H  
Copyright 2018-2019 NXP \*/#include <linux/module.h>#include <linux/debugfs.h>#include "dpaa2-eth.h"#include "dpaa2-eth-debugfs.h"  
Copyright 2018-2019 NXP. \* \* Limitations:  
Copyright 2018-2019 NXP \* \* There is no Shared Descriptor for PKC so that the Job Descriptor must carry \* all the desired key parameters, input and output pointers.  
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Copyright © 2018 Alexey Dobriyan <adobriyan@gmail.com> \* \* Permission to use, copy, modify, and distribute this software for any \* purpose with or without fee is hereby granted, provided that the a  
Copyright 2018 Analog Devices Inc. \*/  
Copyright 2018 Analog Devices Inc. \*/#include <linux/bitfield.h>#include <linux/clk.h>#include <linux/delay.h>#include <linux/device.h>#include <linux/err.h>#include <linux/kernel.h>#include <  
Copyright 2018 Analog Devices Inc. \* \* TODO: Currently CRC is not supported in this driver  
Copyright 2018 Andrej Shadura <andrew@shadura.me>  
Copyright 2018 Angelo Dureghello <angelo@sysam.it> \*/#ifndef \_FSL\_EDMA\_COMMON\_H\_#define \_FSL\_EDMA\_COMMON\_H\_  
Copyright 2018 Anshuman Khandual, IBM Corporation. \*/  
Copyright 2018, Anton Blanchard, IBM Corp. \*/  
Copyright 2018 Arcx Inc \*/  
Copyright 2018 Arm Limited \* Author: Dave Martin <Dave.Martin@arm.com> \*/#include <linux/irqflags.h>#include <linux/sched.h>#include <linux/thread\_info.h>#include <linux/kvm\_host.h>#include <as  
Copyright 2018, Breno Leitao, Gustavo Romero, IBM Corp. \* \* A test case that creates a signal and starts a suspended transaction \* inside the signal handler.  
Copyright 2018, Breno Leitao, Gustavo Romero, IBM Corp. \* \* This test raises a SIGUSR1 signal, and toggle the MSR[TS] \* fields at the signal handler.  
With MSR[TS] being set, the kernel will \* forc  
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Copyright 2018 Cadence Design Systems, Inc. \*  
Copyright 2018, Christophe Leroy CS S.I. \* <christophe.leroy@c-s.fr> \* \* This dumps the content of BATS  
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Copyright 2018 David Lechner <david@lechnology.com> \* \* Based on mi0283qt.c:  
Copyright 2018 DEVIALET \*/  
Copyright 2018 Google, Inc.  
Copyright 2018 Google Inc. \* Author: Eric Dumazet (edumazet@google.com) \* \* Reference program demonstrating tcp mmap() usage, \* and SO\_RCVLOWAT hints for receiver.  
Copyright 2018 Google Inc. \* Author: Soheil Hassas Yeganeh (soheil@google.com) \* \* Simple example on how to use TCP\_INQ and TCP\_CM\_INQ.  
Copyright 2018 Google, Inc \* \* This driver uses the cros-ec interface to communicate with the Chrome OS \* EC about counter sensors. Counters are presented through \* iio sysfs.  
Copyright 2018 Google LLC.  
Copyright 2018 Google LLC. \*/  
Copyright 2018 Google LLC \*/  
Copyright 2018 Google LLC \* \* Author: Eric Biggers <ebiggers@google.com>  
Copyright 2018 Google LLC. \* \* Author: Tomasz Figa <tfiga@chromium.org> \* Author: Yong Zhi <yong.zhi@intel.com>  
Copyright 2018 Google LLC. \* \* Author: Tuukka Toivonen <tuukka.toivonen@intel.com> \* Author: Sakari Ailus <sakari.ailus@linux.intel.com> \* Author: Samu Onkalo <samu.onkalo@intel.com> \* Author: To  
Copyright 2018 Google LLC \* \* The corresponding platform device is typically registered in \* drivers/platform/chrome/wilco\_ec/core.c

```

Copyright 2018 Google LLC * * The Wilco EC is similar to a typical ChromeOS embedded controller. * It uses the same MEC based low-level
communication and a similar * protocol, but with some import
Copyright 2018 Google LLC * * This is the entry point for the drivers that control the Wilco EC. * This driver is responsible for several tasks:
Copyright 2018 Google LLC. *   Tomasz Figa <tfiga@chromium.org> */
Copyright 2018 Google LLC. *   Tomasz Figa <tfiga@chromium.org> * * Based on s5p-mfc driver by Samsung Electronics Co., Ltd.
Copyright 2018 HabanaLabs, Ltd. * All Rights Reserved
Copyright 2018 Hannes Reinecke, SUSE Linux GmbH <hare@suse.com> * * Based on the original DAC960 driver, which has
Copyright 2018 Hans de Goede <hdegoede@redhat.com> */
Copyright 2018 IBM Corp.
Copyright 2018 IBM Corp/* * A FSI master controller, using a simple GPIO bit-banging interface
Copyright 2018 IBM Corporation
Copyright 2018 IBM Corporation. */
Copyright 2018 IBM Corporation
Copyright 2018 IBM Corporation */
Copyright 2018 IBM Corporation * Author: Anshuman Khandual <khandual@linux.vnet.ibm.com> */
Copyright 2018, IBM Corporation. * * This file contains general idle entry/exit functions to save * and restore stack and NVGPRs which allows C code to
call idle * states that lose GPRs, and it
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Copyright © 2018 Intel Corporation. * * Authors: Gayatri Kammela <gayatri.kammela@intel.com> *   Sohil Mehta <sohil.mehta@intel.com> *
Jacob Pan <jacob.jun.pan@linux.intel.com>
Copyright © 2018 Intel Corporation */#include "gt/intel_gt.h"
Copyright 2018, Jann Horn <jannh@google.com>
Copyright 2018 Konsulko Group * * Author: Matt Porter <mporter@konsulko.com>
Copyright 2018 Ladislav Michl <ladis@linux-mips.org>
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Copyright 2018 Lubomir Rintel <lkundrak@v3.sk> */
Copyright 2018 Lubomir Rintel <lkundrak@v3.sk> * * Written by Jonathan Corbet, corbet@lwn.net.
Copyright 2018 Marty E. Plummer <hanetzer@startmail.com>
Copyright 2018 Maxime Roussin-Belanger <maxime.roussinbelanger@gmail.com> */
Copyright 2018, Michael Ellerman, IBM Corporation.
Copyright 2018, Michael Ellerman, IBM Corporation. */
Copyright 2018, Michael Ellerman, IBM Corporation. */#ifndef __ASM_POWERPC_CODE_PATCHING_ASM_H#define
__ASM_POWERPC_CODE_PATCHING_ASM_H
Copyright 2018, Michael Ellerman, IBM Corp. * * Test that an out-of-bounds branch to counter behaves as expected.
Copyright 2018, Murilo Opsfelder Araujo, IBM Corporation. */
Copyright 2018 Nick Piggin, Michael Ellerman, IBM Corp. */
Copyright 2018 Noralf Tr
Copyright 2018 Noralf Tr */
Copyright 2018 NXP
Copyright 2018 NXP.
Copyright 2018 NXP. */
Copyright 2018 NXP */
Copyright 2018 NXP * Author: Dong Aisheng <aisheng.dong@nxp.com> * * Implementation of the SCU IPC functions using MUs (client side).
Copyright 2018 NXP. *   Dong Aisheng <aisheng.dong@nxp.com> */
Copyright 2018 NXP *   Dong Aisheng <aisheng.dong@nxp.com> */
Copyright 2018 NXP * Dong Aisheng <aisheng.dong@nxp.com> */
Copyright 2018 NXP * * Header file for the IPC implementation.
Copyright 2018 NXP */#ifndef __PTP_QORIQ_H__#define __PTP_QORIQ_H__
Copyright 2018 NXP Semiconductors
Copyright 2018 NXP. * * This driver supports the fractional plls found in the imx8m SOCs
Copyright 2018 NXP. * * This driver supports the SCCG plls found in the imx8m SOCs
Copyright 2018 Omnibond Systems, L.L.C. * * See COPYING in top-level directory.
Copyright 2018, Paul Cercueil <paul@crapouillou.net> */
Copyright 2018 Pavel Machek <pavel@ucw.cz> // TI LMU LED common framework, based on previous work from// Milo Kim <milo.kim@ti.com>
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 Copyright 2018 Socionext Inc.// Author: Kunihiko Hayashi <hayashi.kunihiko@socionext.com>  
 Copyright 2018, Socionext Inc. \* Author: Kunihiko Hayashi <hayashi.kunihiko@socionext.com> \*/  
 Copyright 2018 SUSE Linux. \* Author: Coly Li <colyli@suse.de> \*/  
 Copyright 2018 SUSE Linux. \* Author: Coly Li <colyli@suse.de> \*/#include <inttypes.h>#include <stdio.h>  
 Copyright 2018 SUSE Linux GmbH  
 Copyright 2018 Tempo Semiconductor, Inc.// Author: Steven Eckhoff <steven.eckhoff.opensource@gmail.com>  
 Copyright 2018, Thiago Jung Bauermann, IBM Corporation. \*/#include <stdio.h>#include <stdbool.h>#include <semaphore.h>  
 Copyright © 2018 VMware, Inc., Palo Alto, CA., USA  
 Copyright 2019 Advanced Micro Devices, Inc.  
 Copyright © 2019 Alexey Dobriyan <adobriyan@gmail.com> \* \* Permission to use, copy, modify, and distribute this software for any \* purpose with or  
 without fee is hereby granted, provided that the a  
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 Copyright 2019 American Megatrends International LLC. \* \* Author: Karthikeyan Mani <karthikeyanm@amiindia.co.in>  
 Copyright 2019 Analog Devices Inc. \*/  
 Copyright 2019 Analog Devices Inc. \* \* https://wiki.analog.com/resources/fpga/docs/axi\_ip \* https://wiki.analog.com/resources/fpga/docs/hdl/regmap  
 Copyright 2019 Analog Devices Inc. \*/#include <linux/bitfield.h>#include <linux/clk.h>#include <linux/device.h>#include <linux/err.h>#include  
 <linux/gcd.h>#include <linux/kernel.h>#include <li  
 Copyright 2019 Analog Devices Inc. \*/#include <linux/kernel.h>#include <linux/bitfield.h>#include <linux/delay.h>#include <linux/errno.h>#include  
 <linux/init.h>#include <linux/module.h>#includ  
 Copyright 2019 Annaliese McDermond \* \* Author: Annaliese McDermond <nh6z@nh6z.net>  
 Copyright 2019 ARM Limited \* \* 32 Bit compat layer by Stefani Seibold <stefani@seibold.net> \* sponsored by Rohde & Schwarz GmbH & Co. KG  
 Munich/Germany  
 Copyright 2019 Arm Limited \* Author: Andrew Murray <Andrew.Murray@arm.com> \*/#include <linux/kvm\_host.h>#include  
 <linux/perf\_event.h>#include <asm/kvm\_hyp.h>  
 Copyright 2019 Arm Limited \* Authors: Mark Rutland <mark.rutland@arm.com> \* Amit Daniel Kachhap <amit.kachhap@arm.com> \*/  
 Copyright 2019 ARM Ltd. \* \* Generic implementation of update\_vsyscall and update\_vsyscall\_tz.  
 Copyright 2019 Cadence \* \* Authors:  
 Copyright 2019 Collabora Ltd.  
 Copyright 2019 Collabora Ltd  
 Copyright © 2019 Collabora Ltd. \*/#ifndef \_PANFROST\_DRM\_H\_#define \_PANFROST\_DRM\_H\_  
 Copyright 2019 Collabora Ltd#ifndef \_\_PANFROST\_PERFCNT\_H\_#define \_\_PANFROST\_PERFCNT\_H\_  
 Copyright 2019 Collabora Ltd.#include <linux/bitfield.h>#include <linux/bitmap.h>#include <linux/delay.h>#include <linux/dma-mapping.h>#include  
 <linux/interrupt.h>#include <linux/io.h>#include  
 Copyright 2019 Collabora Ltd.#include <linux/delay.h>#include <linux/interrupt.h>#include <linux/io.h>#include <linux/platform\_device.h>#include  
 <linux/pm\_runtime.h>#include <linux/dma-resv.h>#  
 Copyright 2019 Collabora Ltd.#include <linux/devfreq.h>#include <linux/platform\_device.h>#include <linux/pm\_opp.h>#include <linux/clk.h>#include  
 <linux/regulator/consumer.h>  
 Copyright 2019 dvanced Micro Devices, Inc.  
 Copyright 2019 Finn Thain \* \* derived in part from: \*/  
 Copyright 2019 Google Inc  
 Copyright 2019 Google LLC.  
 Copyright 2019 Google LLC. \*/  
 Copyright 2019 Google LLC \*/  
 Copyright 2019 Google LLC \*/#ifndef \_UAPI\_LINUX\_FSVERITY\_H#define \_UAPI\_LINUX\_FSVERITY\_H  
 Copyright 2019 Google LLC \* \* See Documentation/ABI/testing/debugfs-wilco-ec for usage.  
 Copyright 2019 Google LLC \* \* See Documentation/ABI/testing/sysfs-class-power and \* Documentation/ABI/testing/sysfs-class-power-wilco for  
 userspace interface \* and other info.  
 Copyright 2019 Google LLC \* \* Sysfs properties to view and modify EC-controlled features on Wilco devices. \* The entries will appear under  
 /sys/bus/platform/devices/GOOG000C:00/  
 Copyright 2019 Google LLC \* \* The Wilco Embedded Controller can create custom events that \* are not handled as standard ACPI objects. These events  
 can \* contain information about changes in EC con  
 Copyright 2019 Google LLC \* \* The Wilco Embedded Controller is able to send telemetry data \* which is useful for enterprise applications. A daemon

running on \* the OS sends a command to the EC via

Copyright 2019 Google LLC \* \* This file is a rework and part of the code is ported from \* drivers/mfd/cros\_ec\_dev.c that was originally written by \* Bill Richardson.

Copyright 2019, Gustavo Romero, Michael Neuling, IBM Corp. \* \* This test will spawn two processes. Both will be attached to the same \* CPU (CPU 0). The child will be in a loop writing to FP registe

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Copyright 2019 IBM Corp.#include <linux/idr.h>#include "ocxl\_internal.h"

Copyright 2019 IBM Corp.#include <linux/module.h>#include "ocxl\_internal.h"

Copyright 2019 IBM Corp.#include <linux/sched/mm.h>#include "trace.h"#include "ocxl\_internal.h"

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Copyright © 2019 Intel Corporation \* \* Authors:

Copyright © 2019 Intel Corporation \*/#ifndef \_\_I915\_GEM\_CLIENT\_BLT\_H\_\_#define \_\_I915\_GEM\_CLIENT\_BLT\_H\_\_

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Copyright 2019 Linaro, Ltd., Rob Herring <robh@kernel.org>#ifndef \_\_PANFROST\_FEATURES\_H\_\_#define \_\_PANFROST\_FEATURES\_H\_\_

Copyright 2019 Linaro, Ltd, Rob Herring <robh@kernel.org>#include <linux/atomic.h>#include <linux/bitfield.h>#include <linux/delay.h>#include <linux/dma-mapping.h>#include <linux/interrupt.h>#in

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Copyright 2019 NXP \* Author: Daniel Baluta <daniel.baluta@nxp.com> \* \* Implementation of the DSP IPC interface (host side)

Copyright 2019 NXP. \* \* FlexSPI is a flexible SPI host controller which supports two SPI \* channels and up to 4 external devices. Each channel supports

\* Single/Dual/Quad/Octal mode data transfer

Copyright 2019 NXP \* \* Header file for the DSP IPC implementation

Copyright 2019 NXP \* \* Implementation of the SCU IRQ functions using MU.

Copyright 2019 NXP \*/#include <linux/device.h>#include <linux/debugfs.h>#include <linux/fsl/ptp\_qoriq.h>

Copyright 2019 NXP#include <linux/of\_mdio.h>#include "enetc\_mdio.h"

Copyright 2019 NXP \* \* Peng Fan <peng.fan@nxp.com>

Copyright 2019 Ondrej Zary \* \* Original driver by \* Rickard E. Faith, faith@cs.unc.edu

Copyright 2019 Ondrej Zary \* \* The initial developer of the original code is David A. Hinds \* <dahinds@users.sourceforge.net>. Portions created by

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Copyright Alan Cox GW4PTS (alan@lxorguk.ukuu.org.uk)
Copyright Alistair Popple, IBM Corporation 2014. */#include <linux/bitops.h>#include <linux/irq.h>#include <linux/irqchip.h>#include
<linux/irqdomain.h>#include <linux/interrupt.h>#include <lin
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copyright as above. * * Modified for Sun3 OBIO i82586 by Sam Creasey (sammy@sammy.net)
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copyright asserted, 1 = copyright not asserted#define CS8427_ORIG (1<<0) /* SCMS generation indicator, 0 = 1st generation or higher, 1
= original */
Copyright asserted flag -- do not modify#define SPCS_NOTAUDIODATA 0x00000002 /* 0 = Digital audio, 1 = not audio *//#define
SPCS_PROFESSIONAL 0x00000001 /* 0 = Consumer (IEC-958), 1 = pro (AES3-1992)
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copyright bit#define CM_DAC2SPDO 0x00200000 /* enable wave+fm_midi -> SPDIF/OUT *//#define CM_INVIDWEN 0x00100000
/* internal vendor ID write enable, model 039? *//#define CM_SETRETRY 0x00100000 /
COPYRIGHT) bits else bits |= 0x08; switch (diga->status[0] & IEC958_AES0_CON_EMPHASIS) {
default:
COPYRIGHT)) bits if ((diga->status[0] & IEC958_AES0_CON_EMPHASIS) == IEC958_AES0_CON_EMPHASIS_NONE)
bits |= 0x40; if (!(diga->status[1] & IEC958_AES1_CON_ORIGINAL)) bits |= 0x80; if ((d
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The Unicorn' (MOTU) * MidiTimePiece
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Copyright by Micha */#ifndef LINUX_CB710_MMC_H#define LINUX_CB710_MMC_H
Copyright by Micha */#ifndef LINUX_CB710_SG_H#define LINUX_CB710_SG_H
Copyright by Micha */#include <linux/cb710.h>#include <linux/kernel.h>#include <linux/module.h>

```

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Copyright by Micha */#include <linux/kernel.h>#include <linux/module.h>#include <linux/cb710.h>
Copyright by Micha */#include <linux/kernel.h>#include <linux/module.h>#include <linux/pci.h>#include <linux/delay.h>#include "cb710-mmc.h"
Copyright by Micha */#include <linux/kernel.h>#include <linux/module.h>#include <linux/pci.h>#include <linux/spinlock.h>#include
<linux/idr.h>#include <linux/cb710.h>#include <linux/gfp.h>
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generic 7990.c LANCE code. */
Copyright (C) 05/1998 Peter Maydell <pmaydell@chiark.greenend.org.uk> * * Most of this stuff was obtained by looking at other LANCE drivers, * in
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Copyright (C) 1990, 1991 Free Software Foundation, Inc. FIXME: Can someone provide a transliteration of this name into ASCII? Using the following
chars caused a compiler warning on HIUX (so I re
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Copyright (C) 1990 - 1992, 1999 Silicon Graphics, Inc. */#ifndef _ASM_FPREGDEF_H#define _ASM_FPREGDEF_H
Copyright (C) 1990, 1999, 2000 Silicon Graphics, Inc. * 2004 pf */#ifndef _ASM_MACH_IP28_SPACES_H#define _ASM_MACH_IP28_SPACES_H
Copyright (C) 1990, 1999, 2000 Silicon Graphics, Inc. */#ifndef _ASM_AR7_SPACES_H#define _ASM_AR7_SPACES_H
Copyright (C) 1990, 1999, 2000 Silicon Graphics, Inc. */#ifndef _ASM_BCM63XX_SPACES_H#define _ASM_BCM63XX_SPACES_H
Copyright (C) 1990, 1999, 2000 Silicon Graphics, Inc. */#ifndef _ASM_BMIPS_SPACES_H#define _ASM_BMIPS_SPACES_H
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Copyright (C) 1990, 1999, 2000 Silicon Graphics, Inc. */#ifndef _ASM_TX39XX_SPACES_H#define _ASM_TX39XX_SPACES_H
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Copyright (C) 1990, 1999 by Silicon Graphics, Inc. */#ifndef _ASM_MACH_IP27_SPACES_H#define _ASM_MACH_IP27_SPACES_H
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Copyright (C) 1991, 1992, 1993, 1994 Linus Torvalds */
Copyright (C) 1991, 1992, 1993, 1994 Linus Torvalds * * Dave Engebretsen <engebret@us.ibm.com> * Rework for PPC64 port.
Copyright (C) 1991, 1992, 1993, 1994 Linus Torvalds */#include <linux/mm.h>#include <asm/mmu.h>
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Copyright (C) 1991, 1992, 1993, 1994 Linus Torvalds * * Modified by Fred N. van Kempen, 01/29/93, to add line disciplines * which can be dynamically
activated and de-activated by the line * disci
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Removed race in async swapping. 14.4.1996. Bruno H
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kswapdctl limits, and swap out as many pages as neede
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 Copyright (C) 1991, 1992 Linus Torvalds \* \* affs symlink handling code  
 Copyright (C) 1991, 1992 Linus Torvalds \* \* Based on bootsect.S and setup.S \* modified by more people than can be counted  
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 Copyright (C) 1991, 1992 Linus Torvalds \* \* coh/dir.c  
 Copyright (C) 1991, 1992 Linus Torvalds \* \* coh/file.c  
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 Copyright (C) 1991, 1992 Linus Torvalds \* \* Enhanced CPU detection and feature setting code by Mike Jagdis \* and Martin Mares, November 1997.  
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 Copyright (C) 1991, 1992 Linus Torvalds \*/#include <linux/fs.h>#include <linux/ext2\_fs.h>#include <linux/blockgroup\_lock.h>#include <linux/percpu\_counter.h>#include <linux/rbtree.h>  
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 Copyright (C) 1991, 1992 Linus Torvalds \* \* minix directory handling functions  
 Copyright (C) 1991, 1992 Linus Torvalds \* \* minix regular file handling primitives  
 Copyright (C) 1991, 1992 Linus Torvalds \* \* Modified to make sys\_syslog() more flexible: added commands to \* return the last 4k of kernel messages, regardless of whether \* they've been read or no  
 Copyright (C) 1991, 1992 Linus Torvalds \* \* More specifically, the first copied function was strtobool, which \* was introduced by: \*  
 Copyright (C) 1991, 1992 Linus Torvalds \* \* proc base directory handling functions  
 Copyright (C) 1991, 1992 Linus Torvalds \* \* proc root directory handling functions  
 Copyright (C) 1991, 1992 Linus Torvalds \* \* super.c contains code to handle: - mount structures \* - super-block tables \*  
 - fil  
 Copyright (C) 1991, 1992 Linus Torvalds \* \* table of configured filesystems  
 Copyright (C) 1991, 1992 Linus Torvalds \* \* This file contains the interface functions for the various time related \* system calls: time, stime, gettimeofday, settimeofday, adjtime  
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 Copyright (C) 1991-1998 Linus Torvalds \* Re-organised Feb 1998 Russell King \* \* We now have independent partition support from the \* block drivers, which allows all the partition code to \* b  
 Copyright (C) 1991-1998 Linus Torvalds \* \* Thanks to Branko Lankester, lankeste@fwi.uva.nl, who found a bug \* in the early extended-partition checks and added DM partitions  
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 messages as in 1.2 \* Fixed up the unchecked user spac  
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 accounting right. \* Occurs in sever  
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 Tosatti \*/#ifndef LINUX\_LP\_H#define LINUX\_LP\_H  
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 Copyright (C) 1992 Rick Sladkey \*/#include <linux/fs.h>#include <linux/file.h>#include <linux/falloc.h>#include <linux/nfs\_fs.h>#include  
 "delegation.h"#include "internal.h"#include "iostat.h"  
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<linux/interrupt.h>#include <linux/kernel.h>#include <lin
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previously, so we have the \* same table format for a  
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<linux/mman.h>#include <linux/sched/signal.h>#inc
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<linux/mman.h>#include <linux/nodemask.h>#inclu
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Copyright (C) 1996 Mike Shaver (shaver@zeroknowledge.com) */#include <linux/mm.h>#include <linux/sysctl.h>#include <linux/init.h>#include
<net/ax25.h>#include <net/netrom.h>
Copyright (C) 1996 Mike Shaver (shaver@zeroknowledge.com) */#include <linux/mm.h>#include <linux/sysctl.h>#include <linux/init.h>#include

```

```

<net/ax25.h>#include <net/rose.h>
copyright (C) 1996 okir@monad.swb.de).\n";
Copyright (C) 1996 Olaf Kirch * 04 Aug 1998 Ion Badulescu <ionut@cs.columbia.edu> * FIFO's need special handling in NFSv2 */
Copyright (C) 1996 Olaf Kirch <okir@monad.swb.de> */
Copyright (C) 1996, Olaf Kirch <okir@monad.swb.de> */
Copyright (C) 1996, Olaf Kirch <okir@monad.swb.de> */#ifndef _LINUX_SUNRPC_DEBUG_H_#define _LINUX_SUNRPC_DEBUG_H_
Copyright (C) 1996 Olaf Kirch, <okir@monad.swb.de> * * TCP NFS related read + write fixes
Copyright (C) 1996 Paul Gortmaker
Copyright (C) 1996 Paul Gortmaker (drivers/char/rtc.c)
Copyright (C) 1996 Paul Gortmaker * * This driver allows use of the real time clock (built into * nearly all computers) from user space. It exports the
/dev/rtc * interface supporting various ioct
Copyright (C) 1996 Paul H. Hargrove
Copyright (C) 1996 Paul Mackerras.
Copyright (C) 1996 Paul Mackerras. */
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Copyright (C) 1996 Paul Mackerras
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{engebret|bergner|mikejc}@us.ibm.com
Copyright (C) 1996 Paul Mackerras * Amiga/APUS changes by Jesper Skov (jskov@cygnus.co.uk). * * Derived from "arch/i386/mm/init.c"
Copyright (C) 1996 Paul Mackerras and Mark Abene
Copyright (C) 1996 Paul Mackerras and Mark Abene. * * based on skeletonfb.c:
Copyright (C) 1996 Paul Mackerras * * and on the PowerMac ATI/mach64 display driver: *
Copyright (C) 1996 Paul Mackerras. * * Apr. 21 2002 - BenH Rework bus reset code for new error handler * Add delay after
initial bus reset *
Copyright (C) 1996 Paul Mackerras * * Derived from "arch/i386/mm/init.c"
Copyright (C) 1996 Paul Mackerras. */#ifndef _LINUX_CUDA_H#define _LINUX_CUDA_H
Copyright (C) 1996 Paul Mackerras. */#ifndef _MAC53C94_H#define _MAC53C94_H
Copyright (C) 1996 Paul Mackerras. */#ifndef _MESH_H#define _MESH_H
Copyright (C) 1996 Paul Mackerras. */#include <asm/ppc_asm.h>#include <asm/export.h>#include <asm/cache.h>
Copyright (C) 1996 Paul Mackerras. */#include <asm/processor.h>#include <asm/errno.h>#include <asm/ppc_asm.h>#include
<asm/export.h>#include <asm/kasan.h>
Copyright (C) 1996 Paul Mackerras. */#include <linux/kernel.h>#include <linux/delay.h>#include <linux/types.h>#include <linux/string.h>#include
<linux/slab.h>#include <linux/blkdev.h>#include
Copyright (C) 1996 Paul Mackerras. */#include <stdarg.h>#include <linux/types.h>#include <linux/errno.h>#include <linux/kernel.h>#include
<linux/delay.h>#include <linux/adb.h>#include <linux/c
Copyright (C) 1996 Paul Mackerras. * * Modified to declare controllers as structures, added * client notification of bus reset and handles PowerBook *
sleep, by Benjamin Herrenschmidt.
Copyright (C) 1996 Paul Mackerras. * MPC8xx modifications by Dan Malek
Copyright (C) 1996 Paul Mackerras. * * NOTE: assert(sizeof(buf) > 23 * sizeof(long))
Copyright (C) 1996 Paul Mackerras (Paul.Mackerras@cs.anu.edu.au)
Copyright (c) 1996 Paul Mackerras <paulus@cs.anu.edu.au>
Copyright (C) 1996 Paul Mackerras (paulus@cs.anu.edu.au) */#include <linux/slab.h>#include <linux/seq_file.h>#include <linux/radix-tree.h>#include
<linux/module.h>#include <linux/of.h>#include
Copyright (c) 1996 Paul Mackerras <paulus@cs.anu.edu.au> * Low-level exception handlers, MMU support, and rewrite.
Copyright (c) 1996 Paul Mackerras <paulus@cs.anu.edu.au> * Low-level exception handlers, MMU support, and rewrite.
Copyright (C) 1996 Paul Mackerras <paulus@cs.anu.edu.au> * Low-level exception handlers, MMU support, and rewrite.
Copyright (C) 1996 Paul Mackerras (paulus@cs.anu.edu.au) * * This file contains the code used by various IRQ handling routines:
Copyright (C) 1996 Paul Mackerras (paulus@cs.anu.edu.au) * * This file contains the code used to make IRQ descriptions in the * device tree to actual
irq numbers on an interrupt controller * drive
Copyright (C) 1996 Paul Mackerras (paulus@samba.org) * * Derived from "arch/alpha/kernel/setup.c"
Copyright (C) 1996 Paul Mackerras. * * platinumfb.c: Console support for PowerMac "platinum" display adaptor.
Copyright (C) 1996 Paul Mackerras * PPC44x/36-bit changes by Matt Porter (mporter@mvista.com) * * Derived from "arch/i386/mm/init.c"
Copyright (C) 1996 Paul Mackerras. * * The so far unpublished platinumfb.c
Copyright (C) 1996 Paul Mackerras. * * This file contains low-level assembler routines for managing * the PowerPC MMU hash table. (PPC 8xx
processors don't use a * hash table, so this file is
Copyright (C) 1996 Paul Mackerras * * Updated to 2.5 framebuffer API by Ben Herrenschmidt * <benh@kernel.crashing.org>, Paul Mackerras
<paulus@samba.org>, * and James Simmons <jsimmons@infradea
Copyright (C) 1996 Paul Mackerras * with considerable contributions from Ben Herrenschmidt and others. *
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Copyright (C) 1996 Russell King. \* \* Changelog:

Copyright (C) 1996 Russell King \*/#define VIDMEM ((char \*)SCREEN\_START)

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Copyright (C) 1996 Thomas K. Dyas (tdyas@eden.rutgers.edu) \* \* Chris Davis (cdavis@cois.on.ca) 03/27/1998 \* Added support for the intersil on the sun4/4200

Copyright (C) 1996 Thomas K. Dyas (tdyas@noc.rutgers.edu)

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Copyright (C) 1997, 1998, 2001, 03, 05, 06 by Ralf Baechle \*/#include <linux/linkage.h>#include <linux/init.h>#include <linux/rtc/ds1286.h>#include <linux/interrupt.h>#include <linux/kernel.h>#

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Copyright (C) 1997,1998 Jakub Jelinek (jj@sunsite.mff.cuni.cz) \* Heavily based on arch/sparc/kernel/irq.c. \*/

Copyright (C) 1997,1998 Jakub Jelinek (jj@sunsite.mff.cuni.cz) \*/#ifndef \_SPARC\_IO\_UNIT\_H#define \_SPARC\_IO\_UNIT\_H

```

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Copyright (C) 1997, 1998 Jay Estabrook
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Copyright (C) 1997, 1998 Krzysztof G. Baranowski * * This file contains code for allocating/freeing inodes and for read/writing * the superblock.
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Copyright (C) 1997, 1998 Ralf Baechle (ralf@gnu.org)
Copyright (C) 1997, 1998 Ralf Baechle (ralf@gnu.org) */#include <linux/init.h>#include <linux/kernel.h>#include <linux/kdev_t.h>#include
<linux/types.h>#include <linux/console.h>#include <linux
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Copyright (C) 1997,1998 Russell King * * Architecture Specific TIME specifications
Copyright (C) 1997-1998 Russell King * * message queue handling
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Copyright (C) 1997-1999 Russell King *
Copyright (C) 1997-1999 Russell King * * E and F format directory handling
Copyright (C) 1997-1999 Russell King * from: *
Copyright (C) 1997-1999 Russell King */#include <linux/buffer_head.h>#include <linux/writeback.h>#include "adfs.h"
Copyright (C) 1997-1999 Russell King */#include <linux/module.h>#include <linux/init.h>#include <linux/parser.h>#include <linux/mount.h>#include
<linux/seq_file.h>#include <linux/slab.h>#inclu
Copyright (C) 1997-1999 Russell King */#include <linux/slab.h>#include "adfs.h"#include "dir_fplus.h"
Copyright (C) 1997-1999 Russell King * * Modifications:
Copyright (C) 1997-1999 Russell King * * Structure passed to kernel to tell it about the * hardware it's running on. See Documentation/arm/setup.rst *
for more info.
Copyright (C) 1997 - 2000, 2003, 04 Ralf Baechle (ralf@linux-mips.org) */#include <linux/kernel.h>#include <linux/init.h>#include <linux/pci.h>
Copyright (C) 1997 - 2000, 2003 Ralf Baechle <ralf@linux-mips.org> */#include <linux/kernel.h>#include <linux/pci.h>#include
<linux/types.h>#include <asm/sni.h>
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<linux/init.h>#include <linux/kernel.h>#include <linux/sched.h>#

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Copyright (C) 1997-2002 Russell King \*/#include <asm/unaligned.h>#include "adfs.h"

Copyright (C) 1997-2002 Russell king \*/#include <linux/linkage.h>#include <linux/init.h>#include <asm/assembler.h>#include

<asm/memory.h>#include <asm/page.h>#include "proc-macros.S"

Copyright (C) 1997-2002 Russell king \*/#include <linux/linkage.h>#include <linux/init.h>#include <asm/assembler.h>#include <asm/page.h>#include "proc-macros.S"

Copyright (C) 1997-2002 Russell King \* Modified for ARMv7 by Catalin Marinas \* \* ARM architecture version 6 TLB handling functions. \* These assume a split I/D TLB.

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Copyright (C) 1997-2005 Russell King \*/#include <linux/module.h>#include <linux/blkdev.h>#include <linux/kernel.h>#include

<linux/string.h>#include <linux/ioport.h>#include <linux/proc\_fs.h>#i

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```

<asm/branch.h>#include <linux/uaccess.h>
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project. Contact Peter Braam <coda@cs.cmu.edu>.
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Copyright (c) 1997 Dan error_act (dmalek@jlc.net)
Copyright (c) 1997 Dan error_act (dmalek@jlc.net) ** In addition to the individual control of the communication * channels, there are a few functions
that globally affect the * communication proc
Copyright (c) 1997 Dan Malek (dmalek@jlc.net)
Copyright (C) 1997 Dan Malek (dmalek@jlc.net). */
Copyright (c) 1997 Dan Malek (dmalek@jlc.net). /*#define BD_SC_EMPTY ((ushort)0x8000) /* Receive is empty */#define BD_SC_READY
((ushort)0x8000) /* Transmit is ready */#define BD_SC_WRAP ((ushort)
Copyright (c) 1997 Dan Malek <dmalek@jlc.net> * PowerPC 8xx modifications.
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Copyright (C) 1997 Dan Malek <dmalek@jlc.net> * PowerPC 8xx modifications.
Copyright (c) 1997 Dan Malek (dmalek@jlc.net) ** Right now, I am very wasteful with the buffers. I allocate memory * pages and then divide them
into 2K frame buffers. This way I know I * have b
Copyright (c) 1997 Dan Malek (dmalek@jlc.net) ** The I/O on the MPC860 is comprised of blocks of special registers * and the dual port ram for the
Communication Processor Module. * Within this sp
Copyright (C) 1997 Dan Malek (dmalek@jlc.net). ** This file contains low-level support and setup for PowerPC 8xx * embedded processors, including
trap and interrupt dispatch.
Copyright (c) 1997 Dan Malek (dmalek@jlc.net) ** This file contains structures and information for the communication * processor channels. Some CPM
control and status is available * through the
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interrupt dispatch. * (The PPC 8xx embedded
Copyright (C) 1997 Dan Malek (dmalek@jlc.net). ** This file contains the system call entry code, context switch * code, and exception/interrupt return
code for PowerPC.
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Copyright (C) 1997 David S. Miller (davem@caip.rutgers.edu)
Copyright (C) 1997 David S. Miller (davem@caip.rutgers.edu) ** Derived from "arch/i386/kernel/signal.c"
Copyright (C) 1997 David S. Miller (davem@caip.rutgers.edu) ** These routines maintain argument size conversion between 32bit and 64bit *
environment.
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would have an own structure, with instances
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Copyright (C) 1997 Eddie C. Dost (ecd@skynet.be) */
Copyright (C) 1997 Eddie C. Dost (ecd@skynet.be) ** Adaptation to new UART layer is: *
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Copyright (C) 1997 Eddie C. Dost (ecd@skynet.be) ** Port to new UART layer is: *
Copyright (C) 1997 Eric Youngdale ** SCSI error/timeout handling * Initial versions: Eric Youngdale. Based upon conversations with *
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<linux/kd.h>#include <linux/selection.h>#include <
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 Copyright (C) 1998, 1999 Russell King, Phil Blundell \*/#include <linux/module.h>#include <linux/ioport.h>#include <linux/kernel.h>#include  
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 <linux/irq.h>#include <linux/spinlock.h>#inc  
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Copyright (c) 1999, 2000 Silicon Graphics, Inc. */#include <linux/bitops.h>#include <linux/irqflags.h>#include <linux/export.h>
Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include <linux/compiler.h>#include <linux/kernel.h>#include <linux/sched.h>#include
<linux/timer.h>#include <linux/smp.h>#include <linux/mmzone
Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include <linux/cpu_pm.h>#include <linux/hardirq.h>#include <linux/init.h>#include
<linux/highmem.h>#include <linux/kernel.h>#include <linux/li
Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include <linux/kernel.h>#include <linux/export.h>#include <linux/pci.h>#include
<linux/smp.h>#include <linux/dma-direct.h>#include <linux/plat
Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include <linux/kernel.h>#include <linux/sched.h>#include <linux/interrupt.h>#include

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Copyright (C) 1999,2000 Walt Drummond <drummond@valinux.com> * * 00/04/19 D. Mosberger   Rewritten to mirror more closely the x86 I/O *
APIC code. In particular, we now have separate *          handle
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*          Data locality study on the checksum buffer. *          More op
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05/22/2000 eranian Added support for stacked register calls * 05
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*/#include <linux/init.h>#include <linux/string.h>#include <l
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2002/12/09 Fix rotating register handling (off-by-1 error,
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Copyright (C) 1999 Niibe Yutaka */#include <linux/irq.h>#include <linux/interrupt.h>#include <linux/module.h>#include
<linux/kernel_stat.h>#include <linux/seq_file.h>#include <linux/trace.h>
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Copyright (C) 1999 Silicon Graphics, Inc. */#include <asm/mipsregs.h>#include <asm/regdef.h>#include <asm/stackframe.h>
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 <linux/reboot.h>#include <linux/string.h>  
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 <linux/interrupt.h>#include <linux/ioport.h>#i

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<linux/utsname.h>#include <linux/hardirq.h>#includ
Copyright (C) 2000, 2001, 2002 Andi Kleen, SuSE Labs */#include <linux/sched/debug.h>#include <linux/kallsyms.h>#include
<linux/kprobes.h>#include <linux/uaccess.h>#include <linux/hardirq.h>#in
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environment. In 2.5 most of this should be moved to a
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<linux/interrupt.h>#include <linux/smp.h>#include <linux/sp
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Copyright (C) 2000,2001,2004 Broadcom Corporation */#include <linux/clockchips.h>#include <linux/interrupt.h>#include <linux/percpu.h>#include
<linux/smp.h>#include <linux/irq.h>
Copyright (C) 2000,2001,2004 Broadcom Corporation */#include <linux/clocksource.h>#include <linux/sched_clock.h>
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Copyright (C) 2000, 2001 Broadcom Corporation */#ifndef __ASM_SMP_OPS_H#define __ASM_SMP_OPS_H
Copyright (C) 2000, 2001 Broadcom Corporation */#include <linux/clockchips.h>#include <linux/interrupt.h>#include <linux/irq.h>#include
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<linux/topology.h>#include <linux/nodemask.h>
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<linux/smp.h>#include <linux/mm.h>#include <linux/export.h
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<linux/interrupt.h>#include <linux/amba/bus.h>#include <linux/i
Copyright (C) 2000-2001 Deep Blue Solutions Ltd. */#include <linux/module.h>#include <linux/tty.h>#include <linux/tty_flip.h>#include
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Copyright (C) 2000, 2001 Paolo Alberelli */#include <linux/rwsem.h>#include <linux/module.h>#include <linux/smp.h>#include
<linux/user.h>#include <linux/elfcore.h>#include <linux/sched.h>#inc
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Copyright (C) 2000-2002 Paul Bristow <paul@paulbristow.net>  
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 <linux/amba/bus.h>#include <linux/io.h>#include <linux/i  
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 <linux/device.h>#include <linux/export.h>#include <linux/spinl  
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 <linux/netfilter\_ipv4/ip\_tables.h>#include <linux/netdevice.h>#include <linux/skbuff.h>#i  
 Copyright (C) 2000-2004 Netfilter Core Team <coreteam@netfilter.org> \*/#include <linux/module.h>#include  
 <linux/netfilter\_ipv6/ip6\_tables.h>#include <linux/slab.h>#include <net/ipv6.h>  
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 Copyright (C) 2000-2004 Russell King \* \* DMA uncached mapping support.  
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 <linux/interrupt.h>#include <linux/dma-noncoher  
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Copyright (C) 2000 by Bart Hartgers <bart@etpmod.phys.tue.nl>, \* Jaroslav Kysela <perex@perex.cz>  
Copyright (C) 2000 by Colin Ngam \*/#ifndef \_ASM\_SN\_LAUNCH\_H#define \_ASM\_SN\_LAUNCH\_H  
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Copyright (C) 2000 by Tim Waugh (added LPSETTIMEOUT ioctl) \*/  
Copyright (c) 2000 by Zach Brown <zab@zabbo.net> \* Takashi Iwai <tiwai@suse.de> \* \* Most of the hardware init stuffs are based on  
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Copyright (C) 2000 Dave Kennedy \*/#ifndef \_\_ASM\_PARISC\_FLOPPY\_H#define \_\_ASM\_PARISC\_FLOPPY\_H  
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 cache and TLB \* functions on the arm926.  
 Copyright (C) 2000 Deep Blue Solutions Ltd. \*/#ifndef ASM\_ARM\_HARDWARE\_SERIAL\_AMBA\_H#define ASM\_ARM\_HARDWARE\_SERIAL\_AMBA\_H  
 Copyright (C) 2000 Deep Blue Solutions Ltd \*/#ifndef ASM\_GLUE\_PROC\_H#define ASM\_GLUE\_PROC\_H  
 Copyright (C) 2000 Deep Blue Solutions Ltd \*/#ifndef ASM\_PROCFNS\_H#define \_\_ASM\_PROCFNS\_H  
 Copyright (C) 2000 Deep Blue Solutions Ltd. \*/#ifndef LINUX\_SERIAL\_CORE\_H#define LINUX\_SERIAL\_CORE\_H  
 Copyright (C) 2000 Deep Blue Solutions Ltd \*/#include <linux/clk.h>#include <linux/clocksource.h>#include <linux/clockchips.h>#include  
 <linux/err.h>#include <linux/interrupt.h>#include <linux/i  
 Copyright (C) 2000 Deep Blue Solutions Ltd \*/#include <linux/kernel.h>#include <linux/io.h>#include <linux/sched\_clock.h>  
 Copyright (C) 2000 Deep Blue Solutions Ltd. \* \* This is a generic driver for ARM AMBA-type serial ports. They \* have a lot of 16550-like features, but  
 are not register compatible. \* Note that alt  
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 Copyright (C) 2000 Franz Sirl. \* \* Adapted to ADB changes and support for more devices by \* Benjamin Herrenschmidt. Adapted from code in MkLinux \*  
 and reworked.  
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 Copyright (C) 2000 Grant Grundler, Hewlett-Packard\*/#ifndef \_PARISC\_PTRACE\_H#define \_PARISC\_PTRACE\_H  
 Copyright (C) 2000 Grant Grundler, Hewlett-Packard\*/#ifndef \_UAPI\_PARISC\_PTRACE\_H#define \_UAPI\_PARISC\_PTRACE\_H

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 Copyright (C) 2000 Greg Banks, Mitch Davis \* C API to interface to the standard LinuxSH BIOS \* usually from within the early stages of kernel boot.  
 \*/extern void sh\_bios\_console\_write(const char \*  
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 Copyright (c) 2000 Jaroslav Kysela <perex@perex.cz> \* \* This code also contains alpha support for SIS 735 chipsets provided \* by Mike Pieper  
 <mptei@users.sourceforge.net>. We have no datasheet  
 Copyright (c) 2000 Jaroslav Kysela <perex@perex.cz> \* \* This is modified (by Sasha Khapyorsky <sashak@alsa-project.org>) version \* of ALSA ICH  
 sound driver intel8x0.c .  
 Copyright (c) 2000 Jaroslav Kysela <perex@perex.cz> \* Tjeerd.Mulder <Tjeerd.Mulder@fujitsu-siemens.com> \* 2002 Takashi  
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 implementations.  
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 Copyright (C) 2000 MIPS Technologies, Inc. \* \* A complete emulator for MIPS coprocessor 1 instructions. This is \* required for #float( switch) or  
 #float(trap), where it catches all \* COP1 instruc  
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 Copyright (C) 2000 MIPS Technologies, Inc. \*/#ifndef \_ASM\_EXEC\_H#define \_ASM\_EXEC\_H  
 Copyright (C) 2000 MIPS Technologies, Inc. \*/#ifndef \_ASM\_IRQFLAGS\_H#define \_ASM\_IRQFLAGS\_H  
 Copyright (C) 2000 MIPS Technologies, Inc. \*/#ifndef \_ASM\_SWITCH\_TO\_H#define \_ASM\_SWITCH\_TO\_H  
 Copyright (C) 2000 MIPS Technologies, Inc. \*/#include <asm/irqflags.h>#include <asm/hazards.h>#include <linux/compiler.h>#include  
 <linux/preempt.h>#include <linux/export.h>#include <linux/strin  
 Copyright (C) 2000 MIPS Technologies, Inc. \*/#include <linux/compat.h>#include <linux/types.h>#include <linux/sched.h>#include  
 <linux/mm.h>#include <linux/kbuild.h>#include <linux/suspend.h>#i  
 Copyright (C) 2000 MIPS Technologies, Inc. \* written by Carsten Langgaard, carsten@mips.com \*/#include <asm/asm.h>#include  
 <asm/cachectl.h>#include <asm/mipsregs.h>#include <asm/asm-offsets.  
 Copyright (C) 2000 MIPS Technologies, Inc. \* written by Carsten Langgaard, carsten@mips.com \*/#include <asm/asm.h>#include  
 <asm/export.h>#include <asm/asm-offsets.h>#include <asm/mipsregs.h>  
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 Copyright (C) 2000 Netgem S.A. \* converted to the generic Reed-Solomon library by Thomas Gleixner <tgx@linutronix.de> \* \* Interface to generic  
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 Copyright (C) 2000 Philipp Rumpf <prumpf@tux.org> \* - based on skeletonfb, which was \* Created 28 Dec 1997 by Geert Uyterhoeven \* - HP Xhp  
 cfb-based X11 window driver for XFree86  
 Copyright (C) 2000 Philipp Rumpf (prumpf@tux.org) \* \* Cache and TLB management  
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 Copyright (C) 2000 Russell King \* \* ad799x.c  
 Copyright (C) 2000 Russell King \* \* Driver for max1363 and similar chips.  
 Copyright (C) 2000 Russell King \*/#include <linux/export.h>#include <linux/sched.h>#include <linux/string.h>#include <linux/cryptohash.h>#include  
 <linux/delay.h>#include <linux/in6.h>#include  
 Copyright (C) 2000 Russell King \*/#include <linux/kernel.h>#include <linux/sched/signal.h>#include <linux/sched/task\_stack.h>#include  
 <linux/mm.h>#include <linux/elf.h>#include <linux/smp.h>#i  
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 Copyright (C) 2000 Serguei Miridonov <mirsev@cicese.mx> \* \* Changes by Ronald Bultje <rbultje@ronald.bitfreak.net> \* - moved over to  
 linux>=2.4.x i2c protocol (9/9/2002)  
 Copyright (C) 2000 Serguei Miridonov <mirsev@cicese.mx> \* - some corrections for Pinnacle Systems Inc. DC10plus card. \* \* Changes by Ronald  
 Bultje <rbultje@ronald.bitfreak.net> \* - moved ove  
 Copyright (C) 2000 Serguei Miridonov <mirsev@cicese.mx> \* \* This code was modify/ported from the saa7111 driver written \* by Dave Perks.  
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 Copyright (C) 2000 Silicon Graphics, Inc. \* Modified for further R[236]000 support by Paul M. Antoine, 1996. \* Kevin D. Kissell, kevink@mips.com and  
 Carsten Langgaard, carstenl@mips.com  
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 Copyright (C) 2000 Silicon Graphics, Inc. \* Written by Ulf Carlsson (ulfc@enr.sgi.com) \*/#include <linux/compiler.h>#include <linux/mm.h>#include  
 <linux/errno.h>#include <linux/file.h>#include  
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```

Copyright (C) 2000 Takashi Iwai <tiwai@suse.de> * * Generic memory management routines for soundcard memory allocation
Copyright (C) 2000 Takashi Iwai <tiwai@suse.de> * * Proc interface for Emu8k/Emu10k1 WaveTable synth
Copyright (C) 2000 Takashi Iwai <tiwai@suse.de> * * Routines for control of EMU10K1 WaveTable synth
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Copyright (C) 2000 Takaya Kinjo <t-kinjo@tc4.so-net.ne.jp> *
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Copyright (C) 2000 Tilmann Bitterberg * (tilmann@bitterberg.de) * * RTAS (Runtime Abstraction Services) stuff * Intention is to provide a clean user
interface * to use the RTAS.
Copyright (C) 2000 Toshiba Corporation */#ifndef __ASM_TXX9_TX3927_H#define __ASM_TXX9_TX3927_H
Copyright (C) 2000 Trond Myklebust * * NFS page cache wrapper.
Copyright (C) 2000 Ulf Carlsson * * At this time Linux/MIPS64 only supports syscall tracing, even for 32-bit * binaries.
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Copyright (C) 2000 Uros Bizjak <uros@kss-loka.si> */
Copyright (C) 2000 VA Linux Co
Copyright (C) 2000 Vinh Truong (vinh.truong@eng.sun.com) * VT - Add all ioctl commands and environment status definitions * VT - Add application
note */#ifndef _SPARC64_ENVCTRL_H#define _SPARC64
Copyright (C) 2000 Vinh Truong (vinh.truong@eng.sun.com) * VT - The implementation is to support Sun Microelectronics (SME) platform *
environment monitoring. SME platforms use pcf8584 as
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Copyright (C) 2000 William Greathouse (wgreathouse@smva.com)
Copyright (C) 2000 * William Greathouse (wgreathouse@smva.com) * * This program is largely derived from work by the linux-usb group * and
associated source files. Please see the usb/serial
Copyright (C) 2000 Wolfgang Grandegger (wolfgang@ces.ch) * * This driver is for the device MCT USB-RS232 Converter (25 pin, Model No. * U232-P25)
from Magic Control Technology Corp. (there is also
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belkin_sa.[ch]). All of the information about the devic
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Copyright (C) 2001-2010 GUAN Xue-tao * * By Ross Biro 1/23/92
Copyright (C) 2001-2010 GUAN Xue-tao * * Debugging macro include header
Copyright (C) 2001-2010 Guan Xuetao */#define FPSCR          s31
Copyright (C) 2001-2010 GUAN Xue-tao * * Delay routines, using a pre-computed "loops_per_second" value.
Copyright (C) 2001-2010 GUAN Xue-tao * * Do not include any C declarations in this file - it is included by * assembler source.
Copyright (C) 2001-2010 GUAN Xue-tao * * Generate definitions needed by assembly language modules. * This code generates raw asm output which is
post-processed to extract * and format the required
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Copyright (C) 2001-2010 GUAN Xue-tao */#ifndef __MACH_PUV3_DMA_H__#define __MACH_PUV3_DMA_H__
Copyright (C) 2001-2010 GUAN Xue-tao */#ifndef __MACH_PUV3_MEMORY_H__#define __MACH_PUV3_MEMORY_H__
Copyright (C) 2001-2010 GUAN Xue-tao */#ifndef __PUV3_PM_H__#define __PUV3_PM_H__
Copyright (C) 2001-2010 GUAN Xue-tao */#ifndef __UNICORE_CACHEFLUSH_H__#define __UNICORE_CACHEFLUSH_H__
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Copyright (C) 2001-2010 GUAN Xue-tao */#ifndef __UNICORE_CPU_SINGLE_H__#define __UNICORE_CPU_SINGLE_H__
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Copyright (C) 2001-2010 GUAN Xue-tao */#ifndef __UNICORE_IRQ_H__#define __UNICORE_IRQ_H__
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Copyright (C) 2001-2010 GUAN Xue-tao */#ifndef __UNICORE_PCI_H__#define __UNICORE_PCI_H__
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Copyright (C) 2001-2010 GUAN Xue-tao */#ifndef __UNICORE_PGTABLE_H__#define __UNICORE_PGTABLE_H__
Copyright (C) 2001-2010 GUAN Xue-tao */#ifndef __UNICORE_PGTABLE_HWDEF_H__#define __UNICORE_PGTABLE_HWDEF_H__
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Copyright (C) 2001-2010 GUAN Xue-tao */#ifndef __UNICORE_TLBFLUSH_H__#define __UNICORE_TLBFLUSH_H__
Copyright (C) 2001-2010 GUAN Xue-tao */#ifndef __UNICORE_TLB_H__#define __UNICORE_TLB_H__
Copyright (C) 2001-2010 GUAN Xue-tao */#ifndef __UNICORE_TRAP_H__#define __UNICORE_TRAP_H__
Copyright (C) 2001-2010 GUAN Xue-tao */#ifndef __UNICORE_UACCESS_H__#define __UNICORE_UACCESS_H__
Copyright (C) 2001-2010 GUAN Xue-tao */#include <asm/hwdef-copro.h>
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/console.h>#include <linux/init.h>#include <linux/string.h>#include <mach/ocd.h>
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/errno.h>#include <linux/signal.h>#include <linux/personality.h>#include
<linux/uaccess.h>#include <linux/tracehook.h>#include <linux/elf.h>
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/extable.h>#include <linux/signal.h>#include <linux/mm.h>#include
<linux/hardirq.h>#include <linux/init.h>#include <linux/kprobes.h>#include
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/extable.h>#include <linux/uaccess.h>
Copyright (C) 2001-2010 Guan Xuetao */#include <linux/init.h>#include <linux/errno.h>#include <linux/interrupt.h>#include <linux/irq.h>#include
<linux/timex.h>#include <linux/clockchips.h>
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/init.h>#include <linux/linkage.h>#include <asm/assembler.h>#include
<asm/hwcap.h>#include <asm/pgtable-hwdef.h>#include <asm/pgtable.h>
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/init.h>#include <linux/linkage.h>#include <asm/assembler.h>#include
<asm/page.h>#include <asm/tlbflush.h>#include "proc-macros.S"
Copyright (C) 2001-2010 Guan Xuetao */#include <linux/init.h>#include <linux/module.h>#include <linux/suspend.h>#include
<linux/errno.h>#include <linux/slab.h>#include <linux/io.h>
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/kernel_stat.h>#include <linux/module.h>#include <linux/signal.h>#include
<linux/ioport.h>#include <linux/interrupt.h>#include <linux/irq.h>
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/linkage.h>#include <asm/assembler.h>
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/linkage.h>#include <asm/assembler.h>#include <asm/errno.h>
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Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/linkage.h>#include <mach/memory.h>
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/mm.h>#include <linux/gfp.h>#include <linux/highmem.h>

```

```

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<linux/slab.h>#include <linux/mm.h>#include <linux/sem.h>#include <linux/
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h>#include <linux/kernel.h>#include <linux/errno.h>#include <linux/init.h>#include
<linux/mman.h>#include <linux/nodemask.h>#inclu
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h>#include <linux/kernel.h>#include <linux/stddef.h>#include
<linux/ioport.h>#include <linux/delay.h>#include <linux/utsname.h>#in
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Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h>#include <linux/mm.h>#include <linux/pagemap.h>
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h>#include <linux/moduleloader.h>#include <linux/kernel.h>#include
<linux/mm.h>#include <linux/elf.h>#include <linux/vmalloc.h>#in
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h>#include <linux/sched.h>#include <linux/personality.h>#include
<linux/binfmts.h>#include <linux/elf.h>
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h>#include <linux/sched.h>#include <linux/sched/debug.h>#include
<linux/stacktrace.h>
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h>#include <linux/sched.h>#include <linux/string.h>#include
<linux/cryptohash.h>#include <linux/delay.h>#include <linux/in6.h>#inc
Copyright (C) 2001-2010 GUAN Xue-tao */#include <linux/module.h>#include <linux/types.h>#include <linux/kernel.h>#include
<linux/signal.h>#include <linux/sched/signal.h>#include <linux/init.h>
Copyright (C) 2001-2010 GUAN Xue-tao */#include <stdarg.h>
Copyright (C) 2001-2010 Guan Xuetao *//* in FPGA, no GPIO support */
Copyright (C) 2001-2010 GUAN Xue-tao * * IP checksum routines
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Copyright (C) 2001-2010 GUAN Xue-tao * * Note: this file should not be included by non-asm/.h files
Copyright (C) 2001-2010 GUAN Xue-tao */OUTPUT_ARCH(unicore32)ENTRY(_start)SECTIONS
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Copyright (C) 2001-2010 GUAN Xue-tao * * PCI bios-type initialisation for PCI machines
Copyright (C) 2001-2010 GUAN Xue-tao * * This file contains the hardware definitions for PKUnity architecture
Copyright (C) 2001-2010 GUAN Xue-tao * * This is the "shell" of the UniCore-v2 processor support.
Copyright (C) 2001-2010 GUAN Xue-tao * * 'traps.c' handles hardware exceptions after we have saved some state. * Mostly a debugging aid, but will
probably kill the offending process.
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<linux/blkdev.h>#include <linux/ui.h>#include <linux/iocontext.h>#
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Copyright (C) 2001 Jens Axboe <axboe@suse.de> */#include <linux/kernel.h>#include <linux/errno.h>#include <linux/string.h>#include
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```



```

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information from the state machine * upwards to
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Copyright (C) 2001 MIPS Technologies, Inc. */#include <linux/errno.h>#include <asm/asm.h>#include <asm/asmmacro.h>#include
<asm/irqflags.h>#include <asm/mipsregs.h>#include <asm/regdef.h>#incl
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```

```

<linux/types.h>#include <linux/reboot.h>#include <linux/delay.h>
    Copyright (C) 2001 MIPS Technologies, Inc. */#include <linux/kernel.h>#include <linux/sched/signal.h>#include <linux/signal.h>#include
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    Copyright (C) 2001 MontaVista Software, Inc. */#include <asm/system_misc.h>#include "hardware.h"#include "iop3xx.h"
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<linux/gpio.h>#include <linux/types.h>#include <linux/init
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<linux/vmalloc.h>#include <linux/etherdevice.h>#include <linux/io-64-nonat
Copyright(c) 2002-2010 Exar Corp.\n");   DBG_PRINT(ERR_DBG, "%s: Neterion %s (rev %d)\n", dev->name,      sp->product_name, pdev-
>revision);DBG_PRINT(ERR_DBG, "%s: Driver version %s\n", dev->name,
Copyright(c) 2002-2010 Exar Corp.\n");   pr_info("Driver version: %s\n", DRV_VERSION);
Copyright (c) 2002-2010, Intel Corporation.
Copyright (C) 2002 - 2010 Paul Mundt
Copyright (C) 2002 - 2010 Paul Mundt */#include <linux/kernel.h>#include <linux/ptrace.h>#include <linux/hardirq.h>#include <linux/init.h>#include
<linux/spinlock.h>#include <linux/kallsyms.h>
Copyright (C) 2002 - 2010 Paul Mundt */#include <linux/mm.h>#include <linux/init.h>#include <linux/mutex.h>#include <linux/fs.h>#include
<linux/smp.h>#include <linux/highmem.h>#include <linux
Copyright (C) 2002 - 2010 Paul Mundt */#include <linux/screen_info.h>#include <linux/ioport.h>#include <linux/init.h>#include
<linux/initrd.h>#include <linux/console.h>#include <linux/root_dev.

```

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 Copyright (C) 2002 - 2012 Paul Mundt \*/#include <linux/kallsyms.h>#include <linux/ftrace.h>#include <linux/debug\_locks.h>#include <linux/sched/debug.h>#include <linux/sched/task\_stack.h>#inclu  
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 Copyright (C) 2002 Anton Blanchard <anton@au.ibm.com>, IBM \*/#define pr\_fmt(fmt) "numa: " fmt  
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 Copyright (C) 2002 ARM Limited, All Rights Reserved  
 Copyright (c) 2002 ARM Limited. \*/#include <linux/init.h>#include <linux/device.h>#include <linux/smp.h>#include <linux/io.h>#include <linux/irqchip/arm-gic.h>  
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 Copyright (C) 2002 ARM Ltd. \*/#include <linux/smp.h>#include <linux/io.h>#include <linux/of\_address.h>#include <linux/delay.h>  
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 Copyright (C) 2002 Benjamin Herrenschmidt (benh@kernel.crashing.org) \* \* Todo: - add support for the OF persistent properties  
 Copyright (c) 2002 Brian Bonnlander, Bill Soudan  
 Copyright (C) 2002 Broadcom Corporation \*/#ifndef \_\_ASM\_SIBYTE\_CARMEL\_H#define \_\_ASM\_SIBYTE\_CARMEL\_H  
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Copyright (C) 2002 by Theodore Ts */#include <linux/types.h>#include <linux/fs.h>#include <linux/f2fs_fs.h>#include <linux/cryptohash.h>#include
<linux/pagemap.h>#include <linux/unicode.h>
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Copyright (C) 2002, David McCullough <davidm@snapgear.com> * modified for 2.6 by Hyok S. Choi <hyok.choi@samsung.com> */typedef struct {
unsigned long end_brk;#ifndef CONFIG_BINFMT_ELF_FDPICuns
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<linux/delay.h>#include <linux/tty.h>#include <linux/t
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```

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 Copyright (C) 2002 Dominik Brodowski <devel@brodo.de>  
 Copyright (C) 2002 Dominik Brodowski <devel@brodo.de> \* \* TBD: \* 1. Support more than one IRQ resource entry per link device (index). \*

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 <linux/platform\_data/sa11x0-serial.h>#include <linux/platform\_devic  
 Copyright (C) 2002 Hewlett-Packard Company \*/#include <linux/module.h>#include <linux/kernel.h>#include <linux/device.h>#include  
 <linux/errno.h>#include <linux/init.h>  
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 <asm/byteorder.h>#include <linux/crypto.h>#include <li  
 Copyright (c) 2002 James Morris <jmorris@intercode.com.au> \*/#include <linux/types.h>#include <linux/crypto.h>#include <linux/errno.h>#include  
 <linux/string.h>#include "internal.h"  
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 McKinley \* 3/08/02 davidm Some more tweaking  
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 Copyright (C) 2002 Maciej W. Rozycki \*/#ifndef \_ASM\_MC146818RTC\_H#define \_ASM\_MC146818RTC\_H  
 Copyright (C) 2002 Maciej W. Rozycki \*/#ifndef \_ASM\_PGTABLE\_BITS\_H#define \_ASM\_PGTABLE\_BITS\_H  
 Copyright (C) 2002 Maciej W. Rozycki \*/#ifndef \_ASM\_WBFLUSH\_H#define \_ASM\_WBFLUSH\_H  
 Copyright (C) 2002 Maciej W. Rozycki \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/sched.h>#include  
 <linux/sched/debug.h>#include <linux/sched/signal.h>#include <asm/traps.h>  
 Copyright (C) 2002 Maciej W. Rozycki \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/signal.h> /\* for SIGBUS \*/#include  
 <linux/sched.h> /\* schow\_regs(), force\_sig() \*/#include  
 Copyright (C) 2002 Maciej W. Rozycki \*/#include <linux/kernel.h>#include <linux/sched.h>#include <linux/smp.h>#include <linux/mm.h>  
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<linux/types.h>#include <linux/time.h>
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<asm/freq.h>#include <asm/io.h>
Copyright (C) 2002 M. R. Brown <mrbrown@linux-sh.org> */#include <linux/kernel.h>#include <linux/init.h>#include <linux/profile.h>#include
<linux/timex.h>#include <linux/sched.h>#include <lin
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flags, compilation options, and * global var
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Copyright (C) 2002 Paul Mackerras, IBM Corp. */#include <asm/processor.h>#include <asm/ppc_asm.h>#include <asm/export.h>#include <asm/asm-
compat.h>#include <asm/feature-fixups.h>
Copyright (C) 2002 Paul Mackerras, IBM Corp. */#include <asm/processor.h>#include <asm/ppc_asm.h>#include <asm/export.h>#include <asm/asm-
compat.h>#include <asm/feature-fixups.h>#include <asm/k
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Copyright (C) 2002 Paul Mundt */#include <linux/init.h>#include <linux/of_fdt.h>#include <linux/smp.h>#include <linux/io.h>#include
<asm/processor.h>#include <asm/cache.h>
Copyright (C) 2002 Paul Mundt */#include <linux/signal.h>#include <linux/sched.h>#include <linux/kernel.h>#include <linux/errno.h>#include
<linux/string.h>#include <linux/types.h>#include <li
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Copyright (C) 2002 Peter Hettkamp <peter.hettkamp@htp-tel.de> based on work
Copyright (C) 2002 Peter Hettkamp <peter.hettkamp@htp-tel.de> * * large parts based on the bttv driver
Copyright (C) 2002 Pete Zaitcev (zaitcev@yahoo.com)
Copyright (c) 2002 Pete Zaitcev (zaitcev@yahoo.com) * - Incorporating suggestions made by Linus Torvalds and Dave Miller */
Copyright (C) 2002 Pete Zaitcev <zaitcev@yahoo.com> * * The algorithm to search a zero bit string is geared towards its application. * We expect a
couple of fixed sizes of requests, so a rotating c
Copyright (c) 2002 Pete Zaitcev (zaitcev@yahoo.com) * * We used to warn all over the code: DO NOT USE prom_printf(), * and yet people do. Anton's

```

```

banking code was outputting banks * with prom_prin
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Copyright (C) 2002 Ralf Baechle
Copyright (C) 2002 Ralf Baechle DO1GRB (ralf@gnu.org) */#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include
<linux/in.h>#include <linux/kernel.h>#include <linux/ji
Copyright (C) 2002 Ralf Baechle DO1GRB (ralf@gnu.org) */#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include
<linux/in.h>#include <linux/kernel.h>#include <linux/mo
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Copyright (C) 2002, Rohit Seth <rohit.seth@intel.com>
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copyright (c) 2002 Romain Lievin <roms@lpg.ticalc.org>.\n"           "Based on the source code from Roman Zippel.\n";
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Copyright (C) 2002 Roman Zippel <zippel@linux-m68k.org> */
Copyright (C) 2002 Roman Zippel <zippel@linux-m68k.org> * * Introduced single menu mode (show all sub-menus in one large tree). * 2002-11-06 Petr
Baudis <pasky@ucw.cz>
Copyright (C) 2002 Roman Zippel <zippel@linux-m68k.org>.\n"
Copyright (C) 2002 Roman Zippel <zippel@linux-m68k.org> */%option nostdinit noyywrap never-interactive full ecs%option 8bit nodefault yylineno%x
ASSIGN_VAL_HELP_STRING
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Copyright (C) 2002 Russell King * *   Commonly used scsi driver functions.
Copyright (C) 2002 Russell King * *   Experimentation shows that on a StrongARM, it appears to be faster *   to use the "invalidate whole tlb" rather than
"invalidate single * tlb" for this.
Copyright (C) 2002 Russell King. */#ifndef __ASM_ARM_THREAD_INFO_H#define __ASM_ARM_THREAD_INFO_H
Copyright (C) 2002 Russell King */#include <linux/module.h>#include <linux/init.h>#include <linux/input.h>#include <linux/serio.h>#include
<linux/errno.h>#include <linux/interrupt.h>#include <
Copyright (C) 2002 Russell King. */#include <linux/module.h>#include <linux/serio.h>#include <linux/errno.h>#include <linux/interrupt.h>#include
<linux/ioport.h>#include <linux/device.h>#inclu
Copyright (C) 2002 Russell King. *   Modified for nommu by Hyok S. Choi * *   Module allocation method suggested by Andi Kleen.
Copyright (C) 2002 Sam Creasey <sammy@sammy.net>
Copyright (C) 2002 SGI - Silicon Graphics, Inc <linux-xfss@oss.sgi.com> */#ifndef _POSIX_ACL_XATTR_H#define _POSIX_ACL_XATTR_H
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 benq gspca subdriver which is: \*

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 Copyright (c) 2002 STMicroelectronics Ltd \* Modified from memcpy.S and micro-optimised for SH4 \* Stuart Menefy (stuart.menefy@st.com) \*  
 Copyright (C) 2002 Stuart Menefy \*/#include <linux/io.h>#include <linux/irq.h>#include <linux/interrupt.h>#include <linux/signal.h>#include  
 <cpu/irq.h>#include <asm/page.h>  
 Copyright (C) 2002 Stuart Menefy (stuart.menefy@st.com) \*/#include <asm/cache.h>#include <asm/tlb.h>#include <cpu/mmu\_context.h>#include  
 <cpu/registers.h>  
 Copyright (C) 2002 Sun Microsystems, Inc. \* Tim Hockin <thockin@sun.com> \* \* A few notes on this MTD map: \* \* This was developed with a small  
 number of SCB2 boards to test on.  
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 Copyright (C) 2002 \* Tapio Laxström (tapio.laxstrom@iptime.fi) \*/#include <linux/kernel.h>#include <linux/errno.h>#include <linux/slab.h>#include  
 <linux/module.h>#include <linux/usb.h>#include  
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 Copyright (c) 2002 Thibaut Varene <varenet@parisc-linux.org>  
 Copyright (c) 2002 Thibaut Varene <varenet@parisc-linux.org> \* \* Pieces of code based on linux-2.4's hp\_mouse.c & hp\_keyb.c  
 Copyright (C) 2002 Thibaut Varene <varenet@parisc-linux.org> \* \* TODO:- handle processor number on SMP systems (Reporting Entity ID) \*  
 handle message ID \* - handle timestamps  
 Copyright (c) 2002 Thiemo Seufer \*/#include <linux/tty.h>#include <linux/major.h>#include <linux/init.h>#include <linux/console.h>#include  
 <linux/fs.h>#include <asm/setup.h>#include <asm/sgial  
 Copyright (C) 2002 Thomas Gleixner (tglx@linutronix.de) \*/  
 Copyright (c) 2002 Thomas Gleixner <tgxl@linutronix.de> \* \* Converted to platform driver by Janusz Krzysztofik <jkrzysz@tis.icnet.pl> \* Partially  
 stolen from plat\_nand.c  
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 Copyright (C) 2002 Trond Myklebust \* \* structure passed from user-space to kernel-space during an nfsv4 mount  
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 Copyright (C) 2002 Vivien Chappelier \*/#include <linux/module.h>#include <linux/init.h>#include <linux/serio.h>#include <linux/errno.h>#include  
 <linux/interrupt.h>#include <linux/ioport.h>#include  
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 along with other modification \* to bet  
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__ASM_MACH_MSP71XX_CPU_FEATURE_OVERRIDES_H
Copyright (C) 2003, 04, 07 Ralf Baechle (ralf@linux-mips.org) */#ifndef __ASM_MACH_SIBYTE_CPU_FEATURE_OVERRIDES_H#define
__ASM_MACH_SIBYTE_CPU_FEATURE_OVERRIDES_H
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Copyright (C) 2003, 04 Ralf Baechle (ralf@linux-mips.org) */#include <linux/kernel.h>#include <linux/mm.h>#include <linux/pci.h>#include
<linux/init.h>#include <linux/types.h>#include <linux/dm
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* * Additions/Fixe
Copyright (C) 2003, 06 Ralf Baechle (ralf@linux-mips.org) */#include <linux/bcd.h>#include <linux/i8253.h>#include <linux/init.h>#include
<linux/irq.h>#include <linux/kernel.h>#include <linux/i
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__ASM_MACH_IP22_CPU_FEATURE_OVERRIDES_H
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Copyright (C) 2003, 2004, 2005 Paul Mundt */#include <linux/init.h>#include <linux/module.h>#include <linux/spinlock.h>#include
<linux/proc_fs.h>#include <linux/seq_file.h>#include <linux/list
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<linux/ptrace.h>#include <linux/stddef.h>
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Copyright (C) 2003-2004 Hewlett-Packard Co * David Mosberger-Tang <davidm@hpl.hp.com> */extern const struct dma_map_ops *dma_ops;
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```

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<linux/interrupt.h>#include <mach/sysasic.h>#include <mach/dma.h>#inclu
Copyright (C) 2003, 2004 Paul Mundt * * User space memory access functions
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Copyright (C) 2003, 2004 Richard Curnow */
Copyright (C) 2003, 2004 Richard Curnow */#include <linux/errno.h>#include <linux/init.h>#include <linux/sys.h>#include <cpu/registers.h>#include
<asm/processor.h>#include <asm/unistd.h>#incl
Copyright (C) 2003, 2004 Richard Curnow */#include <linux/sched.h>#include <linux/sched/debug.h>#include <linux/kernel.h>#include
<linux/string.h>#include <linux/errno.h>#include <linux/ptrace
Copyright (C) 2003, 2004 Richard Curnow */#include <linux/threads.h>#include <asm/processor.h>#include <asm/page.h>
Copyright (C) 2003, 2004 Richard Curnow * * Provides a debugfs file that lists out the ASIDs currently associated * with the processes.
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Copyright (c) 2003, 2004 Zultys Technologies */#include <stdarg.h>#include <stddef.h>#include "types.h"#include "elf.h"#include "string.h"#include
"stdio.h"#include "page.h"#include "ops.h"#
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<linux/device.h>#include <linux/mutex.h>#include <linux/err.h>#in
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<linux/interrupt.h>#include <linux/module.h>#include <linux/spinlock.h>#i
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<asm/assembler.h>#include <asm/asm-offsets.h>#include <asm/hwcap.h>#incl
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Copyright (C) 2003-2006, Marvell International Ltd. */#include <linux/spinlock.h>#include <linux/device.h>#include <linux/kthread.h>#include
<net/mac80211.h>

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Copyright (C) 2003 - 2006 Paul Mundt */#include <linux/init.h>#include <linux/kernel.h>#include <linux/module.h>#include
<linux/interrupt.h>#include <asm/cacheflush.h>#include <mach/sysasic.h>
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<linux/platform_device.h>#include <linux/of.h>#include <linux/of_device
Copyright (C) 2003 - 2007 Paul Mundt */#include <linux/init.h>#include <linux/io.h>#include <linux/kernel.h>#include <cpu/irq.h>
Copyright (C) 2003 - 2007 Paul Mundt */#include <linux/init.h>#include <linux/io.h>#include <linux/string.h>#include <asm/processor.h>#include
<asm/cache.h>#include <asm/tlb.h>
Copyright (C) 2003 - 2007 Tensilica Inc. */
Copyright (C) 2003-2007 Yoichi Yuasa <yuasa@linux-mips.org> */#include <linux/cpu.h>#include <linux/errno.h>#include <linux/init.h>#include
<linux/ioport.h>#include <linux/kernel.h>#include <l
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<linux/io.h>#include <linux/ioport.h>#include <linux/in

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offsets.h>#include <asm/thread\_info.h>#include <asm/unistd.h>#  
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Copyright (C) 2003 by Ralf Baechle \*/#include <linux/export.h>#include <linux/init.h>#include <linux/mm.h>#include <asm/fixmap.h>#include <asm/pgtable.h>#include <asm/pgalloc.h>#include <asm/t  
Copyright (C) 2003 by Ralf Baechle \*/#include <linux/init.h>#include <linux/mm.h>#include <linux/memblock.h>#include <linux/highmem.h>#include <asm/fixmap.h>#include <asm/pgtable.h>#include <a  
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 <linux/irqchip.h>#include <linux/of\_irq.h>#include <linux/of\_addr  
 Copyright (c) 2003 \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include <linux/kernel.h>  
 Copyright (c) 2003 Dimitromanolakis Apostolos <apostol@cs.utoronto.ca>  
 Copyright (c) 2003 Dimitromanolakis Apostolos <apostol@cs.utoronto.ca> \* \* version 0.82: Stable / not all features work yet (no communication with  
 AC97 secondary) \* added 64x/128x oversamp  
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 Copyright (c) 2003 Erik Thyren <erth7411@student.uu.se> \* \* Based on usbmouse.c (Vojtech Pavlik) and xpad.c (Marko Friedemann)  
 Copyright (C) 2003 \* Ethan Benson <erbenson@alaska.net> \* partially derived from linux/fs/ext2/ioctl.c  
 Copyright (c) 2003 Evgeniy Polyakov <zbr@ioremmap.net> \*/  
 Copyright (c) 2003 Felix Domke <tmbinc@elitedvb.net>  
 Copyright (c) 2003 Felix Domke <tmbinc@elitedvb.net> \*/#include <linux/init.h>#include <linux/slab.h>#include <linux/wait.h>#include  
 <linux/fs.h>#include <linux/module.h>#include <linux/usb.h>  
 Copyright (C) 2003 Fenghua Yu <fenghua.yu@intel.com> \* - Change pt\_regs\_off() to make it less dependent on pt\_regs structure. \*/  
 Copyright (C) 2003 Frank Mori Hess <fmhess@users.sourceforge.net> \*/  
 Copyright (C) 2003 Fruhwirth Clemens <clemens@endorphin.org>  
 Copyright (C) 2003 Geert Uytterhoeven <geert@linux-m68k.org> \*/  
 Copyright (C) 2003 Geert Uytterhoeven <geert@linux-m68k.org> \* \* Serial\_console ascii image can be any size, \* but should contain %s to display the  
 version  
 Copyright (C) 2003 Geert Uytterhoeven \* \* Loosely based on drivers/pci/pci-driver.c  
 Copyright (C) 2003 Geert Uytterhoeven \* \* Loosely based on drivers/pci/pci-sysfs.c  
 Copyright (C) 2003 Georges Menie \* \* This driver assumes an already configured controller (e.g. from config.c) \* Keep the code clean of board specific  
 initialization.  
 Copyright (c) 2003 Gerd Knorr  
 Copyright (C) 2003 Greg Kroah-Hartman <greg@kroah.com>  
 Copyright (C) 2003 Greg Kroah-Hartman <greg@kroah.com>  
 Copyright (C) 2003 Greg Kroah-Hartman (greg@kroah.com)  
 Copyright (c) 2003 Greg Kroah-Hartman (greg@kroah.com) \* \* and from the Apple USB BCM5974 multitouch driver,  
 Copyright (C) 2003 Guido Guenther <agx@sigxcpu.org> \*/  
 Copyright (c) 2003 Hans-Frieder Vogt <hfvogt@arcor.de>, Based on SMBus 2.0 driver for AMD-8111 IO-Hub  
 Copyright (C) 2003 Heiko Ronsdorf <hero@ihg.uni-duisburg.de> \*/  
 Copyright (C) 2003 Herbert Valerio Riedel <hvr@gnu.org>  
 Copyright (C) 2003 Herbert Valerio Riedel <hvr@gnu.org> \*/  
 Copyright (C) 2003 Hewlett-Packard Co \* Bjorn Helgaas <bjorn.helgaas@hp.com> \* \* 10/30/2001 jbarnes@sgi.com copied much of  
 Stephane's palinfo \* code to create this file \* Oct 23 2003 kaos@sg  
 Copyright (C) 2003 Hewlett-Packard Co \* David Mosberger-Tang <davidm@hpl.hp.com> \*/  
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 Copyright (C) 2003 Hewlett-Packard Co \* David Mosberger-Tang <davidm@hpl.hp.com> \* \* 25-Sep-03 davidm Implement fsys\_rt\_sigprocmask().  
 \* 18-Feb-03 louisk Implement fsys\_gettimeofday(). \* 28-Feb-  
 Copyright (C) 2003 Hewlett-Packard Co \* David Mosberger-Tang <davidm@hpl.hp.com> \*/#include <linux/init.h>#include <linux/string.h>  
 Copyright (C) 2003 Hewlett-Packard Co \* David Mosberger-Tang <davidm@hpl.hp.com> \* \* Loosely based on patch by Rusty Russell.  
 Copyright (C) 2003 Hewlett-Packard Co \* David Mosberger-Tang <davidm@hpl.hp.com> \* \* There are a number of reasons for patching instructions.  
 Rather than duplicating code \* all over the place, we  
 Copyright (c) 2003 Ian Molton <spyro@f2s.com>  
 Copyright (C) 2003 IBM. \*/  
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```

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code is a 1-to-1 copy.
Copyright (C) 2003 IBM Corporation/* */
Copyright (C) 2003 IBM Corp. * * Original driver for 2.2.x by anonymous */
Copyright (C) 2003 IBM. */#ifndef _RPADLPAR_IO_H_#define _RPADLPAR_IO_H_
Copyright (C) 2003 IBM. */#include <linux/kobject.h>#include <linux/string.h>#include <linux/pci.h>#include <linux/pci_hotplug.h>#include
"rpaphp.h"#include "rpadlpar.h"#include "../pci.h"
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<arun.sharma@intel.com> * * 12/07/98 S. Eranian added pt_regs & switch_st
Copyright (c) 2003 International Business Machines, Corp. * * This file is part of the SCTP kernel implementation
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Copyright (C) 2003 James Bottomley <jejb with parisc-linux.org>
Copyright (c) 2003 James Morris <jmorris@intercode.com.au>
Copyright (c) 2003 James Morris <jmorris@intercode.com.au> * * FIXME: deflate transforms will require up to a total of about 436k of kernel * memory
on i386 (390k for compression, the rest for deco
Copyright (c) 2003 James Morris <jmorris@intercode.com.au> * * Todo:
Copyright (C) 2003 Jamie Honan
Copyright (C) 2003 Jamie Honan */
Copyright (C) 2003 Jana Saout <jana@saout.de>
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Copyright (c) 2003 Jim Hague (jim.hague@acm.org) * * based on 2.4 driver:
Copyright (C) 2003 Joerg Dorchain <joerg AT dorchain.net>
Copyright (c) 2003 Joerg Heckenbach * Many improvements by Bret Wallach <bwallac1@san.rr.com> * Color fixes by by Orion Sky Lawlor
<olawlor@acm.org> (2/26/2000) * OV7620 fixes by Charl P. Botha <cp
Copyright (c) 2003 John Homppi (SCO, leave this notice here) * * derived from the Lego USB Tower driver 0.56:
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Copyright (C) 2003 John Williams <jwilliams@itee.uq.edu.au>
Copyright (C) 2003 John Williams <jwilliams@itee.uq.edu.au>
Copyright (C) 2003 Joshua Wise */
Copyright (c) 2003, Jouni Malinen <j@w1.fi>
Copyright (C) 2003 Jozsef Kadlecik <kadlec@netfilter.org> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/netfilter_ipv4/ip_tables.h>#include <linux/slab.h>
Copyright (C) 2003 Jozsef Kadlecik <kadlec@netfilter.org> */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/netfilter_ipv6/ip6_tables.h>#include <linux/slab.h>
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Copyright (C) 2003 Keith M Wesolowski
Copyright (C) 2003 Kevin Thayer <nufan_wfk@yahoo.com> * * Dual output support:
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Copyright (C) 2003 Krzysztof Halasa <khc@pm.waw.pl>#n"
Copyright (C) 2003 Krzysztof Halasa <khc@pm.waw.pl> * * Status:
Copyright (c) 2003 Kyle McMartin <kyle@debian.org> */
Copyright (c) 2003 Kyle McMartin <kyle@debian.org> */#include <crypto/internal/hash.h>#include <linux/kernel.h>#include
<linux/module.h>#include <linux/mm.h>#include <linux/init.h>#include <lin
Copyright (C) 2003 Ladislav Michl
Copyright (C) 2003 Ladislav Michl (ladis@linux-mips.org)
Copyright (C) 2003 Ladislav Michl (ladis@linux-mips.org) */#include <linux/export.h>
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```

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Copyright (C) 2003 Lineo uSolutions, Inc.  
Copyright (C) 2003 Linus Torvalds \* \* Mon Mar 5, 2007: Davide Libenzi <davidel@xmailserver.org> \* Changed ->read() to return a siginfo structure instead of signal number. \* Fixed lockin  
Copyright (C) 2003 Luis R. Rodriguez <mcgrof@ruslug.rutgers.edu>  
Copyright (C) 2003 Luis R. Rodriguez <mcgrof@ruslug.rutgers.edu> \*/  
Copyright (C) 2003 Maciej W. Rozycki  
Copyright (C) 2003 Maciej W. Rozycki \*/#ifndef \_\_ASM\_MIPS\_DEC\_ECC\_H#define \_\_ASM\_MIPS\_DEC\_ECC\_H  
Copyright (c) 2003 Maciej W. Rozycki \* \* include/asm-mips/time.h \* header file for the new style time.c file and time services.  
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Copyright (c) 2003 Matthew Wilcox <matthew@wil.cx> \* \* This file is where we call all the ethtool\_ops commands to get \* the information ethtool needs.  
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Copyright (C) 2003 Megasolution Inc. <matsu@megasolution.jp> \*/#ifndef \_\_TANBAC\_TB0219\_H#define \_\_TANBAC\_TB0219\_H  
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Copyright (C) 2003 MontaVista Software Inc. \* Author: Jun Sun, jsun@mvista.com or jsun@junsun.net \* \* 031022 - [jsun] add run-time configure and partition setup  
Copyright: (C) 2003 Monta Vista Software, Inc. \* \* Based on drivers/serial/8250.c by Russell King.  
Copyright (C) 2003 Montavista Software, Inc \* \* Based on the code from the 2.4 kernel by \* Dale Farnsworth <dfarnsworth@mvista.com> and Kent Borg.  
Copyright (C) 2003 MontaVista Software, Inc. \* \* Maintainer: Imre Kaloz <kaloz@openwrt.org>  
Copyright: (C) 2003 Monta Vista Software, Inc. \* \* Note 1: This driver is made separate from the already too overloaded \* 8250.c because it needs some kirks of its own and that'll make it \* easier  
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Copyright (c) 2003 Neil Brown <neilb@cse.unsw.edu.au>  
Copyright (c) 2003 Nils-Erik Mattsson, Joacim Haggmark, Magnus Erixson \* \* Changes to meet Linux coding standards, to make it meet latest ccid3 draft \* and to make it work as a loadable module in  
Copyright (c) 2003 Nils-Erik Mattsson, Joacim Haggmark, Magnus Erixson \*/#include <linux/types.h>  
Copyright (c) 2003 Nils-Erik Mattsson, Joacim Haggmark, Magnus Erixson \*/#include <linux/types.h>#include <linux/math64.h>#include "../dccc.h"  
Copyright (C) 2003 Nokia Corporation \* Author: Juha Yrjölä <juha.yrjola@nokia.com> \*

```

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__MACH_OMAP1_LCD_DMA_H__
Copyright (C) 2003 Nokia Corporation * Written by Timo Teräs <ext-timo.teras@nokia.com> * * Added support for H2 & H3 Keypad
Copyright (C) 2003 Oliver Endriss
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Copyright (c) 2003 Open Source Development Lab */
Copyright (c) 2003 Open Source Development Lab * * The driver model core calls device_pm_add() when a device is registered. * This will initialize the
embedded device_pm_info object in the device
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Copyright (C) 2003 Paul Andreassen <paul@andreassen.com.au>
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Copyright (C) 2003 Paul Mundt */#ifndef __ASM_CPU_SH2_ADDRSPACE_H#define __ASM_CPU_SH2_ADDRSPACE_H
Copyright (C) 2003 Paul Mundt */#ifndef __ASM_CPU_SH2_CACHE_H#define __ASM_CPU_SH2_CACHE_H
Copyright (C) 2003 Paul Mundt */#ifndef __ASM_CPU_SH2_MMU_CONTEXT_H#define __ASM_CPU_SH2_MMU_CONTEXT_H
Copyright (C) 2003 Paul Mundt */#ifndef __ASM_SH_DREAMCAST_DMA_H#define __ASM_SH_DREAMCAST_DMA_H
Copyright (C) 2003 Paul Mundt */#ifndef __ASM_SH_FLAT_H#define __ASM_SH_FLAT_H
Copyright (C) 2003 Paul Mundt */#ifndef __ASM_SH_TLB_64_H#define __ASM_SH_TLB_64_H
Copyright (C) 2003 Paul Mundt */#ifndef DMA_REGISTER_H#define DMA_REGISTER_H
Copyright (C) 2003 Paul Mundt */#include <linux/sys.h>#include <linux/linkage.h>
Copyright (C) 2003 Paul Mundt <lethal@linux-sh.org> */
Copyright (C) 2003 Paul Mundt <lethal@linux-sh.org> * * Based on files with the following comments: *
Copyright (C) 2003 Paul Mundt <lethal@linux-sh.org> * * This file is part of the LinuxDC project (www.linuxdc.org)
Copyright (C) 2003 Paul Mundt * * Per-interrupt selective. IRLM=0 (Fixed priority) is not * supported being useless without a cascaded interrupt *
controller.
Copyright (c) 2003 Pavel Machek
Copyright (c) 2003 Pavel Machek */
Copyright (C) 2003 Pavel Machek */
Copyright (C) 2003 Pavel Machek (pavel@ucw.cz)
Copyright (C) 2003 Pavel Machek (pavel@ucw.cz) */
Copyright (C) 2003 Pavel Machek (pavel@ucw.cz) * * These routines maintain argument size conversion between 32bit and 64bit * ioctls.
Copyright (C) 2003 Peter Milne, D-TACQ Solutions Ltd * <Peter dot Milne at D hyphen TACQ dot com>
Copyright (C) 2003 Peter Milne, D-TACQ Solutions Ltd * <Peter dot Milne at D hyphen TACQ dot com> * * With acknowledgements to i2c-
algo-ibm_ocp.c by * Ian DaSilva, MontaVista So
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Copyright (C) 2003 Ralf Baechle * 6/2004 pf */#ifndef __ASM_MACH_IP28_CPU_FEATURE_OVERRIDES_H#define
__ASM_MACH_IP28_CPU_FEATURE_OVERRIDES_H
Copyright (C) 2003 Ralf Baechle * * Handler for RM7000 extended interrupts. These are a non-standard * feature so we handle them separately from
standard interrupts.
Copyright (C) 2003 Ralf Baechle */#ifndef __ASM_ASMMACRO_H#define __ASM_ASMMACRO_H
Copyright (C) 2003 Ralf Baechle */#ifndef __ASM_MACH_GENERIC_CPU_FEATURE_OVERRIDES_H#define
__ASM_MACH_GENERIC_CPU_FEATURE_OVERRIDES_H
Copyright (C) 2003 Ralf Baechle */#ifndef __ASM_MACH_NETLOGIC_CPU_FEATURE_OVERRIDES_H#define
__ASM_MACH_NETLOGIC_CPU_FEATURE_OVERRIDES_H
Copyright (C) 2003 Ralf Baechle */#ifndef __ASM_PGTABLE_H#define __ASM_PGTABLE_H
Copyright (C) 2003 Ralf Baechle <ralf@linux-mips.org> *
Copyright (C) 2003 Ralf Baechle <ralf@linux-mips.org> */#ifndef __ASM_I8259_H#define __ASM_I8259_H
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Copyright (C) 2003 Randolph Chung <tausq at debian . org>
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 Copyright (C) 2003 Red Hat, Inc., James Morris <jmorris@redhat.com> \*/#include <linux/kernel.h>#include <linux/slab.h>#include  
 <linux/string.h>#include <linux/spinlock.h>#include <linux/rcupdate  
 Copyright (C) 2003 Red Hat, Inc., James Morris <jmorris@redhat.com> \*/#include <linux/types.h>#include <linux/stddef.h>#include  
 <linux/kernel.h>#include <linux/slab.h>#include <linux/fs.h>#incl  
 Copyright (C) 2003 Red Hat Inc \* \* libata documentation is available via 'make {ps|pdf}docs', \* as Documentation/driver-api/libata.rst  
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 Copyright (C) 2003 Richard Curnow  
 Copyright (C) 2003 Richard Curnow  
 Copyright (C) 2003 Richard Curnow \*/#include <linux/init.h>#include <linux/io.h>#include <asm/processor.h>#include <asm/cache.h>  
 Copyright (C) 2003 Richard Curnow \*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/mm.h>#include <linux/log2.h>#include  
 <asm/mmu\_context.h>#include <asm/processor.h>#include  
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 Copyright (c) 2003 Ron Lee (ron@debian.org) \* cPad driver for kernel 2.4 \*  
 Copyright (C) 2003 Roy Bulter <rbulter@hetnet.nl> \* \* Based on SAA7126 video encoder driver by Gillem & Andreas Oberritter  
 Copyright (C) 2003 Russell King.  
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 Copyright (C) 2003 Russell King. \* \* Generic SSP driver. This provides the generic core for simple \* IO-based SSP applications.  
 Copyright (C) 2003 Russell King \*/#ifndef \_ASM\_GENERIC\_PCI\_H#define \_ASM\_GENERIC\_PCI\_H  
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 definitions \* Machine VR1000 - Memory map definitions  
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 definition  
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 MAX6650 chip. It should \* also work with the MAX6651. It d  
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 <linux/uaccess.h>#include <linux/crc16.h>#include <linux/  
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 <linux/kernel\_stat.h>#include <asm/io.h>#includ  
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<linux/module.h>#include <linux/init.h>  
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various structure changes, and crypt  
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<linux/module.h>#include <linux/workqueue.h>#include <linux/blkd  
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 <linux/mm.h>#include <linux/clk.h>#include <linux/irq.h>#include <lin  
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\_\_IBM\_NEWEMAC\_DEBUG\_H#define \_\_IBM\_NEWEMAC\_DEBUG\_H  
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base\_addr code - moving address assignment to setup.c  
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<linux/dma-mapping.h>#include <linux/memblock.h>#i  
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<linux/module.h>#include <linux/unistd.h>#include  
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<asm/assembler.h>#include <asm/asm-offsets.h>#include <asm/hwcap.h>#incl  
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<asm/assembler.h>#include <asm/hwcap.h>#include <asm/pgtable-hwdef.h>#in  
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 <linux/platform\_device.h>#include <linux/err.h>  
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 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Driver is originally developed by Pavel Sokolov  
 <psokolov@synopsys.com>  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \*/#ifndef \_\_ASMARC\_SETUP\_H#define \_\_ASMARC\_SETUP\_H  
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Insn Scheduling improvements to csum core routines. \*  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* RajeshwarR: Dec 11, 2007 \* -- Added support for Inter Processor  
 Interrupts  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Vineetg: April 2011 :  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Vineetg: Aug 2009 \* -"C" version of lowest level context switch asm  
 macro called by scheduler \* gcc doesn't genera  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: aug 2009 \* -Implemented CONFIG\_STACKTRACE APIs,  
 primarily save\_stack\_trace\_tsk() \* for displaying task'  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Vineetg: Aug 2009 \* -Moved core context switch macro out of  
 entry.S into this file. \* -This is the more "natural" h  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: Aug 2011 \* -Reintroduce duplicate PD fixup - some  
 customer chips still have the issue

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 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Vineetg: Dec 2007 \* -Check if we are running on Simulator or on  
 real hardware \* to skip certain things during b  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: Jan 2010 (Restarting of timer related syscalls)  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: June 2010 \* -\_\_clear\_user( ) called multiple times during  
 elf load was byte loop \* converted to do as  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: June 2011 \* -"/proc/meminfo | grep PageTables" kept on  
 increasing \* Recently added pgtable dtor was not g  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: March 2009 \* -Implemented task\_pt\_regs( )  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Vineetg: March 2009 (Supporting 2 levels of Interrupts) \* Stack  
 switching code can no longer reliably rely on the fac  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Vineetg: May 16th, 2008 \* - Current macro is now implemented as  
 "global register" r25  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: May 2011 \* -Folded PAGE\_PRESENT (used by VM) and  
 PAGE\_VALID (used by MMU) into 1. \* They are semantical  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: May 2011: for Non-aliasing VIPT D-cache following can be  
 NOPs \* -flush\_cache\_dup\_mm (fork) \* -likewise  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: May 2011 \* -Refactored get\_new\_mmu\_context( ) to only  
 handle live-mm. \* retiring-mm handled in other hook  
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 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: May 2011 \* -Userspace unaligned access emulation  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: May 2011 \* -We had half-optimised memset/memcpy, got  
 better versions of those \* -Added memcmp, strchr, str  
 Copyright (C) 2004, 2007-2010, 2011-2012 Synopsys, Inc. (www.synopsys.com) \* \* Vineetg: Oct 2009 \* No need for ARC specific thread\_info allocator  
 (kmallo/free). This is \* anyways one page alloc  
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 Thompson <mcthomps@us.ibm.com> \*/  
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 functions... \* Add vports, etc \*/#incl  
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 functions... \*/#ifndef SCSI\_TRANSPORT\_FC\_H  
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 \*/  
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 Copyright (C) 2004-2007 John Williams <john.williams@petalogix.com> \* \* derived from arch/v850/kernel/ptrace.c  
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Copyright (C) 2004 - 2007 Paul Mundt \*/#include <linux/init.h>#include <linux/platform\_device.h>#include <linux/mtd/mtd.h>#include <linux/mtd/partitions.h>#include <linux/mtd/physmap.h>#include

Copyright (C) 2004 - 2007 Paul Mundt \*/#include <linux/mm.h>#include <linux/init.h>#include <linux/dma-noncoherent.h>#include <linux/module.h>#include <asm/cacheflush.h>#include <asm/addrspace

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_ASM_C6X_CMPXCHG_H#define _ASM_C6X_CMPXCHG_H
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_ASM_C6X_DELAY_H#define _ASM_C6X_DELAY_H
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_ASM_C6X_SETUP_H#define _ASM_C6X_SETUP_H
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_ASM_C6X_SPECIAL_INSNS_H#define _ASM_C6X_SPECIAL_INSNS_H
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_ASM_C6X_SWITCH_TO_H#define _ASM_C6X_SWITCH_TO_H
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<linux/linkage.h>#include <linux/of_fdt.h>#include <asm/asm
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<linux/mm.h>#include <linux/swap.h>#include <linux/modul
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<linux/module.h>#include <asm/checksum.h>#include <linux
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<linux/module.h>#include <linux/syscalls.h>#include <lin
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Copyright (C) 2004, 2009, 2010, 2011 Texas Instruments Incorporated * Author: Aurelien Jacquot <aurelien.jacquot@ti.com> * * DMA uncached
mapping support.
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_ASM_C6X_BITOPS_H#define _ASM_C6X_BITOPS_H
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_ASM_C6X_CACHEFLUSH_H#define _ASM_C6X_CACHEFLUSH_H
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_ASM_C6X_ELF_H#define _ASM_C6X_ELF_H
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_ASM_C6X_PGTABLE_H#define _ASM_C6X_PGTABLE_H
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_ASM_C6X_TRAPS_H#define _ASM_C6X_TRAPS_H
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<linux/netdevice.h>#include <linux/usb.h>
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2.6.34 \* by Luciano Coelho <luciano.coelho@nokia.com>  
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inclusion \* by Luciano Coelho <luciano.coelho@noki  
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<linux/io.h>#include <linux/delay.h>#include <a  
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<linux/device.h>#include <linux/module.h>#include <linux/slab.h>#in
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<linux/dmaengine.h>#include <linux/dma-mapping.h>#include <linux/pref
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Copyright (C) 2004 Advanced Micro Devices, Inc. */#include <linux/fb.h>#include <linux/delay.h>#include <asm/io.h>#include <asm/delay.h>
Copyright (C) 2004 Advanced Micro Devices, Inc. */#include <linux/fb.h>#include <linux/delay.h>#include <asm/io.h>#include
<asm/delay.h>#include <asm/msr.h>#include <linux/cs5535.h>
Copyright (C) 2004 Advanced Micro Devices, Inc. */#include <linux/spinlock.h>#include <linux/fb.h>#include <linux/delay.h>#include
<asm/io.h>#include <asm/div64.h>#include <asm/delay.h>#include
Copyright (C) 2004 Advanced Micro Devices, Inc. */#include <linux/spinlock.h>#include <linux/fb.h>#include <linux/delay.h>#include
<asm/io.h>#include <asm/div64.h>#include <asm/delay.h>#includ
Copyright (C) 2004 Alan Modra (amodra@au.ibm.com), IBM Corp. */#include <asm/processor.h>#include <asm/ppc_asm.h>#include
<asm/unistd.h>#include <asm/vdso.h>
Copyright (C) 2004 Alan Modra (amodra@au.ibm.com), IBM Corp. */#include <asm/processor.h>#include <asm/ppc_asm.h>#include
<asm/unistd.h>#include <asm/vdso.h>#include <asm/ptrace.h> /* XXX for

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 Copyright (C) 2004 Albert Herranz \* LANDISK/sh4 supported by kogiidena \*/#include <linux/mm.h>#include <linux/kexec.h>#include  
 <linux/delay.h>#include <linux/reboot.h>#include <linux/numa.h>#i  
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 <asm/thread\_info.h>#include <asm/vfpmacros.h>#include <asm  
 Copyright (C) 2004 ARM Limited. \* Written by Deep Blue Solutions Limited. \*/#include <linux/types.h>#include <linux/cpu.h>#include  
 <linux/cpu\_pm.h>#include <linux/hardirq.h>#include <linux/ker  
 Copyright (C) 2004 ARM Limited. \* Written by Deep Blue Solutions Limited. \* \* This code is called from the kernel's undefined instruction trap. \* r9  
 holds the return address for successful handli  
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 <asm/ppc\_asm.h>#include <asm/vdso.h>#include <asm/asm-o  
 Copyright (C) 2004 Benjamin Herrenschmidt (benh@kernel.crashing.org), \* IBM Corp. \*/#include <asm/processor.h>#include  
 <asm/ppc\_asm.h>#include <asm/vdso.h>#include <asm/asm-of  
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 <linux/pci.h>#include <linux/types.h>#include <as  
 Copyright (C) 2004 by Ralf Baechle (ralf@linux-mips.org) \* \* MIPS boards specific PCI support.  
 Copyright (C) 2004 by Ralf Baechle \* \* RTC routines for PC style attached Dallas chip with ARC epoch.  
 Copyright (C) 2004 by Ralf Baechle \* \* The cpustart method is a PMC-Sierra's function to start the secondary CPU. \* Stock PMON 2000 has the smpfork,  
 semlock and semunlock methods instead.  
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 Copyright (C) 2004 by Thomas Rathbone  
 Copyright (C) 2004 by Thomas Rathbone, HP Labs  
 Copyright (C) 2004 Carl-Daniel Hailfinger (invalid MAC handling, insane \* IRQ rate fixes, bigendian fixes, cleanups, verification)  
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issue #291. It should also work with the original s  
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<linux/unistd.h>#include <linux/interrupt.h>#include <linux  
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<stddef.h>#include <init.h>#include <elf\_user.h>#incl  
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 examining the MSFT drivers' debug output \* (insanely lef  
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 userspace (think modprobe, hotplug cpu, \* etc.  
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 July 2000 PAL specs \* 02/05/2001 S.Eranian fix

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Copyright (c) 2004 James Morris <jmorris@redhat.com>, * Red Hat Inc. */#include <linux/rwsem.h>#include <linux/f2fs_fs.h>#include
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<linux/module.h>#include <linux/usb.h>#include <li
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Copyright (C) 2004 Jens Axboe <axboe@kernel.dk> * * Helper functions for setting/querying io priorities of processes. The * system calls closely
mimmick getpriority/setpriority, see the man page fo
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Copyright (C) 2004 Jochen Friedrich <jochen@scram.de> * * This code has basically these routines at the moment:
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Copyright (c) 2004 Joerg Albert <joerg.albert@gmx.de>
Copyright (C) 2004 John Steele Scott <toojays@toojays.net> * * TODO: Need a big cleanup here. Basically, we need to have different *
cpufreq_driver structures for the different type of HW in
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<linux/module.h>#include <linux/string.h>#include <linux/
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<linux/seq\_file.h>#include <asm/bootinfo.h>#include <asm/cpu.h>#include  
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Copyright (C) 2004 Richard Curnow */#include <linux/kallsyms.h>#include <linux/kernel.h>#include <linux/types.h>#include <linux/errno.h>#include
<asm/page.h>#include <asm/ptrace.h>#include <a
Copyright (C) 2004 Richard Curnow */#include <linux/kernel.h>#include <linux/rwsem.h>#include <linux/smp.h>#include <linux/interrupt.h>#include
<linux/init.h>#include <linux/errno.h>#include <
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<linux/kernel.h>#include <linux/signal.h>#include <linu
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<linux/sysctl.h>#include <linux/spinlock.h>#include <linux/skbuff.h>#
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368 standard.
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PHY through the UWB * Radio Control interface.

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the USB and PCI version; * i1480_fw_upload() takes a d
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(USB dongle) or WHCI PCI * card delivers a stream of
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UWB. This is stuff used * across the stack that of
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information that describes the * UWB neighborhood that the
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(struct usb_hcd) for a * Wireless USB Host Contro
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Wireless USB Host * Controllers (struct wusbhc, emb
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Intel Wireless UWB * Link 1480 device (both in th
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behave as a real * Wireless USB HWA adaptor by up
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many as simultaneous * devices the Wireless USB Hos
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the same at the end. MMC * IEs are Wireless USB IEs
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 Copyright (C) 2005-2006 Micronas USA Inc. static int go7007\_usb\_vendor\_request(struct go7007 \*go, u16 request, u16 value, u16 index, void  
 \*transfer\_buffer, int length, int in){ struct go7007\_usb \*u  
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 <linux/gfp.h>#include <linux/module.h>#include <linux/elf.h>#  
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 Copyright (C) 2005, 2009, 2010, 2011 Texas Instruments Incorporated \* Author: Thomas Charleux (thomas.charleux@jaluna.com) \*/#include  
 <linux/moduleloader.h>#include <linux/elf.h>#include <linux/  
 Copyright (C) 2005-2009, 2010 Cavium Networks \*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/msi.h>#include

```

<linux/spinlock.h>#include <linux/interrupt.h>
    Copyright (C) 2005-2009 Atmel Corporation */#ifndef __SOUND_ATMEL_AC97C_H#define __SOUND_ATMEL_AC97C_H
    Copyright (C) 2005-2009 Atmel Corporation */#include <linux/clk.h>#include <linux/delay.h>#include <linux/bitmap.h>#include
<linux/device.h>#include <linux/atmel_pdc.h>#include <linux/gpio/cons
    Copyright (C) 2005-2009 Cavium Networks */
    Copyright (C) 2005-2009 Cavium Networks */#include <linux/kernel.h>#include <linux/init.h>#include <linux/pci.h>#include
<linux/interrupt.h>#include <linux/time.h>#include <linux/delay.h>#incl
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    Copyright (C) 2005-2009 Freescale Semiconductor, Inc. */#ifndef __IPU_PRV_H__#define __IPU_PRV_H__
    Copyright (C) 2005-2009 Freescale Semiconductor, Inc. */#include <linux/export.h>#include <linux/kernel.h>#include <linux/types.h>#include
<linux/errno.h>#include <linux/io.h>#include <linux/er
    Copyright (C) 2005-2009 Freescale Semiconductor, Inc. */#include <linux/export.h>#include <linux/module.h>#include <linux/types.h>#include
<linux/errno.h>#include <linux/delay.h>#include <linux
    Copyright (C) 2005-2009 Freescale Semiconductor, Inc. */#include <linux/export.h>#include <linux/module.h>#include <linux/types.h>#include
<linux/errno.h>#include <linux/io.h>#include <linux/er
    Copyright (C) 2005-2009 Freescale Semiconductor, Inc. */#include <linux/export.h>#include <linux/types.h>#include <linux/errno.h>#include
<linux/io.h>
    Copyright (C) 2005-2009 Freescale Semiconductor, Inc. */#include <linux/io.h>#include "ipu-prv.h"
    Copyright (C) 2005-2009 Freescale Semiconductor, Inc. */#include <linux/module.h>#include <linux/export.h>#include <linux/types.h>#include
<linux/reset.h>#include <linux/platform_device.h>#incl
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    Copyright (C) 2005-2009 Texas Instruments, Inc.
    Copyright (C) 2005-2009 Texas Instruments Inc * * CCDC hardware module for DM355
    Copyright (C) 2005-2009 Texas Instruments Inc */#ifndef __DM355_CCDC_H#define __DM355_CCDC_H#include <media/davinci/ccdc_types.h>#include
<media/davinci/vpfe_types.h>
    Copyright (C) 2005-2009 Texas Instruments Inc */#ifndef __DM355_CCDC_REGS_H#define __DM355_CCDC_REGS_H
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<linux/interrupt.h>#include <linux/timer.h>#inclu
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 Copyright (C) 2005-2018 Andes Technology Corporation#include <asm/sfp-machine.h>#include <math-emu/soft-fp.h>#include <math-emu/double.h>int fcmpd(void \*ft, void \*fa, void \*fb, int cmpop){ FP\_DE  
 Copyright (C) 2005-2018 Andes Technology Corporation#include <asm/sfp-machine.h>#include <math-emu/soft-fp.h>#include <math-emu/single.h>int fcmps(void \*ft, void \*fa, void \*fb, int cmpop){ FP\_DE  
 Copyright (C) 2005-2018 Andes Technology Corporation#include <linux/uaccess.h>  
 Copyright (C) 2005-2019 Andes Technology Corporation#ifndef \_\_FP\_UDF\_IEX\_CTRL\_H#define \_\_FP\_UDF\_IEX\_CTRL\_H  
 Copyright (C) 2005-2019 Andes Technology Corporation#include <linux/uaccess.h>  
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 Copyright (C) 2005-8 Patrick Boettcher <pb@linuxtv.org> \*/#ifndef S5H1420\_H#define S5H1420\_H  
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<mach-dreamcast/mach/dma.h>#include <asm/dma.h>#in  
Copyright (C) 2005 Andriy Skulysh \*/#include <linux/init.h>#include <linux/kernel.h>#include <asm/clock.h>#include <asm/freq.h>#include  
<asm/io.h>  
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Copyright (c) 2005 Arnaldo Carvalho de Melo <acme@conectiva.com.br> \*/#ifndef \_DCCP\_CCID3\_H\_#define \_DCCP\_CCID3\_H\_  
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Copyright (c) 2005 Arnaldo Carvalho de Melo <acme@conectiva.com.br> \*/#include <linux/ktime.h>#include <linux/list.h>#include <linux/slab.h>  
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Copyright (c) 2005 Arnaldo Carvalho de Melo <acme@ghostprotocols.net> \*/  
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<linux/slab.h>#include <linux/export.h>  
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<linux/ethtool.h>#include <linux/workqueue.h>#include <linux/s
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Copyright (C) 2005 by MIPS Technologies, Inc. */#include <linux/cpumask.h>#include <linux/oprofile.h>#include <linux/interrupt.h>#include
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<linux/device.h>#include <linux/parport.h>
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<linux/delay.h>#include <linux/input.h>#include <linux/in
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<linux/interrupt.h>#include <linux/export.h>#in
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Copyright (C) 2005 Paul Mundt */#include <linux/init.h>#include <linux/kernel.h>#include <linux/io.h>#include <asm/clock.h>#include
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Copyright (C) 2005 Paul Mundt */#include <linux/init.h>#include <linux/kernel.h>#include <linux/io.h>#include <linux/clkdev.h>#include
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Copyright (C) 2005 Paul Mundt */#include <linux/kernel.h>#include <linux/init.h>#include <linux/superhway.h>#include <linux/string.h>#include
<asm/addrspace.h>#include <asm/io.h>

```

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 <linux/module.h>#include <linux/skbuff.h>#include <linu  
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 <linux/capability.h>#include <linux/skbuff.h>#include <lin  
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 <linux/types.h>#include <linux/kernel.h>#include <linux/in.h>#include  
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 <asm/clock.h>#include <asm/freq.h>  
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<linux/input.h>#include <linux/platform_device.h>#inc
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<linux/i2c.h>#include <linux/i2c-algo-bit.h>#includ
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defined(CONFIG_MAGIC_SYSRQ)#define SUPPORT_SYSRQ#endif
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<linux/slab.h>#include <linux/delay.h>#include <linux/serial_core.h>#in
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code.
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<linux/err.h>#include <linux/init.h>#include <linux/ker  
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<linux/delay.h>#include <linux/slab.h>#include <linux  
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<linux/serial\_sci.h>#include <linux/sh\_timer.  
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```

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<scsi/scsi\_dh.h>#include <scsi/scsi\_device.h>  
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<linux/slab.h>#include <linux/workqueue.h>#include <li  
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```

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omap.h>#include <linux/mfd/tps65010.h>
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<linux/errno.h>#include <linux/usb.h>#include <linux/platfor
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the timebase. Otherwise we measure cpu time * in jiffies
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Copyright (C) 2006 Paul Mundt */#ifndef __ASM_SH_SHMPARAM_H#define __ASM_SH_SHMPARAM_H
Copyright (C) 2006 Paul Mundt */#include <linux/init.h>#include <linux/io.h>#include <linux/irq.h>#include <linux/platform_device.h>#include
<linux/serial.h>#include <linux/serial_sci.h>#incl
Copyright (C) 2006 Paul Mundt */#include <linux/init.h>#include <linux/slab.h>#include <linux/module.h>#include <linux/interrupt.h>#include
<linux/platform_device.h>#include <asm/push-switch.h
Copyright (C) 2006 Paul Mundt */#include <linux/io.h>#include <linux/module.h>#include <linux/interrupt.h>#include
<linux/platform_device.h>#include <mach/highlander.h>#include <asm/push-switc
Copyright (C) 2006 Paul Mundt */#include <linux/linkage.h>#include <asm/page.h>
Copyright (C) 2006 Paul Mundt */#include <linux/platform_device.h>#include <linux/init.h>#include <linux/serial.h>#include <linux/io.h>#include
<linux/serial_sci.h>#include <linux/sh_dma.h>#i
Copyright (C) 2006 Paul Mundt */#include <linux/platform_device.h>#include <linux/init.h>#include <linux/serial.h>#include
<linux/serial_sci.h>#include <linux/uo_driver.h>#include <linux/sh_t
Copyright (C) 2006 Paul Mundt */#include <linux/platform_device.h>#include <linux/init.h>#include <linux/serial.h>#include
<linux/sh_timer.h>#include <linux/sh_intc.h>#include <linux/serial_sc
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Copyright (C) 2006 Rafael J. Wysocki <rjw@sisk.pl> */#define pr_fmt(x) "hibernate: " x#include <linux/cpu.h>#include <linux/kvm_host.h>#include
<linux/mm.h>#include <linux/pm.h>#include <linux/
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Copyright (C) 2006 Ralf Baechle <ralf@linux-mips.org> */#include <linux/console.h>#include <linux/init.h>#include <linux/interrupt.h>#include
<linux/mc146818rtc.h>#include <linux/param.h>#inclu
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<linux/interrupt.h>#include <linux/compiler.h>#includ
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<linux/export.h>#include <linux/spinlock.h>#include <linux/debug_locks
Copyright (C) 2006 Red Hat, Inc., Ingo Molnar <mingo@redhat.com> */#include <linux/rwsem.h>#include <linux/mutex.h>#include
<linux/ww_mutex.h>#include <linux/sched.h>#include <linux/delay.h>#in
Copyright (C) 2006 Red Hat, Inc., Ingo Molnar <mingo@redhat.com> */#include <linux/sched/task_stack.h>#include <linux/sched/debug.h>#include
<linux/sched.h>#include <linux/kernel.h>#include <lin
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<linux/interrupt.h>#include <linux/irq.h>#include <linux/kallsym
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Klein <hari@vt100.at>
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Copyright (c) 2006 Tejun Heo <teheo@suse.de> \*/  
Copyright (c) 2006 Tejun Heo <teheo@suse.de> \*/ #include <linux/memblock.h> /\* for max\_pfn \*/ #include <linux/acpi.h> #include <linux/dma-  
direct.h> #include <linux/dma-noncoherent.h> #include <linux  
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Copyright (C) 2006 Texas Instruments, Inc \* \* Written by Kalle Jokiniemi \* Peter De Schrijver <peter.de-schrijver@nokia.com> \* Several fixes  
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Copyright (C) 2006 Thomas Bogendoerfer \*/ #include <linux/delay.h> #include <linux/init.h> #include <linux/interrupt.h> #include <linux/irq.h> #include  
<linux/kernel.h>

```

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Copyright (c) 2006 Tower Technologies * Based on ep93xx driver, bits from alim7101_wdt.c * * Authors: Ray Lehtiniemi <rayl@mail.com>, *
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<linux/mtd/partitions.h>#include <linux/mtd/physmap.h>
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<linux/interrupt.h>#include <linux/time.h>#include <l  
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access \* the chip registers if a DME1737, A8000, or  
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Copyright (C) 2007,2008,2009 Albert Herranz \*/#define DRV\_MODULE\_NAME "flipper-pic"#define pr\_fmt(fmt) DRV\_MODULE\_NAME ": " fmt  
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<linux/module.h>#include <linux/skbuff.h>#include <linux/netfilter/x\_table  
Copyright (C) 2007-2008 BalaBit IT Ltd. \* Author: Krisztian Kovacs \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include  
<linux/module.h>#include <linux/skbuff.h>#include <net/tcp.h>#include <n  
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Copyright (C) 2007, 2008 Cavium Networks \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/semaphore.h>#include  
<linux/mtd/mtd.h>#include <linux/mtd/map.h>#include <linux/of\_p  
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<linux/log2.h>#include <linux/list.h>#include <linux/slab.h>#include <  
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 <linux/serial\_sci.h>#include <linux/sh\_timer.h>#include <linux  
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 \*/#include <linux/module.h>#include <linux/ker  
 Copyright (C) 2007-2009 ST-Ericsson AB \* Timer COH 901 328, runs the OS timer interrupt. \* Author: Linus Walleij <linus.walleij@stericsson.com>  
 \*/#include <linux/interrupt.h>#include <linux/time.  
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<linux/cpumask.h>#include <linux/clockdev.h>#include <  
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<linux/io.h>#include <linux/smc91x.h>#include <linux/irq.h>#i  
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<linux/smp.h>#include <linux/stringify.h>  
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Copyright (C) 2007 Alex Dubov <oakad@yahoo.com> \* \* Also ms structures were copied from old broken driver by same author \* These probably come  
from MS spec  
Copyright (C) 2007 Alex Dubov <oakad@yahoo.com> \*/#define DRIVER\_NAME "ms\_block"#define pr\_fmt(fmt) DRIVER\_NAME ": " fmt  
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driver possible.  
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Copyright (C) 2007 Andrew Murray <amurray@mpc-data.co.uk> \* \* Based on arch/sh/kernel/cpu/sh3/clock-sh3.c  
Copyright (C) 2007 Andrew Sharp <andy.sharp@lsi.com> \* \* Real time clock driver for the Dallas 1511 chip, which also \* contains a watchdog timer.  
There is a tiny amount of code that \* platform co  
Copyright (C) 2007 Andrew Victor  
Copyright (C) 2007 Anti Sullin <anti.sullin@artecdesign.ee> \*/#include <linux/linkage.h>#include <linux/clk/at91\_pmc.h>#include "pm.h"#include

```

"pm_data-offsets.h"
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  Copyright (C) 2007 Antonino Daplas <adaplas@pol.net>
  Copyright (c) 2007 Anton Vorontsov <avorontsov@ru.mvista.com> * * Cross-compile with cross-gcc -I/path/to/cross-kernel/include
  Copyright (C) 2007 Antti Palosaari <crope@iki.fi>
  Copyright (C) 2007 Antti Palosaari <crope@iki.fi> * * Thanks to Afatech who kindly provided information.
  Copyright (C) 2007 Antti Palosaari <crope@iki.fi> * * TODO: * - add smart card reader support for Conditional Access (CA) *
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  Copyright (C) 2007 ARM Limited */#include <linux/cpu.h>#include <linux/err.h>#include <linux/init.h>#include <linux/smp.h>#include
<linux/spinlock.h>#include <linux/log2.h>#include <linux/io.h
  Copyright (C) 2007 ARM Limited */#include <linux/init.h>#include <linux/highmem.h>#include <asm/cp15.h>#include <asm/cputype.h>#include
<asm/cacheflush.h>
  Copyright (C) 2007 Arnaldo Carvalho de Melo <acme@redhat.com> * * Based on code in the latency_tracer, that is: *
  Copyright (c) 2007 Arnaldo Carvalho de Melo <acme@redhat.com> */#include <linux/moduleparam.h>#include "ftrc.h"
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  Copyright (C) 2007 Atmel Corporation */#ifndef __ATMEL_LCDC_H__#define __ATMEL_LCDC_H__
  Copyright (C) 2007 Atmel Corporation */#ifndef _LINUX_I2C_GPIO_H#define _LINUX_I2C_GPIO_H
  Copyright (C) 2007 Atmel Corporation */#include <linux/completion.h>#include <linux/debugfs.h>#include <linux/delay.h>#include
<linux/gpio/consumer.h>#include <linux/i2c-algo-bit.h>#include <li
  Copyright (C) 2007 Atmel Corporation */#include <linux/delay.h>#include <linux/kdebug.h>#include <linux/notifier.h>#include
<linux/sched.h>#include <linux/sched/debug.h>#include <linux/hardirq.
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  Copyright (C) 2007 Bluewater Systems Ltd * Author: Ryan Mallon *
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<linux/platform_device.h>#include <linux/serial_8250.h>
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  Copyright (C) 2007 by Ralf Baechle */#ifndef __ASM_SGI_SEEQ_H#define __ASM_SGI_SEEQ_H
  Copyright (C) 2007 by Ralf Baechle */#ifndef __ASM_SGI_WD_H#define __ASM_SGI_WD_H

```

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<linux/console.h>#include <linux/screen\_info.h>#include <linux/platfo  
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Copyright (C) 2007 Cambridge Silicon Radio Ltd.  
Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#ifndef \_\_WHCD\_H#define \_\_WHCD\_H  
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Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/delay.h>#include <linux/kernel.h>#include <linux/module.h>#include  
<linux/pci.h>#include <linux/dma-mapping.h>#include <linux/s  
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Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/dma-mapping.h>  
Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/dma-mapping.h>#include <linux/slab.h>#include  
<linux/usb.h>  
Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/export.h>#include "include/umc.h"  
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Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/gfp.h>#include <linux/dma-mapping.h>  
Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/gfp.h>#include <linux/dma-mapping.h>#include  
<linux/usb.h>  
Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/module.h>  
Copyright (C) 2007 Cambridge Silicon Radio Ltd. \*/#include <linux/kernel.h>#include <linux/sysfs.h>#include <linux/workqueue.h>#include  
<linux/module.h>#include <linux/pci.h>#include "include/u  
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 Copyright (C) 2007 David S. Miller (davem@davemloft.net) \*/#include <linux/kernel.h>#include <linux/interrupt.h>#include <linux/slab.h>#include <linux/irq.h>  
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 Copyright (C) 2007 David S. Miller <davem@davemloft.net> \*/#include <linux/sched.h>#include <linux/device.h>#include <linux/cpu.h>#include <linux/smp.h>#include <linux/percpu.h>#include <linux/  
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 Copyright (C) 2007 Eurek srl \* Author: Claudio Lanconelli <lanconelli.claudio@eptar.com> \* based on enc28j60.c written by David Anders for 2.4 kernel version \* \* \$Id: enc28j60.c,v 1.22 2007/12/20  
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 Copyright (C) 2007 Finn Thain \* \* Converted to DMA API, converted to unified driver model, \* sync'd some routines with mace.c and fixed various bugs.  
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 Copyright (c) 2007 Freescale Semiconductor, Inc. \* \* It is assumed that the firmware (or the platform file) has already set \* up the port.  
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*/#include <linux/module.h>#include <linux/init.h>#include
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<linux/export.h>#include <linux/slab.h>#include <linux/init.h>
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Copyright (c) 2007 Jiri Slaby <jirislaby@gmail.com> * * This code is loosely based on the Linux serial driver, written by * Linus Torvalds,
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<mach-landisk/mach/iodata_landisk.h>#include <
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Copyright (C) 2007 Krzysztof Halasa <khc@pm.waw.pl> * * Ethernet port config (0x00 is not present on IXP42X): *
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Copyright (C) 2007 Krzysztof Helt <krzysztof.h1@wp.pl> * Based on 2.4 driver by Frodo Looijaard <frodol@dds.nl> and * Philip Edelbrock
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Copyright (C) 2007 Krzysztof Helt <krzysztof.h1@wp.pl> * * I don't have documentation for this card. I used the driver * for OSS/Free included in the
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<linux/linkage.h>#include <linux/export.h>#include <linux/sched.h>#i
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<linux/serial.h>#include <linux/serial_sci.h>#include <linux/sh_t
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<linux/init.h>
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<linux/topology.h>#include <linux/node.h>#include <lin
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 <linux/smp.h>#include <linux/irq.h>  
 Copyright (C) 2007 Ralf Baechle <ralf@linux-mips.org> \*/#include <linux/init.h>#include <linux/interrupt.h>#include <linux/irq.h>#include  
 <linux/sched\_clock.h>#include <asm/time.h>#include <asm  
 Copyright (C) 2007 Ralf Baechle (ralf@linux-mips.org) \*/#include <linux/init.h>#include <linux/platform\_device.h>#include  
 <linux/serial\_8250.h>#include <linux rtc/ds1685.h>  
 Copyright (C) 2007 Ralf Baechle (ralf@linux-mips.org) \*/#include <linux/init.h>#include <linux/serial\_8250.h>  
 Copyright(c) 2007, RealTEK Technology Inc. All Right Reserved. \* \* Module: Hal819xUsbDM.h (RTL8192 Header H File)  
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 Copyright (C) 2007 Red Hat, Inc., Peter Zijlstra \*/#include "sched.h"  
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 <linux/leds.h>#include <linux/platform\_device.h>#include  
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 <asm/clock.h>#include <asm/freq.h>#include <asm/i  
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PRM on OMAP4 has a new register layout, and is handled  
Copyright (C) 2008-2010 Nokia Corporation \* \* Written by Henrik Saari <henrik.saari@nokia.com> \* Updates by Felipe Balbi <felipe.balbi@nokia.com>  
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Copyright (C) 2008-2010 Nokia Corporation \* \* Written by Hiroshi DOYU <Hiroshi.DOYU@nokia.com>  
Copyright (C) 2008 - 2010 Paul Mundt \*/#include <linux/init.h>#include <linux/types.h>#include <linux/platform\_device.h>#include  
<linux/interrupt.h>#include <linux/smsc911x.h>#include <linux/i  
Copyright (C) 2008, 2010 Pengutronix \* \* TODO: SPI, use power-down mode for suspend?, interrupt handling?  
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<linux/jhash.h>#include <linux/list.h>#include <linux/rcupdate.h>#  
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<linux/skbuff.h>#include <linux/netfilter/x_tables.h>#inclu
Copyright (c) 2008 Adrian-Ken Rueegsegger <ken@codelabs.ch> */#include <crypto/internal/hash.h>#include <linux/init.h>#include
<linux/module.h>#include <linux/mm.h>#include <linux/types.h>#incl
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Copyright (C) 2008 Atmel Corporation */#ifndef AT_HDMAC_REGS_H#define AT_HDMAC_REGS_H
Copyright (C) 2008 Atmel Corporation */#include <linux/moduleparam.h>#include <linux/export.h>#include <linux/debugfs.h>#include
<linux/fs.h>#include <linux/seq_file.h>#include <linux/slab.h>#
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```

```

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    Copyright (c) 2008 by David Brownell */#include <linux/kernel.h>#include <linux/slab.h>#include <linux/input.h>#include <linux/input/sparse-
keymap.h>#include <linux/platform_device.h>#include <
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    Copyright (C) 2008 Cambridge Silicon Radio Ltd. */#include <linux/kernel.h>#include <linux/export.h>
    Copyright (C) 2008 Cambridge Silicon Radio Ltd. */#include <linux/kernel.h>#include <linux/random.h>#include <linux/slab.h>
    Copyright (C) 2008 Cambridge Silicon Radio Ltd. */#include <linux/kernel.h>#include <linux/slab.h>#include <linux/random.h>#include
<linux/export.h>
    Copyright (C) 2008 Cambridge Silicon Radio Ltd. */#include <linux/kernel.h>#include <linux/slab.h>#include "uwb.h"
    Copyright (C) 2008 Cambridge Silicon Radio Ltd. */#include <linux/kthread.h>#include <linux/freezer.h>#include <linux/slab.h>#include
<linux/delay.h>#include "uwb-internal.h"
    Copyright (C) 2008 Cambridge Silicon Radio Ltd. */#include <linux/slab.h>#include <linux/kernel.h>#include <linux/debugfs.h>#include
<linux/seq_file.h>#include <linux/export.h>
    Copyright (C) 2008 Cambridge Silicon Radio Ltd. */#include "wusbhc.h"
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Copyright (C) 2008 David Brownell \*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/bcd.h>#include <linux/slab.h>#include  
<linux/rtc.h>#include <linux/workqueue.h>  
Copyright (C) 2008 David Daney \*/  
Copyright (C) 2008 David Daney \*/#ifndef \_ASM\_WATCH\_H#define \_ASM\_WATCH\_H  
Copyright (C) 2008 David Gibson, IBM Corporation  
Copyright (C) 2008 David S. Miller <davem@davemloft.net> \*/  
Copyright (C) 2008 David S. Miller <davem@davemloft.net> \*/#include <linux/kernel.h>#include <linux/interrupt.h>#include <linux/numa.h>  
Copyright (C) 2008 David S. Miller <davem@davemloft.net> \*/#include <linux/kernel.h>#include <linux/reboot.h>#include <linux/export.h>#include  
<linux/pm.h>#include <linux/of.h>  
Copyright (C) 2008 David S. Miller <davem@davemloft.net> \*/#include <linux/linkage.h>#include <asm/hypervisor.h>#include "n2rng.h"  
Copyright (C) 2008 Devin Heitmueller <dheitmueller@linuxtv.org>  
Copyright (C) 2008 Dmitri Vorobiev \*/#include <linux/cpu.h>#include <linux/init.h>#include <linux/sched.h>#include <linux/ioport.h>#include  
<linux/irq.h>#include <linux/of\_fdt.h>#include <linu  
Copyright (c) 2008 Dmitry Baryshkov \*/  
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Copyright (C) 2008 Dmitry Baryshkov \* \* Based on Sharp's NAND driver sharp\_sl.c  
Copyright (c) 2008 Dmitry Baryshkov \*/#ifndef CSB726\_H#define CSB726\_H  
Copyright (c) 2008 Dmitry Baryshkov \*/#ifndef TOSA\_BT\_H#define TOSA\_BT\_H  
Copyright (c) 2008 Dmitry Baryshkov \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/power\_supply.h>#include  
<linux/wm97xx.h>#include <linux/delay.h>#include <linux/spinlock.  
Copyright (c) 2008 Dmitry Baryshkov \* \* T7L66 features: \*  
Copyright (c) 2008 Dmitry Eremin-Solenikov \*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/io.h>#include  
<linux/gpio/machine.h>#include <linux/platform\_device.h>#include <linu  
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Copyright (C) 2008 Embedded Alley Solutions, Inc. \*/#ifndef \_\_DRIVERS\_MTD\_NAND\_GPMI\_NAND\_H#define \_\_DRIVERS\_MTD\_NAND\_GPMI\_NAND\_H  
Copyright (C) 2008 Embedded Alley Solutions, Inc. \*/#include <linux/clock.h>#include <linux/delay.h>#include <linux/slab.h>#include

```

<linux/sched/task_stack.h>#include <linux/interrupt.h>#include
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Copyright (c) 2008 Eurotech S.p.A. <info@eurotech.it> ** This code is *strongly* based on EHCI-HCD code by David Brownell since * the chip is a quasi-
EHCI compatible.
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Copyright (C) 2008 Florian Fainelli <florian@openwrt.org> ** This file was derived from: include/asm-mips/cpu-features.h
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Copyright (C) 2008 Freescale Semiconductor, Inc. All rights reserved
Copyright (C) 2008 Freescale Semiconductor, Inc. */#include <linux/err.h>#include <linux/gpio.h>#include <linux/gpio/consumer.h>#include
<linux/kernel.h>#include <linux/leds.h>#include <linux/m
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to support extcon * (originally switch class is supported)
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Copyright (C) 2008, Guennadi Liakhovetski <kernel@pengutronix.de> ** Based on mt9v011 driver
Copyright (C) 2008, Guennadi Liakhovetski <kernel@pengutronix.de> ** Hardware specific bits initially based on former work by Matt Callow *
drivers/media/video/omap/sensor_ov6650.c
Copyright (C) 2008, Guennadi Liakhovetski <kernel@pengutronix.de> */#include <linux/videodev2.h>#include <linux/slab.h>#include
<linux/i2c.h>#include <linux/delay.h>#include <linux/log2.h>#incl
Copyright (C) 2008, Guennadi Liakhovetski <kernel@pengutronix.de> ** This driver provides an interface between platform-specific camera * buses
and camera devices. It should be used if the camera

```

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 Copyright (C) 2008 Hans Verkuil <hverkuil@xs4all.nl>  
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 Copyright (C) 2008 Hewlett-Packard Development Company, L.P. \* David Altobelli <david.altobelli@hp.com> \*/#ifndef \_\_HPILO\_H#define \_\_HPILO\_H  
 Copyright (C) 2008 Hewlett-Packard Development Company, L.P. \* David Altobelli <david.altobelli@hpe.com> \*/#include <linux/kernel.h>#include <linux/types.h>#include <linux/module.h>#include <linux/

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 Copyright (C) 2008 IBM Corporation \* Author: Mimi Zohar <zohar@us.ibm.com> \* \* File: integrity\_audit.c \* Audit calls for the integrity subsystem  
 Copyright (C) 2008 IBM Corporation \* Author: Mimi Zohar <zohar@us.ibm.com> \* \* ima\_policy.c \* - initialize default measure policy rules  
 Copyright (C) 2008 IBM Corporation \* \* Authors:  
 Copyright (C) 2008 IBM Corporation \* Author(s): Brian King (brking@linux.vnet.ibm.com), \*/  
 Copyright (C) 2008 IBM Corporation \* 'find\_last\_bit' is written by Rusty Russell <rusty@rustcorp.com.au> \* (Inspired by David Howell's find\_next\_bit implementation) \* \* Rewritten by Yury Norov <yu

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 January, 17th 2009 Fill set\_voltage with ac

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 Copyright (C) 2008 Igor M. Liplianin (liplianin@me.by)\*/#include <linux/init.h>#include <linux/kernel.h>#include <linux/module.h>#include <linux/string.h>#include <linux/slab.h>#include <linux/

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Josef Pavlik <josef@pavlik.it> Fixed diseqc\_msg, diseqc\_burst

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 Copyright (C) 2008, Intel Corp. \* Author: Huang Ying <ying.huang@intel.com> \* \* Added RFC4106 AES-GCM support for 128-bit keys under the AEAD  
 \* interface for 64-bit kernels. \* Authors: Adri  
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 Copyright (C) 2008, Intel Corp. \* Author: Huang Ying <ying.huang@intel.com> \* Vinodh Gopal <vinodh.gopal@intel.com> \*  
 Kahraman Akdemir \* \* Added RFC4106 AES-GCM support f  
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 Copyright (c) 2008 Intel Corporation \* Author: Matthew Wilcox <willy@linux.intel.com> \* \* Please see kernel/locking/semaphore.c for documentation of  
 these functions  
 Copyright (c) 2008 Intel Corporation \* Author: Matthew Wilcox <willy@linux.intel.com> \* \* This file implements counting semaphores. \* A counting  
 semaphore may be acquired 'n' times before sleeping  
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 Copyright (C) 2008 Intel Corporation \* Authors: Austin Zhang <austin\_zhang@linux.intel.com> \* Kent Liu <kent.liu@intel.com> \*/#include  
 <linux/init.h>#include <linux/module.h>#include <l  
 Copyright (c) 2008 Intel Corporation \* Jesse Barnes <jesse.barnes@intel.com> \* \* Moorestown has an interesting PCI implementation:  
 Copyright (C) 2008 Intel Corporation <linux-wimax@intel.com> \* Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com> \*/  
 Copyright (C) 2008 Intel Corporation <linux-wimax@intel.com> \* Inaky Perez-Gonzalez <inaky.perez-gonzalez@intel.com> \* \* This implements a simple  
 synchronous call to reset a WiMAX device.  
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 Linux Kernel rkill subsystem so that the \* driver  
 Copyright (C) 2008 Intel Corp. Shaohua Li <shaohua.li@intel.com> \* \* See asm-generic/syscall.h for descriptions of what we must do here.  
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depends wildly on alignment on many x86 processors. Th
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Copyright (c) 2008 Jonathan Cameron */#include <linux/irq.h>#include <linux/module.h>#include <linux/atomic.h>
Copyright (c) 2008 Jonathan Cameron */#include <string.h>#include <stdlib.h>#include <stdio.h>#include <stdint.h>#include <dirent.h>#include
<errno.h>#include <ctype.h>#include "iio_utils.h"
Copyright (c) 2008 Jonathan Cameron * * These definitions are meant for use only within the IIO core, not individual * drivers.
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Thomas 2010.
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Copyright (C) 2008 linwizard.sourceforge.net */#include <linux/kernel.h>#include <linux/init.h>#include <linux/platform_device.h>#include
<linux/input.h>#include <linux/delay.h>#include <linux/
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Copyright (c) 2008 Magnus Damm * * Based on videobuf-vmalloc.c,
Copyright (c) 2008 Magnus Damm */#ifndef __ASM_SH_GPIO_H#define __ASM_SH_GPIO_H
Copyright (c) 2008 Magnus Damm */#ifndef __VIDEOBUF_DMA_CONTIG_H#define __VIDEOBUF_DMA_CONTIG_H
Copyright (C) 2008 Magnus Damm */#include <linux/clkdev.h>#include <linux/dma-mapping.h>#include <linux/init.h>#include
<linux/platform_device.h>#include <linux/interrupt.h>#include <linux/inpu
Copyright (C) 2008 Magnus Damm */#include <linux/init.h>#include <linux/irq.h>#include <linux/io.h>#include <mach/highlander.h>
Copyright (C) 2008 Magnus Damm * * Intercept io operations by trapping.
Copyright (C) 2008 Magnus Damm * * Portions of the code based on out-of-tree driver i2c-sh7343.c
Copyright (C) 2008 Manuel Lauss <mano@roarinelk.homelinux.net>
Copyright (c) 2008 Marc Pignat <marc.pignat@hevs.ch> * * The adcxx4s communicates with a host processor via an SPI/Microwire Bus * interface. This
driver supports the whole family of devices with n
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Copyright (c) 2008 Marek Vasut <marek.vasut@gmail.com> * * Based on corgikbd.c
Copyright (C) 2008 Mark Nelson, IBM Corp. */#include <asm/page.h>#include <asm/processor.h>#include <asm/ppc_asm.h>#include <asm/asm-
offsets.h>#include <asm/export.h>#include <asm/feature-fixup
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<linux/platform_device.h>#include <linux/pci.h>#include <
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Copyright (C) 2008 Maxime Bizon <mbizon@freebox.fr> */#include <linux/init.h>#include <linux/interrupt.h>#include <linux/module.h>#include
<linux/clk.h>#include <linux/etherdevice.h>#include <l
Copyright (C) 2008 Maxime Bizon <mbizon@freebox.fr> * * Serial driver for BCM63xx integrated UART.
Copyright (C) 2008 MaxLinear
Copyright (C) 2008 MaxLinear */static u16 MXL5005_RegisterInit(struct dvb_frontend *fe)

```

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actually ENABLE or DISABLE the thing \* once af  
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Copyright (C) 2008 Nicholas Beck <nbeck@mpc-data.co.uk> \*/#include <linux/init.h>#include <linux/irq.h>#include <linux/io.h>#include  
<mach/sdk7780.h>  
Copyright (C) 2008 Nicholas Beck <nbeck@mpc-data.co.uk> \*/#include <linux/init.h>#include <linux/types.h>#include  
<linux/platform\_device.h>#include <linux/ata\_platform.h>#include <asm/machvec.h>  
Copyright (C) 2008 Nicholas Beck <nbeck@mpc-data.co.uk> \*/#include <linux/sh\_intc.h>#include <asm/addrspace.h>  
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<linux/interrupt.h>#include <linux/input.h>#include <linu  
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<linux/string.h>#include <linux/delay.h>#include <linux/mmc/host.h>#  
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Machek <pavel@ucw.cz>

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Copyright (C) 2008 Nokia Corporation * Mikko Ylinoen <mikko.k.ylinoen@nokia.com> */#include <linux/interrupt.h>#include <linux/kernel.h>#include
<linux/module.h>#include <linux/platform_device.h>
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<linux/netdevice.h>#include <linux/etherdevice.h>#inclu
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<linux/lc.h>#include <linux/slab.h>#include <linu
Copyright (c) 2008 Patrick McHardy <kaber@trash.net> */#include <linux/types.h>#include <linux/if_ether.h>#include <linux/if_vlan.h>#include
<net/mrp.h>#include "vlan.h"
Copyright (c) 2008 Patrick McHardy <kaber@trash.net> */#include <linux/types.h>#include <linux/if_vlan.h>#include <net/garp.h>#include "vlan.h"
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Copyright (C) 2008 Paul Mundt */#include <linux/platform_device.h>#include <linux/init.h>#include <linux/serial.h>#include <linux/mm.h>#include
<linux/serial_sci.h>#include <linux/uo_driver.h
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<linux/platform_device.h>#include <linux/interrupt.h>#include <linux/mtd/mtd.
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as10x_bus_adapter_t;struct as102_dev_t;
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<linux/platform\_device.h>#include <linux/regulator/driver.h>#include <linux/sla  
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<linux/err.h>#include <linux/platform\_device.h>#i  
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<linux/log2.h>#include <linux/gpio.h>#include <li  
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<linux/spinlock.h>#include <linux/irqflags.h>#include <linux/uaccess.h>#
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<linux/interrupt.h>#include <linux/init.h>#in
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<linux/interrupt.h>#include <linux/mc146818rtc.h>#include <linux/irq.h>
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#Positions Resistor Options (kOhm) * AD5258          1          64          1, 10, 50, 100 * AD
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<asm/hw_breakpoint.h>
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<linux/perf_event.h>#include <asm/processor.h>
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<linux/init.h>#include <linux/smp.h>
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"util/debug.h"#include "util/dso.h"#include <subcmd/page  
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<linux/io.h>#include <linux/module.h>#include <linux/platform\_devic  
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```

```

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optimum numerator/denominator pairs for * pll config
    Copyright (C) 2009 Eric Paris <Red Hat Inc> * dnotify was largely rewritten to use the new fsnotify infrastructure */#include <linux/fs.h>#include
<linux/module.h>#include <linux/sched.h>#include
    Copyright (C) 2009 Eric Paris <Red Hat Inc> * inotify was largely rewritten to make use of the fsnotify infrastructure */
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<linux/of.h>#include <linux/of_device.h>#include <linux
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 <linux/platform\_device.h>#include <linux/spi/spi.h>  
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 <linux/pci.h>#include <linux/i2c.h>#include <linux/inter  
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offsets.h>#include <asm/regdef.h>#include <asm/asm.h>

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<asm/hpet.h>

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<loongson.h>#include <boot\_param.h>#include <workarounds>

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<linux/pm.h>

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<linux/kernel.h>#include <linux/export.h>

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configure space are defined in cs5536\_modulename.c respect

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Copyright (C) 2009 Lemote, Inc. \* Author: Zhangjin Wu, wuzhangjin@gmail.com \*/#include <linux/init.h>#include <linux/pm.h>

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<asm/clock.h>

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<linux/sh\_clk.h>#include <asm/clock.h>#include <cpu/sh77

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<linux/clkdev.h>#include <linux/sh\_clk.h>#include <asm/cloc

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<asm/suspend.h>#include <linux/uaccess.h>#include <asm/

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<linux/cpuidle.h>#include <linux/export.h>#include <asm

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<linux/serial_sci.h>#include <linux/sh_timer.h>#include <linux/sh_in
  Copyright (C) 2009 Magnus Damm */#include <linux/sys.h>#include <linux/errno.h>#include <linux/linkage.h>#include <asm/asm-offsets.h>#include
<asm/page.h>
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  Copyright (C) 2009 Marvell International Ltd. * Haojian Zhuang <haojian.zhuang@marvell.com> */#include <linux/kernel.h>#include
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Copyright (C) 2009 Nokia Corporation * Author: Tomi Valkeinen <tomi.valkeinen@ti.com> * * Some code and ideas taken from drivers/video/omap/
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Lindgren, Rajendra Nayak, Vikram * Pandita, Sak
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__ARCH_ARM_PLAT_OMAP_INCLUDE_MACH_L4_3XXX_H
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Copyright (c) 2009 Nokia Corporation * Roger Quadros <ext-roger.quadros@nokia.com> * * This is useful for systems with mixed controllable and *
non-controllable regulators, as well as for allowing
Copyright (C) 2009 Nokia Corporation * * Update to use new clocksource/clockevent layers * Author: Kevin Hilman, MontaVista Software, Inc.

```

```

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driver are based. Indirect * thanks also to Maxim
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<linux/platform_device.h>#include <linux/reset-controller.h>#include <li
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  Copyright (C) 2009 Paul Mundt * * Based on setup-sh7709.c
  Copyright (C) 2009 Paul Mundt * * Heavily based on the x86 and PowerPC implementations.
  Copyright (C) 2009 Paul Mundt */#include <linux/init.h>#include <linux/platform_device.h>#include <linux/io.h>#include
<linux/mtd/physmap.h>#include <linux/regulator/fixd.h>#include <linux/re
  Copyright (C) 2009 Paul Mundt */#include <linux/init.h>#include <linux/platform_device.h>#include <linux/sm501.h>#include <linux/sm501-
regs.h>#include <linux/fb.h>#include <linux/mtd/physmap.h
  Copyright (C) 2009 Paul Mundt */#include <linux/kernel.h>#include <linux/init.h>#include <linux/io.h>#include <linux/irq.h>#include
<linux/perf_event.h>#include <asm/processor.h>
  Copyright (C) 2009 Paul Mundt */#include <linux/kernel.h>#include <linux/module.h>#include <asm/dwarf.h>
  Copyright (C) 2009 Paul Mundt */#include <linux/kernel.h>#include <linux/sched.h>#include <linux/perf_event.h>#include <linux/percpu.h>#include
<asm/unwinder.h>#include <asm/ptrace.h>
  Copyright (C) 2009 Paul Mundt */#include <linux/platform_device.h>#include <linux/init.h>#include <linux/serial.h>#include
<linux/serial_sci.h>#include <linux/io.h>#include <linux/mm.h>#inclu
  Copyright (C) 2009 Paul Mundt */#include <linux/platform_device.h>#include <linux/init.h>#include <linux/serial.h>#include
<linux/serial_sci.h>#include <linux/sh_eth.h>#include <linux/sh_timer
  Copyright (C) 2009 Paul Mundt */#include <linux/platform_device.h>#include <linux/init.h>#include <linux/serial.h>#include
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  Copyright (C) 2009 Phil Sutter <n0-1@freewrt.org> * * Based on leds-cobalt-qube.c by Florian Fainelly and * rb-diag.c (my own standalone driver for
both LED and * button of Routerboard532).
  Copyright (C) 2009 picoChip Designs Ltd, Jamie Iles */
  Copyright (C) 2009 picoChip Designs, Ltd., Jamie Iles
  Copyright (C) 2009 picoChip Designs, Ltd., Jamie Iles * * ARMv6 has 2 configurable performance counters and a single cycle counter. * They all share a
single reset bit but can be written to zero so
  Copyright (c) 2009 Promwad Innovation Company// Yauhen Kharuzhy <yauhen.kharuzhy@promwad.com>

```

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Copyright (C) 2009 Provigent Ltd. */#include <linux/acpi.h>#include <linux/clk-provider.h>#include <linux/clk.h>#include <linux/delay.h>#include
<linux/dmi.h>#include <linux/err.h>#include <li
Copyright (C) 2009 Provigent Ltd. */#include <linux/clk.h>#include <linux/delay.h>#include <linux/export.h>#include <linux/errno.h>#include
<linux/err.h>#include <linux/i2c.h>#include <linux/i
Copyright (C) 2009 Provigent Ltd. */#include <linux/delay.h>#include <linux/err.h>#include <linux/errno.h>#include <linux/export.h>#include
<linux/gpio/consumer.h>#include <linux/i2c.h>#includ
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Copyright (C) 2009 Red Hat, Inc. * Author: Michael S. Tsirkin <mst@redhat.com> * * Since the driver does not declare any device ids, you must allocate
* id and bind the device to the driver yourse
Copyright (C) 2009 Red Hat, Inc. * Author: Michael S. Tsirkin <mst@redhat.com> * * test virtio server in host kernel.
Copyright (C) 2009 Red Hat, Inc. * Author: Michael S. Tsirkin <mst@redhat.com> * * virtio-net server in host kernel.
Copyright (C) 2009 Red Hat, Inc., Ingo Molnar <mingo@redhat.com> * * Implements 3 trace clock variants, with differing scalability/precision *
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Copyright (C) 2009 Samsung Electronics \* Kim Kyuwon <q1.kim@samsung.com> \* \* Datasheet:  
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Copyright (C) 2009 Sitdikov Valentin \*/#ifndef \_\_ASM\_CPU\_SH4\_WATCHDOG\_H#define \_\_ASM\_CPU\_SH4\_WATCHDOG\_H  
Copyright (c) 2009 SoftwareBitMaker.  
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Copyright (C) 2009 ST-Ericsson AB \* Handle board-bound regulators and board power not related \* to any devices. \* Author: Linus Walleij <linus.walleij@stericsson.com> \*/#include <linux/device.h>  
Copyright (C) 2009 ST-Ericsson. \* Author: Srinidhi Kasagar <srinidhi.kasagar@stericsson.com> \* \* This file is based on arm realview platform  
Copyright (C) 2009 ST-Ericsson SA  
Copyright (C) 2009, Steven Rostedt <rstedt@redhat.com> \*/#include <dirent.h>#include <stdio.h>#include <stdlib.h>#include <string.h>#include <stdarg.h>#include <sys/types.h>#include <sys/sta  
Copyright (C) 2009 Steven Rostedt <rstedt@redhat.com> \*/#include <linux/ring\_buffer.h>#include <linux/completion.h>#include <linux/kthread.h>#include <uapi/linux/sched/types.h>#include <linux/  
Copyright (C) 2009 Steven Rostedt <rstedt@redhat.com> \*/#include <linux/stringify.h>#include <linux/kallsyms.h>#include <linux/seq\_file.h>#include <linux/uaccess.h>#include <linux/trace.h>#i  
Copyright (C) 2009, Steven Rostedt <rstedt@redhat.com> \*/#include <stdio.h>#include <stdlib.h>#include <string.h>#include <errno.h>  
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<linux/kernel.h>#include <linux/init.h>#include <linux/  
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OMAP-1509, OMAP-1510, and OMAP-1610 \* are differe  
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internal DMA channels.  
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Goede <hdegoede@redhat.com> and * Thomas Kaiser <tho
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<asm/cache.h>#include <asm/thread_info.h>
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Copyright (C) 2009 TMT Services ** 2007-09-04: eric miao <eric.y.miao@gmail.com> * rewrite to align with latest kernel
Copyright (C) 2009 TMT Services ** 2010-01-21 Hennie van der Merwe <hvdmerwe@tmtservies.co.za>
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Copyright (c) 2009 Trond Myklebust <Trond.Myklebust@netapp.com> */#include <linux/kmod.h>#include <linux/module.h>#include
<linux/moduleparam.h>#include <linux/mount.h>#include <linux/namespace.h>#
Copyright (c) 2009 Trond Myklebust <Trond.Myklebust@netapp.com> ** Resolves DNS hostnames into valid ip addresses
Copyright (C) 2009 Uwe Kleine-Koenig, Pengutronix ** Common code for i.MX1, i.MX21 and i.MX27
Copyright (C) 2009 Uwe Kleine-Koenig <u.kleine-koenig@pengutronix.de> * for Pengutronix */#include <linux/export.h>#include <linux/ftrace.h>
Copyright (C) 2009 Valentin Longchamp, EPFL Mobots group */
Copyright (C) 2009 Valentin Sidikov */#ifndef __ASM_SH_WATCHDOG_H#define __ASM_SH_WATCHDOG_H#endif __KERNEL__
Copyright (C) 2009 VIA Technologies, Inc.
Copyright (C) 2009 VIA Technologies, Inc. ** based on existing coretemp.c, which is
Copyright (C) 2009 Vivek Goyal <vgoyal@redhat.com> * Nauman Rafique <nauman@google.com> */
Copyright (C) 2009 Vivek Goyal <vgoyal@redhat.com> * Nauman Rafique <nauman@google.com> ** For policy-specific per-blkcg data:
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Copyright (c) 2009 Wayne Roberts * - tsc2007.c
Copyright (C) 2009 William Hubbs ** This code is based on kobject-example.c, which came with linux 2.6.x.
Copyright (C) 2009 William M. Brack ** Refactored and updated to the latest v4l core frameworks: *
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arch/mips/mm/fault.c which is: *
Copyright (C) 2009 Wind River Systems Inc * Implemented by fredrik.markstrom@gmail.com and ivarholmqvist@gmail.com ** Based on DMA code
from MIPS.
Copyright (C) 2009 Wind River Systems, Inc. * Updated for supporting PPC405EX on Kilauea. * Tiejun Chen <tiejun.chen@windriver.com> */#include
<stddef.h>#include "types.h"#include "string.h"

```

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Copyright (C) 2009 Wind River Systems, * written by Ralf Baechle <ralf@linux-mips.org> */#ifndef __ASM_COP2_H#define __ASM_COP2_H
Copyright (C) 2009 Wind River Systems * Written by Ralf Baechle <ralf@linux-mips.org> */#ifndef __ASM_IOCTL_H#define __ASM_IOCTL_H
Copyright (C) 2009 Wind River Systems, * written by Ralf Baechle <ralf@linux-mips.org> */#include <linux/capability.h>#include
<linux/init.h>#include <linux/irqflags.h>#include <linux/notifier
Copyright (C) 2009 Wind River Systems, * written by Ralf Baechle <ralf@linux-mips.org> */#include <linux/init.h>#include <linux/irqflags.h>#include
<linux/notifier.h>#include <linux/prefetch.h
Copyright (C) 2009 Wind River Systems, * written by Ralf Baechle <ralf@linux-mips.org> */#include <linux/init.h>#include <linux/sched.h>#include
<linux/notifier.h>#include <linux/ptrace.h>
Copyright (C) 2009 Wind River Systems, * written by Ralf Baechle <ralf@linux-mips.org> */#include <linux/module.h>#include <linux/init.h>#include
<linux/slab.h>#include <linux/interrupt.h>#in
Copyright (C) 2009 Wind River Systems, * written by Ralf Baechle <ralf@linux-mips.org> */#include <linux/module.h>#include <linux/init.h>#include
<linux/slab.h>#include <linux/io.h>#include <
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Copyright (C) 2009 Wolfram Sang, Pengutronix e.K. * * Some info: *
Copyright (C) 2009 Wolfram Sang, Pengutronix * * The Maxim MAX7300/1 device is an I2C/SPI driven GPIO expander. There are * 28 GPIOs. 8 of them
can trigger an interrupt. See datasheet for more * d
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Copyright (c) 2009 Xilinx, Inc. * * Based on "ehci-ppc-of.c" by Valentine Barshak <vbarshak@ru.mvista.com> * and "ehci-ppc-soc.c" by Stefan Roesse
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Copyright (C) 2009 Yinghai Lu
Copyright (C) 2009 Yoichi Yuasa <yuasa@linux-mips.org> * * based on code for MX31ADS,
Copyright (C) 2009 Zhang Le */#ifndef __ASM_BOOTINFO_H#define __ASM_BOOTINFO_H
Copyright (c) 2009 Zhang Le <r0bertz@gentoo.org> */#include <linux/errno.h>#include <asm/bootinfo.h>
Copyright (C) 2009 Zhang Le <r0bertz@gentoo.org> * * reference: /proc/cpuinfo, * arch/mips/kernel/cpu-probe.c(cpu_probe_legacy), *
arch/mips/kernel/proc.c(show_cpuinfo), * loongson2f user manual.
Copyright (C) 2009 Zhang Rui <rui.zhang@intel.com> * * Rework for IIO subsystem:
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Copyright (C) 2010-17 Digital Devices GmbH"; stat = ddb_init_ddbridge(); if (stat < 0) return stat; stat =
pci_register_driver(&ddb_pci_driver); if (stat < 0) ddb_exit_ddbridge(0, stat);
Copyright (c) 2010-2010 Analog Devices Inc. */
Copyright (c) 2010-2010 Analog Devices Inc. */#include <linux/types.h>#include <linux/mutex.h>#include <linux/device.h>#include
<linux/spi/spi.h>#include <linux/slab.h>#include <linux/sysfs.h>
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Copyright (c) 2010-2011, Jarod Wilson <jarod@redhat.com> \* \* Based on the original lirc\_mceusb and lirc\_mceusb2 drivers, by Dan \* Conti, Martin  
Blatter and Daniel Melander, the latter of which was  
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Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> \* \* Architecture specific procedures for creating, accessing and \* interpreting the device  
tree.  
Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> \* et al. \*/  
Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> \* et al. \*/ #ifndef \_\_ASM\_OPENRISC\_ELF\_H#define \_\_ASM\_OPENRISC\_ELF\_H  
Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> \* et al. \*/ #ifndef \_\_ASM\_OPENRISC\_PTRACE\_H#define \_\_ASM\_OPENRISC\_PTRACE\_H  
Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> \* et al. \* \* This file is part of OpenRISC 1000 Architectural Simulator.  
Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> \* \* Here we handle the break vectors not used by the system call \* mechanism, as well as  
some general stack/register dumping \* things.  
Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> \* \* Id script for OpenRISC architecture  
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Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se> \* \* This file handles the architecture-dependent parts of initialization  
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Copyright (C) 2010-2011 Mika Westerberg * * Explicit FIFO handling code was inspired by amba-pl022 driver.
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Copyright (C) 2010 - 2011 Samsung Electronics Co., Ltd. * Contact: Sylwester Nawrocki, <s.nawrocki@samsung.com> * * Initial register configuration
based on a driver authored by * HeungJun Kim <riv
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Debski      <k.debski@samsung.com>
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Mixer driver
Copyright (C) 2010-2011 ST-Ericsson * * Implements u8500 semaphore handling for protocol 1, no interrupts.
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Copyright (C) 2010, 2011 Texas Instruments Incorporated * Contributed by: Mark Salter <msalter@redhat.com> */#include <linux/module.h>#include
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Copyright (C) 2010-2012 Matthias Fuchs <matthias.fuchs@esd.eu>, esd gmbh \*/#include <linux/signal.h>#include <linux/slab.h>#include <linux/module.h>#include <linux/netdevice.h>#include <linux/us

Copyright (c) 2010-2012 Mauro Carvalho Chehab \* Driver modified by in order to work with upstream drxk driver, and \* tons of bugs got fixed, and converted to use dvb-usb-v2. \*/

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Copyright (C) 2010 - 2012 Samsung Electronics Co., Ltd.\*/#define pr\_fmt(fmt) "%s:%d " fmt, \_\_func\_\_, \_\_LINE\_\_

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Copyright (C) 2010-2012 Synopsys, Inc. (www.synopsys.com) \* \* vineetg: July 10th 2012 \* -Decoupled the driver from arch/arc \* +Using  
platform\_get\_resource() for irq/membase (thx to bfin\_uart.c  
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 Copyright (c) 2010 Alexander Sverdlin <subaparts@yandex.ru> \* \* This driver support CS4271 codec being master or slave, working \* in control port mode, connected either via SPI or I2C. \* The data  
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Copyright (C) 2010 ARM Ltd., Will Deacon <will.deacon@arm.com> * * This code is based on the sparc64 perf event code, which is in turn based * on
the x86 code.
Copyright (C) 2010 ARM Ltd. * Written by Catalin Marinas <catalin.marinas@arm.com> */
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<asm/outercache.h>
Copyright (C) 2010, Arnaldo Carvalho de Melo <acme@redhat.com> */#include <sys/types.h>#include <sys/time.h>#include <time.h>#include
<dirent.h>#include <errno.h>#include <unistd.h>#include "b
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<linux/spinlock.h>#include <linux/skbuff.h>#include <linux/netdevi
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 Copyright (c) 2010 Cavium Networks, Inc. \*/#ifndef \_ASM\_MIPS\_JUMP\_LABEL\_H#define \_ASM\_MIPS\_JUMP\_LABEL\_H  
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 and PPP can be used. But when that port is in-use by PP  
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<linux/slab.h>#include <linux/mm.h>#include <linux/usb.h>
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<linux/slab.h>#include <linux/module.h>#include <linux/mm.h>#inclu
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<linux/memblock.h>#include <linux/mman.h>#include <lin
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Copyright (C) 2010 Guennadi Liakhovetski <g.liakhovetski@gmx.de> */#ifndef SH_DMA_H#define SH_DMA_H
Copyright (C) 2010, Guennadi Liakhovetski <g.liakhovetski@gmx.de> */#ifndef SH_VOUG_H#define SH_VOUG_H
Copyright (C) 2010, Guennadi Liakhovetski <g.liakhovetski@gmx.de> */#include <linux/clk.h>#include <linux/delay.h>#include <linux/err.h>#include
<linux/hdmi.h>#include <linux/irq.h>#include <li
Copyright (C) 2010, Guennadi Liakhovetski <g.liakhovetski@gmx.de> * * Partially inspired by the IMX074 driver from the Android / MSM tree
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Copyright (C) 2010 Gyungoh Yoo <jack.yoo@maxim-ic.com>
Copyright (C) 2010 Hans de Goede <hdegoede@redhat.com> * * Based on the usbvideo v4l1 konicawc driver which is: *
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Copyright (C) 2010 Henrik Rydberg <rydberg@euromail.se> * * Based on hdaps.c driver:
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Copyright (c) 2010 Herbert Xu <herbert@gondor.apana.org.au> * * The following concept of the memory management is used: *
Copyright (c) 2010 H Hartley Sweeten <hsweeten@visionengravers.com> * * This driver controls the pulse width modulated brightness control output, *
BRIGHT, on the Cirrus EP9307, EP9312, and EP9315
Copyright (C) 2010 Hitoshi Mitake <mitake@dcl.info.waseda.ac.jp> */#include <linux/init.h>#include <linux/irq.h>#include
<linux/regulator/fixed.h>#include <linux/regulator/machine.h>#include <l
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Copyright (C) 2010 Igor M. Liplianin <liplianin@netup.ru> */
Copyright (C) 2010 Igor M. Liplianin <liplianin@netup.ru> */#ifndef __ALTERA_CI_H#define __ALTERA_CI_H
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* notified via SCI, mainly the corrected errors.
Copyright (C) 2010, Intel Corp. * Author: Huang Ying <ying.huang@intel.com> * * CPER is the format used to describe platform hardware error by *
various tables, such as ERST, BERT and HEST etc.
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 Copyright (C) 2010 Intel Corporation <tony.luck@intel.com> \*/  
 Copyright (C) 2010 Intel Corporation <tony.luck@intel.com> \* \* This code is the generic layer to export data records from platform \* level persistent storage via a file system.  
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 Copyright (c) 2010 Janusz Krzysztofik \* \* Thanks to Cliff Lawson for his help  
 Copyright (C) 2010 Jarod Wilson <jarod@redhat.com>  
 Copyright(C) 2010 Jarod Wilson <jarod@wilsonet.com> \* Portions based on the original lirc\_imon driver,  
 Copyright (c) 2010 Jarod Wilson <jarod@wilsonet.com> \* \* This driver was based on the work of Greg Wickham and Adrian \* Dewhurst. It was substantially rewritten to support correct signal \* gaps an  
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 Copyright (C) 2010 Jean-Francois Moine (<http://moinejf.free.fr>) \* \*Notes: \* t613 + tas5130A \* \* Focus to light do not balance well as in win. \* Quality in win is not good, but its kinda better.  
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 Copyright (C) 2010 Johan Hovold <jhovold@gmail.com>  
 Copyright (C) 2010 Johan Hovold (jhovold@gmail.com) \* \* This driver allows a USB IrDA device to be used as a "dumb" serial device. \* This can be useful if you do not have access to a full IrDA stac  
 Copyright (C) 2010 Johan Hovold (jhovold@gmail.com) \* \* This program is largely derived from work by the linux-usb group \* and associated source files. Please see the usb/serial files for \* i  
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 Copyright (C) 2010 John Crispin <john@phrozen.org> \*/#ifndef \_LANTIQ\_H\_#define \_LANTIQ\_H\_  
 Copyright (C) 2010 John Crispin <john@phrozen.org> \*/#include <linux/io.h>#include <linux/export.h>#include <linux/init.h>#include <linux/kernel.h>#include <linux/types.h>#include <linux/clk.h>  
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 Copyright (C) 2010 Juan Jes \* \* Inspired by the original lirc\_it87 and lirc\_ite8709 drivers, on top of the \* skeleton provided by the nuvoton-cir driver.  
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 Copyright (c) 2010 Katalix Systems Ltd \*/  
 Copyright (c) 2010 Kees Cook (kees.cook@canonical.com) \* \* This is a common code for verification whether CPU supports \* long mode and SSE or not. It is not called directly instead this \* file i  
 Copyright (C) 2010 Kent Overstreet <kent.overstreet@gmail.com> \* \* Uses a block device as cache for other block devices; optimized for SSDs. \* All allocation is done in buckets, which should match  
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 Copyright (C) 2010, Lars-Peter Clausen <lars@metafoo.de> \* Driver for chargers which report their online status through a GPIO pin \*/  
 Copyright (C) 2010, Lars-Peter Clausen <lars@metafoo.de> \* JZ4740 platform PWM support \* \* Limitations:  
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 Copyright (C) 2010, Lars-Peter Clausen <lars@metafoo.de> \* JZ7420/JZ4740 DMA definitions \*/  
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 Copyright (C) 2010, Laurent Pinchart <laurent.pinchart@ideasonboard.com>  
 Copyright (C) 2010, Laurent Pinchart <laurent.pinchart@ideasonboard.com> \* \* Based on the MT9M001 driver,  
 Copyright (c) 2010 LG Electronics \* Chan Jeong <chan.jeong@lge.com> \* \* lzo\_wrapper.c  
 Copyright (C) 2010 Linus Torvalds \*/  
 Copyright (C) 2010 Linus Walleij for ST-Ericsson \*/#include <linux/init.h>#include <linux/interrupt.h>#include <linux/irq.h>#include  
 <linux/io.h>#include <linux/clockchips.h>#include <linux/clo  
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 \_\_ASM\_ARCH\_NANOENGINE\_H  
 Copyright (C) 2010 Marcelo Roberto Jimenez <mroberto@cpti.cetuc.puc-rio.br> \*/#include <linux/kernel.h>#include <linux/irq.h>#include  
 <linux/pci.h>  
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 Copyright (C) 2010 Marc Zyngier <maz@misterjones.org> \* \* based on fsg-setup.c:  
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 Copyright (C) 2010 \* Marek Vasut <marek.vasut@gmail.com> \*/#ifndef \_\_INCLUDE\_MACH\_PALM27X\_\_#define \_\_INCLUDE\_MACH\_PALM27X\_\_  
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 Copyright (C) 2010 Maxim Levitsky <maximlevitsky@gmail.com> \*/#include <linux/spinlock.h>  
 Copyright (C) 2010 Maxim Levitsky <maximlevitsky@gmail.com> \* \* Special thanks to:  
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 Copyright (C) 2010 Michael Hennerich, Analog Devices Inc. \*/  
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 Copyright (C) 2010 Miguel Gaio <miguel.gαιο@efixo.com> \*/  
 Copyright (c) 2010 Mika Laitio <lamikr@pilppa.org> \* \* This driver will read and write the value of 4 counters to w1\_slave file in \* sys filesystem. \*

Inspired by the w1\_therm and w1\_ds2431 driver  
 Copyright (c) 2010 Mika Westerberg \* \* Based on s3c-ac97 ASoC driver by Jaswinder Singh.  
 Copyright (c) 2010 Mika Westerberg \* \* Based on snappercl15 machine driver by Ryan Mallon.  
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 Copyright (C) 2010 MIPS Technologies, Inc. \* Author: Deng-Cheng Zhu \* \* This code is based on the implementation for ARM, which is in turn \* based on the sparc64 perf event code and the x86 code.  
 Copyright (C) 2010 Nathan Fontenot  
 Copyright (C) 2010 NEC corporation \*/#include <linux/kernel.h>#include <linux/gfp.h>#include <linux/mm.h>#include <linux/mutex.h>#include "avc.h"#include "services.h"  
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 Copyright (C) 2010 Nicholas A. Bellinger <nab@kernel.org> \* \* Authors: Bryant G. Ly <bryantly@linux.vnet.ibm.com> \* Authors: Michael Cyr <mikecyr@linux.vnet.ibm.com>  
 Copyright (c) 2010 Nikolai Kondrashov \*/  
 Copyright (C) 2010 NISHIMOTO Hiroki \*/#include <linux/io.h>#include <linux/kernel.h>#include <linux/module.h>#include <linux/pinctrl/pinconf-generic.h>#include <linux/regulator/driver.h>#include  
 Copyright (C) 2010 Nobuhiro Iwamatsu \*/#include <linux/kernel.h>#include <linux/types.h>#include <linux/init.h>#include <linux/delay.h>#include <linux/pci.h>#include <linux/sh\_intc.h>#include  
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```

Copyright (C) 2010 Nokia Corporation * * Original Driver Author: Imre Deak <imre.deak@nokia.com> * Based on panel-generic.c by Tomi Valkeinen
<tomi.valkeinen@nokia.com> * Adapted to new DSS2 frame
Copyright (C) 2010 Nokia Corporation * * Paul Walmsley
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Copyright (C) 2010 Nokia Corporation * Paul Walmsley * * This data/structures are to be used while defining OMAP on-chip module * data and their
integration with other OMAP modules and Linux.
Copyright (C) 2010 Nokia Corporation * Tony Lindgren <tony@atomide.com> * Added OMAP4 support - Santosh Shilimkar <santosh.shilimkar@ti.com>
*/#include <linux/kernel.h>#include <linux/init.h>
Copyright (C) 2010 Nokia Corporation * Tony Lindgren <tony@atomide.com> * Added OMAP4 support - Santosh Shilimkar <santosh.shilimkar@ti.com>
* * NOTE: This timer is not the same timer as the old O
Copyright (C) 2010 Nokia Corporation * Tony Lindgren <tony@atomide.com> * Santosh Shilimkar <santosh.shilimkar@ti.com> */#include
<linux/kernel.h>#include <linux/init.h>#include <linux/reboot.h>
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Copyright (C) 2010 OKI SEMICONDUCTOR Co., LTD. */#include <linux/module.h>#include <linux/kernel.h>#include <linux/slab.h>#include
<linux/pci.h>#include <linux/gpio/driver.h>#include <linux/int
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Copyright (C) 2010 OMICRON electronics GmbH
Copyright (C) 2010 OMICRON electronics GmbH */
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*/#include <errno.h>#include <fcntl.h>#include <inttypes.
Copyright (C) 2010 OMICRON electronics GmbH */#ifndef _LINUX_POSIX_CLOCK_H_#define _LINUX_POSIX_CLOCK_H_
Copyright (C) 2010 OMICRON electronics GmbH */#ifndef _PTP_PRIVATE_H_#define _PTP_PRIVATE_H_
Copyright (C) 2010 OMICRON electronics GmbH */#include <linux/capability.h>#include <linux/slab.h>
Copyright (C) 2010 OMICRON electronics GmbH */#include <linux/device.h>#include <linux/err.h>#include <linux/gpio.h>#include
<linux/init.h>#include <linux/interrupt.h>#include <linux/io.h>#inc
Copyright (C) 2010 OMICRON electronics GmbH */#include <linux/device.h>#include <linux/export.h>#include <linux/file.h>#include <linux/posix-
clock.h>#include <linux/slab.h>#include <linux/sysca
Copyright (C) 2010 OMICRON electronics GmbH */#include <linux/errqueue.h>#include <linux/phy.h>#include <linux/ptp_classify.h>#include
<linux/skbuff.h>#include <linux/export.h>
Copyright (C) 2010 OMICRON electronics GmbH */#include <linux/idr.h>#include <linux/device.h>#include <linux/err.h>#include
<linux/init.h>#include <linux/kernel.h>#include <linux/module.h>#inc
Copyright (C) 2010 OMICRON electronics GmbH */#include <linux/module.h>#include <linux/posix-clock.h>#include <linux/poll.h>#include
<linux/sched.h>#include <linux/slab.h>#include <linux/timeke
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```

```

<linux/gfp.h>#include <linux/skbuff.h>#include <linux/netfilter
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Copyright (C) 2010 Paul Mundt */#define pr_fmt(fmt) "PCI: " fmt
Copyright (C) 2010 Paul Mundt */#include <linux/bug.h>#include <linux/init.h>#include <linux/kernel.h>#include <linux/ioport.h>#include
<cpu/pfc.h>
Copyright (C) 2010 Paul Mundt */#include <linux/init.h>#include <linux/interrupt.h>#include <linux/gpio/driver.h>#include <linux/irq.h>#include
<linux/kernel.h>#include <linux/spinlock.h>#include
Copyright (C) 2010 Paul Mundt */#include <linux/init.h>#include <linux/io.h>#include <linux/bcd.h>#include <mach/fpga.h>#include <linux/sizes.h>
Copyright (C) 2010 Paul Mundt */#include <linux/init.h>#include <linux/kernel.h>#include <cpu/shx3.h>
Copyright (C) 2010 Paul Mundt */#include <linux/init.h>#include <linux/kernel.h>#include <linux/clk.h>#include <linux/io.h>#include
<linux/clkdev.h>#include <asm/clock.h>#include <asm/freq.h>
Copyright (C) 2010 Paul Mundt */#include <linux/init.h>#include <linux/kernel.h>#include <linux/string.h>#include <mach/fpga.h>
Copyright (C) 2010 Paul Mundt */#include <linux/init.h>#include <linux/platform_device.h>#include <linux/io.h>#include
<linux/regulator/fixd.h>#include <linux/regulator/machine.h>#include <li
Copyright (C) 2010 Paul Mundt */#include <linux/irq.h>#include <mach/fpga.h>#include <mach/irq.h>
Copyright (C) 2010 Paul Mundt * * Modelled after arch/powerpc/kernel/irq.c.
Copyright (c) 2010 Paul Thomas <pthomas8589@gmail.com> * * You need to have something like this in struct spi_board_info
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Copyright (c) 2010 Pengutronix e.K. * Author: Wolfram Sang <w.sang@pengutronix.de> */
Copyright (C) 2010 Pengutronix, Marc Kleine-Budde <kernel@pengutronix.de> */#include "../hardware.h"#include "devices-common.h"
Copyright (C) 2010 Pengutronix * Uwe Kleine-Koenig <u.kleine-koenig@pengutronix.de> */#include "devices-common.h"
Copyright (C) 2010 Pengutronix * Uwe Kleine-Koenig <u.kleine-koenig@pengutronix.de> */#include "devices/devices-common.h"
Copyright (C) 2010 Pengutronix * Uwe Kleine-Koenig <u.kleine-koenig@pengutronix.de> */#include "../hardware.h"#include "devices-common.h"
Copyright (C) 2010 Pengutronix * Uwe Kleine-Koenig <u.kleine-koenig@pengutronix.de> */#include <linux/dma-mapping.h>
Copyright (C) 2010 Pengutronix * Uwe Kleine-Koenig <u.kleine-koenig@pengutronix.de> */#include <linux/dma-mapping.h>#include <linux/sizes.h>
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Copyright (c) 2010 Red Hat Inc. * Author : Dave Airlie <airlied@redhat.com> *
Copyright (c) 2010 Red Hat Inc. * Author : Dave Airlie <airlied@redhat.com> * * ATPX support for both Intel/ATI
Copyright(C) 2010 Red Hat. */#include <linux/kernel.h>#include <linux/module.h>#include <linux/init.h>#include <linux/mxm-wmi.h>#include
<linux/acpi.h>
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implementation. * Some chunks also taken
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```



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 <linux/ioport.h>#include <linux/device.h>#include <linux/io.h>#include  
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<linux/delay.h>
Copyright (C) 2010 Tanguy Bouzeloc <tanguy.bouzeloc@efixo.com> */
Copyright (C) 2010 Tejun Heo <tj@kernel.org> * * Chunks are allocated as a contiguous kernel memory using gfp * allocation. This is to be
used on nommu architectures.
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<linux/init.h>#include <linux/kthread.h>#include <linux/ex
Copyright (C) 2010 Tejun Heo <tj@kernel.org> * * This is the generic async execution mechanism. Work items as are * executed in process
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<linux/platform_device.h>#include <linux/delay.h>#include <lin
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Copyright (C) 2010 Texas Instruments Inc */#ifndef _VPBE_VENC_H#define _VPBE_VENC_H
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<linux/errno.h>#include <linux/fs.h>#include <linux/string.h>#include
Copyright (C) 2010 Texas Instruments Inc */#include <linux/module.h>#include <linux/mod_devicetable.h>#include <linux/kernel.h>#include
<linux/init.h>#include <linux/ctype.h>#include <linux/del
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Copyright (C) 2010 Texas Instruments Incorporated - http://www.ti.com/ */#include <linux/kernel.h>#include <linux/init.h>#include
<linux/module.h>#include <linux/errno.h>#include <linux/interrupt
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__ARCH_ARM_PLAT_OMAP_INCLUDE_PLAT_L3_2XXX_H#define __ARCH_ARM_PLAT_OMAP_INCLUDE_PLAT_L3_2XXX_H
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__ARCH_ARM_PLAT_OMAP_INCLUDE_PLAT_L3_3XXX_H#define __ARCH_ARM_PLAT_OMAP_INCLUDE_PLAT_L3_3XXX_H
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<linux/percpu.h>#include <linux/input.h>
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buffer setup from sysfs and starts a short capture
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| (r))#define SMIAPP_REG_MK_U16(r) ((SMIAPP_REG_16BIT
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Copyright (C) 2011 ARM Ltd. \* Author: Catalin Marinas <catalin.marinas@arm.com> \* based on arch/arm/mm/proc-v7-2level.S \*/#include <asm/asm/asm.h>  
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\_ASM\_PGTABLE\_3LEVEL\_H  
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\_ASM\_PGTABLE\_3LEVEL\_HWDEF\_H  
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 driver began life based an an old version of th  
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 Copyright (C) 2011 by Kevin Cernekee (cernekee@gmail.com) \* \* Definitions for BMIPS processors  
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 <linux/hrtimer.h>struct sirfsoc\_uart\_param { const char \*u  
 Copyright (c) 2011 Cambridge Silicon Radio Limited, a CSR plc group company. \*/#include <linux/module.h>#include <linux/io.h>#include  
 <linux/of.h>#include <linux/clk.h>#include <linux/pm\_runtime  
 Copyright (c) 2011 Cambridge Silicon Radio Limited, a CSR plc group company. \*/#include <linux/module.h>#include <sound/soc.h>#include  
 <sound/dmaengine\_pcm.h>  
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 Copyright (c) 2011 DENX Software Engineering GmbH \*/#include <linux/kernel.h>#include <linux/errno.h>#include <linux/init.h>#include  
 <linux/module.h>#include <linux/mii.h>#include <linux/phy.h>

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Copyright (c) 2011 Florian Westphal <fw@strlen.de> \*/#define pr\_fmt(fmt) KBUILD\_MODNAME ": " fmt#include <linux/module.h>#include  
<linux/skbuff.h>#include <linux/netdevice.h>#include <linux/rout  
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<linux/ethtool.h>#include <linux/mii.h>#include <linux/usb.h>#incl
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interrupts thus * allowing that driver's code to
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<linux/pinctrl/pinconf-generic.h>
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<linux/io.h>#include <linux/irqchip.h>#include <linux/irqchip/
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Copyright (C) 2011 LAPIS Semiconductor Co., Ltd. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/kernel.h>#include
<linux/module.h>#include <linux/pci.h>#include <linux/delay.h>#in
Copyright (C) 2011 LAPIS Semiconductor Co., Ltd. */#if defined(CONFIG_SERIAL_PCH_UART_CONSOLE) && defined(CONFIG_MAGIC_SYSRQ)#define
SUPPORT_SYSRQ#endif#include <linux/kernel.h>#include <linux/s
Copyright (C) 2011 LAPIS Semiconductor Co., Ltd. */#include <linux/gpio/driver.h>#include <linux/interrupt.h>#include <linux/irq.h>#include
<linux/kernel.h>#include <linux/module.h>#include <li
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<linux/of_address.h>#include <linux/of_platform.h>
Copyright (C) 2011 Magnus Damm */#include <linux/kernel.h>#include <linux/init.h>#include <linux/smp.h>#include <linux/spinlock.h>#include
<linux/io.h>#include <linux/delay.h>#include <linux/
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<linux/delay.h>#include <linux/clk.h>#include <linux/s  
Copyright (C) 2011 Marvell International Ltd. \*/#include <linux/module.h>#include <linux/init.h>#include <linux/platform\_device.h>#include  
<linux/slab.h>#include <linux/dma-mapping.h>#include <  
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<linux/bsg-lib.h>#include <linux/export.h>#i  
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<asm/export.h>#include <asm/regdef.h>  
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<linux/input.h>#include <linux/hid.h>#include <  
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 Copyright (C) 2011 Rafael J. Wysocki <rjw@sisk.pl>, Renesas Electronics Corp. \*/#include <linux/kernel.h>#include <linux/pm\_domain.h>#include <linux/pm\_qos.h>#include <linux/hrtimer.h>#include <  
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```

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<linux/list.h>#include <linux/module.h>#include <linux/platform_
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<linux/init.h>#include <linux/err.h>#include <linux/slab.h>#include <
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 <linux/slab.h>#include <linux/err.h>#include <linux/of.h>  
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_ASM_C6X_DSCR_H
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_ASM_C6X_UACCESS_H
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<linux/memblock.h>
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<linux/delay.h>#include <linux/errno.h>#include <linux/string.h>
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<linux/ctype.h>#include <linux/etherdevice.h>#include <asm/setup
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<linux/of_address.h>#include <linux/io.h>
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<linux/irqdomain.h>#include <linux/interrupt.h>#include <linux/io.h>#include <linux/irq.h>#include <linux/slab.h>#include <linux/export.h>#include <linux/irqdomain.h>#include <linux/interrupt.h>#include  
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Copyright (C) 2011 Tobias Klauser <tklauser@distanz.ch> \* \* Based on the code posted by Kazuyasu on the Altera Forum at:  
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Copyright (c) 2011, Tom Herbert <therbert@google.com> \*/  
<linux/dynamic\_queue\_limits.h>#include <linux/compiler.h>#include <linux/types.h>#include <linux/kernel.h>#include <linux/jiffies.h>#include <linux/dynamic\_queue\_limits.h>#include <linux/compiler.h>  
Copyright (c) 2011, Tom Herbert <therbert@google.com> \* \* This header file contains the definitions for dynamic queue limits (dql). \* dql would be used in conjunction with a producer/consumer type  
Copyright (c) 2011 Tony Prisk <linux@prisktech.co.nz> \* \* This file is based on uhci-glib.c  
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Copyright (c) 2011 Unixphere \* \* This driver adds support for Bosch Sensortec's digital acceleration \* sensors BMA150 and SMB380. \* The SMB380 is fully compatible with BMA150 and only differs in p  
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Copyright (c) 2011 Vista Silicon S.L. \* Javier Martin <javier.martin@vista-silicon.com> \*/  
<linux/slab.h>#include <linux/interrupt.h>#include <linux/module.h>#include <linux/clk.h>#include <linux/slab.h>#include <linux/interrupt.h>#include  
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Copyright (c) 2011 Werner Almesberger <werner@almesberger.net> \* \* USB initialization is  
Copyright (c) 2011 Wind River Systems, Inc. \* \* Author: Anders Wallin <anders.wallin@windriver.com>  
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<linux/mm.h>#include <linux/memblock.h>#include <linux/bug.h>#include <linux/kernel.h>#include <linux/mm.h>#include <linux/memblock.h>#include  
Copyright (C) 2011 Wind River Systems, \* written by Ralf Baechle <ralf@linux-mips.org> \*/  
<linux/compiler.h>#include <linux/elf-

```

randomize.h>#include <linux/errno.h>#include <linux/mm.h
Copyright (C) 2011 Wistron Co.Ltd * Joseph Lai <joseph_lai@wistron.com> and trimmed down by * Alan Cox <alan@linux.intel.com> in turn based on
bma023.c. * Device behaviour based on a misc driver po
Copyright (C) 2011 Wolfram Sang, Pengutronix e.K. */
Copyright (C) 2011 Wolfram Sang, Pengutronix e.K. */#ifndef __LINUX_STMP3XXX_RTC_WDT_H#define __LINUX_STMP3XXX_RTC_WDT_H
Copyright (C) 2011 Xilinx */
Copyright (C) 2011 Xilinx */#define UART_CR_OFFSET          0x00 /* Control Register [8:0] */#define UART_SR_OFFSET          0x2C /*
Channel Status [11:0] */#define UART_FIFO_OFFSET          0x30 /* FIFO [15:0] or [7:0
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Vinod Koul ,vinod.koul@intel.com> */#ifndef _PLATFORM_SST
Copyright (C) 2012-14 Renesas Solutions Corp. * Kuninori Morimoto <kuninori.morimoto.gx@renesas.com> ** This file is based on the
drivers/i2c/busses/i2c-sh7760.c
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Copyright (C) 2012-2013, Guennadi Liakhovetski <g.liakhovetski@gmx.de> ** ATTENTION: This is a temporary API and it shall be replaced by the
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Copyright: (C) 2012-2013 Linaro Limited \* \* Code to handle secondary CPU bringup and hotplug for the cluster power API.  
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used here.  
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Copyright (C) 2012-2013 Linus Walleij \*/#include <linux/clk-provider.h>#include <linux/clkdev.h>#include <linux/err.h>#include <linux/io.h>#include  
<linux/platform\_data/clk-integrator.h>  
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fmt, \_\_func\_\_, \_\_LINE\_\_  
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Roger Quadros <rogerq@ti.com> \*/#include <linux/kernel.h>#  
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Copyright (C) 2012-2013 Tieto Poland \*/  
Copyright (C) 2012-2013 Uwe Kleine-Koenig for Pengutronix \*/#include <linux/kernel.h>#include <linux/io.h>#include <linux/spi/spi.h>#include  
<linux/spi/spi\_bitbang.h>#include <linux/gpio.h>#inc  
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Copyright (C) 2012-2013 Xilinx, Inc.  
Copyright (c) 2012-2013 Xilinx \*/#include <linux/linkage.h>#include <linux/init.h>#include <asm/assembler.h>  
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Copyright (C) 2012-2015 Altera Corporation \*/#include <linux/irqchip.h>#include <linux/of\_address.h>#include <linux/of\_irq.h>#include  
<linux/of\_platform.h>#include <linux/reboot.h>#include <lin  
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<linux/of_platform.h>#include <linux/pinctrl/consumer.h>#include <
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"rmi_driver.h"#include "rmi_2d_sensor.h"
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Copyright (C) 2012-2017 Cavium Inc. */#include <linux/dma-mapping.h>#include <linux/gpio/consumer.h>#include <linux/interrupt.h>#include
<linux/mmc/mmc.h>#include <linux/mmc/slot-gpio.h>#include
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<linux/iopoll.h>#include <linux/irq.h>#include <linux/module.h>#i
Copyright (c) 2012-2017 Mentor Graphics Inc. */#include <linux/delay.h>#include <linux/interrupt.h>#include <linux/module.h>#include
<linux/sched.h>#include <linux/slab.h>#include <linux/spinl
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interrupt remapping code to * the x86 interrupt man
Copyright (C) 2012 Advanced Micro Devices, Inc. * Author: Joerg Roedel <jroedel@suse.de> * * This header file contains stuff that is shared between
different interrupt * remapping drivers but with
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mapping.h>#include <linux/fsl_devices.h>#include <linux/gpio
Copyright (C) 2012 Alan Ott <alan@signal11.us> * Signal 11 Software */
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sensor-hub.h>#include <linux/iio/iio.h>#include <
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Copyright (C) 2012 Altera Corporation */#include <linux/delay.h>#include <linux/init.h>#include <linux/smp.h>#include <linux/io.h>#include
<linux/of.h>#include <linux/of_address.h>
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Copyright (C) 2012 Amit Daniel <amit.kachhap@linaro.org> */#include <linux/clk.h>#include <linux/cpufreq.h>#include <linux/device.h>#include
<linux/err.h>#include <linux/idr.h>#include <linux/
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Copyright (c) 2012 Andreas H * * R.A.T.7, R.A.T.9, M.M.O.7 (USB gaming mice):
Copyright (C) 2012 Andrew Lunn <andrew@lunn.ch> * * Based on the code from: *
Copyright (C) 2012 Andrew Lunn */#include <linux/kernel.h>#include <linux/init.h>#include <linux/platform_device.h>#include
<linux/of_platform.h>#include <linux/module.h>#include <linux/reboot.
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Copyright (C) 2012 Anssi Hannula <anssi.hannula@iki.fi> * * This keymap is for a variant that has a distinctive scrollwheel instead of * up/down buttons
(tested with P/N 40009936 / 20018268), repor
Copyright (C) 2012 Anssi Hannula <anssi.hannula@iki.fi> * * This keymap is for several Medion X10 remotes that have the Windows MCE * button. This
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Copyright (C) 2012 ARM Ltd. \* Author: Marc Zyngier <marc.zyngier@arm.com> \*/#ifndef \_\_ASM\_ARCH\_TIMER\_H#define \_\_ASM\_ARCH\_TIMER\_H  
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Copyright (C) 2012 ARM Ltd. \* \* Based on arch/x86/include/asm/hugetlb.h.  
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Copyright (C) 2012 ARM Ltd. \* \* Based on arch/x86/include/asm/hugetlb.h and Bill Carson's patches  
Copyright (C) 2012 ARM Ltd. \*/#ifndef CONFIG\_COMPAT#define \_\_ARCH\_WANT\_COMPAT\_STAT64#define \_\_ARCH\_WANT\_SYS\_GETHOSTNAME#define  
\_\_ARCH\_WANT\_SYS\_PAUSE#define \_\_ARCH\_WANT\_SYS\_GETPGRP#define \_\_ARCH  
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Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_BARRIER\_H#define \_\_ASM\_BARRIER\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_BITOPS\_H#define \_\_ASM\_BITOPS\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_CACHEFLUSH\_H#define \_\_ASM\_CACHEFLUSH\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_CACHE\_H#define \_\_ASM\_CACHE\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_CMPXCHG\_H#define \_\_ASM\_CMPXCHG\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_COMPAT\_H#define \_\_ASM\_COMPAT\_H#ifndef CONFIG\_COMPAT  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_CPUTYPE\_H#define \_\_ASM\_CPUTYPE\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_DEBUG\_MONITORS\_H#define \_\_ASM\_DEBUG\_MONITORS\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_DEVICE\_H#define \_\_ASM\_DEVICE\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_ELF\_H#define \_\_ASM\_ELF\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_EXCEPTION\_H#define \_\_ASM\_EXCEPTION\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_EXEC\_H#define \_\_ASM\_EXEC\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_FB\_H#define \_\_ASM\_FB\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_FP\_H#define \_\_ASM\_FP\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_FUTEX\_H#define \_\_ASM\_FUTEX\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_HARDIRQ\_H#define \_\_ASM\_HARDIRQ\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_HW\_BREAKPOINT\_H#define \_\_ASM\_HW\_BREAKPOINT\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_HWCAP\_H#define \_\_ASM\_HWCAP\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_IO\_H#define \_\_ASM\_IO\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_IRQFLAGS\_H#define \_\_ASM\_IRQFLAGS\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_MMU\_CONTEXT\_H#define \_\_ASM\_MMU\_CONTEXT\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_MMU\_H#define \_\_ASM\_MMU\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_MODULE\_H#define \_\_ASM\_MODULE\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_PAGE\_H#define \_\_ASM\_PAGE\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_PGALLOC\_H#define \_\_ASM\_PGALLOC\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_PGTABLE\_H#define \_\_ASM\_PGTABLE\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_PGTABLE\_HWDEF\_H#define \_\_ASM\_PGTABLE\_HWDEF\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_PROCESSOR\_H#define \_\_ASM\_PROCESSOR\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_PROCFNS\_H#define \_\_ASM\_PROCFNS\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_PTRACE\_H#define \_\_ASM\_PTRACE\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_SHMPARAM\_H#define \_\_ASM\_SHMPARAM\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_SIGNAL32\_H#define \_\_ASM\_SIGNAL32\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_SMP\_H#define \_\_ASM\_SMP\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_SPARSEMEM\_H#define \_\_ASM\_SPARSEMEM\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_SPINLOCK\_H#define \_\_ASM\_SPINLOCK\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_SPINLOCK\_TYPES\_H#define \_\_ASM\_SPINLOCK\_TYPES\_H  
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Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_STAT\_H#define \_\_ASM\_STAT\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_SYSCALL\_H#define \_\_ASM\_SYSCALL\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_SYSTEM\_MISC\_H#define \_\_ASM\_SYSTEM\_MISC\_H  
Copyright (C) 2012 ARM Ltd. \*/#ifndef \_\_ASM\_THREAD\_INFO\_H#define \_\_ASM\_THREAD\_INFO\_H

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Copyright (C) 2012 ARM Ltd. */#ifndef __ASM_TLBFLUSH_H#define __ASM_TLBFLUSH_H
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Copyright (C) 2012 ARM Ltd. */#ifndef __ASM_TRAP_H#define __ASM_TRAP_H
Copyright (C) 2012 ARM Ltd. */#ifndef __ASM_UACCESS_H#define __ASM_UACCESS_H
Copyright (C) 2012 ARM Ltd. */#ifndef __ASSEMBLY__#error "Only include this from assembly code"#endif
Copyright (C) 2012 ARM Ltd. */#ifndef __CLKSOURCE_ARM_ARCH_TIMER_H#define __CLKSOURCE_ARM_ARCH_TIMER_H
Copyright (C) 2012 ARM Ltd. */#include <linux/kernel.h>#include <linux/export.h>#include <linux/ftrace.h>#include <linux/kprobes.h>#include
<linux/sched.h>#include <linux/sched/debug.h>#include
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<linux/device.h>#include <linux/platform_device.h>#include
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Copyright (C) 2012 Bernd Krumböck (krumböck@universalnet.at) * * This driver is inspired by the 3.2.0 version of drivers/net/can/usb/ems_usb.c *
and drivers/net/can/usb/esd_usb2.c
Copyright (c) 2012 Bernhard Seibold
Copyright (C) 2012 Bertrand Achard (nvram access fixes) */
Copyright (C) 2012 Bill Pemberton (wfp5p@virginia.edu) * * These devices all have only 1 bulk in and 1 bulk out that is shared * for all serial ports.
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Copyright (c) 2012 Bj * * The probing code is heavily inspired by cdc_ether, which is:
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Copyright (c) 2012 Broadcom Corporation */#ifndef WL_CFGP2P_H#define WL_CFGP2P_H
Copyright (c) 2012 Broadcom Corporation */#include <linux/debugfs.h>#include <linux/netdevice.h>#include <linux/module.h>#include
<linux/devcoredump.h>
Copyright (c) 2012 Broadcom Corporation */#include <linux/netdevice.h>
Copyright (c) 2012 Broadcom Corporation */#include <linux/slab.h>#include <linux/netdevice.h>#include <linux/etherdevice.h>#include
<linux/rtnetlink.h>#include <net/cfg80211.h>
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<linux/nfs4_mount.h>#include <linux/nfs_fs.h>#include "delegation.h"#include
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Copyright (c) 2012 Coraid, Inc. See COPYING for GPL terms./ \* \* aoemain.c \* Module initialization routines, discover timer  
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Copyright (c) 2012 David Dillow <dave@thedillows.org>  
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<masami.hiramatsu.pt@hitachi.com> \*/  
Copyright (C) 2012 Huacai Chen (chenhc@lemote.com) \*/#ifndef \_\_ASM\_MACH\_LOONGSON64\_KERNEL\_ENTRY\_H#define  
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 <linux/interrupt.h>#include <linux/irq.h>#include <linux/ke  
 Copyright (c) 2012, Intel Corporation. \*/#include <linux/device.h>#include <linux/platform\_device.h>#include <linux/module.h>#include  
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 <linux/iio/buffer.h>#include <linux/regmap.h>#in  
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 <linux/platform\_device.h>#include <linux/gpio/consumer.h>#include <linux/of\_pl  
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<asm/mach/arch.h>#include <asm/mach/map.h>
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mapping.h>#include <linux/platform_device.h>#include "mmpf
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<linux/ioport.h>#include <linux/uaccess.h>#inclu
Copyright (C) 2012 MIPS Technologies, Inc. */#include <linux/kernel.h>#include <linux/sched.h>#include <linux/smp.h>#include
<linux/mm.h>#include <linux/proc_fs.h>
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<linux/kernel.h>#include <linux/ioport.h>#include <cpu/pfc.h>
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<linux/interrupt.h>#include <linux/input.h>#include <linux/smsc911x
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\_\_LINUX\_OF\_GRAPH\_H  
Copyright (C) 2012 Renesas Electronics Corp. \* Author: Guennadi Liakhovetski <g.liakhovetski@gmx.de> \*/#ifndef \_\_V4L2\_FWNODE\_H#define  
\_\_V4L2\_FWNODE\_H  
Copyright (C) 2012 Renesas Electronics Corp. \* Author: Guennadi Liakhovetski <g.liakhovetski@gmx.de> \*/#include <linux/acpi.h>#include  
<linux/kernel.h>#include <linux/mm.h>#include <linux/module  
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Copyright (C) 2012 Renesas Electronics Europe \*/#include <linux/init.h>#include <linux/types.h>#include <linux/platform\_device.h>#include  
<linux/interrupt.h>#include <linux/input.h>#include <li  
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Copyright (C) 2012 Renesas Electronics Europe Ltd \*/#include <linux/platform\_device.h>#include <linux/init.h>#include <linux/serial.h>#include  
<linux/serial\_sci.h>#include <linux/usb/r8a66597.h  
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Copyright (C) 2012 Renesas Solutions Corp. \*/#ifndef \_\_SH\_PFC\_CORE\_H\_#define \_\_SH\_PFC\_CORE\_H\_  
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<linux/err.h>#include <linux/irq.h>#include <linux/interrupt.  
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Copyright (C) 2012 Robin Gareus <robin@gareus.org>  
Copyright (C) 2012 ROHM CO.,LTD. \*/#include <linux/delay.h>#include <linux/firmware.h>#include <linux/i2c.h>#include <linux/input.h>#include  
<linux/input/mt.h>#include <linux/interrupt.h>#incl  
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Copyright (C) 2012 Russell King \*/#ifndef ARMADA\_CRTC\_H#define ARMADA\_CRTC\_H  
Copyright (C) 2012 Russell King \*/#ifndef ARMADA\_DRM\_H#define ARMADA\_DRM\_H  
Copyright (C) 2012 Russell King \*/#ifndef ARMADA\_FB\_H#define ARMADA\_FB\_H  
Copyright (C) 2012 Russell King \*/#ifndef ARMADA\_GEM\_H#define ARMADA\_GEM\_H  
Copyright (C) 2012 Russell King \*/#ifndef ARMADA\_IOCTL\_H#define ARMADA\_IOCTL\_H  
Copyright (C) 2012 Russell King \*/#ifndef VIRT\_DMA\_H#define VIRT\_DMA\_H  
Copyright (C) 2012 Russell King \*/#include <linux/device.h>#include <linux/dmaengine.h>#include <linux/module.h>#include <linux/spinlock.h>  
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Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com * * Common Header for S3C24XX SoCs
Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */#define GC_THREAD_MIN_WB_PAGES 1 /*
* a threshold to determine * whether IO subsystem is idle
Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */#ifndef _LINUX_F2FS_FS_H#define _LINUX_F2FS_FS_H
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Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */#include <linux/blkdev.h>#include <linux/backing-dev.h>
Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */#include <linux/fs.h>#include <linux/bio.h>#include
<linux/mpage.h>#include <linux/writeback.h>#include
Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */#include <linux/fs.h>#include <linux/f2fs_fs.h>#include
"f2fs.h"#include "node.h"#include "segment.h"
Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */#include <linux/fs.h>#include <linux/f2fs_fs.h>#include
<linux/bio.h>#include <linux/blkdev.h>#include <
Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */#include <linux/fs.h>#include <linux/f2fs_fs.h>#include
<linux/buffer_head.h>#include <linux/backing-dev.
Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */#include <linux/fs.h>#include <linux/f2fs_fs.h>#include
<linux/buffer_head.h>#include <linux/mpage.h>#in
Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */#include <linux/fs.h>#include <linux/f2fs_fs.h>#include
<linux/mpage.h>#include <linux/backing-dev.h>#in
Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */#include <linux/fs.h>#include <linux/f2fs_fs.h>#include
<linux/pagemap.h>#include <linux/sched.h>#includ
Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */#include <linux/fs.h>#include <linux/f2fs_fs.h>#include
<linux/sched/signal.h>#include <linux/unicode.h>
Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */#include <linux/fs.h>#include <linux/f2fs_fs.h>#include
<linux/stat.h>#include <linux/buffer_head.h>#inc
Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */#include <linux/fs.h>#include <linux/module.h>#include
<linux/backing-dev.h>#include <linux/init.h>#incl
Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */#include <linux/module.h>#include <linux/init.h>#include
<linux/fs.h>#include <linux/statfs.h>#include <
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(Rotator) driver
Copyright (c) 2012 Samsung Electronics Co., Ltd. * http://www.samsung.com/ */* start node id of a node block dedicated to the given node id
*/#define START_NID(nid) (((nid) / NAT_ENTR
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*/#ifndef S5C73M3_H#define S5C73M3_H
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Copyright (C) 2012 Samsung Electronics// SangYoung Son <hello.son@samsung.com>
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<linux/clk.h>#include <linux/io.h>#include <linux/clkdev.h>#include
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<linux/of.h>#include <video/display_timing.h>#include <vi
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Linus Walleij <linus.walleij@linaro.org>
Copyright (C) 2012 ST-Ericsson SA * Written on behalf of Linaro for ST-Ericsson * This interface is used in the core to keep track of pins. * * Author:
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Copyright (c) 2012 Texas Instruments, Inc \* \* Initially based on sound/soc/codecs/wm8350.c  
Copyright (C) 2012 Texas Instruments\*/#include <linux/module.h>#include <linux/delay.h>#include <linux/i2c.h>#include <linux/leds.h>#include  
<linux/slab.h>#include <linux/platform\_device.h>#in  
Copyright (C) 2012 Texas Instruments\*/#include <linux/module.h>#include <linux/slab.h>#include <linux/i2c.h>#include <linux/backlight.h>#include  
<linux/err.h>#include <linux/delay.h>#include <  
Copyright (C) 2012 Texas Instruments\*/#include <linux/module.h>#include <linux/slab.h>#include <linux/i2c.h>#include <linux/leds.h>#include  
<linux/backlight.h>#include <linux/err.h>#include <  
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Copyright (C) 2012 Texas Instruments Incorporated - http://www.ti.com \* Author: Sebastian Andrzej Siewior <bigeasy@linutronix.de> \* \* A lot of code  
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Copyright (C) 2012 Texas Instruments, Inc. \* Paul Walmsley \* \* A small number of IP blocks need custom reset and preprogramming \* functions. The  
stubs in this file provide a standard way for the  
Copyright (C) 2012 Texas Instruments, Inc. \* Paul Walmsley \* \* Based on the I2C reset code in arch/arm/mach-omap2/i2c.c by \* Avinash.H.M  
<avinashhm@ti.com>  
Copyright (C) 2012 Texas Instruments, Inc. \* Paul Walmsley \*/#ifndef ARCH\_ARM\_MACH\_OMAP2\_HDQ1W\_H#define  
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database or other technical documentation.  
Copyright (C) 2012 Texas Instruments, Inc. \* Paul Walmsley \* \* XXX handle crossbar/shared link difference for L3? \* XXX these should be marked

```

initdata for multi-OMAP kernels
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Copyright (C) 2012 Texas Instruments, Inc. * Written by Tony Lindgren <tony@atomide.com> * * Modified for clock framework by Paul Mundt
<paul.mundt@nokia.com>.
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<linux/compiler.h>#include <linux/errno.h>#include <linux/s
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<linux/memblock.h>#include <linux/of\_address.h>  
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<linux/smp.h>#include <linux/cpu.h>  
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 <stdbool.h>#include <string.h>#include <linux/bpf.h>  
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<linux/kernel.h>#include <linux/of.h>#include <linux/module.h>#in  
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<linux/notifier.h>#include <linux/mfd/syscon.h>#  
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<linux/spinlock.h>#include <linux/io.h>#include <linux/of.h>#include <l  
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<linux/vmalloc.h>#include <linux/lz4.h>#include <crypto/in  
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\_\_builtin\_ctz, and therefore they can't be remov  
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<linux/slab.h>#include <linux/err.h>#include "thermal\_hwmon.h"  
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<linux/dmapool.h>#include <linux/dmaengine.h>#include <linux/init  
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Copyright (C) 2013 Imagination Technologies Ltd. \*/#include <linux/device.h>#include <linux/fs.h>#include <linux/err.h>#include  
<linux/wait.h>#include <linux/sched.h>#include <linux/interrupt.h  
Copyright (C) 2013 Imagination Technologies Ltd. \*/#include <linux/device.h>#include <linux/fs.h>#include <linux/err.h>#include  
<linux/wait.h>#include <linux/sched.h>#include <linux/smp.h>  
Copyright (C) 2013 Imagination Technologies Ltd. \*/#include <linux/errno.h>#include <linux/sched.h>#include <linux/sched/debug.h>#include  
<linux/sched/task.h>#include <linux/sched/task\_stack.h>

```

Copyright (C) 2013 Imagination Technologies Ltd. */#include <linux/kernel.h>#include <linux/device.h>#include <linux/fs.h>#include
<linux/slab.h>#include <linux/export.h>
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<linux/moduleloader.h>#include <linux/atomic.h>#include <lin
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controller is located in the South Bridge *
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<linux/ctype.h>#include <linux/errno.h>#include
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<linux/kernel.h>#include <linux/kprobes.h>#include <linux/extable.h>  
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Copyright (C) 2013 Magnus Damm */#include <linux/cpu.h>#include <linux/delay.h>#include <linux/init.h>#include <linux/io.h>#include
<linux/smp.h>#include <asm/cacheflush.h>#include <asm/smp_p
Copyright (C) 2013 Magnus Damm */#include <linux/cpu_pm.h>#include <linux/delay.h>#include <linux/init.h>#include <linux/io.h>#include
<linux/ioport.h>#include <linux/of_address.h>#include <l
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<linux/ctype.h>#include <linux/errno.h>#include <linux/unistd.h>#inclu
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Copyright (c) 2013 * Minchan Kim <minchan@kernel.org> */#include <linux/types.h>#include <linux/mutex.h>#include <linux/slab.h>#include
<linux/buffer_head.h>#include <linux/sched.h>#include <
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<linux/gpio/machine.h>#include <linux/init.h>#include <linux/kernel.h
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<linux/clk-provider.h>#include <linux/err.h>#include

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<linux/module.h>#include <linux/of.h>#include <linux/mutex.h>#include
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 drivers/devfreq/exynos/exynos\_ppmu.c  
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Copyright (c) 2014-2016 Christoph Hellwig. \*/#include <linux/sunrpc/svc.h>#include <linux/blkdev.h>#include <linux/nfs4.h>#include <linux/nfs\_fs.h>#include <linux/nfs\_xdr.h>#include <linux/pr.h  
Copyright (c) 2014-2016 Christoph Hellwig. \*/#include <linux/sunrpc/svc.h>#include <linux/exportfs.h>#include <linux/iomap.h>#include <linux/nfs4.h>  
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Copyright (c) 2014-2016, Intel Corporation. \*/#include "test/nfit\_test.h"#include <linux/blkdev.h>#include <pmem.h>#include <nd.h>  
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Copyright (c) 2014-2016 Mentor Graphics Inc. \*/#include <media/v4l2-device.h>#include <media/v4l2-subdev.h>#include "imx-media.h"#include "imx-ic.h"  
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Copyright (C) 2014-2016 Pratyush Anand <panand@redhat.com> \*/#include <linux/highmem.h>#include <linux/ptrace.h>#include

```

<linux/uprobes.h>#include <asm/cacheflush.h>
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    Copyright (c) 2014-2016 Qualcomm Atheros, Inc. */#ifndef _THERMAL_#define _THERMAL_
    Copyright (C) 2014-2016 Ricardo Ribalda, Qtechnology A/S */#include <linux/module.h>#include <linux/pci.h>#include <linux/pnp.h>#include
<linux/kernel.h>#include <linux/serial_core.h>#include <
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<linux/io.h>#include <linux/pci.h>  
Copyright (C) 2014 Akihiro Tsukada <tskd08@gmail.com> \*/#include <linux/dma-mapping.h>#include <linux/kernel.h>#include <linux/pci.h>  
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<linux/hw\_random.h>#include <linux/slab.h>#inc  
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\_\_LINUX\_FS\_NFS\_NFS4\_2XDR\_H  
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<linux/nfs.h>#include <linux/nfs3.h>#include <linux/nfs4.h>#include

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 \*/#include <linux/cpu.h>#include <linux/interrupt.h>#include <li  
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 Copyright (C) 2014 ARM Ltd. \*/#ifndef \_\_ASM\_PTDUMP\_H#define \_\_ASM\_PTDUMP\_H  
 Copyright (C) 2014 ARM Ltd.#ifndef \_\_ASM\_PTDUMP\_H#define \_\_ASM\_PTDUMP\_H  
 Copyright (C) 2014 ARM Ltd. \*/#include <asm/arch\_timer.h>#include <asm/cache.h>#include <asm/cpu.h>#include <asm/cputype.h>#include  
 <asm/cpufeature.h>#include <asm/fpsimd.h>  
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Copyright (c) 2014 Broadcom Corporation */#ifndef CONFIG_OFvoid brcmf_of_probe(struct device *dev, enum brcmf_bus_type bus_type,
struct brcmf_mp_device *settings);#elsestatic void brcmf_of_p
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Copyright (c) 2014 Broadcom Corporation */#include <linux/init.h>#include <linux/of.h>#include <linux/of_irq.h>
Copyright (c) 2014 Broadcom Corporation */#include <linux/kernel.h>#include <linux/delay.h>#include <linux/list.h>#include
<linux/ssb/ssb_regs.h>#include <linux/bcma/bcma.h>#include <linux/bcma
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<linux/sched.h>#include <linux/sunrpc/addr.h>
Copyright (c) 2014 Christoph Hellwig. */#include "xfs.h"#include "xfs_shared.h"#include "xfs_format.h"#include "xfs_log_format.h"#include
"xfs_trans_resv.h"#include "xfs_mount.h"#include "xfs_
Copyright (C) 2014 Christoph Hellwig * * SCSI queueing library. * Initial versions: Eric Youngdale (eric@andante.org). * Based upon
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Copyright (C) 2014 Imagination Technologies Ltd. */#ifndef __UAPI_ASM_INST_H#define __UAPI_ASM_INST_H
Copyright (C) 2014, Imagination Technologies Ltd. */#include <linux/bitops.h>#include <linux/bug.h>#include <linux/compiler.h>#include
<linux/context_tracking.h>#include <linux/cpu_pm.h>#includ
Copyright (C) 2014, Imagination Technologies Ltd. */#include <linux/cache.h>#include <linux/context_tracking.h>#include <linux/irqflags.h>#include
<linux/sched.h>#include <linux/mm.h>#include <
Copyright (C) 2014 Imagination Technologies Ltd. */#include <linux/errno.h>#include <asm/asm.h>#include <asm/asmmacro.h>#include
<asm/irqflags.h>#include <asm/mipsregs.h>#include <asm/regdef.h>
Copyright (C) 2014 Imagination Technologies Ltd. */#include <linux/errno.h>#include <asm/asm.h>#include <asm/asm-offsets.h>#include
<asm/export.h>#include <asm/regdef.h>
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<linux/interrupt.h>#include <linux/irq.h>#include <linux/sl  
Copyright (c) 2014, Intel Corporation. \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/init.h>#include <linux/pci.h>#include  
<linux/interrupt.h>#include <linux/platform\_dev  
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Copyright (C) 2014 Lad, Prabhakar <prabhakar.csengg@gmail.com> * * TODO : add support for VBI & HBI data service * add static buffer allocation
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_ASM_GENERIC_SECCOMP_H
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the ACPI 5.0+ * specification. It is a mailbox like m
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Copyright (C) 2014 Linaro Ltd. * Author: Rob Herring <robh@kernel.org> * * Based on 8250 earlycon:
Copyright (C) 2014 Linaro Ltd. * Author: Rob Herring <robh@kernel.org> */#include <linux/kernel.h>#include <linux/console.h>#include
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<linux/platform_device.h>#include <linux/of.h>#include <linux/of_address.h>
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<linux/mfd/syscon.h>#include <linux/module.h>#include <linux/notifi
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<linux/module.h>#include <linux/of_irq.h>#include <
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<asm/mach/arch.h>#include <linux/of.h>#include <li
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 <linux/of.h>#include <linux/of\_platform.h>#include <linux/plat  
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 <linux/kernel.h>#include <linux/cdev.h>#include <linux/ioctl.h>#  
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Copyright(c) 2015-17 Intel Corporation.#include <sound/soc.h>
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<linux/io.h>#include <linux/kernel.h>#include <linux/modu
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core.h>#include <linux/fpga/fpga-mgr.h>#inclu
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<linux/module.h>#include <linux/of_device.h>
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<linux/ctype.h>#include <linux/delay.h>#include <linux/err
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<linux/module.h>#include "nvmet.h"
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"nvmet.h"
Copyright (c) 2015-2016 HGST, a Western Digital Company. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/init.h>#include
<linux/miscdevice.h>#include <linux/module.h>#include <linux
Copyright (c) 2015-2016 HGST, a Western Digital Company. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/kernel.h>#include
<linux/module.h>#include <linux/slab.h>#include <linux/sta
Copyright (c) 2015-2016 HGST, a Western Digital Company. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/init.h>#include <linux/slab.h>#include <rdma/mr_poo
Copyright (c) 2015-2016 HGST, a Western Digital Company. */#define pr_fmt(fmt) KBUILD_MODNAME ": " fmt#include <linux/module.h>#include
<linux/random.h>#include <linux/rculist.h>#include <linux/
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<linux/rculist.h>
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<linux/blk-mq.h>#include <linux/nvme.h>#include <linu
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<linux/iopoll.h>#include <linux/module.h>#includ
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 Copyright (c) 2015-2016, Linaro Limited \*/#include <linux/device.h>#include <linux/dma-buf.h>#include <linux/fdtable.h>#include  
 <linux/idr.h>#include <linux/sched.h>#include <linux/slab.h>#inc  
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 \_\_DTS\_IOMMU\_PORT\_MT8173\_H  
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 <linux/device.h>#include <linux/err.h>#include <linux  
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 <linux/clk.h>#include <linux/component.h>#include <lin  
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Copyright (C) 2015 - 2016 ZTE Corporation. \*/#ifndef \_\_DT\_BINDINGS\_CLOCK\_ZX296718\_H#define \_\_DT\_BINDINGS\_CLOCK\_ZX296718\_H  
Copyright (C) 2015, 2017-2018 \* Author: Matt Ranostay <matt.ranostay@konsulko.com> \* \* TODO: interrupt mode, and signal strength reporting  
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Copyright (C) 2015-2017 Pengutronix, Uwe Kleine-K \*/  
Copyright (C) 2015-2017 Pengutronix, Uwe Kleine-K \*/#include <linux/kernel.h>#include <linux/device.h>#include <linux/module.h>#include  
<linux/slab.h>#include <linux/sysfs.h>  
Copyright (C) 2015-2017 Pengutronix, Uwe Kleine-K \*/#include <linux/kernel.h>#include <linux/kthread.h>#include <linux/siox.h>  
Copyright (c) 2015-2017 QLogic Corporation  
Copyright (c) 2015-2017 Qualcomm Atheros, Inc.  
Copyright (c) 2015,2017 Qualcomm Atheros, Inc.  
Copyright (c) 2015,2017 Qualcomm Atheros, Inc. \*/#ifndef \_WOW\_H\_#define \_WOW\_H\_  
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Copyright (C) 2015-2017 Renesas Electronics Corporation \* \* This is based on the phy-rcar-gen2 driver:  
Copyright (C) 2015-2017 Russell King. \*/#include <linux/interrupt.h>#include <linux/io.h>#include <linux/module.h>#include  
<linux/platform\_device.h>#include <linux/sched.h>#include <linux/slab.  
Copyright (C) 2015-2017 Sebastian Reichel <sre@kernel.org> \*/  
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*/
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Copyright (C) 2015, 2018 * Author: Matt Ranostay <matt.ranostay@konsulko.com> * * Datasheets:
Copyright (C) 2015, 2018 * Author: Matt Ranostay <matt.ranostay@konsulko.com> * * TODO: enable pulse length controls via device tree properties
Copyright (C) 2015, 2018 * Author: Matt Ranostay <matt.ranostay@konsulko.com> * * TODO: gesture + proximity calib offsets
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Copyright (C) 2015-2018 Linaro Ltd. */#ifndef QC_MSM_CAMSS_CSID_H#define QC_MSM_CAMSS_CSID_H
Copyright (C) 2015-2018 Linaro Ltd. */#ifndef QC_MSM_CAMSS_H#define QC_MSM_CAMSS_H
Copyright (C) 2015-2018 Linaro Ltd. */#ifndef QC_MSM_CAMSS_ISPIF_H#define QC_MSM_CAMSS_ISPIF_H
Copyright (C) 2015-2018 Linaro Ltd. */#ifndef QC_MSM_CAMSS_VFE_H#define QC_MSM_CAMSS_VFE_H
Copyright (C) 2015-2018 Linaro Ltd. */#ifndef QC_MSM_CAMSS_VIDEO_H#define QC_MSM_CAMSS_VIDEO_H
Copyright (C) 2015-2018 Linaro Ltd. */#include <linux/clk.h>#include <linux/completion.h>#include <linux/interrupt.h>#include <linux/io.h>#include
<linux/iopoll.h>#include <linux/kernel.h>#inc
Copyright (C) 2015-2018 Linaro Ltd. */#include <linux/clk.h>#include <linux/completion.h>#include <linux/interrupt.h>#include <linux/io.h>#include
<linux/kernel.h>#include <linux/of.h>#include
Copyright (C) 2015-2018 Linaro Ltd. */#include <linux/clk.h>#include <linux/completion.h>#include <linux/interrupt.h>#include
<linux/iommu.h>#include <linux/mutex.h>#include <linux/of.h>#inclu
Copyright (C) 2015-2018 Linaro Ltd. */#include <linux/clk.h>#include <linux/media-bus-format.h>#include <linux/media.h>#include
<linux/module.h>#include <linux/platform_device.h>#include <linux
Copyright (C) 2015-2018 Linaro Ltd. */#include <linux/slab.h>#include <media/media-entity.h>#include <media/v4l2-dev.h>#include <media/v4l2-
device.h>#include <media/v4l2-ioct.h>#include <media
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Sean Wang <sean.wang@mediatek.com> */
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Copyright (C) 2015-2018 Netronome Systems, Inc.#include <linux/debugfs.h>#include <linux/module.h>#include <linux/rtnetlink.h>
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 Copyright (C) 2015 Alban Bedel <albeu@free.fr> \*/  
 Copyright (C) 2015 Aleksa Sarai <cyphar@cyphar.com> \*/  
 Copyright (C) 2015 Alexander Sverdlin \* \* The driver uses polling to get the conversion status. According to EP93xx \* datasheets, reading ADCResult register starts the conversion, but user is also  
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 Copyright (C) 2015 Andrew Lutomirski  
 Copyright (c) 2015 Andrew Lutomirski \* \* Based on asm and ptrace code by many authors. The code here originated \* in ptrace.c and signal.c.  
 Copyright (C) 2015 Andrew Lutomirski <luto@kernel.org> \*/  
 Copyright (c) 2015 Andrew Lutomirski \* \* On AMD CPUs, SYSRET can return with a valid SS descriptor with with \* the hidden attributes set to an unusable state. Make sure the kernel \* doesn't let t  
 Copyright (c) 2015 Andrew Lutomirski \* \* These are little helpers that make it easier to switch bitness on \* the fly.  
 Copyright (c) 2015 Andrew Lutomirski \* \* This makes sure that sa\_restorer == NULL keeps working on 32-bit \* configurations. Modern glibc doesn't use it under any circumstances, \* so it's easy to  
 Copyright (C) 2015 Andrey Smetanin <asmetanin@virtuozzo.com> \* \* Authors:  
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 Copyright (C) 2015 Annapurna Labs Ltd. \*/  
 Copyright (C) 2015 Anshuman Khandual, IBM Corporation.  
 Copyright (C) 2015 Anshuman Khandual, IBM Corporation. \*/#define GPR\_1 1#define GPR\_2 2#define GPR\_3 3#define GPR\_4 4  
 Copyright (C) 2015 Anshuman Khandual, IBM Corporation. \*/#define TAR\_1 10#define TAR\_2 20#define TAR\_3 30#define TAR\_4 40#define TAR\_5 50  
 Copyright (C) 2015 Anshuman Khandual, IBM Corporation. \*/#define VEC\_MAX 128#define VSX\_MAX 32#define VMX\_MAX 32  
 Copyright (C) 2015 Anshuman Khandual, IBM Corporation. \*/#include <inttypes.h>#include <unistd.h>#include <stdlib.h>#include <string.h>#include <malloc.h>#include <errno.h>#include <time.h>#i  
 Copyright (C) 2015 Anshuman Khandual, IBM Corporation. \*/#include "ptrace.h"#include "ptrace-gpr.h"#include "reg.h"  
 Copyright (C) 2015 Anshuman Khandual, IBM Corporation. \*/#include "ptrace.h"#include "ptrace-gpr.h"#include "tm.h"  
 Copyright (C) 2015 Anshuman Khandual, IBM Corporation. \*/#include "ptrace.h"#include "ptrace-tar.h"  
 Copyright (C) 2015 Anshuman Khandual, IBM Corporation. \*/#include "ptrace.h"#include "ptrace-vsx.h"

Copyright (C) 2015 Anshuman Khandual, IBM Corporation. \*/#include "ptrace.h"#include "tm.h"  
Copyright (C) 2015 Anshuman Khandual, IBM Corporation. \*/#include "ptrace.h"#include "tm.h"#include "ptrace-tar.h"  
Copyright (C) 2015 Anshuman Khandual, IBM Corporation. \*/#include "ptrace.h"#include "tm.h"#include "ptrace-vsx.h"  
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Copyright (C) 2015 Antti Palosaari <crope@iki.fi> \*/  
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<linux/interrupt.h>#include <linux/io.h>#include  
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PM support is currently not available.  
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Copyright (C) 2015 ARM Limited \* \* Author: Will Deacon <will.deacon@arm.com>  
Copyright (C) 2015 ARM Limited \*/#include <linux/perf\_event.h>#include <linux/uaccess.h>  
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Copyright (C) 2015 ARM Ltd. \* Author: Lorenzo Pieralisi <lorenzo.pieralisi@arm.com> \*/#include <linux/acpi.h>#include <linux/irq.h>#include  
<linux/irqdomain.h>#include <linux/of.h>  
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Copyright (C) 2015 - ARM Ltd \* Author: Marc Zyngier <marc.zyngier@arm.com> \*/#include <linux/jump\_label.h>  
Copyright (C) 2015 ARM Ltd. \* Author: Marc Zyngier <marc.zyngier@arm.com> \*/#include <linux/kvm\_host.h>#include <kvm/arm\_vgic.h>#include  
<linux/uaccess.h>#include <asm/kvm\_mmu.h>#include <asm/c  
Copyright (C) 2015 ARM Ltd. \*/#ifndef \_\_ASM\_ARCH\_GICV3\_H#define \_\_ASM\_ARCH\_GICV3\_H  
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Copyright (C) 2015 Atmel Corporation, \* Nicolas Ferre <nicolas.ferre@atmel.com> \* \* Based on clk-programmable & clk-peripheral drivers  
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making sure the structure is aligned and packed.  
Copyright (C) 2015 Azael Avalos <coproscefalo@gmail.com> \* \* Thanks to Matthew Garrett for background info on ACPI innards which \* normal people  
aren't meant to understand :-)  
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Copyright (C) 2015 Bert Vermeulen <bert@biot.com> \* \* This file was based on the patches for Linux 2.6.27.39 published by \* MikroTik for their RouterBoard 4xx series devices.  
Copyright (c) 2015 BMW Car IT GmbH  
Copyright (c) 2015 BMW Car IT GmbH \*/#include <stdio.h>#include <unistd.h>#include <stdlib.h>#include <signal.h>#include <linux/bpf.h>#include <bpf/bpf.h>#include "bpf\_load.h"  
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Copyright (C) 2015 Boris Barbulovski <bbarbulovski@gmail.com>.\n\n"Bug reports and feature request can also be entered at http://bugzilla.kernel.org/\n";  
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Copyright (C) 2015 Broadcom Corporation \* Author: Florian Fainelli <f.fainelli@gmail.com> \*/#include <linux/kernel.h>#include <linux/io.h>#include <linux/spinlock.h>#include <linux/reset/bcm63xx  
Copyright(c) 2015 Bryan O \*/#ifndef \_IMR\_H#define \_IMR\_H  
Copyright(c) 2015 Bryan O \* \* IMR registers define an isolated region of memory that can \* be masked to prohibit certain system agents from accessing memory. \* When a device behind a masked port p  
Copyright(c) 2015 Bryan O \* \* IMR self test. The purpose of this module is to run a set of tests on the \* IMR API to validate it's sanity. We check for overlapping, reserved \* addresses and setup/  
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Copyright (C) 2015 Cadence Design Systems Inc. \* \* Chris Zankel <chris@zankel.net>  
Copyright (C) 2015 Cadence Design Systems Inc. \*/#include <linux/export.h>#include <linux/sched.h>#include <linux/stacktrace.h>  
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Copyright (c) 2015, Daniel Thompson \*/  
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Copyright (C) 2015 Dan Streetman, IBM Corp \* \* See 842.h for details of the 842 compressed format.  
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Copyright (c) 2015 David Ahern <dsa@cumulusnetworks.com> \*/  
Copyright (c) 2015 David Ahern <dsa@cumulusnetworks.com> \* \* Based on dummy, team and ipvlan drivers  
Copyright (c) 2015 David Ahern <dsa@cumulusnetworks.com> \*/#ifndef \_NET\_L3MDEV\_H\_#define \_NET\_L3MDEV\_H\_  
Copyright (C) 2015 Davidlohr Bueso. \*/  
Copyright (C) 2015 Davidlohr Bueso. \* \* Block a bunch of threads and let parallel waker threads wakeup an \* equal amount of them. The program  
output reflects the avg latency \* for each individual  
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Copyright (c) 2015 Denys Vlasenko \* \* These are little helpers that make it easier to switch bitness on \* the fly.  
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<asm/cacheflush.h>#include <asm/fw/fw.h>#include <asm/sect  
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_CRYPTO_AKCIPHER_H#include <linux/crypto.h>
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_CRYPTO_AKCIPHER_INT_H#include <crypto/akcipher.h>#include <crypt
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<linux/types.h>
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<linux/kernel.h>#include <linux/module.h>#include <linux/seq_file.h>#
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<linux/export.h>#include <linux/err.h>#include <linux/fips.h>#include
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<linux/regmap.h>#include <linux/iio/iio.h>#include "inv_mpu_iio.h"
Copyright (c) 2015, Intel Corporation. */#include <linux/debugfs.h>          /* debugfs_create_u32()          */#include <linux/mm_types.h>
/* mm_struct, vma, etc... */#include <linux/pkeys.h>
Copyright (c) 2015, Intel Corporation. */#include <linux/kernel.h>#include <linux/module.h>#include <linux/init.h>#include <linux/acpi.h>#include
<linux/thermal.h>#include "int340x_thermal_zone
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<linux/tee_drv.h>#include "tee_private.h"
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Copyright (C) 2015 Linaro Ltd. */#include <linux/bitops.h>#include <linux/device.h>#include <linux/errno.h>#include <linux/gpio/driver.h>#include
<linux/irqchip/chained_irq.h>#include <linux/in
Copyright (C) 2015 Linaro Ltd. */#include <linux/platform_device.h>#include <linux/init.h>#include <linux/cpumask.h>#include
<linux/export.h>#include <linux/dma-direct.h>#include <linux/dma-map
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devices around the host kernel. Posted * Interr
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<linux/irqchip.h>#include <linux/irqchip/arm-gic.
Copyright (C) 2015 Linus Walleij */#include <linux/smp.h>#include <linux/io.h>#include <linux/of.h>#include <linux/of_address.h>#include
<linux/regmap.h>#include <linux/mfd/syscon.h>
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<linux/ethtool.h>#include <linux/phy.h>#include <linux/microchipphy.h>  
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 <linux/iopoll.h>#include <linux/mfd/syscon.h>#i  
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 <stdbool.h>#include <string.h>#include <time.h>#include  
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<linux/io.h>#include <linux/of\_platform.h>#include <lin  
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<linux/gpio/driver.h>#include <linux/init.h>#include <linux/kern  
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<linux/string.h>#include <sound/core.h>#include <s  
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<linux/slab.h>#include <linux/err.h>#include <linux/init.h>#inc  
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<linux/regulator/consumer.h>#include <linux/spi/spi.h>  
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 <linux/mm.h>#include <linux/filter.h>#include <linux/perf\_e  
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 direction.h>#include <linux/dma-mapping.h>#include <linux  
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 <linux/module.h>#include <linux/mutex.h>#include <linux/slab.h  
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 <linux/of\_device.h>#include <linux/platform\_dev  
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 <linux/tty.h>#include <linux/tty\_driver.h>#include <linux/poll.h>  
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"qedf_hsi.h"#include <linux/qed/qed_if.h>
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Copyright (c) 2016-2018 Cavium Inc. */#ifndef _SCSI_FW_FUNC_S_H#define _SCSI_FW_FUNC_S_H#include <linux/qed/common_hsi.h>#include
<linux/qed/storage_common.h>#include <linux/qed/fcoe_common.h>
Copyright (c) 2016-2018 Cavium Inc. */#include "drv_fcoe_fw_funcs.h"#include "drv_scsi_fw_funcs.h"
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Copyright (c) 2016-2018 Cavium Inc. */#include <linux/init.h>#include <linux/kernel.h>#include <linux/module.h>#include <linux/pci.h>#include
<linux/device.h>#include <linux/highmem.h>#include
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<linux/iomap.h>#include <linux/pagemap.h>#include <linux/uid.h>#i
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<media/v4l2-async.h>#include <media/v4l2-event.h>#include  
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<linux/virtio_config.h>#include <linux/module.h>#include "blk-
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<errno.h>#include <string.h>#include <assert.h>#i
Copyright (c) 2016 Facebook */#define __GNU_SOURCE#include <sched.h>#include <errno.h>#include <stdio.h>#include <sys/types.h>#include
<asm/unistd.h>#include <fcntl.h>#include <unistd.h>#incl
Copyright (c) 2016 Facebook */#define __GNU_SOURCE#include <sched.h>#include <stdio.h>#include <sys/types.h>#include <asm/unistd.h>#include
<unistd.h>#include <assert.h>#include <sys/wait.h>#
Copyright (c) 2016 Facebook */#define __GNU_SOURCE#include <stdio.h>#include <unistd.h>#include <errno.h>#include <string.h>#include
<assert.h>#include <sched.h>#include <stdlib.h>#include <t
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Copyright (c) 2016 Facebook */#ifndef __PERCPU_FREELIST_H__#define __PERCPU_FREELIST_H__#include <linux/spinlock.h>#include
<linux/percpu.h>
Copyright (c) 2016 Facebook */#ifndef __SAMPLES_BPF_XDP_TX_IPTNL_COMMON_H__#define __SAMPLES_BPF_XDP_TX_IPTNL_COMMON_H
Copyright (c) 2016 Facebook */#ifndef __TEST_IPTNL_COMMON_H__#define __TEST_IPTNL_COMMON_H
Copyright (c) 2016 Facebook */#include <linux/bpf.h>#include <linux/btf.h>#include <linux/jhash.h>#include <linux/filter.h>#include

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  Copyright (c) 2016 Facebook */#include <linux/bpf.h>#include <linux/if_link.h>#include <assert.h>#include <errno.h>#include <signal.h>#include
<stdio.h>#include <stdlib.h>#include <string.h>
  Copyright (c) 2016 Facebook */#include <linux/bpf.h>#include <linux/jhash.h>#include <linux/filter.h>#include <linux/stacktrace.h>#include
<linux/perf_event.h>#include <linux/elf.h>#include <l
  Copyright (c) 2016 Facebook */#include <linux/cpumask.h>#include <linux/spinlock.h>#include <linux/percpu.h>
  Copyright (c) 2016 Facebook */#include <linux/kernel.h>#include <linux/types.h>#include <linux/slab.h>#include <linux/bpf.h>#include
<linux/bpf_perf_event.h>#include <linux/filter.h>#include <
  Copyright (c) 2016 Facebook */#include <linux/unistd.h>#include <linux/bpf.h>
  Copyright (c) 2016 Facebook */#include "percpu_freelist.h"
  Copyright (c) 2016 Facebook */#include <stdio.h>#include <unistd.h>#include <stdlib.h>#include <signal.h>#include <linux/bpf.h>#include
<string.h>#include <linux/perf_event.h>#include <errno.
  Copyright (c) 2016 Facebook */#include <stdio.h>#include <unistd.h>#include <stdlib.h>#include <stdbool.h>#include <string.h>#include
<fcntl.h>#include <poll.h>#include <sys/ioctl.h>#include
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<linux/mtd/partitions.h>#include <linux/etherdevice.h>#include
  Copyright (C) 2016 Felix Fietkau <nbd@nbd.name> */#include <linux/of.h>#include "mt76.h"
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Copyright (c) 2016 HGST, a Western Digital Company. */#ifndef __RDMA_RW_H__#define __RDMA_RW_H__
Copyright (c) 2016 HGST, a Western Digital Company. */#include <linux/moduleparam.h>#include <linux/slab.h>#include <linux/pci-
p2pdma.h>#include <rdma/mr_pool.h>#include <rdma/rw.h>
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<linux/mfd/syscon.h>#include <linux/mutex.h>#include <linux/
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<linux/mutex.h>#include <linux/of.h>#include <linux/platfo
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<linux/kernel.h>#include <linux/signal.h>#include <linux
Copyright (C) 2016, Imagination Technologies Ltd. */#include <linux/compiler.h>#include <linux/errno.h>#include <linux/signal.h>#include
<linux/sched/signal.h>#include <linux/uaccess.h>
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_CRYPTO_SCOMP_INT_H#include <linux/crypto.h>
Copyright (c) 2016, Intel Corporation * Author: Giovanni Cabiddu <giovanni.cabiddu@intel.com> */#include <linux/errno.h>#include
<linux/kernel.h>#include <linux/module.h>#include <linux/seq_file
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*/#include <linux/radix-tree.h>#include <linux/sla
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_CRYPTO_ECDH_
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_CRYPTO_KPP_INT_H_#include <crypto/kpp.h>#include <crypto/a
Copyright (c) 2016, Intel Corporation * Authors: Salvatore Benedetto <salvatore.benedetto@intel.com> */#include <linux/errno.h>#include
<linux/kernel.h>#include <linux/module.h>#include <linux/s
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<linux/export.h>#include <linux/err.h>#include <linux/str
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_CRYPTO_ACOMP_H#define _CRYPTO_ACOMP_H#inclu
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*/#include <linux/errno.h>#include <linux/kernel.h>#in
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<linux/platform_device.h>#include <linux/spi/pxa2xx_spi.h>
Copyright (c) 2016, Intel Corporation. */#include <linux/device.h>#include <linux/mod_devicetable.h>#include <linux/i2c.h>#include
<linux/module.h>#include <linux/acpi.h>#include <linux/regmap.
Copyright (c) 2016, Intel Corporation. */#include "test/nfit_test.h"#include <linux/mm.h>#include "../..../drivers/dax/dax-private.h"
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<linux/netdevice.h>#include <linux/export.h>#include <asm/unalign
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mq.h>#include <linux/elevator.h>#include <linux/bio.
Copyright (C) 2016 Jens Axboe */#include <linux/kernel.h>#include <linux/module.h>#include <linux/blk-mq.h>
Copyright (C) 2016 Jens Axboe */#include <linux/kernel.h>#include <linux/rculist.h>#include <linux/blk-mq.h>
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Copyright (c) 2016 Jiri Pirko <jiri@mellanox.com> */
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<errno.h>#include <signal.h>#include <stdio.h>#incl
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Copyright (C) 2016 Linaro Ltd. \* Author: Linus Walleij <linus.walleij@linaro.org> \* Loosely based on the previous BH1780 ALS misc driver  
Copyright (C) 2016 Linaro Ltd. \* Author: Linus Walleij <linus.walleij@linaro.org> \* \* This hardware was found in the very first Nexus One handset from Google/HTC \* and an early endeavour into mobil  
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Copyright (C) 2016, Linaro Ltd - Daniel Lezcano <daniel.lezcano@linaro.org>#define pr\_fmt(fmt) "irq\_timings: " fmt  
Copyright (C) 2016 Linaro Ltd. \*/#include <linux/clk-provider.h>#include <linux/device.h>#include <linux/kernel.h>#include <linux/of\_address.h>#include <linux/of\_device.h>#include <linux/platf  
Copyright (C) 2016 Linaro Ltd \*/#include <linux/module.h>#include <linux/ulpi/driver.h>#include <linux/ulpi/regs.h>#include <linux/clk.h>#include <linux/regulator/consumer.h>#include <linux/of\_  
Copyright (C) 2016 Linaro Ltd \*/#include <linux/module.h>#include <linux/ulpi/driver.h>#include <linux/ulpi/regs.h>#include <linux/phy/phy.h>#include <linux/pinctrl/consumer.h>#include <linux/p  
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Copyright (C) 2016 Marcel Hasler \* <mahasler--a.t--gmail.com> \* \* Based on Easycap driver by R.M. Thomas  
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Copyright (C) 2016 Marek Vasut <marex@denx.de> \*/  
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Copyright (c) 2016 Matthew Wilcox <willy@infradead.org> \*/#include <linux/bitmap.h>#include <linux/idr.h>#include <linux/slab.h>#include

```

<linux/kernel.h>#include <linux/errno.h>
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<linux/debugfs.h>#include <linux/firmware.h>#include <linux/interrupt.h>#i
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 Copyright (c) 2016 Mentor Graphics Inc. \*/#ifndef \_IMX\_MEDIA\_H#define \_IMX\_MEDIA\_H  
 Copyright (c) 2016 Mentor Graphics Inc. \*/#include <linux/delay.h>#include <linux/irq.h>#include <linux/module.h>#include  
 <linux/platform\_device.h>#include <linux/slab.h>#include <linux/spinloc  
 Copyright (c) 2016 Mentor Graphics Inc. \*/#include <linux/module.h>#include "imx-media.h"  
 Copyright (c) 2016 Mentor Graphics Inc. \*/#include <linux/of\_platform.h>#include <media/v4l2-ctrls.h>#include <media/v4l2-device.h>#include  
 <media/v4l2-fwnode.h>#include <media/v4l2-subdev.h>#i  
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 <linux/kernel.h>#include <linux/module.h>#inc  
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 Copyright (C) 2016 National Instruments Corp.#include <linux/leds.h>#include <linux/phy.h>#include <linux/phy\_led\_triggers.h>#include  
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Copyright (C) 2016 Red Hat, Inc. \* Author: Michael S. Tsirkin <mst@redhat.com> \* \* Command line processing and common functions for ring benchmarking.  
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drivers.
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Copyright (C) 2016 Robert Jarzmik */#include <linux/module.h>#include <linux/platform_device.h>#include <linux/of.h>#include
<linux/of_device.h>#include <linux/pinctrl/pinctrl.h>
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audio streams are simply sinked to and * source
Copyright (C) 2016 Russell King */#include <linux/device.h>#include <linux/module.h>#include <linux/gpio/consumer.h>#include
<linux/slab.h>#include "max1600.h"
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<linux/slab.h>#include <linux/spinlock.h>
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Copyright (C) 2016 Sang Engineering */#include <linux/delay.h>#include <linux/err.h>#include <linux/interrupt.h>#include <linux/io.h>#include
<linux/module.h>#include <linux/of_device.h>#include
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Copyright (c) 2016 Tom Haynes <loghyr@primarydata.com> \*/#include <linux/sunrpc/svc.h>#include <linux/nfs4.h>  
Copyright (c) 2016 Tom Haynes <loghyr@primarydata.com> \*\* The following implements a super-simple flex-file server \* where the NFSv4.1 mds is  
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<linux/if_ether.h>#include <linux/if_packet.h>#include <linux/ip.
Copyright (c) 2017-2018 Covalent IO, Inc. http://covalent.io#include <stdio.h>#include <stdlib.h>#include <sys/socket.h>#include
<sys/ioctl.h>#include <sys/select.h>#include <netinet/in.h>#incl
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*/
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<errno.h>#include <signal.h>#include <stdio.h>#include  
Copyright (c) 2017 Chao Yu <chao@kernel.org> \*/#include <linux/compiler.h>#include <linux/proc\_fs.h>#include <linux/f2fs\_fs.h>#include  
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<errno.h>#include <signal.h>#include <stdio.h>#include <st  
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Copyright (c) 2017, EPAM Systems \*/#include <linux/device.h>#include <linux/dma-buf.h>#include <linux/genalloc.h>#include <linux/slab.h>#include

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    Copyright (C) 2017 Eric Nelson */#include <linux/module.h>#include <linux/i2c.h>#include <linux/slab.h>#include <linux/rtc.h>#include
<linux/init.h>#include <linux/err.h>#include <linux/errno.h>
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    Copyright (c) 2017 Eva Rachel Retuya <eraretuya@gmail.com> */
    Copyright (c) 2017 Eva Rachel Retuya <eraretuya@gmail.com> ** 7-bit I2C slave address: 0x1D (ALT ADDRESS pin tied to VDDIO) or * 0x53 (ALT
ADDRESS pin grounded)
    Copyright (c) 2017 Eva Rachel Retuya <eraretuya@gmail.com> ** Datasheet: http://www.analog.com/media/en/technical-documentation/data-
sheets/ADXL345.pdf
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    Copyright (C) 2017 Facebook// Author: Roman Gushchin <guro@fb.com>
    Copyright (c) 2017 Facebook */#ifndef __MAP_IN_MAP_H__#define __MAP_IN_MAP_H__
    Copyright (C) 2017 Facebook Inc.
    Copyright (c) 2017 Facebook */#include <linux/bpf.h>#include <linux/slab.h>#include <linux/vmalloc.h>#include <linux/etherdevice.h>#include
<linux/filter.h>#include <linux/sched/signal.h>#incl
    Copyright (c) 2017 Facebook */#include <linux/slab.h>#include <linux/bpf.h>
    Copyright (c) 2017 Facebook */#include <stddef.h>#include <linux/bpf.h>#include <linux/pkt_cls.h>#include "bpf_helpers.h"
    Copyright (c) 2017 Facebook#include <stddef.h>#include <stdbool.h>#include <string.h>#include <linux/pkt_cls.h>#include <linux/bpf.h>#include
<linux/in.h>#include <linux/if_ether.h>#include <l
    Copyright (c) 2017 Facebook */#include <stddef.h>#include <string.h>#include <linux/bpf.h>#include <linux/if_ether.h>#include
<linux/if_packet.h>#include <linux/ip.h>#include <linux/ipv6.h>#i
    Copyright (c) 2017 Facebook */#include <stddef.h>#include <string.h>#include <linux/bpf.h>#include <linux/pkt_cls.h>#include "bpf_helpers.h"
    Copyright (c) 2017 Facebook */#include <stdio.h>#include <unistd.h>#include <fcntl.h>#include <stdlib.h>#include <signal.h>#include
<linux/bpf.h>#include <string.h>#include <linux/perf_event.h>
    Copyright (c) 2017 Facebook */#include <sys/resource.h>#include <sys/socket.h>#include <arpa/inet.h>#include <stdint.h>#include
<assert.h>#include <errno.h>#include <stdlib.h>#include <stdio.h>
    Copyright (c) 2017 Facebook */#include "test_progs.h"#include "cgroup_helpers.h"#include "bpf_rlimit.h"#include <argp.h>#include <string.h>
    Copyright (c) 2017 Facebook */#include <uapi/linux/bpf.h>#include "bpf_helpers.h"
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    Copyright (C) 2017 Free Electrons * Maxime Ripard <maxime.ripard@free-electrons.com> */
    Copyright (C) 2017 Free Electrons, * Quentin Schulz <quentin.schulz@free-electrons.com> ** The Sama5d2 SoC has two audio PLLs (PMC
and PAD) that shares the same parent * (FRAC). FRAC can
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    Copyright (c) 2017, Fuzhou Rockchip Electronics Co., Ltd ** David Wu <david.wu@rock-chips.com>
    Copyright (C) 2017 Gateworks Corporation */#ifndef _DT_BINDINGS_MEDIA_TDA1997X_H#define _DT_BINDINGS_MEDIA_TDA1997X_H
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    Copyright (C) 2017 Glider bvba */
    Copyright (C) 2017 Glider bvba ** Based on clk-rcar-gen2.c
    Copyright (C) 2017 Glider bvba ** Based on r8a7795-cpg-mssr.c
    Copyright (C) 2017 Glider bvba */#ifndef __DT_BINDINGS_CLOCK_R8A77995_CPG_MSSR_H__#define

```

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__DT_BINDINGS_CLOCK_R8A77995_CPG_MSSR_H__
Copyright (C) 2017 Glider bvba */#ifndef __DT_BINDINGS_POWER_R8A77995_SYSC_H__#define __DT_BINDINGS_POWER_R8A77995_SYSC_H__
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Copyright (c) 2017 Google, Inc. * * Based on it87_wdt.c by Oliver Schuster * gpio-it8761e.c by Denis Turishev * gpio-stmpe.c by
Rabin Vincent
Copyright (C) 2017 Google, Inc. * Matthew Garrett <mjg59@google.com> * Thiebaud Weksteen <tweek@google.com> */#include
<linux/efi.h>#include <linux/tpm_eventlog.h>#include <asm/efi.h>
Copyright (C) 2017 Google, Inc. * Thiebaud Weksteen <tweek@google.com> */
Copyright (c) 2017 Google Inc * * VOUT_MODE is not supported by the device. The driver fakes VOUT linear16 * mode with exponent value -8 as direct
mode with m=256/b=0/R=0.
Copyright (c) 2017 Hans de Goede <hdegoede@redhat.com> */
Copyright (C) 2017 Hans de Goede <hdegoede@redhat.com> */
Copyright (C) 2017 Hans de Goede <hdegoede@redhat.com> * * Based on various non upstream patches to support the CHT Whiskey Cove PMIC:
Copyright (C) 2017 Hans de Goede <hdegoede@redhat.com> * * Loosely based on android x86 kernel code which is: *
Copyright (C) 2017 Hans de Goede <hdegoede@redhat.com> * * Note the quirks in this file are shared with fbdev/efibf and as such * must not depend
on other drm code.
Copyright (C) 2017 Hans de Goede <hdegoede@redhat.com> * * Some Intel Cherry Trail based device which ship with Windows 10, have * this weird
INT33FE ACPI device with a CRS table with 4 I2cSerialBu
Copyright (C) 2017 Hans P. Moller <hmoller@uc.cl> */
Copyright (C) 2017 Hari Bathini, IBM Corporation */
Copyright (C) 2017 Hauke Mehrtens <hauke@hauke-m.de> */
Copyright (C) 2017 Hauke Mehrtens <hauke@hauke-m.de> * Based on EP93xx wdt driver */
Copyright (C) 2017 Hauke Mehrtens <hauke@hauke-m.de> */#ifndef __DT_BINDINGS_MIPS_LANTIQ_RCU_GPHY_H#define
__DT_BINDINGS_MIPS_LANTIQ_RCU_GPHY_H
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Copyright (C) 2017 Helge Deller <deller@gmx.de> * * based on arch/arm/kernel/topology.c
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code is based on the uncore PMUs like arm-cci and arm-cc
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Copyright (c) 2017 HiSilicon Technologies Co., Ltd. * * Simple HiSilicon phase clock implementation.
Copyright (C) 2017 Horms Solutions Ltd., Simon Horman
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Copyright (c) 2017 Huawei Corporation.";static struct hnae3_client client;
Copyright(c) 2017 Huawei Technologies Co., Ltd
Copyright(c) 2017 Huawei Technologies Co., Ltd */
Copyright (c) 2017, I2SE GmbH * * Permission to use, copy, modify, and/or distribute this software * for any purpose with or without fee is hereby
granted, provided * that the above copyright
Copyright (C) 2017 IBM Corp. */
Copyright (C) 2017 IBM Corp. * * Driver for the Nuvoton W83773G SMBus temperature sensor IC. * Supported models: W83773G
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Copyright (C) 2017 Impinj, Inc. ** Author: Andrey Smirnov <andrew.smirnov@gmail.com>
Copyright (c) 2017, Impinj, Inc. ** i.MX7 System Reset Controller (SRC) driver
Copyright (C) 2017 Intel Corp * Author: Jaikrishna Nemallapudi <jaikrishna.nemallapudi@intel.com> * Author: Subhransu S. Prusty
<subhransu.s.prusty@intel.com> * ~~~~~
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Copyright (c) 2017, Intel Corporation. * Author: Andi Kleen */#include <linux/list.h>#include <linux/zalloc.h>#include <stdlib.h>#include
<sys/mman.h>#include <sys/stat.h>#include <fcntl.h>#i
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Copyright (C) 2017 Intel Corporation ** Based partially on Intel IPU4 driver written by * Sakari Ailus <sakari.ailus@linux.intel.com> * Samu Onkalo
<samu.onkalo@intel.com> * Jouni Högander <j
Copyright (C) 2017 Intel Corporation/* Functions to access TPS68470 power management chip. */
Copyright (C) 2017 Intel Corporation */#ifndef LINUX_FPGA_MGR_H#define LINUX_FPGA_MGR_H
Copyright (C) 2017, Intel Corporation */#include <linux/clk-provider.h>#include <linux/io.h>#include <linux/slab.h>#include "stratix10-clk.h"#include
"clk.h"
Copyright (c) 2017, Intel Corporation. */#include <linux/device.h>#include <linux/hid-sensor-hub.h>#include <linux/iiio/buffer.h>#include
<linux/iiio/hid.h>#include <linux/iiio/triggered_buffer.h>
Copyright (C) 2017 Intel Corporation */#include <linux/fpga/fpga-bridge.h>#include <linux/fpga/fpga-mgr.h>#include <linux/fpga/fpga-
region.h>#include <linux/idr.h>#include <linux/kernel.h>#incl
Copyright (C) 2017 Intel Corporation */#include <linux/fpga/fpga-bridge.h>#include <linux/idr.h>#include <linux/kernel.h>#include
<linux/module.h>#include <linux/of_platform.h>#include <linux/s
Copyright (C) 2017, Intel Corporation */#include <linux/slab.h>#include <linux/clk-provider.h>#include <linux/io.h>
Copyright (C) 2017, Intel Corporation */#include <linux/slab.h>#include <linux/clk-provider.h>#include <linux/of_device.h>#include
```

```

<linux/of_address.h>#include <linux/platform_device.h>
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Copyright (c) 2017 Jack Andersen */
Copyright (C) 2017 Jacopo Mondì */
Copyright (C) 2017 Jacopo Mondì <jacopo *
Copyright (C) 2017 James.Bottomley@HansenPartnership.com */#include <linux/slab.h>#include "tpm-dev.h"
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Copyright (C) 2017 Jernej Skrabec <jernej.skrabec@siol.net>
Copyright (C) 2017 Jernej Skrabec <jernej.skrabec@siol.net> * * Coefficients are taken from BSP driver, which is:
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Copyright (c) 2017 Jesper Dangaard Brouer, Red Hat Inc. */
Copyright (c) 2017 Jesper Dangaard Brouer, Red Hat Inc. * * Example howto extract XDP RX-queue info
Copyright (c) 2017 Jesper Dangaard Brouer, Red Hat Inc. */#ifndef __LINUX_NET_XDP_H__#define __LINUX_NET_XDP_H__
Copyright (c) 2017 Jesper Dangaard Brouer, Red Hat Inc. */#include <linux/bpf.h>#include <linux/filter.h>#include <linux/types.h>#include
<linux/mm.h>#include <linux/netdevice.h>#include <linux
Copyright(c) 2017 Jesper Dangaard Brouer, Red Hat, Inc. */#include <uapi/linux/if_ether.h>#include <uapi/linux/if_packet.h>#include
<uapi/linux/if_vlan.h>#include <uapi/linux/ip.h>#include <uapi
Copyright(c) 2017 Jesper Dangaard Brouer, Red Hat, Inc. */static const char *__doc__ = "XDP monitor tool, based on tracepoints\n"
Copyright(c) 2017 Jesper Dangaard Brouer, Red Hat, Inc. */static const char *__doc__ = " XDP redirect with a CPU-map type
\"BPF_MAP_TYPE_CPUMAP\"";
Copyright (c) 2017 Jesper Dangaard Brouer, Red Hat Inc. */static const char *__doc__ = " XDP RX-queue info extract example\n\n" "Monitor how
many packets per sec (pps) are received\n" "per NIC RX
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Copyright (C) 2017 Josh Poimboeuf <jpoimboe@redhat.com> */
Copyright (C) 2017 Josh Poimboeuf <jpoimboe@redhat.com> */#ifndef _ORC_LOOKUP_H#define _ORC_LOOKUP_H
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Copyright (c) 2017 Krzysztof Kozlowski <krzk@kernel.org>
Copyright (c) 2017 Krzysztof Kozlowski <krzk@kernel.org> * * Loosely based on old driver from drivers/char/hw_random/exynos-rng.c:
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http://www.ti.com/lit/ds/symlink/dac122s085.pdf * http://www
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Copyright (C) 2017 Linaro Ltd. */#ifndef __VENUS_FIRMWARE_H__#define __VENUS_FIRMWARE_H__
Copyright (C) 2017 Linaro Ltd. */#ifndef __VENUS_HELPERS_H__#define __VENUS_HELPERS_H__
Copyright (C) 2017 Linaro Ltd. */#ifndef __VENUS_HFI_CMDS_H__#define __VENUS_HFI_CMDS_H__
Copyright (C) 2017 Linaro Ltd. */#ifndef __VENUS_HFI_HELPER_H__#define __VENUS_HFI_HELPER_H__
Copyright (C) 2017 Linaro Ltd. */#ifndef __VENUS_HFI_MSGS_H__#define __VENUS_HFI_MSGS_H__
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Copyright (C) 2017 Linaro Ltd. */#ifndef __VENUS_HFI_VENUS_IO_H__#define __VENUS_HFI_VENUS_IO_H__
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Copyright (C) 2017 Linaro Ltd. */#ifndef __VENUS_VENC_H__#define __VENUS_VENC_H__
Copyright (C) 2017 Linaro Ltd. */#include <linux/clk.h>#include <linux/init.h>#include <linux/ioctl.h>#include <linux/list.h>#include
<linux/module.h>#include <linux/of_device.h>#include <linux
Copyright (C) 2017 Linaro Ltd. */#include <linux/clk.h>#include <linux/iopoll.h>#include <linux/list.h>#include <linux/mutex.h>#include
<linux/pm_runtime.h>#include <linux/slab.h>#include <med
Copyright (C) 2017 Linaro Ltd. */#include <linux/clk.h>#include <linux/module.h>#include <linux/mod_devicetable.h>#include
<linux/platform_device.h>#include <linux/pm_runtime.h>#include <linux/
Copyright (C) 2017 Linaro Ltd. */#include <linux/errno.h>#include <linux/hash.h>
Copyright (c) 2017, Linaro Ltd. */#include <linux/firmware.h>#include <linux/module.h>#include <linux/notifier.h>#include <linux/slab.h>#include
<linux/interrupt.h>#include <linux/io.h>#include
Copyright (C) 2017 Linaro Ltd. */#include <linux/hash.h>#include <linux/list.h>#include <linux/slab.h>#include <media/videobuf2-v4l2.h>
Copyright (C) 2017 Linaro Ltd. */#include <linux/kernel.h>#include <linux/module.h>#include <linux/debugfs.h>#include <linux/device.h>#include
<linux/platform_device.h>#include <linux/qtr.h>#
Copyright (C) 2017 Linaro Ltd. */#include <linux/kernel.h>#include <linux/module.h>#include <linux/device.h>#include <linux/qtr.h>#include
<linux/net.h>#include <linux/completion.h>#include <
Copyright (C) 2017 Linaro Ltd. */#include <linux/slab.h>#include <linux/mutex.h>#include <linux/list.h>#include <linux/completion.h>#include
<linux/platform_device.h>#include <linux/videodev2.h
Copyright (C) 2017 Linaro Ltd. */#include <linux/slab.h>#include <linux/uaccess.h>#include <linux/module.h>#include <linux/kernel.h>#include
<linux/errno.h>#include <linux/string.h>#include <l
Copyright (C) 2017 Linaro Ltd. */#include <linux/types.h>#include <media/v4l2-ctrls.h>
Copyright (C) 2017 Linaro, Ltd., Rob Herring <robh@kernel.org> * * Based on hci_ldisc.c: *
Copyright (C) 2017 Linus Walleij */#include <linux/kernel.h>#include <linux/module.h>#include <linux/platform_device.h>#include
<linux/gpio/consumer.h>#include <linux/of.h>#include <linux/of_de
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MCU with some firmware in its internal 8KB flash.  
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Copyright (c) 2017 Microsemi Corporation */#include <asm/machine.h>#include <asm/prom.h>
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<linux/of_irq.h>#include <linux/irqchip.h>#include <linux/irqchip
Copyright (c) 2017 Microsemi Corporation */#include <linux/delay.h>#include <linux/io.h>#include <linux/notifier.h>#include
<linux/mfd/syscon.h>#include <linux/of_address.h>#include <linux/of_d
Copyright (c) 2017 Microsemi Corporation */#include <linux/etherdevice.h>#include <linux/ethtool.h>#include <linux/if_bridge.h>#include
<linux/if_ether.h>#include <linux/if_vlan.h>#include <lin
Copyright (c) 2017 Microsemi Corporation */#include <linux/interrupt.h>#include <linux/module.h>#include <linux/of_net.h>#include
<linux/netdevice.h>#include <linux/of_mdio.h>#include <linux/of
Copyright (c) 2017 Microsemi Corporation */#include <linux/io.h>#include <linux/kernel.h>#include <linux/platform_device.h>
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XArray.
Copyright (C) 2017, Microsoft Corporation. * * Author(s): Long Li <longli@microsoft.com>
Copyright (c) 2017, Microsoft Corporation. */#include <linux/module.h>#include <linux/vmalloc.h>#include <linux/hyperv.h>#include
<net/sock.h>#include <net/af_vsock.h>
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 Copyright (C) 2017 Paolo Valente <paolo.valente@linaro.org> \* \* BFQ is a proportional-share I/O scheduler, with some extra \* low-latency capabilities.  
 BFQ also supports full hierarchical \* schedul  
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 Copyright (C) 2017 Pengutronix, Juergen Borleis <kernel@pengutronix.de> \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/gpio/consumer.h>#include <linux/regmap.h>#include <lin  
 Copyright (C) 2017 Pengutronix, Juergen Borleis <kernel@pengutronix.de> \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/i2c.h>#include <linux/of.h>  
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 Copyright (c) 2017-present, Facebook, Inc. \*/#include <linux/crypto.h>#include <linux/init.h>#include <linux/interrupt.h>#include <linux/mm.h>#include <linux/module.h>#include <linux/net.h>#in  
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 Copyright (C) 2017 Red Hat Inc. \*/#include <linux/device.h>#include <linux/err.h>#include <linux/init.h>#include <linux/kernel.h>#include

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<linux/module.h>#include <uapi/linux/kvm_para.h>#inclu
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DEFINITION FOR AUDIO DEVICES Release 3.0.
Copyright (c) 2017 Sagi Grimberg. */#include <linux/blk-mq.h>#include <linux/blk-mq-rdma.h>#include <rdma/ib_verbs.h>
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<linux/iopoll.h>#include <linux/mdio.h>#include <linux/mfd/syscon.h>#i
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<linux/mfd/mt6323/registers.h>#include <linux/mfd/mt6397/core.h>#include <linux/m
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Copyright (C) 2017 Sebastian Reichel <sre@kernel.org>
Copyright (C) 2017 Sebastian Reichel <sre@kernel.org> */#include <linux/kernel.h>#include <linux/module.h>#include
<linux/mod_devicetable.h>#include <linux/init.h>#include <linux/device.h>#incl
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Copyright (c) 2017 Siemens AG * * This IIO device driver is designed to work with the following * analog to digital converters from Texas Instruments:
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Copyright (C) 2017 SiFive * * All RISC-V systems have a timer attached to every hart. These timers can be * read from the "time" and "timeh" CSRs,
and can use the SBI to setup * events.
Copyright (C) 2017 SiFive */#ifndef __ASM_HWCAP_H#define __ASM_HWCAP_H
Copyright (C) 2017 SiFive */#ifndef __UAPI_ASM_HWCAP_H#define __UAPI_ASM_HWCAP_H

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and cpcap-usb-det.c:  
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 Copyright (C) 2017 William Breathitt Gray \*/#include <linux/bitmap.h>#include <linux/bitops.h>#include <linux/device.h>#include <linux/errno.h>#include <linux/gpio/driver.h>#include <linux/inte

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 Copyright (c) 2018-2019 Christoph Hellwig \*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/errno.h>#include <linux/syscalls.h>#include <linux/compat.h>#include <linux/refcount.

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Copyright (C) 2018-2019 Intel Corporation \*/#ifndef \_\_NET\_WIRELESS\_CORE\_H#define \_\_NET\_WIRELESS\_CORE\_H#include <linux/list.h>#include  
<linux/netdevice.h>#include <linux/rbtree.h>#include <linux  
Copyright (C) 2018 - 2019 Intel Corporation \*/#ifndef \_\_PMSR\_H#define \_\_PMSR\_H#include <net/cfg80211.h>#include "core.h"#include  
"nl80211.h"#include "rdev-ops.h"  
Copyright (C) 2018-2019 Intel Corporation \*/#include <linux/export.h>#include <linux/bitops.h>#include <linux/etherdevice.h>#include  
<linux/slab.h>#include <linux/ieee80211.h>#include <net/cfg8  
Copyright (C) 2018-2019 Intel Corporation \*/#include <linux/kernel.h>#include <linux/slab.h>#include <linux/module.h>#include  
<linux/netdevice.h>#include <linux/wireless.h>#include <linux/nl802  
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Copyright (C) 2018-2019 Jens Axboe \*/#include <stdio.h>#include <errno.h>#include <assert.h>#include <stdlib.h>#include <stddef.h>#include  
<signal.h>#include <inttypes.h>  
Copyright (C) 2018-2019 Jens Axboe \*/#include <stdio.h>#include <fcntl.h>#include <string.h>#include <stdlib.h>#include <unistd.h>#include  
<assert.h>#include <errno.h>#include <inttypes.h>#i  
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```

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Copyright (c) 2018-2019, Vladimir Oltean <olteanv@gmail.com> */#ifndef _SJA1105_STATIC_CONFIG_H#define _SJA1105_STATIC_CONFIG_H
Copyright (c) 2018-2019, Vladimir Oltean <olteanv@gmail.com> */#include <linux/packing.h>#include <linux/module.h>#include
<linux/bitops.h>#include <linux/errno.h>#include <linux/types.h>
Copyright (c) 2018-2019, Vladimir Oltean <olteanv@gmail.com> */#include <linux/packing.h>#include "sja1105.h"
Copyright (c) 2018-2019, Vladimir Oltean <olteanv@gmail.com> */#include <linux/spi/spi.h>#include <linux/packing.h>#include "sja1105.h"
Copyright (c) 2018-2019, Vladimir Oltean <olteanv@gmail.com> */#include "sja1105.h"
Copyright (c) 2018-2019, Vladimir Oltean <olteanv@gmail.com> */#include "sja1105_static_config.h"#include <linux/crc32.h>#include
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Copyright (c) 2018 Amlogic, inc. * Author: Qiufang Dai <qiuqiang.dai@amlogic.com> */#include <linux/clock-provider.h>#include
<linux/platform_device.h>#include <linux/reset-controller.h>#include <
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Copyright (C) 2018 Andes Technology Corporation * * Perf_events support for RISC-V platforms.
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Copyright (C) 2018 Andreas Brauchli <andreas.brauchli@sensirion.com> * * I2C slave address: 0x58
Copyright (c) 2018 Andrew Lutomirski */
Copyright (C) 2018 Anup Patel <anup@brainfault.org> */#include <linux/kernel.h>#include <linux/console.h>#include <linux/init.h>#include
<linux/serial_core.h>#include <asm/sbi.h>
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Copyright (C) 2018 ARM Ltd. */
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Copyright (C) 2018 ARM Ltd. */#include <linux/device.h>#include <linux/types.h>
Copyright (C) 2018 ARM Ltd. * Sudeep Holla <sudeep.holla@arm.com> */
Copyright (c) 2018, Arm Ltd. * Written by: Quentin Perret, Arm Ltd. */
Copyright (C) 2018, ARM * * This file implements parsing of the Processor Properties Topology Table * which is optionally used to describe the processor
and cache topology. * Due to the relative p
Copyright (C) 2018 * Author: Jeff LaBundy <jeff@labundy.com> * * 7-bit I2C slave address: 0x13
Copyright (C) 2018 * Author: Jeff LaBundy <jeff@labundy.com> * * These devices require firmware exported from a PC-based configuration tool * made
available by the vendor. Firmware files may be pu
Copyright (C) 2018 * Author: Mathieu Othacehe <m.othacehe@gmail.com> * * 7-bit I2C slave address: 0x57
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Copyright (c) 2018 Axentia Technologies AB * Author: Peter Rosin <peda@axentia.se> * * Datasheet:
http://www.microchip.com/downloads/en/DeviceDoc/22147a.pdf

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Copyright (C) 2018 BayLibre, SAS \* Author: Maxime Jourdan <mjourdan@baylibre.com> \* \* The Elementary Stream Parser is a HW bitstream parser. \*  
It reads bitstream buffers and feeds them to the VIFI  
Copyright (C) 2018 BayLibre, SAS \* Author: Maxime Jourdan <mjourdan@baylibre.com> \* \* VDEC\_1 is a video decoding block that allows decoding of \*  
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Copyright (C) 2018 Broadcom \*/#include <linux/delay.h>#include <linux/device.h>#include <linux/io.h>#include <linux/module.h>#include  
<linux/of.h>#include <linux/platform\_device.h>#include <li  
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Copyright (C) 2018 Cavium Inc. \* Author: Ganapatrao Kulkarni <gkulkarni@cavium.com> \*/  
Copyright (c) 2018 Cavium, Inc. \*/#include <linux/bcd.h>#include <linux/delay.h>#include <linux/i2c.h>#include <linux/module.h>#include  
<linux/mutex.h>#include <linux/nvmem-provider.h>#include  
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Copyright (C) 2018 Christophe Leroy CS Systemes d \* \* Inspired from glibc implementation  
Copyright (C) 2018 Christoph Hellwig \*/  
Copyright (C) 2018 Christoph Hellwig \*/#define pr\_fmt(fmt) "plic: " fmt#include <linux/interrupt.h>#include <linux/io.h>#include <linux/irq.h>#include  
<linux/irqchip.h>#include <linux/irqdomain  
Copyright (C) 2018 Christoph Hellwig. \* \* DMA operations that map physical memory directly without using an IOMMU.  
Copyright (c) 2018 Christoph Hellwig. \*/#include <linux/module.h>#include <linux/compiler.h>#include <linux/fs.h>#include <linux/iomap.h>#include  
<linux/pagemap.h>#include <linux/pagevec.h>  
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Copyright (C) 2018 Cogent Embedded, Inc. \* \* Based on r8a7795-cpg-mssr.c  
Copyright (C) 2018 Cogent Embedded, Inc. \*/#ifndef \_\_DT\_BINDINGS\_CLOCK\_R8A77980\_CPG\_MSSR\_H\_\_#define  
\_\_DT\_BINDINGS\_CLOCK\_R8A77980\_CPG\_MSSR\_H\_\_  
Copyright (C) 2018 Cogent Embedded, Inc. \*/#ifndef \_\_DT\_BINDINGS\_POWER\_R8A77980\_SYSC\_H\_\_#define  
\_\_DT\_BINDINGS\_POWER\_R8A77980\_SYSC\_H\_\_  
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Copyright (c) 2018, Covalent IO, Inc. http://covalent.io  
Copyright (c) 2018 Covalent IO, Inc. http://covalent.io#define SOCKMAP#define TEST\_MAP\_TYPE BPF\_MAP\_TYPE SOCKMAP  
Copyright (c) 2018 Covalent IO, Inc. http://covalent.io \*/#include <uapi/linux/btf.h>#include <linux/kernel.h>#include <linux/types.h>#include  
<linux/slab.h>#include <linux/bpf.h>#include <linu  
Copyright (c) 2018 Covalent IO, Inc. http://covalent.io#undef SOCKMAP#define TEST\_MAP\_TYPE BPF\_MAP\_TYPE SOCKHASH  
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Copyright (C) 2018 David Lechner <david@lechnology.com> \* \* Based on arch/arm/mach-davinci/clock.c  
Copyright (C) 2018 Davidlohr Bueso. \* \* Benchmark the various operations allowed for epoll\_ctl(2). \* The idea is to concurrently stress a single epoll  
instance  
Copyright (C) 2018 Davidlohr Bueso. \* \* This program benchmarks concurrent epoll\_wait(2) monitoring multiple \* file descriptors under one or two load  
balancing models. The first, \* and default, is  
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Copyright (C) 2018 Digital Devices GmbH  
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Copyright (C) 2018 embedded brains GmbH \*/#include "libfdt\_env.h"  
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```

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Copyright (c) 2018 Facebook#ifdef _UAPI_LINUX_BT_H_#define _UAPI_LINUX_BT_H_
Copyright (c) 2018 Facebook, Inc. * Author: Johannes Weiner <hannes@cmpxchg.org> * * Polling support by Suren Baghdasaryan
<surenb@google.com>
Copyright (c) 2018 Facebook#include <linux/bpf.h>#include "bpf_helpers.h"
Copyright (c) 2018 Facebook*/#include <linux/bpf.h>#include <linux/err.h>#include <linux/sock_diag.h>#include <net/sock_reuseport.h>
Copyright (c) 2018 Facebook#include <stddef.h>#include <linux/bpf.h>#include <linux/types.h>#include "bpf_helpers.h"
Copyright (c) 2018 Facebook#include <uapi/linux/bpf.h>#include "bpf_helpers.h"
Copyright (C) 2018 Felix Fietkau <nbd@nbd.name> */#include "mt76.h"
Copyright (C) 2018 Florian Fainelli <f.fainelli@gmail.com> */
Copyright (C) 2018 Free Software Foundation, Inc. */
Copyright (c) 2018 Fuzhou Rockchip Electronics Co., Ltd */
Copyright (C) 2018 Gao Xiang <gaoxiang25@huawei.com> */#ifndef __EROFS_FS_TAGPTR_H#define __EROFS_FS_TAGPTR_H
Copyright (C) 2018 Gateworks Corporation */
Copyright (C) 2018 Gateworks Corporation */#include <linux/delay.h>#include <linux/hdmi.h>#include <linux/i2c.h>#include <linux/init.h>#include
<linux/interrupt.h>#include <linux/kernel.h>#inc
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Copyright (C) 2018 Google, Inc.
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Copyright (C) 2018 Google, Inc. * * Author: Hans Verkuil <hans.verkuil@cisco.com> * Author: Sakari Ailus <sakari.ailus@linux.intel.com>
Copyright (C) 2018 Google, Inc. * * Author: schuffelen@google.com
Copyright (C) 2018 Google, Inc. */#ifndef __APEX_H_#define __APEX_H_
Copyright (C) 2018 Google, Inc.#ifndef __GASKET_CONSTANTS_H_#define __GASKET_CONSTANTS_H_
Copyright (C) 2018 Google, Inc.*/#ifndef __GASKET_CORE_H_#define __GASKET_CORE_H_
Copyright (C) 2018 Google, Inc.*/#ifndef __GASKET_H_#define __GASKET_H_
Copyright (C) 2018 Google, Inc.*/#ifndef __GASKET_INTERRUPT_H_#define __GASKET_INTERRUPT_H_
Copyright (C) 2018 Google, Inc.#ifndef __GASKET_IOCTL_H_#define __GASKET_IOCTL_H_
Copyright (C) 2018 Google, Inc.#include "gasket.h"#include "gasket_ioctl.h"#include "gasket_constants.h"#include "gasket_core.h"#include
"gasket_interrupt.h"#include "gasket_page_table.h"#inclu
Copyright (C) 2018 Google, Inc.#include "gasket_sysfs.h"
Copyright (c) 2018 Google, Inc. * * When CPU, memory and IO are contended, tasks experience delays that * reduce throughput and introduce latencies
into the workload. Memory * and IO contention, i
Copyright (C) 2018 Google LLC */
Copyright (C) 2018, Google LLC.
Copyright (C) 2018, Google LLC. */
Copyright (C) 2018, Google LLC. * * Adaptions for s390x:
Copyright (C) 2018, Google LLC. * * Header file that describes API to the sparsebit library. * This library provides a memory efficient means of storing *
the settings of bits indexed via a uint64
Copyright (C) 2018, Google LLC. * * IA32_TSC_ADJUST test
Copyright (C) 2018, Google LLC. */#ifndef SELFTEST_KVM_UTIL_H#define SELFTEST_KVM_UTIL_H
Copyright (C) 2018, Google LLC. * * This is a regression test for the bug fixed by the following commit:
Copyright (C) 2018, Google LLC. * * Verifies expected behavior of x86 KVM_CAP_SYNC_REGS functionality, * including requesting an invalid register set,
updates to/from values * in kvm_run.s.regs wh
Copyright (c) 2018 Guenter Roeck */
Copyright (c) 2018 Guillaume Dou * * Implementation inspired by existing x86 platform drivers, in special * asus/eeepc/fujitsu-laptop, thanks to their
authors.
Copyright (C) 2018 Hangzhou C-SKY Microsystems co.,ltd.
Copyright (C) 2018 Hangzhou C-SKY Microsystems co.,ltd.#include <linux/kernel.h>#include <linux/module.h>#include <linux/init.h>#include
<linux/delay.h>
Copyright (c) 2018 Hanno Zulla <kontakt@hanno.de> */
Copyright (C) 2018 Hans de Goede <hdegoede@redhat.com> * * Based on various non upstream patches for ISP support:
Copyright (C) 2018 Helge Deller <deller@gmx.de> *
Copyright (C) 2018 Himanshu Jha <himanshujha199640@gmail.com> * * 7-Bit I2C slave address is:
Copyright (C) 2018 Himanshu Jha <himanshujha199640@gmail.com> * * Datasheet:
Copyright (C) 2018 Himanshu Jha <himanshujha199640@gmail.com> */#include <linux/acpi.h>#include <linux/module.h>#include
<linux/of.h>#include <linux/regmap.h>#include <linux/spi/spi.h>
Copyright (c) 2018 Hisilicon Limited. */
Copyright (c) 2018 HiSilicon Technologies Co., Ltd. */
Copyright (C) 2018 HUAWAI, Inc. * http://www.huawei.com/ * Created by Gao Xiang <gaoxiang25@huawei.com> */#ifndef
__EROFS_FS_ZDATA_H#define __EROFS_FS_ZDATA_H

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__EROFS_FS_ZPVEC_H#define __EROFS_FS_ZPVEC_H
Copyright (C) 2018 HUAWEI, Inc. * http://www.huawei.com/ * Created by Gao Xiang <gaoxiang25@huawei.com> */#include
"internal.h"#include <linux/pagevec.h>
Copyright (C) 2018 HUAWEI, Inc. * http://www.huawei.com/ * Created by Gao Xiang <gaoxiang25@huawei.com> */#include "zdata.h"#include
"compress.h"#include <linux/prefetch.h>
Copyright (c) 2018 IBM Corp.
Copyright (C) 2018 IBM Corp. All rights reserved
Copyright (C) 2018 IBM Corporation */
Copyright (C) 2018 IBM Corporation. */#include <limits.h>#include <linux/kernel.h>#include <sys/mman.h>#include <sys/types.h>#include
<sys/stat.h>#include <sys/time.h>#include <sys/resource.h>
Copyright (C) 2018 IBM Corporation */#include <linux/efi.h>#include <linux/module.h>#include <linux/ima.h>
Copyright (C) 2018 IBM Corporation. */#include "ptrace.h"#include "child.h"
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Copyright (C) 2018 Intel Corporation * * Author: Lu Baolu <baolu.lu@linux.intel.com>
Copyright (C) 2018 Intel Corporation * Author: Rafael J. Wysocki <rafael.j.wysocki@intel.com> * * The idea of this governor is based on the observation
that on many systems * timer events are two
Copyright (C) 2018 Intel Corporation * Author: Rafael J. Wysocki <rafael.j.wysocki@intel.com> * * This driver is based on Section 9.18 of the ACPI 6.2
specification revision.
Copyright (C) 2018 Intel Corporation * * Author: Reinette Chatre <reinette.chatre@intel.com>
Copyright (C) 2018, Intel Corporation * Authors: Andy Shevchenko <andriy.shevchenko@linux.intel.com> * Mika Westerberg
<mika.westerberg@linux.intel.com> */
Copyright(c) 2018 Intel Corporation. * * Author(s): Björn Töpel <bjorn.topel@intel.com> * Magnus Karlsson <magnus.karlsson@intel.com>
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Copyright (C) 2018, Intel Corporation * Copied from reset-sunxi.c */
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Copyright (C) 2018 Intel Corporation */#ifndef __NET_WIRELESS_NL80211_H#define __NET_WIRELESS_NL80211_H
Copyright (C) 2018 Intel Corporation */#include <linux/completion.h>#include <linux/fpga/fpga-mgr.h>#include <linux/firmware/intel/stratix10-svc-
client.h>#include <linux/module.h>#include <linux
Copyright (C) 2018 Intel Corporation */#include <linux/slab.h>#include <linux/kernel.h>#include <linux/if_arp.h>#include
<linux/netdevice.h>#include <linux/rtnetlink.h>#include <net/mac80211.h>
Copyright (C) 2018 Intel Corporation */#include <linux/types.h>#include <net/cfg80211.h>#include "ieee80211_i.h"#include "sta_info.h"#include
"driver-ops.h"
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header files.
Copyright (C) 2018 Intel Corporation * * Portions of this file are derived from the ipw3945 project, as well * as portions of the ieee80211 subsystem
header files.
Copyright (C) 2018 Intel Corporation * * Specification for this interface is available at: *
Copyright (C) 2018 Intel Corporation * * Transmit and frame generation functions.
Copyright (C) 2018, Intel, Inc. * * Author : Yi Sun <yi.y.sun@intel.com>
Copyright(c) 2018 Ira Weiny, Intel Corporation */
Copyright (C) 2018 IVI Foundation, Inc. */
Copyright (C) 2018 IVI Foundation, Inc. * * This file holds USB constants defined by the USB Device Class * and USB488 Subclass Definitions for Test
and Measurement devices * published by the USB-
Copyright (C) 2018 Jacob Feder * * Authors: Jacob Feder <jacobsfeder@gmail.com>
Copyright (C) 2018 Jacopo Mondi <jacopo
Copyright (C) 2018 Jacopo Mondi <jacopo */
Copyright (C) 2018 Jacopo Mondi <jacopo *
Copyright (C) 2018 Jacopo Mondi <jacopo * * Based on Renesas R-Car M3-W System Controller
Copyright (C) 2018 Jacopo Mondi <jacopo */#ifndef __DT_BINDINGS_CLOCK_R8A77965_CPG_MSSR_H__#define
__DT_BINDINGS_CLOCK_R8A77965_CPG_MSSR_H__
Copyright (C) 2018, Jacopo Mondi <jacopo@jmondi.org> *
Copyright (C) 2018 Jacopo Mondi <jacopo@jmondi.org> * * Based on mt9v032 driver
Copyright (c) 2018 James.Bottomley@HansenPartnership.com * * CFB is a stream cipher mode which is layered on to a block * encryption scheme. It
works very much like a one time pad where * the pad
Copyright (C) 2018 Jan Breuer <jan.breuer@jaybee.cz> * * Improve driver for i2cdetect from i2c-tools to detect i2c devices on the bus.
Copyright (c) 2018 Jernej Skrabec <jernej.skrabec@siol.net>
Copyright (c) 2018 Jernej Skrabec <jernej.skrabec@siol.net> */
Copyright (C) 2018 Jernej Skrabec <jernej.skrabec@siol.net>
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Copyright (c) 2018 Jesper Dangaard Brouer, Red Hat Inc.
Copyright (c) 2018 Jesper Dangaard Brouer, Red Hat Inc. * * Example howto transfer info from XDP to SKB, e.g. skb->mark
Copyright (c) 2018 Jesper Dangaard Brouer, Red Hat Inc. */static const char * __doc__ = "Libbpf test program for loading BPF ELF object files";
Copyright(c) 2018 Jesper Dangaard Brouer. * * XDP/TC VLAN manipulation example
Copyright (c) 2018 Jim Wilson (jimw@sifive.com) */
Copyright (C) 2018 Joe Lawrence <joe.lawrence@redhat.com>
Copyright (C) 2018 Joel Fernandes (Google) <joel@joelfernandes.org> */
Copyright (C) 2018 Johan Hovold <johan@kernel.org> */
Copyright (C) 2018 Johannes Thumshirn <jth@kernel.org> */#include <linux/io.h>#include <linux/kernel.h>#include <linux/mcb.h>#include
<linux/module.h>#include <linux/watchdog.h>
Copyright (c) 2018 Johannes Thumshirn, SUSE Linux GmbH */
Copyright (c) 2018 Johannes Thumshirn, SUSE Linux GmbH * * This is entirely based on drivers/nvme/host/trace.h
Copyright (C) 2018 John Crispin <john@phrozen.org> */
Copyright (c) 2018 Jonathan Marek <jonathan@marek.ca> * * Based on msm_pwm_vibrator.c from downstream Android sources:
Copyright (C) 2018 Josef Bacik */#include <linux/kernel.h>#include <linux/blk_types.h>#include <linux/backing-dev.h>#include
<linux/module.h>#include <linux/timer.h>#include <linux/memcontrol.h
Copyright (c) 2018 Juergen Gross */
Copyright (c) 2018 Julian Sax <jsbc@gmx.de> *
Copyright (C) 2018 Kent Gustavsson <kent@minoris.se> */#include <linux/clk.h>#include <linux/delay.h>#include <linux/err.h>#include
<linux/iio/iio.h>#include <linux/module.h>#include <linux/reg
Copyright (C) 2018 Kirill Marinushkin <kmarinushkin@birdec.com>
Copyright (C) 2018 Kirill Marinushkin <kmarinushkin@birdec.com> */
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```

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Copyright (C) 2018 Laurent Pinchart <laurent.pinchart@ideasonboard.com> * * Based on work from Jyri Sarha <jsarha@ti.com>
Copyright (C) 2018 Laurent Pinchart <laurent.pinchart@ideasonboard.com> */#ifndef __RCAR_DU_OF_H__#define __RCAR_DU_OF_H__
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Copyright (C) 2018 Linaro Ltd. * * Author: Stanimir Varbanov <stanimir.varbanov@linaro.org>
Copyright (C) 2018 Linaro Ltd * * Based on OV5640 Sensor Driver
Copyright (c) 2018 Linaro Ltd. */#ifndef DT_BINDINGS_POWER_OWL_S900_POWERGATE_H#define
DT_BINDINGS_POWER_OWL_S900_POWERGATE_H
Copyright (C) 2018 Linaro Ltd.#ifndef __VENUS_HFI_PARSER_H__#define __VENUS_HFI_PARSER_H__
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Copyright (C) 2018 Linus Walleij <linus.walleij@linaro.org> * Parts of this file were based on the MCDE driver by Marcus Lorentzon
Copyright (C) 2018 Linus Walleij * * This module binds to the "arm,vexpress-muxfpga" device on the * Versatile Express configuration bus and sets up
which CLCD instance * gets muxed out on the DVI
Copyright (C) 2018 Linus Walleij <linus.walleij@linaro.org> * Includes portions of code from the firmware uploader by:
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Copyright (C) 2018 Lorenzo Bianconi <lorenzo.bianconi83@gmail.com> */
Copyright (C) 2018 Lorenzo Bianconi <lorenzo.bianconi83@gmail.com> */#include <linux/kernel.h>#include <linux/firmware.h>
Copyright (C) 2018 Lorenzo Bianconi <lorenzo.bianconi83@gmail.com> */#include <linux/kernel.h>#include <linux/firmware.h>#include
<linux/module.h>
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Copyright (c) 2018, Mahesh Bandewar <maheshb@google.com> */
Copyright (c) 2018 Manivannan Sadhasivam */
Copyright (c) 2018 Manivannan Sadhasivam * * RDA8810PL has two independent timers: OSTIMER (56 bit) and HWTIMER (64 bit). * Each timer
provides optional interrupt support. In this driver, OSTIMER i
Copyright (C) 2018 Marcus Folkesson <marcus.folkesson@gmail.com>
Copyright (C) 2018 Marcus Folkesson <marcus.folkesson@gmail.com> */
Copyright (C) 2018 Marcus Folkesson <marcus.folkesson@gmail.com> */#include <linux/bitops.h>#include <linux/iio/iio.h>#include
<linux/iio/sysfs.h>#include <linux/init.h>#include <linux/module.h>
Copyright (C) 2018 Marek Behun <marek.behun@nic.cz> */
Copyright (C) 2018 Marek Vasut <marek@denx.de> */#include <linux/delay.h>#include <linux/ethtool.h>#include <linux/kernel.h>#include
<linux/mii.h>#include <linux/module.h>#include <linux/phy.h>

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Copyright (c) 2018 MediaTek Inc. * Author: Yong Wu <yong.wu@mediatek.com> */#ifndef __DTS_IOMMU_PORT_MT8183_H#define
__DTS_IOMMU_PORT_MT8183_H
Copyright (C) 2018 MediaTek Inc. * * Author: ZH Chen <zh.chen@mediatek.com>
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Copyright (C) 2018 MediaTek Inc. * * Author: Zhiyong Tao <zhiyong.tao@mediatek.com>
Copyright (c) 2018 MediaTek Inc. */#include <linux/bitfield.h>#include <linux/io.h>#include <linux/mfd/syscon.h>#include <linux/module.h>#include
<linux/of.h>#include <linux/of_device.h>#includ
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Copyright (c) 2018 Microsemi Corporation #ifndef \_\_PHY\_OCELOT\_SERDES\_H\_\_ #define \_\_PHY\_OCELOT\_SERDES\_H\_\_  
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Copyright (C) 2018 Nuvoton Technology Corp \* \* Author: Wang, Joseph C <joequant@gmail.com> \* Co-author: John Hsu <KCHSU0@nuvoton.com> \*  
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Copyright (c) 2018, Oleksandr Andrushchenko, EPAM Systems Inc.  
Copyright (C) 2018 Oleksij Rempel <linux@rempel-privat.de> \* \* Driver for Alcor Micro AU6601 and AU6621 controllers  
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Copyright (c) 2018 Pengutronix, Philipp Zabel \* \* based on vim2m  
Copyright (C) 2018 Pengutronix, Philipp Zabel \*/ #include <linux/module.h> #include <linux/delay.h> #include <linux/fs.h> #include  
<linux/sched.h> #include <linux/slab.h> #include <video/imx-ipu-v3.h>  
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Copyright (c) 2018 Peter Ganzhorn <peter.ganzhorn@gmail.com> \*/  
Copyright (C) 2018 Phil Reid <preid@electromag.com.au> \* \* Datasheet: [http://www.analog.com/media/en/technical-documentation/data-sheets/AD5272\\_5274.pdf](http://www.analog.com/media/en/technical-documentation/data-sheets/AD5272_5274.pdf)  
Copyright (c) 2018 Politecnico di Torino #define MAP\_TYPE BPF\_MAP\_TYPE\_QUEUE  
Copyright (c) 2018 Politecnico di Torino #define MAP\_TYPE BPF\_MAP\_TYPE\_STACK  
Copyright (c) 2018 Politecnico di Torino \*/ #include <linux/bpf.h> #include <linux/list.h> #include <linux/slab.h> #include <linux/capability.h> #include

```

"percpu_freelist.h"
Copyright (c) 2018 Politecnico di Torino#include <stddef.h>#include <string.h>#include <linux/bpf.h>#include <linux/if_ether.h>#include
<linux/ip.h>#include <linux/pkt_cls.h>#include "bpf_helpe
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Copyright (C) 2018, Red Hat, Inc. (code style cleanup and fuzzing driver) * * This library provides functions to support a memory efficient bit array, *
with an index size of 2^64. A sparsebit arr
Copyright (C) 2018, Red Hat, Inc. */#ifndef SELFTEST_KVM_PROCESSOR_H#define SELFTEST_KVM_PROCESSOR_H
Copyright (C) 2018 Red Hat, Inc. */#ifndef __SS_SIDTAB_H__define __SS_SIDTAB_H__
Copyright (C) 2018, Red Hat, Inc. */#include "kvm_util.h"
Copyright (C) 2018, Red Hat, Inc. */#include "kvm_util.h"#include "../kvm_util_internal.h"
Copyright (C) 2018 Red Hat, Inc. */#include <linux/errno.h>#include <linux/kernel.h>#include <linux/slab.h>#include <linux/sched.h>#include
<linux/spinlock.h>#include <asm/barrier.h>#include "
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Copyright (C) 2018, Red Hat, Inc. * * Tests for SMM.
Copyright (C) 2018, Red Hat, Inc. * * Tests for vCPU state save/restore, including nested guest state.
Copyright (c) 2018 Red Hat, Inc. * * This is a test "dust" device, which fails reads on specified * sectors, emulating the behavior of a hard disk drive
sending * a "Read Medium Error" sense.
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Copyright (C) 2018 Renesas Electronics Corp. * * Based on Renesas R-Car M3-W System Controller
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__DT_BINDINGS_CLOCK_R8A7744_CPG_MSSR_H__
Copyright (C) 2018 Renesas Electronics Corp. */#ifndef __DT_BINDINGS_CLOCK_R8A77470_CPG_MSSR_H__#define
__DT_BINDINGS_CLOCK_R8A77470_CPG_MSSR_H__
Copyright (C) 2018 Renesas Electronics Corp. */#ifndef __DT_BINDINGS_CLOCK_R8A774A1_CPG_MSSR_H__#define
__DT_BINDINGS_CLOCK_R8A774A1_CPG_MSSR_H__
Copyright (C) 2018 Renesas Electronics Corp. */#ifndef __DT_BINDINGS_CLOCK_R8A774C0_CPG_MSSR_H__#define
__DT_BINDINGS_CLOCK_R8A774C0_CPG_MSSR_H__
Copyright (C) 2018 Renesas Electronics Corp. */#ifndef __DT_BINDINGS_CLOCK_R8A77990_CPG_MSSR_H__#define
__DT_BINDINGS_CLOCK_R8A77990_CPG_MSSR_H__
Copyright (C) 2018 Renesas Electronics Corp. */#ifndef __DT_BINDINGS_POWER_R8A7744_SYSC_H__#define
__DT_BINDINGS_POWER_R8A7744_SYSC_H__
Copyright (C) 2018 Renesas Electronics Corp. */#ifndef __DT_BINDINGS_POWER_R8A77470_SYSC_H__#define
__DT_BINDINGS_POWER_R8A77470_SYSC_H__

```

Copyright (C) 2018 Renesas Electronics Corp. \*/#ifndef \_\_DT\_BINDINGS\_POWER\_R8A774A1\_SYSC\_H\_\_#define  
\_\_DT\_BINDINGS\_POWER\_R8A774A1\_SYSC\_H\_\_  
Copyright (C) 2018 Renesas Electronics Corp. \*/#ifndef \_\_DT\_BINDINGS\_POWER\_R8A774C0\_SYSC\_H\_\_#define  
\_\_DT\_BINDINGS\_POWER\_R8A774C0\_SYSC\_H\_\_  
Copyright (C) 2018 Renesas Electronics Corp. \*/#ifndef \_\_DT\_BINDINGS\_POWER\_R8A77990\_SYSC\_H\_\_#define  
\_\_DT\_BINDINGS\_POWER\_R8A77990\_SYSC\_H\_\_  
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Copyright (c) 2018 Rockchip, Inc. \* \* Author: Shawn Lin <shawn.lin@rock-chips.com>  
Copyright (c) 2018 Rockchip, Inc. \* \* Author: Shawn Lin <shawn.lin@rock-chips.com> \* Simon Xue <xxm@rock-chips.com>  
Copyright (c) 2018 Rodrigo Rivas Costa <rodrigorivascosta@gmail.com> \* \* Supports both the wired and wireless interfaces.  
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<antoine.tenart@bootlin.com>  
Copyright (c) 2018 Samsung Electronics Co., Ltd.// Author: Marek Szyprowski <m.szyprowski@samsung.com>// Common Clock Framework support for  
Exynos5 power-domain dependent clocks  
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```
Copyright (C) 2018 Stefan Wahren <stefan.wahren@i2se.com> */#include <linux/device.h>#include <linux/err.h>#include <linux/hwmon.h>#include
<linux/module.h>#include <linux/platform_device.h>#in
Copyright (C) 2018 STMicroelectronics * Author(s): Amelie Delaunay <amelie.delaunay@st.com>. */#include <linux/bitfield.h>#include
<linux/clk.h>#include <linux/delay.h>#include <linux/io.h>#inc
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Copyright (C) 2018 Texas Instruments Incorporated - http://www.ti.com * Author: Peter Ujfalusi <peter.ujfalusi@ti.com> */
Copyright (C) 2018 Texas Instruments Incorporated - http://www.ti.com/ * * Based on cpsw-phy-sel.c driver created by Mugunthan V N
<mugunthanvnm@ti.com>
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Copyright (C) 2018 Western Digital Corporation */
Copyright (C) 2018 Western Digital Corporation */#ifndef SCSI_BSG_UFS_H#define SCSI_BSG_UFS_H
Copyright (C) 2018 Western Digital Corporation */#ifndef UFS_BSG_H#define UFS_BSG_H
Copyright (C) 2018 Western Digital Corporation */#include "ufs_bsg.h"
Copyright (C) 2018 William Breathitt Gray
Copyright (C) 2018 William Breathitt Gray */#ifndef _COUNTER_ENUM_H_#define _COUNTER_ENUM_H_
Copyright (C) 2018 William Breathitt Gray */#ifndef _COUNTER_H_#define _COUNTER_H_
Copyright (C) 2018 William Breathitt Gray */#include <linux/counter.h>#include <linux/device.h>#include <linux/err.h>#include
<linux/export.h>#include <linux/fs.h>#include <linux/gfp.h>#includ
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Copyright (C) 2018 Xue Liu <liuxuenetmail@gmail.com> \*/#include <linux/kernel.h>#include <linux/module.h>#include <linux/gpio/consumer.h>#include <linux/spi/spi.h>#include <linux/workqueue.h>#i

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Copyright (c) 2019 Baylibre SAS. \* Author: Neil Armstrong <narmstrong@baylibre.com> \*/#include <linux/clock-provider.h>#include <linux/platform\_device.h>#include <linux/reset-controller.h>#include

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Copyright (C) 2019 Christoph Hellwig \*/#ifndef LINUX\_IO\_URING\_H#define LINUX\_IO\_URING\_H

Copyright (c) 2019 Christoph Hellwig. \*/#include "xfs.h"

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Copyright (C) 2019 Dell Inc \* by Narendra K <Narendra.K@dell.com> \* \* System firmware advertises the address of the RC12 Table via \* an EFI Configuration Table entry. This code retrieves the RC12

Copyright (C) 2019, Dell Inc \* \* The PCIe Link Bandwidth Notification provides a way to notify the \* operating system when the link width or data rate changes. This \* capability is required for a

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Copyright (c) 2019 Facebook#define STACK_MAX_LEN 100
Copyright (c) 2019 Facebook#define STACK_MAX_LEN 180
Copyright (c) 2019 Facebook#define STACK_MAX_LEN 50
Copyright (c) 2019 Facebook#define STACK_MAX_LEN 600/* clang will not unroll the loop 600 times. * Instead it will unroll it to the amount it deemed *
appropriate, but the loop will still execute
Copyright (c) 2019 Facebook#define STACK_MAX_LEN 600#define NO_UNROLL/* clang will not unroll at all. * Total program size is around 2k insns */
Copyright (c) 2019 Facebook#ifndef __ASM_GOTO_WORKAROUND_H#define __ASM_GOTO_WORKAROUND_H
Copyright (c) 2019 Facebook#ifndef __BPF_SK_STORAGE_H#define __BPF_SK_STORAGE_H
Copyright (c) 2019 Facebook */#ifndef __LIBBPF_HASHMAP_H#define __LIBBPF_HASHMAP_H
Copyright (c) 2019 Facebook Inc *
Copyright (c) 2019 Facebook#include <features.h>
Copyright (c) 2019 Facebook#include <linux/bpf.h>#include "bpf_helpers.h"#define ATTR __always_inline#include "test_jhash.h"
Copyright (c) 2019 Facebook#include <linux/bpf.h>#include "bpf_helpers.h"#define ATTR __attribute__((noinline))#include "test_jhash.h"
Copyright (c) 2019 Facebook#include <linux/bpf.h>#include <linux/version.h>#include "bpf_helpers.h"
Copyright (c) 2019 Facebook#include <linux/compiler.h>#include <linux/err.h>
Copyright (c) 2019 Facebook#include <linux/rculist.h>#include <linux/list.h>#include <linux/hash.h>#include <linux/types.h>#include
<linux/spinlock.h>#include <linux/bpf.h>#include <net/bpf_sk_
Copyright (c) 2019 Facebook#include <linux/sched.h>#include <linux/ptrace.h>#include <stdint.h>#include <stddef.h>#include <stdbool.h>#include
<linux/bpf.h>#include "bpf_helpers.h"
Copyright (c) 2019 Facebook */#include <stdbool.h>
Copyright (c) 2019 Facebook#include <stddef.h>#include <string.h>#include <linux/bpf.h>#include <linux/if_ether.h>#include
<linux/if_packet.h>#include <linux/ip.h>#include <linux/ipv6.h>#inclu
Copyright (c) 2019 Facebook */#include <stdint.h>#include <stdlib.h>#include <stdio.h>#include <errno.h>#include <linux/err.h>#include
"hashmap.h"
Copyright (c) 2019 Facebook */#include <stdio.h>#include <errno.h>#include <linux/err.h>#include "hashmap.h"
Copyright (c) 2019 Facebook#include <test_progs.h>static int libbpf_debug_print(enum libbpf_print_level level, const char *format,
va_list args){ if (level != LIBBPF_DEBUG) { vprintf(fo
Copyright (C) 2019 Facebook * * One challenge of controlling IO resources is the lack of trivially * observable cost metric. This is distinguished from CPU
and memory where * wallclock time and t
Copyright (c) 2019 Facebook */* ----- START-EXPECTED-OUTPUT ----- */enum e1 { A = 0, B = 1,
Copyright (c) 2019 Facebook */* ----- START-EXPECTED-OUTPUT ----- */struct packed_trailing_space { int a; short b;} __attribute__((packed));
Copyright (c) 2019 Facebook */* ----- START-EXPECTED-OUTPUT ----- */struct padded_implicitly { int a; long int b; char c;
Copyright (c) 2019 Facebook */* ----- START-EXPECTED-OUTPUT ----- */struct s1 {};
Copyright (c) 2019 Facebook */* ----- START-EXPECTED-OUTPUT ----- */struct S { int S; int U;
Copyright (c) 2019 Facebook */* ----- START-EXPECTED-OUTPUT ----- */typedef int arr_t[2];
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Copyright (C) 2019 Glider bvba */
Copyright (c) 2019 Google Inc. */
Copyright (C) 2019 Google, Inc.
Copyright (c) 2019, Google Inc. * Author - Rajat Jain * * This code instantiates platform devices for intel_pmc_core driver, only * on supported platforms
that may not have the ACPI devices in the
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Copyright (C) 2019 Google, Inc. * Tomasz Figa <tfiga@chromium.org> */
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Copyright (c) 2019 Google LLC * * Based on aesni-intel_glue.c by:
Copyright (C) 2019 Google LLC <ebiggers@google.com>
Copyright (C) 2019, Google LLC. * * Test for KVM_CAP_MAX_VCPUS and KVM_CAP_MAX_VCPU_ID.
Copyright (C) 2019, Google LLC. * * This test verifies the integrity of calling the ioctl KVM_SET_NESTED_STATE.
Copyright (c) 2019 Google LLC * * Updated RFC4106 AES-GCM testing. * Authors: Aidan O'Mahony (aidan.o.mahony@intel.com) * Adrian
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Copyright (C) 2019 Helge Deller <deller@gmx.de> */
Copyright (C) 2019 Helge Deller <deller@gmx.de> * * Based on arch/arm64/kernel/jump_label.c
Copyright (c) 2019 Hisilicon Limited.
Copyright (c) 2019 HiSilicon Limited.#ifndef HISI_ACC_QM_H#define HISI_ACC_QM_H

```

```

Copyright (c) 2019 HiSilicon Limited. #ifndef HISI_ACC_SGL_H #define HISI_ACC_SGL_H
Copyright (c) 2019 HiSilicon Limited. #ifndef HISI_ZIP_H #define HISI_ZIP_H
Copyright (c) 2019 HiSilicon Limited. #include <asm/page.h> #include <linux/bitmap.h> #include <linux/debugfs.h> #include <linux/dma-
mapping.h> #include <linux/io.h> #include <linux/irqreturn.h> #incl
Copyright (c) 2019 HiSilicon Limited. #include <crypto/internal/acompress.h> #include <linux/bitfield.h> #include <linux/dma-mapping.h> #include
<linux/scatterlist.h> #include "zip.h"
Copyright (c) 2019 HiSilicon Limited. #include <linux/acpi.h> #include <linux/aer.h> #include <linux/bitops.h> #include <linux/debugfs.h> #include
<linux/init.h> #include <linux/io.h> #include <linux/
Copyright (c) 2019 HiSilicon Limited. #include <linux/dma-mapping.h> #include <linux/module.h> #include "./sgl.h"
Copyright (C) 2019 HUAWEI, Inc. * http://www.huawei.com/ * Created by Gao Xiang <gaoxiang25@huawei.com> */ #ifndef
__EROFS_FS_COMPRESS_H #define __EROFS_FS_COMPRESS_H
Copyright (C) 2019 HUAWEI, Inc. * http://www.huawei.com/ * Created by Gao Xiang <gaoxiang25@huawei.com> */ #include
"compress.h" #include <linux/module.h> #include <linux/lz4.h>
Copyright (C) 2019 IBM Corp.
Copyright (C) 2019 IBM Corp. #include <linux/bitops.h> #include <linux/init.h> #include <linux/io.h> #include <linux/kernel.h> #include
<linux/mfd/syscon.h> #include <linux/mutex.h> #include <linux/of
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Copyright (c) 2019, Intel Corporation. * Author: Andy Shevchenko <andriy.shevchenko@linux.intel.com> */
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Copyright (C) 2019 Intel Corporation * * Author: Lu Baolu <baolu.lu@linux.intel.com>
Copyright (C) 2019 Intel Corporation * Author: Luis Carlos Cobo <luisca@cozybit.com> */
Copyright (C) 2019 Intel Corporation * Author: Luis Carlos Cobo <luisca@cozybit.com> */ #include <linux/gfp.h> #include <linux/kernel.h> #include
<linux/random.h> #include <linux/rculist.h>
Copyright (C) 2019, Intel Corporation * Author: Mika Westerberg <mika.westerberg@linux.intel.com> */
Copyright (C) 2019 Intel Corporation. * * Authors:
Copyright (C) 2019 Intel Corporation. * * Author: Zhang Rui <rui.zhang@intel.com>
Copyright (C) 2019 Intel Corporation * * Contact Information:
Copyright (c) 2019, Intel Corporation. */ #define pr_fmt(fmt) KBUILD_MODNAME ": " fmt
Copyright (c) 2019, Intel Corporation. * * Heterogeneous Memory Attributes Table (HMAT) representation
Copyright (C) 2019 Intel Corporation */ #include <linux/export.h> #include <net/mac80211.h> #include "ieee80211_i.h" #include "driver-ops.h"
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granted, provided that the above * copyrig
Copyright (C) 2019 Intel Corporation * * Portions of this file are derived from the ipw3945 project, as well * as portions of the ieee80211 subsystem
header files.
Copyright(c) 2019 Intel Corporation * * This module allows virtio devices to be used over a vhost-user socket.
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Copyright (c) 2019 Jeff Layton * * I/O and data path helper functionality.
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 Copyright (c) 2019 Linaro Ltd. \* Author: Manivannan Sadhasivam <manivannan.sadhasivam@linaro.org> \*/  
 Copyright (c) 2019, Linaro Ltd \*/#include <dt-bindings/power/qcom-aoss-qmp.h>#include <linux/clk-provider.h>#include <linux/interrupt.h>#include  
 <linux/io.h>#include <linux/mailbox\_client.h>#in  
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 Copyright (C) 2019 Martin Blumenstingl <martin.blumenstingl@googlemail.com> \* \* Based on the BSP (called "UGW") driver:  
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 Copyright (c) 2019, Martin Kelly. \* \* IIO core driver for BMI160, with support for I2C/SPI busses  
 Copyright (c) 2019 Max Staudt <max@enpas.org> \* \* This started as a fork of i2c-elektor.c and has evolved since. \* Thanks go to its authors for  
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Copyright (c) 2019 MediaTek Inc. * Author: Yong Liang <yong.liang@mediatek.com> */
Copyright (C) 2019 MediaTek Inc. */#ifndef __PINCTRL_MTK_MT8516_H#define __PINCTRL_MTK_MT8516_H
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<linux/bcd.h>#include <linux/rtc.h>#include <linux/module.h>#inc
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Copyright (C) 2019 Paul Cercueil <paul@crapouillou.net> * * Based on jz4780_bch.c
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Copyright (c) 2019 Paulo Alcantara <palcantara@suse.de> */#include <linux/init.h>#include <linux/fs.h>#include <linux/types.h>#include
<linux/ctype.h>#include <linux/string.h>#include <linux/ro
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structs

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Copyright (C) 2019, Red Hat Inc, Arnaldo Carvalho de Melo <acme@redhat.com>#define __GNU_SOURCE#include <unistd.h>
Copyright (C) 2019, Red Hat Inc, Arnaldo Carvalho de Melo <acme@redhat.com>#ifndef __PERF_EVSWITCH_H#define __PERF_EVSWITCH_H 1
Copyright (C) 2019 Red Hat, Inc. * * Author(s): Pankaj Gupta <pagupta@redhat.com>
Copyright (C) 2019 Red Hat, Inc. */#include "kvm_util.h"
Copyright (C) 2019, Red Hat, Inc. * * Test expected behavior of the KVM_CAP_SYNC_REGS functionality.
Copyright (C) 2019, Red Hat, Inc. * * Verify that nothing bad happens if a KVM user exits with open * file descriptors while executing a nested guest.
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RENESAS_USB_DRIVER_H#define RENESAS_USB_DRIVER_H
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RENESAS_USB_MOD_H#define RENESAS_USB_MOD_H
Copyright (C) 2019 Renesas Electronics Corporation * Kuninori Morimoto <kuninori.morimoto.gx@renesas.com> */#include <linux/clk.h>#include
<linux/err.h>#include <linux/gpio.h>#include <linux/io.
Copyright (C) 2019 Renesas Electronics Corporation * Kuninori Morimoto <kuninori.morimoto.gx@renesas.com> */#include <linux/delay.h>#include
<linux/dma-mapping.h>#include <linux/io.h>#include <l
Copyright (C) 2019 Renesas Electronics Corporation * Kuninori Morimoto <kuninori.morimoto.gx@renesas.com> */#include <linux/delay.h>#include
<linux/io.h>#include <linux/scatterlist.h>#include "c
Copyright (C) 2019 Renesas Electronics Corporation * Kuninori Morimoto <kuninori.morimoto.gx@renesas.com> */#include <linux/interrupt.h>
Copyright (c) 2019 Robert Eshleman <bobbyeshleman@gmail.com> * * Datasheet: https://datasheets.maximintegrated.com/en/ds/MAX44009.pdf
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driver does not currently support them.
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Copyright (C) 2019 STMicroelectronics * Author(s): Amelie Delaunay <amelie.delaunay@st.com>. */#include <linux/bitfield.h>#include
<linux/i2c.h>#include <linux/interrupt.h>#include <linux/irq.h>
Copyright (C) 2019 STMicroelectronics * Author(s): Amelie Delaunay <amelie.delaunay@st.com>. */#include <linux/gpio/driver.h>#include
<linux/interrupt.h>#include <linux/mfd/stmfx.h>#include <lin
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<linux/dsa/8021q.h>#include <linux/packing.h>#include "dsa\_priv.h"  
Copyright (c) 2019, Vladimir Oltean <olteanv@gmail.com> \*/#include "sja1105.h"  
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taggers that rely on 802.1Q VLAN tags to use. The  
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<linux/i2c.h>#include <linux/interrupt.h>#include <lin  
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lib.h>#include <linux/idr.h>#include <net/tcp.h>#include <s  
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<linux/moduleparam.h>#include <linux/io.h>#include <s
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<linux/interrupt.h>#include <linux/sched_clock.h>#i

```

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 <linux/io.h>#include <linux/irqchip.h>#include <linux/i  
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 <linux/clocksource.h>#include <linux/sched\_clock.h>#in  
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 <linux/types.h>#include <linux/socket.h>#include <  
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 <linux/socket.h>#include <linux/in.h>#include <linux/kernel.h>#includ  
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 <linux/fttrace.h>  
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 <linux/socket.h>#include <linux/in.h>#include <linux/kernel.h>#include <  
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 <linux/socket.h>#include <linux/slab.h>#include <linux/in.h>#include <li  
 Copyright (C) Jonathan Naylor G4KLX (g4klx@g4klx.demon.co.uk) \*/#include <linux/module.h>#include <linux/proc\_fs.h>#include  
 <linux/kernel.h>#include <linux/interrupt.h>#include <linux/fs.h>#inc  
 Copyright (C) Jonathan Naylor G4KLX (g4klx@g4klx.demon.co.uk) \*/#include <linux/types.h>#include <linux/slab.h>#include <linux/socket.h>#include  
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unsigned int period_ns; unsigned char reg_value;
Copyright: Copyright (c) 1996 John Shifflett, GeoLog Consulting */static inline int round_period(unsigned int period)
Copyright: Copyright (c) 1996 John Shifflett, GeoLog Consulting */static unsigned char __maybe_unused calc_sync_xfer(unsigned int period,
unsigned int offset)
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<linux/i2c.h>#include <linux/interrupt.h>#include <linux/ke
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Copyright (C) Paul Mackerras 1997. */#include <stdarg.h>#include <stddef.h>#include "types.h"#include "elf.h"#include "string.h"#include
"stdio.h"#include "page.h"#include "ops.h"
Copyright (C) Paul Mackerras 1997. */#include <stdarg.h>#include <stddef.h>#include "types.h"#include "elf.h"#include "string.h"#include
"stdio.h"#include "page.h"#include "ops.h"#include "i
Copyright (C) Paul Mackerras 1997. */#include <stddef.h>#include "types.h"#include "elf.h"#include "string.h"#include "stdio.h"#include
"page.h"#include "ops.h"
Copyright (C) Paul Mackerras 1997. */#include <stdio.h>#include <stdlib.h>#include <unistd.h>#include <fcntl.h>#include <string.h>#include
"rs6000.h"
Copyright (C) Paul Mackerras 1997. * * Matt Porter <mporter@kernel.crashing.org>
Copyright (C) Paul Mackerras 1997. * * NOTE: this code runs in 32 bit mode and is packaged as ELF32.
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Copyright (c) Paul Mundt 2001 */#include <linux/init.h>#include <linux/slab.h>#include <linux/sched.h>#include <linux/delay.h>#include
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 1#define VDMA\_END 2  
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__PINCTRL_STM32_H
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<linux/of.h>#include <linux/platform_device.h>
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<linux/smp.h>#include <linux/interrupt.h>#include <linux/kernel_st
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<linux/in.h>#include <linux/kernel.h>#include <linux/tim
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<linux/capability.h>#include <linux/errno.h>#include <linux/types.h>#inclu
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copyright + flash_ver images, 0 for failure.static u32 chk_cfg(u8 *cfg, u32 length, u32 *flash_ver){ u16 *pw = (u16 *)cfg - 1; u32 sz = 0; u32
len = length;
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copyright follows: * -----
copyright follows: *
Copyright follows: *
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copyright for the ov511 driver is: *
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putc(*c, stderr); putc('\\n', stderr); }
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<linux/export.h>#include <asm/qdio.h>  
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Copyright IBM Corp. 2008 \* Author(s): Martin Schwidefsky (schwidefsky@de.ibm.com) \*/#include <asm/vdso.h>#include <asm/asm-  
offsets.h>#include <asm/unistd.h>#include <asm/dwarf.h>  
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offsets.h>#include <asm/unistd.h>#include <asm/dwarf.h>#include <asm/  
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<asm/cacheflush.h>#include <asm/facility.h>#include <asm/pg  
Copyright IBM Corp. 2011 \* Author(s): Jan Glauber <jang@linux.vnet.ibm.com> \*/#include <linux/uaccess.h>#include  
<linux/stop\_machine.h>#include <linux/jump\_label.h>#include <asm/ipl.h>  
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pr\_fmt(fmt) KMSG\_COMPONENT ": " fmt  
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pr_fmt(fmt) KMSG_COMPONENT ": " fmt
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<linux/init.h>#include <linux/mm.h>#include <li
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<linux/cpu.h>#include <linux/sched.h>#include <asm/fpu/types.h>#include
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Copyright IBM Corp. 2016 * Author(s): Harald Freudenberger <freude@de.ibm.com> * * Currently there are two tracepoint events defined here. * An
s390_zcrypt_req request event occurs as soon as the
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Copyright IBM Corp. 2016 * Author(s): Holger Dengler (hd@linux.vnet.ibm.com) * Harald Freudenberger <freude@de.ibm.com> */#ifndef

```

```

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Copyright IBM Corp. 2016 * Author(s): Janosch Frank <frankja@linux.vnet.ibm.com> */#include <linux/errno.h>#include <linux/pagemap.h>#include
<linux/vmalloc.h>#include <linux/syscalls.h>#includ
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<asm/sclp.h>#include "boot.h"
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_ASM_S390_CPU_MCF_H
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KMSG_COMPONENT "cpum_cf_diag"#define pr_fmt(fmt) KMSG_C
Copyright IBM Corp. 2019 Author(s): Hendrik Brueckner <brueckner@linux.ibm.com> * Thomas Richter <tmricht@linux.ibm.com> */#ifndef

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<linux/init.h>#include <linux/module.h>#include <linux/cpufeature.h>#
    Copyright IBM Corp. 2019 * Author(s): Joerg Schmidbauer (jschmidb@de.ibm.com) */#include <crypto/internal/hash.h>#include <linux/init.h>#include
<linux/module.h>#include <linux/cpufeature.h>#in
    Copyright IBM Corp. 2019 */#include <asm/mem_detect.h>#include <asm/pgtable.h>#include <asm/cpacf.h>#include <asm/timex.h>#include
<asm/scpl.h>#include "compressed/decompressor.h"#include "boo
    Copyright IBM Corp 2019/* * The DPS310 is a barometric pressure and temperature sensor. * Currently only reading a single temperature is supported
by * this driver.
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    Copyright IBM Corporation, 2007 *      Author: Mike Anderson <andmike@linux.vnet.ibm.com> */#include <linux/list.h>#include
<linux/slab.h>#include <linux/kobject.h>#include <linux/dm-ioct.h>#inclu
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    Copyright IBM Corporation, 2007 *      Author: Srivatsa Vaddagiri <vatsa@linux.vnet.ibm.com> * * Scaled math optimizations by Thomas Gleixner
    Copyright IBM Corporation, 2007 * * Based on Bamboo code by Josh Boyer <jwboyer@linux.vnet.ibm.com>
    Copyright IBM Corporation 2007 */#ifndef JITDUMP_H#define JITDUMP_H
    Copyright IBM Corporation 2007 */#include <sys/types.h>#include <sys/stat.h> /* for mkdir() */#include <stdio.h>#include <errno.h>#include
<string.h>#include <stdlib.h>#include <stdint.h>#inc
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KVM functions specific to running nested KVM-HV g
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IEC958_AES2_CON_CHANNEL_UNSPEC;   cs[3] = IEC958_AES3_CON_CLOCK_1000PPM | IEC958_AES
COPYRIGHT |          IEC958_AES0_CON_EMPHASIS;   ucontrol->value.iec958.status[1] =
IEC958_AES1_CON_ORIGINAL |          IEC958_AES1_CON_CATEGORY;   ucontrol->value.iec958.status[3] = IEC958_AES3_CON
COPYRIGHT |          IEC958_AES0_CON_EMPHASIS;   ucontrol->value.iec958.status[1] = IEC958_AES1_CON_ORIGINAL |
IEC958_AES1_CON_CATEGORY;   ucontrol->value.iec958.status[3] = IEC958_AES3_CON
COPYRIGHT | IEC958_AES0_CON_EMPHASIS;   value->value.iec958.status[1] =          IEC958_AES1_CON_CATEGORY |
IEC958_AES1_CON_ORIGINAL;   return 0;}
COPYRIGHT (IEC958_AES1_CON_ADC_COPYRIGHT_ID#define IEC958_AES1_CON_ADC_COPYRIGHT_OTHER
(IEC958_AES1_CON_ADC_COPYRIGHT_ID|0x60)#define IEC958_AES1_CON_SOLIDMEM_MASK0x0f#define IEC958_AES1_CON_SOLID
COPYRIGHT) \          | ((IEC958_AES1_CON_MIXER \          | IEC958_AES1_CON_ORIGINAL) << 8) \          | (0x10 <<
16) \          | ((IEC958_AES3_CON_FS_48000) << 24))
copyright IN
copyright ;-) */#include <linux/etherdevice.h>
copyright info and historical * changelogs. */#include <linux/capability.h>#include <linux/clocksource.h>#include <linux/workqueue.h>#include
<linux/hrtimer.h>#include <linux/jiffies.h>#include
copyright information. * -----
copyright information. * -----
copyright information. */
copyright information
copyright information */
Copyright Information:
copyright information. * 03-16-15 02.00.10 Updated for MPI v2.6. *          Added MPI2_SATA_PT_REQ_PT_FLAGS_FPDMA. * -----
-----
copyright information. * 03-16-15 02.00.17 Updated for MPI v2.6. *          Added MPI26_SCSIIO_IOFLAGS_ESCAPE_PASSTHROUGH. *
Added MPI2_SEP_REQ_SLOTSTATUS_DEV_OFF a
copyright information. * 03-16-15 02.00.26 Updated for MPI v2.6. *          Added MPI2_EVENT_ACTIVE_CABLE_EXCEPTION and *
MPI26_EVENT_DATA_ACTIVE_CABLE_EXCEPT. *          Added
copyright information. * 08-25-16 02.00.14 Added new values for the Flags field of Toolbox Clean *          Tool Request Message. * 07-22-18
02.00.15 Added defines for new TOOLBOX_PC
copyright information. *          Added MPI2_BIOSPAGE1_OPTIONS_ADVANCED_CONFIG. *          Added AdapterOrderAux fields to BIOS
Page 3. * 03-16-15 02.00.31 Updated for MPI v
copyright information. * * a header file containing define's for the USB device supported by the * various drivers.
copyright information. * * a header file containing prototypes and types for internal use of the dvb-usb-lib
copyright information and source see the end of this file.static u8 mac_reader[] = { 0x06, 0x00, 0x00, 0xea, 0x04, 0x00, 0x00, 0xea, 0x03, 0x00, 0x00,
0xea, 0x02, 0x00, 0x00, 0xea, 0x01, 0x00, 0x00
copyright* information as follows:
copyright information. *          Bumped MPI2_HEADER_VERSION_UNIT. * 03-16-15 02.00.37 Bumped MPI2_HEADER_VERSION_UNIT. *
Added Scratchpad registers to *
copyright information */#define FC_LOG_PREFIX "flexcop_usb"#include "flexcop-usb.h"#include "flexcop-common.h"
copyright information. Do not change the version information unless an improvement has been made.Merely removing my name, as Compex has done in
the past, does not count as an improvement.
copyright information */#ifndef __FLEXCOP_COMMON_H__#define __FLEXCOP_COMMON_H__
copyright information */#ifndef __FLEXCOP_H__#define __FLEXCOP_H__
copyright information */#ifndef __FLEXCOP_REG_H__#define __FLEXCOP_REG_H__
copyright information */#ifndef __FLEXCOP_USB_H_INCLUDED__#define __FLEXCOP_USB_H_INCLUDED__
copyright information */#include "flexcop.h"
copyright information */#include <media/tuner.h>#include "flexcop.h"#include "mt312.h"#include "stv0299.h"#include "s5h1420.h"#include
"itd1000.h"#include "cx24113.h"#include "cx24123.h"#inc
copyright information of those functions. *

```

```

copyright information. pr_info("NetWinder Floating Point Emulator V0.97 ("
copyright information. * * the headerfile, all dvb-usb-drivers have to include.
copyright information. * * This file contains functions for (de-)initializing an I2C adapter.
copyright information. * * This file contains functions for downloading the firmware to Cypress FX 1 and 2 based devices.
copyright information. * * This file contains functions for downloading the firmware to Cypress FX 1 * and 2 based devices.
copyright information. * * This file contains functions for initializing and handling the * linux-dvb API.
copyright information. * * This file contains functions for initializing the input-device and for handling remote-control-queries.
copyright information. * * This file is automatically generated, do not edit things here. */#ifndef __FLEXCOP_IBI_VALUE_INCLUDED__#define
__FLEXCOP_IBI_VALUE_INCLUDED__
copyright information. * * This file keeps functions for initializing and handling the * BULK and ISOC USB data transfers in a generic way. * Can be used
for DVB-only and also, that's the plan, fo
copyright information. * * This file keeps functions for initializing and handling the * USB and URB stuff.
Copyright: Intel Corp 2005 * Jonathan Cameron <jic23@cam.ac.uk> 2009 */
Copyright Intel Corp. 2018#include <linux/init.h>#include <linux/module.h>#include <linux/moduleparam.h>#include <linux/nd.h>#include
"pmem.h"#include "pfn.h"#include "nd.h"#include "nd-core.h
Copyright: Intel Corp. * * Modified 2009: Jonathan Cameron <jic23@cam.ac.uk>
Copyright Intel Corporation (C) 2014-2016. All Rights Reserved
Copyright Intel Corporation (C) 2017. All Rights Reserved
Copyright Intel Corporation (C) 2017. * * Based on the i2c-axxia.c driver.
Copyright © International Business Machines Corp., 2006
Copyright © International Business Machines Corp., 2006-2008 * * DESCRIPTION * This test excercises the futex syscall op codes needed for
requeuing * priority inheritance aware POSIX co
Copyright © International Business Machines Corp., 2006-2008 * * DESCRIPTION * This test exercises the futex_wait_requeue_pi() signal handling
both * before and after the requeue. The f
Copyright © International Business Machines Corp., 2009
Copyright © International Business Machines Corp., 2009 * * DESCRIPTION * 1. Block a thread using FUTEX_WAIT * 2. Attempt to use
FUTEX_CMP_QUEUE_PI on the futex from 1. * 3. The
Copyright © International Business Machines Corp., 2009 * * DESCRIPTION * Block on a futex and wait for timeout.
Copyright © International Business Machines Corp., 2009 * * DESCRIPTION * GCC atomic builtin wrappers * http://gcc.gnu.org/onlinedocs/gcc-
4.1.0/gcc/Atomic-Builtins.html
Copyright © International Business Machines Corp., 2009 * * DESCRIPTION * Glibc independent futex library for testing kernel functionality.
Copyright © International Business Machines Corp., 2009 * * DESCRIPTION * Test if FUTEX_WAIT op returns -EWOULDBLOCK if the futex value
differs * from the expected one.
copyright I put the copyright notice from the * frle-0.22 device driver. * Some code is based on the nicstar driver by M. Welsh. * *
Author: Rui Prior (rprior@inescn.pt) * P
copyright is: *
Copyright is a null terminated ASCII string containing the JR3 * copyright notice. */
copyright); ixgbev_f_wq = create_singlethread_workqueue(ixgbev_driver_name); if (!ixgbev_f_wq) { pr_err("%s: Failed to create
workqueue\n", ixgbev_driver_name); return -ENOMEM; }
Copyright: J */
Copyright: Jamal Hadi Salim (2002-13) */
copyright Jamal Hadi Salim (2002-4) */
copyright Jamal Hadi Salim (2015)*/
copyright Jamal Hadi Salim (2016)*/
Copyright: jfclere@sinix.net */
copyright Joerg Dorchain (dorchain@mpi-sb.mpg.de) *
Copyright Joern Engel <joern@lazybastard.org># Inspired by Linus Torvalds# Original idea maybe from Keith Owens# s390 port and big speedup
by Arnd Bergmann <arnd@bergmann-dalldorf.de># Mips port b
Copyright Jonathan Naylor G4KLX (g4klx@g4klx.demon.co.uk)
Copyright Jonathan Naylor G4KLX (g4klx@g4klx.demon.co.uk) */#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include
<linux/in.h>#include <linux/kernel.h>#include <linux
Copyright Jonathan Naylor G4KLX (g4klx@g4klx.demon.co.uk) */#include <linux/module.h>#include <linux/proc_fs.h>#include
<linux/kernel.h>#include <linux/interrupt.h>#include <linux/fs.h>#include
Copyright Keith Owens <kaos@ocs.com.au>. GPL.
Copyright KOSAKI Motohiro <kosaki.motohiro@jp.fujitsu.com> * * DESCRIPTION * Internally, Futex has two handling mode, anon and file. The
private file * mapping is special. At first it be
Copyright KOSAKI Motohiro <kosaki.motohiro@jp.fujitsu.com> * * DESCRIPTION * Wait on uninitialized heap. It shold be zero and FUTEX_WAIT
should * return immediately. This test is intent
copyright label on that
Copyright Larry Finger <Larry.Finger@lwfinger.net> * * A huge thanks goes to Sara V. Nari who forgives me when I'm * sitting in front of my laptop at
evening, week-end, night...

```

```

Copyright LG Electronics Inc., 2014 * Written by:
Copyright Linas Vepstas 2005, 2006
Copyright Linas Vepstas 2005, 2006 */
Copyright Linas Vepstas <linas@austin.ibm.com> 2004 */
Copyright Linas Vepstas <linas@austin.ibm.com> 2007 * * All rights reserved
Copyright Linas Vepstas <linas@linas.org> 2004, 2005 * * All rights reserved
Copyright line includes x86 and tile ones. */
Copyright Linus Torvalds and others. *
copyright Linus Torvalds and others. * Heavily rewritten. */
Copyright Management Information (UDF 2.50 3.3.4.5.1.2)struct DVDCopyrightImpUse { __le16      headerChecksum;      uint8_t
CGMSInfo; uint8_t      dataType; uint8_t      protectionSystemInfo[4];} __packed;
Copyright: Marvell International Ltd. */
Copyright: Marvell Semiconductors Inc. */
Copyright Matthew Wilcox for Intel Corp, 2010
Copyright Matthew Wilcox for Intel Corp, 2010 * * This file contains definitions taken from the * USB Mass Storage Class Specification Overview
Copyright Matt Mackall <mpm@selenic.com>, 2003, 2004, 2005 *
copyright MaxLinear
Copyright message. * 0.0.17 * Add iec958 file in proc file system to show status of SPDIF in. * 0.0.18 * Implement support for Line-in capture on
SB Live 24bit. * * This code was initia
Copyright message. * 0.0.17 * Implement Mic and Line in Capture. * 0.0.18 * Add support for mute control on SB Live 24bit (cards w/ SPI DAC) *
* This code was initially based on code fr
Copyright message. * 0.0.19 * Added I2C and SPI registers. Filled in interrupt enable. * 0.0.20 * Added GPIO info for SB Live 24bit. * 0.0.21 *
Implement support for Line-in capture o
Copyright message. * 0.0.19 * Finally fix support for SB Live 24 bit. SB0410 and SB0413. * The output codec needs resetting, otherwise all output is
muted. * 0.0.20 * Merge "pci_disabl
copyright message * below) provided to Red Hat by VIA Networking Technologies, Inc. for * addition to the Linux kernel. * * The code has been merged
into one source file, cleaned up to follow * L
copyright message in any source redistribution in whole or part. *
copyright messages: *
Copyright Micro Memory Inc 2001-2002. All rights reserved
Copyright Micro Memory INC 2001. All rights reserved
copyright) * Modifications: 2006 (c) MontaVista Software, Inc. * * Modified by: Mark A. Greer <mgreer@mvista.com>
COPYRIGHT);MODULE_LICENSE("GPL");
COPYRIGHT);MODULE_LICENSE("GPL");MODULE_ALIAS_MISCDEV(TUN_MINOR);
COPYRIGHT);MODULE_LICENSE("GPL");MODULE_FIRMWARE("ipw2200-ibss.fw");#ifdef CONFIG_IPW2200_MONITORMODULE_FIRMWARE("ipw2200-
sniffer.fw");#endifMODULE_FIRMWARE("ipw2200-bss.fw");
COPYRIGHT);MODULE_LICENSE("GPL
v2");MODULE_VERSION(DRV_VERSION);MODULE_FIRMWARE(FIRMWARE_D101M);MODULE_FIRMWARE(FIRMWARE_D101S);MODULE_FIRMWARE(FIRMWAR
E_D102E);
Copyright MontaVista Software Inc
Copyright: MontaVista Software Inc. *
Copyright: Monta Vista Software, Inc. */
Copyright: Monta Vista Software, Inc. */
Copyright: MontaVista Software Inc. */
Copyright: MontaVista Software Inc. *
Copyright: MontaVista Software, Inc. */
Copyright: MontaVista Software Inc. * * Code common to all PXA machines.
Copyright: MontaVista Software Inc. * * Code specific to PXA21x/25x/26x variants.
Copyright: MontaVista Software Inc. * * Code specific to PXA27x aka Bulverde.
Copyright: MontaVista Software, Inc. */#ifndef __ASM_ARCH_DMA_H#define __ASM_ARCH_DMA_H
Copyright: MontaVista Software Inc. */#ifndef __ASM_MACH_IRQS_H#define __ASM_MACH_IRQS_H
Copyright: MontaVista Software Inc. */#include <linux/bitops.h>#include <linux/init.h>#include <linux/module.h>#include <linux/interrupt.h>#include
<linux/syscore_ops.h>#include <linux/io.h>#1
Copyright: MontaVista Software Inc. */#include <linux/clkdev.h>#include <linux/gpio.h>#include <linux/gpio/gpio-reg.h>#include
<linux/gpio/machine.h>#include <linux/module.h>#include <linux/ker
Copyright: MontaVista Software Inc. */#include <linux/gpio/consumer.h>#include <linux/module.h>#include <linux/init.h>#include
<linux/interrupt.h>#include <linux/kernel.h>#include <linux/errno.
Copyright: MontaVista Software Inc. */#include <linux/gpio.h>#include <linux/gpio/gpio-reg.h>#include <linux/gpio/machine.h>#include
<linux/init.h>#include <linux/platform_device.h>#include <li
Copyright: MontaVista Software Inc. */#include <linux/gpio.h>#include <linux/init.h>#include <linux/platform_device.h>#include
<linux/syscore_ops.h>#include <linux/interrupt.h>#include <linux/s

```



```

Copyright: MontaVista Software Inc. */#include <linux/module.h>#include <linux/clock.h>#include <linux/err.h>#include <linux/gpio/driver.h>#include
<linux/gpio-pxa.h>#include <linux/init.h>#incl
Copyright: MontaVista Software Inc. * * Originally based upon linux/drivers/pcmcia/sa1100_neponset.c
Copyright: MontaVista Software, Inc. * * Splitting done by: Marek Vasut <marek.vasut@gmail.com> * If something doesn't work and it worked before
splitting, e-mail me, * dont bother Nicolas please ;-
Copyright Motorola 1984 (
copyright;      } mpeg_format_info; } format_info;
COPYRIGHT);      new      new |= ((ucontrol->value.iec958.status[3] & IEC958_AES3_CON_FS) << 24);      if ((new &
IEC958_AES0_CON_EMPHASIS) == IEC958_AES0_CON_EMPHASIS_5015)      val |= 1<<3;
copyright notes. */
copyright notes. */#include <api/fs/fs.h>#include <errno.h>#include <inttypes.h>#include <poll.h>#include "cpumap.h"#include
"util/mmap.h"#include "thread_map.h"#include "target.h"#include "
copyright not found\n");#endif /* Validate checksum */      for (i = s = 0; i < EEPROM_CHECKSUM; i++)      s += e[i]; s &= 0xFF;if (s !=
e[EEPROM_CHECKSUM]) {      printk(KERN_ERR DEV_LABEL "(itf %d): EEPR
Copyright Novell Inc 2010 * * Authors: Alexander Graf <agraf@suse.de>
Copyright Novell Inc. 2010 * * Authors: Alexander Graf <agraf@suse.de>
Copyright NXP *
Copyright NXP 2016 *
copyright of original i386 version is: *
copyright of skeleton.c was as follows: * * skeleton.c Written 1993 by Donald Becker.
copyright * of their respective owners. */
copyright of the      respective owners.*/#include <linux/kernel.h>#include <linux/init.h>#include <linux/module.h>#include <linux/string.h>#include
<linux/slab.h>#include <linux/delay.h>#inc
copyright of who own it
Copyright: Phytel Messtechnik GmbH * e-Mail:      armlinux@phytel.de * * based on Intel Mainstone Board
Copyright Pierre Ossman\n");
copyright preserved *      below. *      Steve Whitehouse: Some bug fixes, cleaning up some code to make it *
compatible with my routing layer. *
copyright ;-) * * PS wx handler mostly stolen from hostap, copyright who
Copyright RDA Microelectronics Company Limited
Copyright Realtek corp. * (available in drivers/staging/rtl8187se directory of Linux 3.14) * - other GPL, unpublished (until now), Linux driver code,
Copyright Red Hat 2003
Copyright Red Hat, 2009
Copyright Red Hat Inc 2010.
Copyright Red Hat Inc 2010. * Author : Dave Airlie <airlied@redhat.com> */
Copyright Red Hat, Inc. 2011 * * Authors:
Copyright Red Hat, Inc. 2013-2014 * * Authors:
Copyright Red Hat, Inc. 2014 * * Authors:
Copyright Red Hat, Inc. 2019 * * Author(s): Cornelia Huck <cohuck@redhat.com>
Copyright Red Hat, Inc. 2019 * * Author(s): Dong Jia Shi <bjsdjsi@linux.vnet.ibm.com> *      Cornelia Huck <cohuck@redhat.com>
Copyright Red Hat, Inc. 2019 * * Author(s): Dong Jia Shi <bjsdjsi@linux.vnet.ibm.com> *      Xiao Feng Ren <renxiaof@linux.vnet.ibm.com> *
Cornelia Huck <cohuck@redhat.com>
Copyright RedHat, Inc. * * libata documentation is available via 'make {ps|pdf}docs', * as Documentation/driver-api/libata.rst
Copyright RedHat, Inc. * * This driver probably works with non-Apple versions of the * Broadcom chipset...
COPYRIGHT;      regmap_write(ak4104->regmap, AK4104_REG_CHN_STATUS(0), val);
copyright      ReturnCode - One of the ERR_CAPTURE_...
COPYRIGHT:      return dvd_read_copyright(cdi, s, cgc);
COPYRIGHT);      }      return pci_register_driver()
copyright);return pci_register_driver()module_init(atl2_init_module);
COPYRIGHT:      return "SET_COPYRIGHT"; case CX2341X_ENC_SET_EVENT_NOTIFICATION:
COPYRIGHT:      return "SET_COPYRIGHT"; case CX2341X_ENC_SET_EVENT_NOTIFICATION:
Copyright Roman Zippel, 1997. All rights reserved
Copyright Rusty Russell IBM Corporation 2007.#ifndef __KERNEL__#include <stdint.h>#endif#include <linux/types.h>#include <linux/virtio_types.h>
Copyright Samuel Mendoza-Jonas, IBM Corporation 2018.
Copyright Samuel Mendoza-Jonas, IBM Corporation 2018. */
Copyright Sarah Sharp for Intel Corp, 2010 */
COPYRIGHT))      sbits      if (val & AC_DIG1_LEVEL)      sbits |= (IEC958_AES1_CON_ORIGINAL << 8);      sbits |= val &
(0x7f << 8);      }      return sbits;
copyright Sebastian Carlier
Copyright © Sebastian Cla *      Jan Engelhardt, 2007-2010 * *      based on ipt_ROUTE.c from Cédric de Launois *      <delaunois@info.ucl.be>
copyright; see LICENSE.FlashPoint for details.

```

```

copyright: %s\n", copy);}
COPYRIGHT); snd_emu10k1x_ptr_write(chip, SPCS1, 0, chip->spdif_bits[1] = SPCS_CLKACCY_1000PPM |
SPCS_SAMPLERATE_48 | SPCS_CHANNELNUM_LEFT | SPCS_SOURCENUM_UNSPEC |
COPYRIGHT); snd_emu10k1x_ptr_write(chip, SPCS2, 0, chip->spdif_bits[2] = SPCS_CLKACCY_1000PPM |
SPCS_SAMPLERATE_48 | SPCS_CHANNELNUM_LEFT | SPCS_SOURCENUM_UNSPEC |
COPYRIGHT) snd_iprintf(buffer, " Non-Copyright"); if (digi1 & AC_DIG1_NONAUDIO) snd_iprintf(buffer, " Non-Audio"); if (digi1 &
AC_DIG1_PROFESSIONAL) snd_iprintf(buffer, " Pro"); if (digi1 & A
COPYRIGHT) { snd_iprintf(buffer, "permitted\n"); } else { snd_iprintf(buffer, "protected\n"); }
snd_iprintf(buffer, "Emphasis: "); if ((status[0] & IEC958_AES0_CON_EMPHASIS) != IEC958_AE
Copyright : %s\n", (status : %s\n", emphasis[(status & SPCS_EMPHASISMASK) >> 3]);
snd_iprintf(buffer, "Mode : %i\n", (status
COPYRIGHT | SPCS_NOTAUDIODATA; snd_emu10k1_ptr_write(emu, SPCS0 + i, 0, bits); }
Copyright (ST) 2011 Vipin Kumar (vipin.kumar@st.com) */
Copyright (ST) 2012 Rajeev Kumar (rajeevkumar.linux@gmail.com)
Copyright (ST) 2012 Rajeev Kumar (rajeevkumar.linux@gmail.com) */
Copyright (ST) 2012 Rajeev Kumar (rajeevkumar.linux@gmail.com)*/
Copyright (ST) 2012 Vipin Kumar (vipin.kumar@st.com) */
copyright) */static inline int in_8(const volatile unsigned char{int ret;
Copyright status#define AC97_ALC650_PRE 0x0038 /* Preemphasis status */#define AC97_ALC650_PRE_SHIFT 3#define
AC97_ALC650_MODE 0x00C0 /* Preemphasis status */#define
Copyright status#define AC97_SC_PRE 0x0008 /* Preemphasis status */#define AC97_SC_CC_MASK 0x07f0 /* Category Code mask
*/#define AC97_SC_CC_SHIFT 4#define AC97_SC_L 0x0800 /* Generation Level
copyright status * of the DEC code. */
Copyright Stephan Mueller <smueller@chronox.de>, 2014 * * Redistribution and use in source and binary forms, with or without * modification, are
permitted provided that the following conditions *
Copyright Stephan Mueller <smueller@chronox.de>, 2015 - 2019 * * Design
Copyright Stephan Mueller <smueller@chronox.de>, 2015 * * Redistribution and use in source and binary forms, with or without * modification, are
permitted provided that the following conditions *
Copyright ST-Ericsson 2012. * * Author: Arun Murthy <arun.murthy@stericsson.com>
Copyright STMicroelectronics, 2007. */
copyright string __le32 int_fw_offset; /* internal firmware image offset */ __le32 int_fw_len; /* internal firmware image length */ __le32
ext_fw_offset; /* external firmware image offset */ __le32
copyright(struct cdrom_device_info struct packet_command *cgc){ int ret; u_char buf[8]; const struct cdrom_device_ops
*cdo = cdi->ops;
copyright(struct comedi_device{ struct jr3_block __iomem *block = dev->mmio; struct jr3_sensor __iomem *sensor0 = &block[0].sensor;
Copyright; struct regid appIdent; struct timestamprecordingDateAndTime; struct regid impIdent; uint8_t
impUse[64]; __le32 predecessorVolDescSeqLocation; __le16 flags; uint8_t reserve
Copyright SUSE Linux Products GmbH 2009 * * Authors: Alexander Graf <agraf@suse.de>
Copyright SUSE Linux Products GmbH 2010
Copyright SUSE Linux Products GmbH 2010 * * Authors: Alexander Graf <agraf@suse.de>
COPYRIGHT; switch (bits case 0x10: diga->status[0] |= IEC958_AES0_CON_EMPHASIS_NONE; break; case 0x00: diga-
>status[0] |= IEC958_AES0_CON_EMPHASIS_5015; break; } if (!(bits & 0x80)) diga-
Copyright SysKonnect 1998,1999. * * The information in this file is provided "AS IS" without warranty.
copyright text*/
copyright text 2001jul05 change message format to improve OVERRUN case
copyright text 2001jul05 change message format to improve OVERRUN case
copyright text 2001jul05 change message format to improve OVERRUN case*/
copyright = 'The kernel development communityauthor = 'The kernel development community'
Copyright Theodore Ts */
Copyright Theodore Ts * rights reserved. * * Redistribution and use in source and binary forms, with or without * modification, are permitted provided
that the following conditions * are met: *
COPYRIGHT * This file is distributed under the terms of the GNU General Public
COPYRIGHT * This file is distributed under the terms of the GNU General Public
COPYRIGHT * This file is distributed under the terms of the GNU General Public
Copyright Thomas Gleixner <tglx@linutronix.de>
Copyright: Toby Churchill Ltd * Cribbed from mainstone.c, by Nicholas Pitre */
Copyright: Toby Churchill Ltd * Derived from mainstone.c, by Nico Pitre */
Copyright: Toby Churchill Ltd * Derived from pxa2xx_mainstone.c, by Nico Pitre * * Various modification by Marek Vasut <marek.vasut@gmail.com>
Copyright Tomi Manninen OH2BNS (oh2bns@sral.fi) */#include <linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include
<linux/in.h>#include <linux/kernel.h>#include <linux/timer.h>
Copyright Tomi Manninen OH2BNS (oh2bns@sral.fi) */#include <linux/types.h>#include <linux/slab.h>#include <linux/socket.h>#include

```

```

<linux/timer.h>#include <net/ax25.h>#include <linux/skbuff.h>
Copyright Torsten Duwe <duwe@informatik.uni-erlangen.de> 1993
copyright = True
copyright {__u8 type;
COPYRIGHT; ucontrol->value.iec958.status IEC958_AES1_CON_ORIGINAL; return 0;}
COPYRIGHT; ucontrol->value.iec958.status IEC958_AES1_CON_ORIGINAL; ucontrol->value.iec958.status[3] =
IEC958_AES3_CON_FS; return 0;}
Copyright unasserted) * AN = 0 (Audio data) * P = 0 (Consumer) */ def_bits = SPCS_CLKACCY_1000PPM |
SPCS_SAMPLERATE_48 | SPCS_CHANNELNUM_LEFT | SPCS
Copyright unasserted) * AN = 0 (Audio data) * P = 0 (Consumer) */ emu->spdif_bits[0] = emu->spdif_bits[1] =
emu->spdif_bits[2] = SPCS_CLKACCY_1000PPM
Copyright unasserted) * AN = 0 (Audio data) * P = 0 (Consumer) */ snd_emu10k1x_ptr_write(chip, SPCS0, 0,
chip->spdif_bits[0] = SPCS
Copyright */ val = (cfg->iec60958_cfg->status
COPYRIGHT)) val if (sbits & (IEC958_AES1_CON_ORIGINAL << 8)) val |= AC_DIG1_LEVEL; val |= sbits &
(IEC958_AES1_CON_CATEGORY << 8); } return val;
COPYRIGHT)) val val |= ((new >> 8) & 0xff) << 4;// category + original switch ((new >> 24) & 0xff) { case
IEC958_AES3_CON_FS_44100: val |= 0<<12; break; case IEC958_AES3_CON_FS_48000: val
Copyright: Vovan888 at gmail com
copyright was as follows: * * skeleton.c Written 1993 by Donald Becker.
copyright was changed, and the change * is retroactive to all "true" BSD software (ie everything * from UCB as opposed to other peoples code that just
carried
Copyright who own it
Copyright who own it */#include "ieee80211.h"
Copyright who own it */#include "rtllib.h"
Copyright: Wolfson Microelectronics. PLC. */
copyright * * WPA code stolen from the ipw2200 driver.
Copyright Yaegashi Takeshi, 2001 * Adrian McMenamin, 2008 - 2009 */
(C) ordering is still observed by the pmu driver. */ if (!rb->aux_overwrite) { aux_tail = READ_ONCE(rb->user_page->aux_tail);
handle->wakeup = rb->aux_wakeup + rb->aux_watermark; if (aux_head
(C) or pretend (B,A) that the write * was successfully written on the peer. */ __RQ_NET_OK,
(C) Peter T. Breuer (ptb@ot.uc3m.es) 2003 * * bitmap_create - sets up the bitmap structure * bitmap_destroy - destroys the bitmap structure
(C) programmable clock divider controlled by <Select * (D) constant div-by-3 clock divider * (E) programmable clock divider bypass controlled by
<Switch> * (F) constant div-by-3 clock mux co
(C) protects the consumer from speculatively loading the data before * the producer pointer actually has been read. If we do not have this * barrier,
some architectures could load old data as specul
(C) pxor GHASH_MUL %xmm8, %xmm13, %xmm9, %xmm10, %xmm11, %xmm5, %xmm6 # final GHASH computation movdqa
SHUF_MASK(%rip), %xmm10 PSHUFB_XMM %xmm10, %xmm8
(C) Roman Zippel (assigned DJD, 1999) *
(C) Rusty Russell, IBM 2002 * * Generalized futexes, futex queueing, misc fixes by Ingo Molnar
(C) * 'R * * Write * '' = Other Information * '' = Must not be written (D), value ignored when written (R,A,F) * 'W' = Can be written
(C) SBE Inc., used by permission. */
(C) SGI 2006, Christoph Lameter * Cleaned up and restructured to ease the addition of alternative * implementations of SLAB allocators.
(C) SK" ;#define LINT_USE(x)#else#define LINT_USE(x) (x)=(x)#endif#define MS2BCLK(x) ((x)*12500L)
(C) SK" ;#endif
(C) SK" ;#endif
(C) State timer broadcast controlstatic void lapic_timer_state_broadcast(struct acpi_processor *pr, struct acpi_processor_cx *cx,
int broadcast){ int state = cx - pr->power.st
(C) ST-Ericsson SA 2013 */
(C) ST-Ericsson SA 2013 */#include <drm/drm_simple_kms_helper.h>
(C) ST-Ericsson SA 2013 */#include <linux/clk.h>#include <linux/delay.h>#include <linux/dma-buf.h>
(C) Steve Whitehouse 1999 derived from the * ipv4 netfilter header file which is * (C)1998 Rusty Russell -- This code is GPL. */
(C) * STORE * smp_wmb() (B) smp_mb() (D) * STORE ->data_head STORE ->data_tail * }
(C) * STORE * smp_wmb() (B) smp_mb() (D) * STORE ->data_head STORE ->data_tail * }
(C) * STORE * smp_wmb() (B) smp_mb() (D) * STORE ->producer STORE ->consumer * }
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(C) using AArch64 CRC instructions \*  
(C) using ARM CRC, NEON and Crypto Extensions instructions \*  
(C) UV\_Meta\_Plane \* (D) UV\_UBWC\_Plane \* \* Y\_Meta\_Plane consists of meta information to decode compressed \* tile data in  
Y\_UBWC\_Plane. \* Y\_UBWC\_Plane consists of Y data in compressed macro-ti  
(C) UV\_Meta\_Top\_Field\_Plane \* (D) UV\_UBWC\_Top\_Field\_Plane \* (E) Y\_Meta\_Bottom\_Field\_Plane \* (F) Y\_UBWC\_Bottom\_Field\_Plane  
\* (G) UV\_Meta\_Bottom\_Field\_Plane \* (H) UV\_UBWC\_Bottom\_Field\_Plane  
(C) Waldorf Electronics, Germany \* Written by Andreas Busse \* \* NOTE: most of the structure definitions here are endian dependent. \* If you want to  
use this driver on big endian machines, the data  
(C) Xilinx Inc. \* \* This driver has originally been pushed by Xilinx using a Zynq-branding. This \* still shows in the naming of this file, the kconfig symbols  
and some symbols \* in the code.  
(C) YOKOTA Hiroshi <yokota (at) netlab. is. tsukuba. ac. jp>  
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lldpd 1.0.4 debian:lldpd/1.0.4-1  
(C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test yes != "\$lt\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
belf CFLAGS=\$SAVE\_CFLAGS fi ;\*-\*so  
(C)/!b go :more \./!{ N s|n# | | b more  
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(C) pcOP_LOADNIL/* A B R(A) := ... := R(B) := nil */OP_GETUPVAL/* A B R(A) := UpValue[B] */
lwIP STABLE-1_3_0 gnu:lwip/lwip-contrib:STABLE-1_3_0
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(C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define  
IS\_ABSOLUTE\_PATH(P) (ISLASH ((P)[0]) || HAS\_DEVICE (P))  
(C) ((C) ==# define IS\_ABSOLUTE\_PATH(P) ISLASH ((P)[0])# define IS\_PATH\_WITH\_DIR(P) (strchr (P, '/') != NULL)#endif  
(C) ((C) == DIRECTORY\_SEPARATOR)#endif  
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(C) tests whether C is a directory separator character. IS\_ABSOLUTE\_PATH(P) tests whether P is an absolute path. If it is not,  
it may be concatenated to a direct  
mingw-dbus 1.14.4 fedora:mingw-dbus/1.14.4-1.fc38/noarch  
(C) 2003, 2004, 2005 Thomas Vander Stichele <thomas at apestaart dot org>  
(C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test yes != "\$!t\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
belf CFLAGS=\$SAVE\_CFLAGS fi ;;\*.\*so  
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 for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;  
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//! To compose them, we obviously can't  
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(C) Instrumented cond\_wait. @c mysql\_cond\_wait is a drop-in replacement for @c native\_cond\_wait.\*/#define mysql\_cond\_wait(C, M)  
mysql\_cond\_wait\_with\_src(C, M, \_\_FILE\_\_, \_\_LINE\_\_)  
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copyright string.       uprv\_memset(dest + sizeof(header), 0, headerSize - (int32\_t)sizeof(header));       dest += headerSize;       capacity -=  
headerSize;       } else {  
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 copyright = ORACLE\_WELCOME\_COPYRIGHT\_NOTICE(COPYRIGHT\_NOTICE\_CURRENT\_YEAR); } else {#define FIRST\_YEAR\_CONSTANT "\$first\_year\$"  
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 copyright probably resides with UCB.\*/#ifndef TCPTV\_MIN#define TCPTV\_MIN (1\*PR\_SLOWHZ) /\* minimum allowable value \*/#endif#endif  
 TCPTV\_REXMTMAX#define TCPTV\_REXMTMAX (64\*PR\_SLOWHZ) /  
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Network Grep - ngrep 1.45 fedora:ngrep/1.45-19.git20131221.16ba99a.fc23/armv7hl
(C) AC_TRY_LINK( AC_LANG_POP] if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS="$SAVE_CFLAGS" fi ;;sp
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utils

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NIH Utility Library 1.0.3 unknown:undefined
  ©], [(C) AC_DEFINE([PACKAGE_COPYRIGHT], [NIH_PACKAGE_COPYRIGHT],
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need
for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_PATH_WITH_DIR(P) \ (strchr (P, '/') != NULL ||
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)#endif
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])#endif
(C) ((C) ==# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define FILE_SYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) == DIRECTORY_SEPARATOR)#endif
(C) ((C) ==#endif
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copyright all * present. */ TEST_FEATURE ("with path"); TEST_ALLOC_FAIL { str = output_preamble (NULL, "path.c");
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copying\n"          " * conditions.\n"          " *\n"          "\n");
Copyright (C) 2009 Joe Bloggs."; program_name = "test"; nih_error_init ();
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copyright = copyright;
copyright && *copyright)
copyright;extern const charextern const char *package_string;
COPYRIGHT macro. **/
COPYRIGHT macros. **/#ifndef PACKAGE_COPYRIGHT
copyright may be NULL/empty if (bugreport && *bugreport) package_bugreport = bugreport;
Copyright Message");
copyright message and GPL notice. */ TEST_FUNCTION ("nih_main_version"); nih_main_init_full ("test", "wibble", "1.0", NULL,
copyright message are set * to NULL if empty strings are passed instead. */ TEST_FEATURE ("with empty arguments");
Copyright Message\n"); TEST_FILE_EQ (output, "\n"); TEST_FILE_EQ_N (output, "This is free software;"); TEST_FILE_EQ_N
(output, "warranty; not even for"); TEST_FILE_END (output);
copyright message of this package. AC_SUBST([PACKAGE_COPYRIGHT], ["$1"])
Copyright Message"); TEST_ALLOC_FAIL { argc = 0; argv[argc++] = "ignored"; argv[argc++] = "help"; argv[argc] =
NULL;
copyright message. */ TEST_FEATURE ("with missing arguments");
copyright){nih_assert (argv0 nih_assert (package != NULL); nih_assert (version != NULL);
copyright)} { nih_free (code); return NULL; }
copyright = NULL;
COPYRIGHT NULL#endif /* PACKAGE_COPYRIGHT *//#define nih_main_init(argv0) \do { \ nih_main_init_gettext (); \ nih_main_init_full
(argv0, PACKAGE_NAME, PACKAGE_VERSION, \
copyright = NULL; nih_main_init_full ("argv0", "package", "1.0", "", "");
copyright = NULL; nih_main_init_full ("argv0", "package", "1.0", NULL, NULL);
copyright: package copyright message from configure. * * Should be called at the beginning of main() to initialise the various * global variables exported
from this module. For autoconf-using pack
copyright preamble * and the list of includes. */ source = output_preamble (NULL, source_path); if (! source) {
nih_error_raise_no_memory (); return -1;
copyright preamble, * sentinel and list of includes. */ header = output_preamble (NULL, NULL); if (! header) {
nih_error_raise_no_memory (); return -1;
copyright present. */ TEST_FEATURE ("with no path"); TEST_ALLOC_FAIL { str = output_preamble (NULL, NULL);
copyright) printf ("
copyright); printf ("\n");
copyright: * * The copyright message for the package, taken from the autoconf
COPYRIGHT); \ } while (0);
COPYRIGHT# -----# Wraps the Autoconf AC_COPYRIGHT but also defines PACKAGE_COPYRIGHT,# required for
nih_main_initAC_DEFUN([NIH_COPYRIGHT],[AC_COPYRIGHT([$1])m4_ifndef([NIH_PACKAGE_COPYR
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,

```

it may be concatenated to a direct

```
(C) tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if defined
__WIN32 || defined __WIN32__ || defined __C
nokogiri 1.8.0 ubuntu:ruby-nokogiri/1.8.0-1
(c) 1998-2000, TFI Technology Ltd" name="copyright"/> <META content="TFI Technology" name="author"/> <META content="1 day"
name="revisit-after"/> <META content="ALL" name="Robot"/>
(C) AC_LINK_IFELSE( AC_LANG_POP) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;;*-*so
(C)/!b go :more /\.!{ N s|\n# || b more
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(C) C# elif defined(\_\_arch64\_\_)# define SHA\_LONG64 unsigned long

(C) C# elif defined(\_\_arch64\_\_)typedef long i64;typedef unsigned long u64;

```
(C) C#elif defined(__arch64__)typedef unsigned long u64;
(C) C# else# define SHA_LONG64 unsigned long long
(C) C#elsetypedef long long i64;typedef unsigned long long u64;
(C) C#elsetypedef unsigned long long u64;
(C) C# endif
```

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pangomm 2.42.0 ubuntu:libpangomm-1.4-doc/2.42.0-2/all
copyright-format/1.0/Upstream-Name: pangommUpstream-Contact: Murray Cumming <murrayc@murrayc.com>
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 belf CFLAGS=\$SAVE\_CFLAGS fi ;;\*-\*so  
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pkg-config 0.28 fedora:pkgconfig/1:0.28-9.fc23/armv7hl
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for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
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"mnt" assert truncated_tuple_equal( p
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(C) with self.assertRaises(TypeError): subclass(C, PNonCall) self.assertIsInstance(C(), PNonCall) # check that non-
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 (C) 2002-2005 James Su <suzhe@tsinghua.org.cn>\n\n");  
 (C) AC\_TRY\_LINK( AC\_LANG\_POP]) if test x"\$lt\_cv\_cc\_needs\_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
 belf CFLAGS="\$SAVE\_CFLAGS" fi ;;sp  
 (C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define  
 IS\_ABSOLUTE\_PATH(P) (ISLASH ((P)[0]) || HAS\_DEVICE (P))  
 (C) ((C) ==# define HAS\_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define  
 IS\_PATH\_WITH\_DIR(P) \ (strchr (P, '/') != NULL ||  
 (C) ((C) ==# define IS\_ABSOLUTE\_PATH(P) ISLASH ((P)[0])# define IS\_PATH\_WITH\_DIR(P) (strchr (P, '/') != NULL)#endif  
 (C) ((C) ==# define IS\_ABSOLUTE\_PATH(P) ISLASH ((P)[0])#endif  
 (C) ((C) ==# define IS\_PATH\_WITH\_DIR(P) (strchr (P, '/') != NULL)# define FILE\_SYSTEM\_PREFIX\_LEN(P) 0#endif  
 (C) ((C) == DIRECTORY\_SEPARATOR)#endif  
 (C) ((C) ==#endif  
 copyright = 0x0a9;static const scim\_bridge\_key\_code\_t SCIM\_BRIDGE\_KEY\_CODE\_ordfeminine = 0x0aa;static const scim\_bridge\_key\_code\_t  
 SCIM\_BRIDGE\_KEY\_CODE\_guillemotleft = 0x0ab;static const scim\_bridg  
 copyright = 0xafb;static const scim\_bridge\_key\_code\_t SCIM\_BRIDGE\_KEY\_CODE\_caret = 0xafc;static const scim\_bridge\_key\_code\_t  
 SCIM\_BRIDGE\_KEY\_CODE\_singlelowquotemark = 0xafd;static const scim\_bridg  
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 (C) tests whether C is a directory separator character. IS\_ABSOLUTE\_PATH(P) tests whether P is an absolute path. If it is not,  
 it may be concatenated to a direct  
 (C) tests whether C is a directory separator character. IS\_PATH\_WITH\_DIR(P) tests whether P contains a directory specification. \*/#if defined  
 \_\_WIN32 || defined \_\_WIN32\_\_ || defined \_\_C  
 Secret Rabbit Code 0.1.9 unknown:undefined  
 (C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test yes != "\$!t\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
 belf CFLAGS=\$SAVE\_CFLAGS fi ;;\*-so  
 (C)!b go :more /\.!{ N s|\n# | | b more  
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copyright-format/1.0/Upstream-Name: smartmontoolsUpstream-Contact: Christian Franke <Christian.Franke@t-online.de>Source:
http://smartmontools.sourceforge.net/
copyright messagesstatic int parse_options(int argc, char **argv){ // Init default path names#ifdef _WIN32 configfile =
SMARTMONTTOOLS_SYSCONFDIR "/smartd.conf"; warning_script = SMARTMONTTOOL
copyright"\n");
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Copyright (C) 2002-19, Bruce Allen, Christian Franke, www.smartmontools.org\n", prog_name, smi()->get_os_version_str().c_str() ); if (!full)
return info;
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 SQLite 3.7.3 debian:sqlite3/3.7.3-1  
 (C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test x"\$lt\_cv\_cc\_needs\_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need  
 for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;  
 (C) case\_operand(X) case\_exprlist(Y) case\_else(Z) END(E). A.pExpr = sqlite3PEExpr(pParse, TK\_CASE, X, Z, 0); if( A.pExpr ){ A.pExpr->x.pList = Y;  
 sqlite3ExprSetHeight(pParse, A.pExpr); }e  
 (C) (((c=C))>=0x42#endif  
 (C) (((c=C))>=0x42#endif#endif /\* SQLITE\_AMALGAMATION \*/  
 (C) JOIN. { X = sqlite3JoinType(pParse,&A,&B,&C); }  
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```
(C) sortorder(Z). Expr *p = 0; if( C.n>0 ){ p = sqlite3Expr(pParse->db, TK_COLUMN, 0); sqlite3ExprSetCollByToken(pParse, p, &C); } A =
sqlite3ExprListAppend(pParse,X, p); sqlite3Expr
(C) sortorder(Z). Expr *p = 0; if( C.n>0 ){ p = sqlite3Expr(pParse, TK_COLUMN, 0, 0); sqlite3ExprSetCollByToken(pParse, p, &C); } A =
sqlite3ExprListAppend(pParse,0, p); sqlite3E
(C) ((sqlite3CtypeMap#endof#ifdef SQLITE_EBCDICconst char sqlite3IsEbcididChar[] = { /* x0 x1 x2 x3 x4 x5 x6 x7 x8 x9 xA xB xC xD xE xF */ 0, 0,
1, 1, 1, 1, 1, 1, 1, 0, 0, 0, 0, 0, 0, /*
(C) ((sqlite3CtypeMap#endof#ifdef SQLITE_EBCDICextern const char sqlite3IsEbcididChar[];
(C) t1.x=t2.y OR (t1.x=t2.z AND t1.y=15)** (D) x=expr1 OR (y>11 AND y<22 AND z LIKE 'hello')** (E) (p.a=1 AND q.b=2 AND r.c=3)
OR (p.x=4 AND q.y=5 AND r.z=6)
(C) The content of every column in the row.
(C) trigger_event(D) ON fullname(E) foreach_clause when_clause(G). { sqlite3BeginTrigger(pParse, &B, &Z, C, D.a, D.b, E, G, T, NOERR);
A = (Z.n==0?B:Z);}
SQLite 3.7.9 ubuntu:sqlite3/3.7.9-2ubuntu1
(C) The content of every column in the row.
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(C) JOIN. { X = sqlite3JoinType(pParse,&A,&B,&C); }
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(C) ((sqlite3CtypeMap#endof#ifdef SQLITE_EBCDICconst char sqlite3IsEbcididChar[] = { /* x0 x1 x2 x3 x4 x5 x6 x7 x8 x9 xA xB xC xD xE xF */ 0, 0,
1, 1, 1, 1, 1, 1, 1, 0, 0, 0, 0, 0, 0, /*
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(C) sortorder(Z). Expr *p = 0; if( C.n>0 ){ p = sqlite3Expr(pParse, TK_COLUMN, 0, 0); sqlite3ExprSetCollByToken(pParse, p, &C); } A =
sqlite3ExprListAppend(pParse,0, p); sqlite3E
(C) sortorder(Z). Expr *p = 0; if( C.n>0 ){ p = sqlite3Expr(pParse->db, TK_COLUMN, 0); sqlite3ExprSetCollByToken(pParse, p, &C); } A =
sqlite3ExprListAppend(pParse,X, p); sqlite3Expr
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(C) case_operand(X) case_exprlist(Y) case_else(Z) END(E). A.pExpr = sqlite3Expr(pParse, TK_CASE, X, Z, 0); if( A.pExpr ){ A.pExpr->x.pList = Y;
sqlite3ExprSetHeight(pParse, A.pExpr); }e
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 OR (p.x=4 AND q.y=5 AND r.z=6)  
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 (C) ((sqlite3CtypeMap#endif#ifndef SQLITE\_EBCDICextern const char sqlite3IsEbcidIdChar[];  
 (C) trigger\_event(D) ON fullname(E) foreach\_clause when\_clause(G). { sqlite3BeginTrigger(pParse, &B, &Z, C, D.a, D.b, E, G, T, NOERR);  
 A = (Z.n==0?B:Z);}  
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 for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;  
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 sqliteodbc 0.91 debian:sqliteodbc/0.91-3  
 (C) 2006, Sam Clegg <samo@debian.org> and  
 (C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test x"\$lt\_cv\_cc\_needs\_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need  
 for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;  
 (C) AC\_TRY\_LINK( AC\_LANG\_POP]) if test x"\$lt\_cv\_cc\_needs\_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -  
 belf CFLAGS="\$SAVE\_CFLAGS" fi ;;sp  
 (C)/!b go :more \./!{ N s/\n# // b more  
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 "SQLITEODBCU.DLL\0"#else VALUE "OriginalFile  
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 VALUE "PrivateBuild", "\0" VALUE  
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(C) // extension instance. // // FIXME: This needs to be reworked to go through a C API. auto *extension = registrationFn(); if (!extension) {
sys::CloseLibrary(handle); return {};
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(C) struct CXXRecordDecl::DefinitionData(D);
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copyright message found. "You should have a line: "Copyright [year] <Copyright Owner>""
Copyright (c) 1998, 2001 The NetBSD Foundation, Inc. * All rights reserved
copyright COPYRIGHT) _cpack_nuget_variable_fallback_and_wrap_into_element(tags TAGS LIST_GLUE " ")
Copyright 2012 Google, Inc. Package foo does bar.", 27, "" {"All Rights reserved
Copyright (C) 2012 - 2016, Marc Hoersken, <info@marc-hoersken.de>
(C) StringRef // FIXME: Avoid the allocation/copy if at all possible. std::transform(clobbers.begin(), clobbers.end(), Clobbers, [&](StringRef
Clobber) { return
(C) 'tp// value.// (D) 'tp' is not 'file_time_type::min()' or the filesystem supports the min// value.inline bool
TimeIsRepresentableByFilesystem(file_time_type tp) { TimeSpec ts = {};
Copyright (C) 2008-2015, International Business Machines Corporation and* others. All Rights Reserved
Copyright (C) 2002-2014 Free Software Foundation, Inc.
Copyright: 2007-2012 Aleksey Kravchenko <rhash.admin@gmail.com>
(C) VarTemplateSpecializationDecl::SpecializedPartialSpecialization(); PS->PartialSpecialization =
cast<VarTemplatePartialSpecializationDecl>(InstD); PS->TemplateArgs =
Copyright (C) 1997 - 2017, Daniel Stenberg, <daniel@haxx.se>, et al.
(C) 1995-2005 Jean-loup Gailly END END BLOCK "VarFileInfo" BEGIN VALUE "Translation", 0x0409, 1252 END
Copyright (c) 2020 Apple Inc. and the Swift project authors
Copyright (c) 2012, Noah Spurrier <noah@noah.org>
Copyright (C) 2001, 2003, 2005, 2008 Free Software Foundation, Inc.
Copyright 2000-(20 file{STRINGS "$${CMAKE_CURRENT_LIST_DIR}/../Copyright.txt" copyright_line LIMIT_COUNT 1 REGEX "${copyright_line_regex}")
if(copyright_line MATCHES "${copyright_line_regex}")
Copyright (C) 1996, 1997, 1998, 1999, 2000, 2001, 2002, 2003, 2004,# 2005, 2006, 2007, 2008, 2009 Free Software Foundation, Inc.# This file is free
software; the Free Software Foundation# gives un
copyright (C) 1996-2007 Julian R Seward. Allrights reserved.
(C) VarTemplateSpecializationDecl::ExplicitSpecializationInfo; ExplicitInfo->TypeAsWritten = TyInfo; ExplicitInfo->ExternLoc = readSourceLocation();
ExplicitInfo->TemplateKeywordLoc = read
COPYRIGHT SIGN { 0x00AA, 0xBB }, /* FEMININE ORDINAL INDICATOR */ { 0x00AB, 0xC7 }, /* LEFT-POINTING DOUBLE ANGLE QUOTATION
MARK */ { 0x00AC, 0xC2 }, /* NOT SIGN */ { 0x00AE, 0xA8 }, /
Copyright (c) 1997-2000 Thai Open Source Software Center Ltd
Copyright (c) 2012 Alexandre K. I. de Mendonca <alexandre.keunecke@gmail.com>, Paulo Pizarro <paulo.pizarro@gmail.com>
Copyright (c) 2017, Apple Inc. and the Swift project authors
(C) CHECK_OCTHEX_CONST(C)#define CHECK_HEX_CONST(C) \ CHECK_OCTHEX_CONST(0x##C); \
CHECK_OCTHEX_CONST(0x##C);
Copyright (c) 2009-2012,2014 Michihiro NAKAJIMA
(C)<<2)                (__v4sf)(__m128)(W), \                (__mmask8)(U))
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copyright Berkeley Software Design Inc, Kenneth MackKay and Emergya (Cloud4all, FP7/2007-2013, grant agreement

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Copyright (C) 2009, Markus Moeller, <markus_moeller@compuserve.com>
(C) Stmt for (unsigned i = 0; i != args.size(); i++) { if (args[i]->isTypeDependent()) ExprBits.TypeDependent = true; if (args[i]-
>isValueDependent()) ExprBits.ValueDependent = t
(C) or (GA // etc., since getelementpointer is variadic. We can't use // SelectionDAG::FoldSymbolOffset because it expects the GA to be accessible
// while in this case the GA may be furth
Copyright (C) 2015 - 2019, Daniel Stenberg, <daniel@haxx.se>, et al.
Copyright (C) 1995-2005 Jean-loup Gailly. * For conditions of distribution and use, see copyright notice in zlib.h * * Compile this file with -
DNO_GZCOMPRESS to avoid the compression code.
(C) CXXDefaultArgExpr(CXXDefaultArgExprClass, Loc, Param, UsedContext); }
Copyright (c) 2008 Stepan Kasal <skasal@redhat.com>
Copyright (C) 2014 Free Software Foundation, Inc.
(C) IdentifierInfo std::copy(names, names + NumExprs, Names);
Copyright (c) 1998 Geoffrey Keating
Copyright (c) 2015 Paul Norman <penorman@mac.com>
Copyright (C) 1996-2007 Julian Seward <jseward@bzip.org>
Copyright (C) 2004-2005, 2007-2008, 2011-2015 Free Software# Foundation, Inc.# Written by Gary V. Vaughan, 2004
(C) ASSERT_CONST_TYPE((C), C99_SUFul_TYPE((C)))
Copyright (C) 2010-2012, International Business Machines* Corporation and others. All Rights Reserved
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Copyright 2015 The Go Authors. All rights reserved
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copyright Alexander Chemeris. Three
Copyright (C) 2017 - 2018, Yiming Jing, <jingyiming@baidu.com>
(C) VTable->setComdat(C);
Copyright 2005-2007 Universiteit Leiden
Copyright (C) 2009-2011, International Business Machines Corporation and * others. All Rights Reserved
Copyright (C) 1996-2001 Internet Software Consortium. * * Permission to use, copy, modify, and distribute this software for any * purpose with or
without fee is hereby granted, provided that the a
(C) ASSERT_CONST_TYPE((C), unsigned long long int)
Copyright (C) 2008-2013, International Business Machines Corporation and* others. All Rights Reserved
(C) for (int i=0; i<100; i#pragma omp target teams distribute parallel for num_teams(T) // expected-error {{'T' does not refer to a value}} for (int i=0;
i<100; i++) foo());#pragma omp target tea
Copyright (c) 2003-2006 Tim Kientzle * All rights reserved
Copyright (C) 2004, 2011-2015 Free Software Foundation, Inc.# Written by Scott James Remnant, 2004
Copyright (c) 1996-2014 Anthony Green, Red Hat, Inc and others.
copyright no longer supported"); return true; } else if ((strcmp(argv[i], "--version") == 0) || (strcmp(argv[i], "--version") == 0) ||
(strcmp(argv[i], "/V") ==
Copyright (c) 2001-2004 Damien Miller <djm@openbsd.org> * * Permission to use, copy, modify, and distribute this software for any * purpose with or
without fee is hereby granted, provided that the
Copyright (C) 2010-2014, International Business Machines Corporation and* others. All Rights Reserved
Copyright (c) 1998, 2001, 2007, 2008, 2011, 2014 Red Hat
Copyright (C) 1999-2014, International Business Machines Corporation* and others. All Rights Reserved
(C) foo();#pragma omp parallel num_threads(T(23)) foo(); return 0;}
copyright\nAbout" ID_APP_EXIT "Quit the application; prompts to save documents\nExit"ID_FILE_MRU_FILE1 "Open this document"
ID_FILE_MRU_FILE2 "Open this document" ID_FILE_
Copyright (C) 2002-2014, International Business Machines* Corporation and others. All Rights Reserved
(C) C->Parent = NewN; C = NewN; N = N->Parent; } if (!NewNode) return;
Copyright (C) 2014, Bill Nagel <wnagel@tycoint.com>, Exacq Technologies
Copyright (c) 2015 Advanced Micro Devices, Inc.
Copyright 2012 The Open Source Electronic Health Record Agent
(C) Stmt std::copy(exprs.begin(), exprs.end(), Exprs);
Copyright 2010-2018 Kitware, Inc. All rights reserved
Copyright (C) Microsoft Corporation. All rights reserved
Copyright (C) 1996, 1998, 2000, 2001, 2002, 2003, 2004, 2005, 2008# Free Software Foundation, Inc.
(C) __compiler_check_default_language_standard(C 1.10 90 1.10 99 2.10 11)
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Copyright (C) 2000-2011, International Business Machines* Corporation and others. All Rights Reserved
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Copyright (c) 2015 Apple Inc. and the Swift project authors */
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Copyright (c) 2012, 2013 Anthony Green Moxie Assembly glue.
Copyright (c) 2015 - 2019 Apple Inc. and the Swift project authors
Copyright (c) 2017-2018, Apple Inc. and the Swift project authors
copyright cm_zlib_inflate_copyright#define inflate_table cm_zlib_inflate_table#define _dist_code cm_zlib__dist_code#define _length_code
cm_zlib__length_code#define _tr_align cm_zlib__tr_align#define
(C) foo();#pragma omp target teams thread_limit(T) // expected-error {{'T' does not refer to a value}} foo();#pragma omp target teams thread_limit
// expected-error {{expected '(' after 'thread_
(C) struct LambdaDefinitionData(R, Info, Dependent, IsGeneric, CaptureDefault); R->setMayHaveOutOfDateDef(false); R-
>setImplicit(true); C.getTypeDeclType
Copyright (C) 1991, 1999 Free Software Foundation, Inc.
Copyright (c) 2013 Matthew Arsenault
(C) TRY_TO(IndexCtx.handleReference(C, D->getLocation(), D, D, SymbolRoleSet())); SourceLocation CategoryLoc = D-
>getCategoryNameLoc(); if (!Categor
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Copyright (c) 2003-2007 Tim Kientzle
Copyright 2008-2010 Apple, Inc. Permission is hereby granted, free of charge,
(C) for (int j=0; j<100; j#pragma omp target teams distribute parallel for simd thread_limit(T) // expected-error {{'T' does not refer to a value}} for
(int j=0; j<100; j++) foo();#pragma omp ta
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Copyright (c) 2007 Joerg Sonnenberger * All rights reserved
Copyright (c) 2008 Joerg Sonnenberger * All rights reserved
Copyright (C) 1998 - 2010, Florin Petriuc, <petriuc.florin@gmail.com>
COPYRIGHT VALUE "LegalCopyright", RC_COPYRIGHT#endif END END
Copyright (c) 2010 Apple Inc.All rights reserved
Copyright (c) 2013 Roy Stogner <roystgnr@ices.utexas.edu>
Copyright (c) 1999-2018, Apple Inc. All rights reserved
(C) CXXBindTemporaryExpr(Temp, SubExpr);}
COPYRIGHT: * Copyright (c) 1997-2011, International Business Machines Corporation and * others. All Rights Reserved
Copyright (c) 2015 Okhotnikov Kirill * All rights reserved
Copyright (C) Microsoft Corporation. All rights reserved
Copyright (c) 2002 Bo Thorsen
(C) CXXBaseSpecifier data().NumVBases = VBases.size(); for (int I = 0, E = VBases.size(); I != E; ++I) { QualType Type = VBases[I]->getType();
if (!Type->isDependentType()) addedClas
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Copyright Joyent, Inc. and other Node contributors. All rights reserved
Copyright (c) 1996-1998 John D. Polstra. All rights reserved
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Copyright (C) 1999, 2000, 2001, 2003, 2004, 2005, 2008# Free Software Foundation, Inc.
Copyright 2015-2016 Kitware, Inc.
Copyright (c) 2009 Michihiro NAKAJIMA * All rights reserved
Copyright (c) 2014 - 2017 Apple Inc. and the Swift project authors
(C) Constraints.push_back(std::move(C)); }
Copyright (c) 2014 - 2016 Apple Inc. and the Swift project authors
Copyright 2010 Firebreath Dev Team)set(FBSTRING_PluginFileName "np${PLUGIN_NAME}.dll")set(FBSTRING_ProductName
"CFTTestPlugin")set(FBSTRING_FileExtents "")set(FBSTRING_PluginName "CFTTestPlugin")
(C) mergeable priority(C) in_reduction(-: argc)// CHECK-NEXT: foo();// CHECK: template<> int tmain<int, 5>(int argc, int *argv) //{ CHECK-NEXT: int b
= argc, c, d, e, f, g;// CHECK-NEXT: static int
Copyright (c) 2013-2016 Apple Inc. All rights reserved
(C)(D)(E)(F)(G)(H)(I)(J)(K)(L)(M)" "(N)(O)(P)(Q)(R)(S)(T)(U)(V)(W)(X)(Y)(Z)$)"
Copyright 2003 Google Inc.// All rights reserved
Copyright (C) 2005-2013, International Business Machines * Corporation and others. All Rights Reserved
(C) Offset = C->Offset; if (getVersion() >= 5) { if (DA.getData().data() == nullptr) return None; Offset += Header.getFormat() ==

```



```

dwarf::DwarfFormat::DWARF32 ? 8 : 16; // Look
Copyright (c) 2011 Plausible Labs Cooperative, Inc.
(C) ASSERT_CONST_TYPE((C), C99_SUFI_DEC_TYPE((C)))#define CHECK_SUFI_OCTHEX_TYPE(C) \ ASSERT_CONST_TYPE((C),
C99_SUFI_OCTHEX_TYPE((C)))
Copyright (C) 2001 - 2019, Daniel Stenberg, <daniel@haxx.se>, et al. * * All rights reserved
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Copyright (c) 2003-2009 <author(s)>All rights reserved
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Copyright (c) 2004 Simon Posnjak
Copyright (C) 2011",IDC_STATIC,40,25,119,8 DEFPUSHBUTTON "OK",IDOK,178,7,50,16,WS_GROUPEND
(C)<<2) (__v8df)(__m512d)(W), \ (__mmask8)(U), \
_MM_FROUND_CUR_DIRE
Copyright (C) 1999 - 2017, Daniel Stenberg, <daniel@haxx.se>, et al.
Copyright (c) 2003-2011 Tim Kientzle
(C) return replaceInstUsesWith(I, ConstantInt::getTrue(I.getType()));
COPYRIGHT ERROR_QUIET
Copyright 1995-2005 Jean-loup Gailly "/* If you use the zlib library in a product, an acknowledgment is welcome in the documentation of your product.
If for some reason you cannot include such
(C) CXXBaseSpecifier data().NumBases = NumBases; for (unsigned i = 0; i < NumBases; ++i) { data().getBases()[i] = *Bases[i]; // Keep track of
inherited vbases for this base class. const
(C) with signal (ss where# 'Sss' step (S) with signal (ss where 'ss' is two hex digits)#-----sub
dump_continue_with_signal_cmd
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Copyright (c) 2002 Roger Sayle
copyright byother parties and/or subject to additional restrictions.
Copyright (c) 2013 Synopsys, Inc. (www.synopsys.com)
Copyright (C) 1995-2005 Jean-loup Gailly. * For conditions of distribution and use, see copyright notice in zlib.h */
Copyright 2010-2011 INRIA Saclay
Copyright (c) 2009 Matteo Frigo
Copyright (c) 2013 Imagination Technologies Ltd.
Copyright (C) 2017 Google Inc.
Copyright (C) 2004 - 2019, Daniel Stenberg, <daniel@haxx.se>, et al.
(C) available for inlining. When we process // the C call site we don't want to do any alloca merging between X and Y // because their scopes are not
disjoint. We could make this smarter by //
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;*-so
(C) CXXRecordDecl::LambdaDefinitionData(D, nullptr, false, false, LCD_None); else
Copyright (c) 2012 Anthony Green
Copyright (c) 2003 Jakub Jelinek <jakub@redhat.com>
copyright <value> *
(C) __compiler_check_default_language_standard(C 7.10 99) set(CMAKE_C_OUTPUT_EXTENSION ".r90")
Copyright (C) 2000--2006 Erik Edelmann <erik.edelmann@iki.fi>
(C) std::swap(LHS, RHS); }
Copyright (C) 1994 X Consortium
(C) thread_limit(d// CHECK-NEXT: foo())// CHECK-NEXT: #pragma omp target// CHECK-NEXT: #pragma omp teams reduction(^: e,f) reduction(&&: g)//
CHECK-NEXT: foo())// CHECK: template<> int tmain<int, 5
Copyright: 2013 Aleksey Kravchenko <rhash.admin@gmail.com>
Copyright (c) 2014 Michihiro NAKAJIMA* All rights reserved
Copyright (c) 1999-2003 Steve Purcell
(C) ? Alpha : (isWhitespace(C) || (!RemoveStars && TemplateTypenameCntr == 0 && C == '*')) ? Space
: Punctuation; if (Nex
Copyright(filename, lines, error):
Copyright (C) 2004, 2005, 2007, 2008 Free Software Foundation, Inc.# Written by Gary V. Vaughan, 2004
(C) proc_bind(master) reduction( foo());
(C)<<2) (__v4sf)_mm_setzero_ps(), \ (__mmask8)-1)

```

```

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Copyright 2011 Sven Verdoolaege
Copyright (C) 2018, Daniel Stenberg, <daniel@haxx.se>, et al.
(C) DeclStmt(DeclGroupRef(V), VarRange.getBegin(), VarRange.getEnd());}
(C)<<2) (__v4df)_mm256_setzero_pd(), \ (__mmask8)(U))
Copyright (c) 2013 Karl Dubost
Copyright (c) 2004-2018, Apple Inc. and the Swift project authors
Copyright (c) 2004-2017, Apple Inc. and the Swift project authors
Copyright (c) 1992, 1993, 1994 * The Regents of the University of California. All rights reserved
(C) > sizeof(void C f(C c) { return c; }
(C)(int);};#if __cplusplus <= 201703L// expected-warning@-3 {{this expression will be parsed as explicit(bool) in C++2a}}#if
defined(__cpp_conditional_explicit)#error "the feature test macro __cp
(C) Copyright John maddock 1999. Distributed under the Boost
Copyright (c) 2019 Apple Inc. All rights reserved
Copyright (C) 1996, MPEG Software Simulation Group. All Rights Reserved
(C) sign bits. if (match(U->getOperand(1), m_APIInt(Denominator))) {
Copyright (C) 1997-2013, International Business Machines * Corporation and others. All Rights Reserved
Copyright (C) 2002-2017 Free Software Foundation, Inc.
Copyright (c) 2007-2009
(C) and (D) memory_resource *expect = &R; memory_resource *old = set_default_resource(expect); assert(old != nullptr);
assert(old == new_delete_resource());
Copyright 2008 Google Inc.// All Rights Reserved
(C) IntegerLiteral(Empty);}
(C) //expected-error@end
Copyright 2019 Apple Inc. and the Swift project authors
Copyright 2014 The Go Authors. All rights reserved
(C) ClassTemplateSpecializationDecl::ExplicitSpecializationInfo; ExplicitInfo->TypeAsWritten = TyInfo; ExplicitInfo->ExternLoc =
readSourceLocation(); ExplicitInfo->TemplateKeywordLoc = re
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Copyright (c) 2006-2018, Apple Inc. and the Swift project authors
Copyright (C) 2006-2014 Free Software Foundation, Inc.
(C) num_threads(s) proc_bind(close) reduction( { foo();#pragma omp section foo();
(C) mergeable priority(C) in_reduction(-: argc) foo(); return 0;};
(C)<<2) (__v8df)(__m512d)(W), \ (__mmask8)(U), (int)(R))
(C) dlog_t dlog; // Opaque value passed to the logger void* dlogArg;
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Copyright (c) 2013-2018, Apple Inc. and the Swift project authors
Copyright 2017 Apple Inc. and the Swift project authors
Copyright (C) 2004 Anthony Green
Copyright 2010 INRIA Saclay
Copyright (c) 2011 Anthony Green IA64 Foreign Function Interface
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Copyright (C) 2001-2017 Free Software Foundation, Inc.
Copyright 2016-2017 Tobias Grosser
Copyright (C) 2012-2016, Yann Collet.
(C) foo();#pragma omp target parallel map(i) foo();#pragma omp target parallel map(a[0:10], i) foo();#pragma omp target parallel map(to: i)
map(from: j) foo();#pragma omp target parallel m
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Copyright (C) 2007-2015, International Business Machines* Corporation and others. All Rights Reserved
Copyright (c) 2008 Sven Verdoolaege <skimo@kotnet.org>
Copyright (C) 2011 Anthony Green
(C) Stmt std::copy(Exprs, Exprs + NumExprs, this->Exprs);
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```

```

(C) uint64_t std::copy(Words, Words + NumWords, pVal); } else if (NumWords == 1) VAL = Words[0]; else VAL = 0;}
(C) if (I.isExact() Value *ShAmt = ConstantInt::get(Op1->getType(), Op1C->exactLogBase2()); return BinaryOperator::CreateExactAShr(Op0,
ShAmt, I.getName()); }
Copyright Filename. * This file shall be described in the Root Directory * and containing a copyright statement. */ unsigned int
copyright_file:1;#define OPT_COPYRIGHT_FILE_DEFAULT 0 /* Not s
Copyright (C) 2001-2014 Free Software Foundation, Inc.
Copyright (c) 2017-2019 by the contributors listed in CREDITS.TXT
Copyright (c) 1998 Geoffrey Keating
Copyright (c) 2003-2007 Kees Zeelenberg * All rights reserved
copyright: Copyright 2007-2010 by the Sphinx team, see AUTHORS.
(C) num_threads(s) proc_bind(close) reduction(// CHECK-NEXT: foo(// CHECK: template<> int tmain<int, 5>(int argc, int *argv) {// CHECK-NEXT: int b
= argc, c, d, e, f, g;// CHECK-NEXT: static int
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Copyright (c) 2008 Red Hat, Inc
Copyright 2001-2018 Howard Chu, Symas Corp.
copyright file="Setup.Configuration.h" company="Microsoft Corporation">
(C)<<2) (__v2df)_mm_setzero_pd(), \ (__mmask8)-1)
Copyright (C) 1995-2004 Jean-loup Gailly * For conditions of distribution and use, see copyright notice in zlib.h */
Copyright (c) 2013, Ben Noordhuis <info@bnoordhuis.nl> * * Permission to use, copy, modify, and/or distribute this software for any * purpose with or
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(C) C->atTemplateEnd(TheSema, Inst); }
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Copyright (c) 1996 - 2019, Daniel Stenberg, <daniel@haxx.se>, and manycontributors, see the THANKS file.
Copyright (c) 2009 Bradley Smith <brad@brad-smith.co.uk>
(C) __compiler_check_default_language_standard(C 1.10 90 2.10 99 4.10 11)
Copyright (C) 1996-2001, 2003-2015 Free Software Foundation, Inc.# Written by Gordon Matzigkeit, 1996
Copyright (C) 2013-2015, Daniel Stenberg, <daniel@haxx.se>, et al.
(C) MemberSpecializationInfo(ED, TSK);}
Copyright (c) 1996, 1998, 2005 Red Hat, Inc. MIPS Foreign Function Interface
(C) return replaceInstUsesWith(I, ConstantInt::getFalse(I.getType())); if (Op1Min == Op0Max) // A <s B -> A != B if max(A) == min(B) return
new ICmpInst(ICmpInst::ICMP_NE, Op0, Op1);
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```

```

(C) Visit(C); }
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(C) return ConstantAsMetadata::get(C); return nullptr;}
(C) FixedPointLiteral(C, V, type, l, Scale);}
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Copyright (c) 2002, 2007 Bo Thorsen <bo@suse.de> x86-64 Foreign Function Interface
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(C) make: (AddressVector<C> return C(v);}@end
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(C)<<2) (__v16sf)_mm512_setzero_ps(), \ (__mmask16)(U), \
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(C) ignored struct { B int } // missing field (A) ignored; data will be dropped

```

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 (C) \_\_compiler\_check\_default\_language\_standard(C 1.10 90 1.10 99 4.10 11)  
 (C) ClassTemplateSpecializationDecl:: SpecializedPartialSpecialization(); PS->PartialSpecialization =

cast<ClassTemplatePartialSpecializati  
 Copyright (c) 2016 Apple Inc. All rights reserved  
 (C)// expected-note@template <class C>void h(C \*hp, C \*hp2, C \*hq, C \*lin) { b = 0;}  
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 Copyright 2011 Sven Verdoolaege. All rights reserved  
 (C) GV->setComdat(C); else return TokError("unknown global variable property!"); }  
 (C)<<2) (\_\_v4sf)\_mm\_setzero\_ps(), \ (\_\_mmask8)(U))  
 (C) f(m map fmt.Println("C.f()"))  
 Copyright (C) 2012, Marc Hoersken, <info@marc-hoersken.de>, et al.  
 Copyright (c) 2014-2017, Apple Inc. and the Swift project authors  
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 (C) CXXConstCastExpr(EmptyShell());  
 (C) \*C=NULL; if (D)DEFINE\_WEAK\_CFNETWORK\_FUNC\_FAIL(Boolean, \_CFURLWriteDataAndPropertiesToResource, (CFURLRef A, CFDataRef B,

CFDictionaryRef C, SInt32 \*D), (A, B, C, D), if (D) \*D = kCFURLImproperAr  
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 COPYRIGHT Copyright  
 Copyright by Tim Kientzleand is subject to the copyright notice reproduced at the bottom ofthis file.  
 (C) \_\_compiler\_check\_default\_language\_standard(C 1.10 90 4.10 99) set(CMAKE\_C\_OUTPUT\_EXTENSION ".r85")  
 Copyright (c) 2011 Michihiro NAKAJIMA \* All rights reserved  
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 Copyright message appears at the top of the file. ""  
 (C) for (int i=0; i<100; i#pragma omp target#pragma omp teams distribute num\_teams(T) // expected-error {{'T' does not refer to a value}} for (int  
 i=0; i<100; i++) foo());#pragma omp target#pra  
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 (C) return true; }  
 Copyright (c) 1996-2003 Red Hat, Inc.  
 Copyright (C) 2010, 2011, Hoi-Ho Chan, <hoiho.chan@gmail.com>  
 Copyright (c) 2013-2014 Apple Inc. All rights reserved  
 (C) foo();#pragma omp target#pragma omp teams num\_teams(T) // expected-error {{'T' does not refer to a value}} foo();#pragma omp  
 target#pragma omp teams num\_teams // expected-error {{expected  
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 Copyright (c) 1996, David Mazieres <dm@uun.org>  
 (C) returns all currently known interfaces implemented by C.func (r \*rta) interfaces(C types.Type) []\*types.Interface { // Ascertain set of interfaces C  
 implements // and update 'implements' relati  
 (C) case ProtocolType(Any) case CEnumType(Simple) case ADTEnumType(ADT) case Recursive(GuineaPig) } var GP\_StructType =  
 GuineaPig.StructType(12) var GP\_TupleType = GuineaPig.Tupl  
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 (C) return isColdCount(  
 Copyright (c) 1985, 1986, 1992, 1993 \* The Regents of the University of California. All rights reserved  
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 (C) copyin(S<T>::TS) proc\_bind(master) reduction(// CHECK-NEXT: { // CHECK-NEXT: foo(); // CHECK-NEXT: }  
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copyrightawk -fawk -f ${pickout2} ${nfdtmp} >> ${outfile}echo "#endif /* ARCHIVE_STRING_COMPOSITION_H_INCLUDED */" >> ${outfile}echo ""
>> ${outfile}
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COPYRIGHT SIGN 0x2122, /* TRADE MARK SIGN */ 0x00B4, /* ACUTE ACCENT */ 0x00A8, /* DIAERESIS */ 0x2260, /* NOT EQUAL TO */
0x00C6, /* LATIN CAPITAL LIGATURE AE */ 0x00D8, /* LA
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(C) ConstrainedTemplateDeclInfo; auto *const New = new (C, DC) ClassTemplateDecl(CTDI, C, DC, L, Name, Params, Decl); New-
>setAssociatedConstraints(AssociatedConstraints); return New;}
Copyright (C) 1996, 1997, 2000, 2001, 2003, 2005, 2008# Free Software Foundation, Inc.
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(C) for (int i = 0; i < 100; i foo();#pragma omp target#pragma omp teams#pragma omp distribute parallel for simd num_threads(T(23)) for (int i =
0; i < 100; i++) foo(); return 0;}
copyright in MacRoman and Unicode) not to break existing apps // Will use a flag passed in from upper layers to switch restriction mode for this
case in the next release if ((extraByte
Copyright (c) 2004 Renesas Technology M32R Foreign Function Interface
(C)<<2) (__v2df)_mm_setzero_pd(), \ (__mmask8)(U))
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Copyright (C) 1996-2014, International Business Machines Corporation and others. * All Rights Reserved
(C) BinaryOperator(const_cast<Expr const_cast<Expr*>(RHS), Op,
C.getLogicalOperationType(),
(C) BinaryOperator(const_cast<Expr BO_Assign, Ty, VK_RValue, OK_Ordinary, SourceLocation(),
FPOptions());}
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```

```

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(C) PartialDiagnostic::Storage;
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<paulo.pizarro@gmail.com>
copyright/licensing statement at the beginning of the file. If any donot, please let me know and I will rectify it. The following is
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(C) p); // expected-error
(C) DEFER_VAR_NAME(C)#define DEFER_UNIQUE_VAR_NAME DEFER_INTERMEDIATE(__COUNTER__)
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copyright cm_zlib_deflate_copyright#define gzcerror cm_zlib_gzcerror#define gzcerror cm_zlib_gzcerror#define gzcerror cm_zlib_gzcerror#define
gzdopen cm_zlib_gzdopen#define gzeof cm_zlib_gzeof
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(C)<<2) (__v8df)_mm512_undefined_pd(), \ (__mmask8)-1, (int)(R))
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Copyright (c) 2011 Plausible Labs Cooperative, Inc.
Copyright (c) 2011 Timothy Wall
(C) MarshalJSON() ( return []byte("<&>"), nil}
Copyright (C) 2012, 2013 Anthony Green Moxie Foreign Function Interface
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INCLUDING ALL IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS
(C) ((C) ==#endif
(C) orOMPClause *Parser::ParseOpenMPVarListClause(OpenMPDirectiveKind DKind, OpenMPClauseKind Kind,
bool ParseO
Copyright (c) 2005-2014 Aleksey Kravchenko <rhash.admin@gmail.com>
(C) DependentDiagnostic(PDiag, DiagStorage);
Copyright (c) 2002, 2007 Bo Thorsen <bo@suse.de>
(C) for (int i = 0; i < 100; i foo());#pragma omp target#pragma omp teams distribute parallel for num_threads(T(23)) for (int i = 0; i < 100; i++)
foo(); return 0;}
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Copyright (C) 2004, 2005, 2007, 2008, 2009 Free Software Foundation, # Inc.# Written by Gary V. Vaughan, 2004
(C) MemberSpecializationInfo(FD, TSK); TemplateOrSpecialization = Info;}
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(C) IdentifierInfo std::copy(Names, Names + NumExprs, this->Names);
Copyright (C) 1995-2005 Mark Adler * For conditions of distribution and use, see copyright notice in zlib.h */
Copyright (c) 2011, 2013 Anthony Green
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 (C) num\_threads(s) proc\_bind(close) reduction(// CHECK-NEXT: {/ CHECK-NEXT: foo();// CHECK-NEXT: #pragma omp section// CHECK-NEXT: foo();//  
 CHECK-NEXT: }// CHECK: template<> int tmain<int, 5>(i  
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 Copyright 2011 INRIA Saclay  
 Copyright 2012 The Go Authors. All rights reserved  
 Copyright © 2004 Scott James Remnant <scott@netsplit.com>.  
 (C) or Y > abs(C) Constant \*PosDividendC = ConstantInt::get(Ty, C->abs()); Constant \*NegDividendC = ConstantInt::get(Ty, -C->abs()); if  
 (isICmpTrue(CmpInst::ICMP\_SLT, Y, NegDividendC  
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 (C) FloatingLiteral(C, V, isexact, Type, L);} ;  
 Copyright (c) 2011, 2012 Anthony Green  
 Copyright (c) 2004 Anthony Green FR-V Assembly glue.  
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 (C) for (int i=0; i<100; i#pragma omp target#pragma omp teams distribute parallel for num\_teams(T) // expected-error {{'T' does not refer to a  
 value}} for (int i=0; i<100; i++) foo();#pragma om  
 Copyright (C) 2004, 2005, 2007, 2009 Free Software Foundation, Inc.# Written by Scott James Remnant, 2004.  
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 for some reason you cannot include such an ac  
 (C) for (int j=0; j<100; j#pragma omp target teams distribute simd thread\_limit(T) // expected-error {{'T' does not refer to a value}} for (int j=0;  
 j<100; j++) foo();#pragma omp target teams di  
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 (C) CXXTemporary(Destructor);} ;  
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 (C) for (int i=0; i<100; i#pragma omp target#pragma omp teams distribute simd num\_teams(T) // expected-error {{'T' does not refer to a value}} for  
 (int i=0; i<100; i++) foo();#pragma omp target  
 Copyright (c) 2000 Software AG  
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 (C) foo();#pragma omp target teams num\_teams(T) // expected-error {{'T' does not refer to a value}} foo();#pragma omp target teams num\_teams //  
 expected-error {{expected '(' after 'num\_teams'}} ;  
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 (C) ? SafeC : C; } return ConstantVector::get(Out);  
 Copyright (C) 2009 The Android Open Source Project  
 (C) Copyright ..." is shown in the HTML footer. Default is True.  
 Copyright (c) 2008 Anselm Strauss  
 copyright symbol/// &trade; the trade mark symbol/// &reg; the registered trade mark symbol/// &nbsp; a non-breakable space./// &Delta; Greek letter  
 Delta Δ./// &Gamma; Greek letter Gamma Γ.void  
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```

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(C)<<2) (__v8df)_mm512_setzero_pd(), \ (__mmask8)(U), (int)(R))
(C)<<2) (__v8sf)_mm256_setzero_ps(), \ (__mmask8)-1)
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Copyright (C) 1997, 2000, 2001, 2003, 2004, 2005, 2006, 2008# Free Software Foundation, Inc.
Copyright (c) 2003, 2004, 2006, 2007, 2012 Kaz Kojima
copyright-file" == 0) r = get_str_opt(a, &(iso9660->copyright_file_identifier), COPYRIGHT_FILE_SIZE,
key, value); iso9660->opt.copyright_file = r == ARCHIVE_OK; return (r); }
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(C) 2014-2016 John MacFarlane\n"); exit(0); } else if (strcmp(argv[i], "--sourcepos") == 0) { options |= CMARK_OPT_SOURCEPOS; } else if
(strcmp(argv[i], "--hardbreaks") == 0) {
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(C) >> 1; } BT.put(BitTracker::RegisterRef(NewR), NewRC); return true;
Copyright (C) 2010 , Yahoo *****
Copyright (c) 2004 by Internet Systems Consortium, Inc. ("ISC")
Copyright 2016-2017 Sven Verdoolaege
Copyright (c) 1996,1998,2001-2003,2005,2008,2010 Red Hat, Inc. X86 Foreign Function Interface
(C) for (int j=0; j<100; j
(C) num_threads(s) proc_bind(close) reduction(// CHECK-NEXT: foo(// CHECK-NEXT: #pragma omp target parallel if(target: argc > 0)// CHECK-NEXT:
foo(// CHECK-NEXT: #pragma omp target parallel if(p
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Copyright\\(C\\) MPI_${MPI_VARIABLE}_CMDLINE "${MPI}_${MPI_VARIABLE}_CMDLINE}" string(REGEX REPLACE "(^| )" "\\1"-
MPI_${MPI_VARIABLE}_CMDLINE "${MPI}_${MPI_VARIABLE}_CMDLINE}")
(C) StringLiteral std::copy(Clobbers, Clobbers + NumClobbers, this->Clobbers);}
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(C) UnaryOperator( /* input=*/ M.makeImplicitCast(M.makeLvalueToRvalue(Deref, DerefType), DerefType,
CK_IntegralToBoolean), /* opc=*/ UO_LNot, /* QualT
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(C)<<2) (__v16sf)(__m512)(W), \ (__mmask16)(U), (int)(R))
Copyright (c) 2004, 2010 Free Software Foundation, Inc.
(C) f(func (*C) g()

```

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(C) for (int j=0; j<100; j#pragma omp target#pragma omp teams distribute thread_limit(T) // expected-error {{'T' does not refer to a value}} for (int
j=0; j<100; j++) foo());#pragma omp target#
Copyright (C) 1991 Texas Instruments Incorporated.
(C) for (int j=0; j<100; j#pragma omp target#pragma omp teams distribute parallel for simd thread_limit(T) // expected-error {{'T' does not refer to a
value}} for (int j=0; j<100; j++) foo());#p
Copyright (c) 2015-present libuv project contributors.
(C) IntegerLiteral(C, V, type, l);}
copyright(){cat >/*-
(C) c = C(); }
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(C) make: (AddressVector<C>@end
Copyright (C) 2002, 2003, 2005, 2006, 2007, 2008, 2011 Free Software# Foundation, Inc.
Copyright (c) 2003-2010 Python Software FoundationThis module is free software, and you may redistribute it and/or modify
Copyright (C) 1998 - 2016, Daniel Stenberg, <daniel@haxx.se>, et al.
(C) proc_bind(master) reduction(// CHECK-NEXT: foo()
Copyright: None. -----
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(C) << 1); }
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Copyright (c) 2004 __MyCompanyName__. All rights reserved
(C) Common; C.addDestruction(CommonPtr); return CommonPtr;}
Copyright (C) 2001, 2003, 2005, 2011 Free Software Foundation, Inc.
(C) InitListExpr(C, lBraceLoc, None, rBraceLoc); ILE->setType(baseExpr->getType()); BaseAndUpdaterExprs[1] = ILE;}
(C) " << R.getName() << "Attr(A->getLocation(), C"; for (auto const &ai : Args) { OS << ", "; ai->writeTemplateInstantiationArgs(OS); }
OS << " , A->getSpellingListIndex());\n
Copyright (C) 2007 Free Software Foundation, Inc.
Copyright 2015 Sven Verdoolaege
(C) / operator C() pair to construct the B from the B temporary // here. In C++17, we initialize the B object directly using 'A::operator B()'. B b = a;#if
__cplusplus <= 201402L // expected-er
Copyright (c) 2007-2018, Apple Inc. and the Swift project authors
(C) ObjCBoolLiteralExpr(Val, Ty, SourceLocation());}
Copyright (c) 2001-2017 Expat maintainers
(C) __compiler_check_default_language_standard(C 1.10 90 6.10 99 8.10 11)
Copyright (c) 2017-2018, Apple Inc. and the Swift project authors
(C)// CHECK-NEXT: foo()// CHECK-NEXT: #pragma omp target parallel map(tofrom: i)// CHECK-NEXT: foo()// CHECK-NEXT: #pragma omp target
parallel map(tofrom: a[0:10],i)// CHECK-NEXT: foo()// CHECK-
Copyright (c) 1998, 1999, 2017 Kungliga Tekniska H * (Royal Institute of Technology, Stockholm, Sweden). *
(C) ASSERT_CONST_TYPE((C), long long int)#define CHECK_SUFIL_OCTHEX_TYPE(C) \ ASSERT_CONST_TYPE((C),
C99_SUFIL_OCTHEX_TYPE((C)))
Copyright (c) 2004 Apple Computer, Inc. All rights reserved
Copyright 2012 Universiteit Leiden
Copyright (c) 2010-2012 Michihiro NAKAJIMA
Copyright 2011 Google Inc. All Rights Reserved
Copyright (C) 1995-2005 Mark Adler * For conditions of distribution and use, see copyright notice in zlib.h * * Thanks to Rodney Brown
<rbrown64@csc.com.au> for his contribution of faster * CRC me
Copyright (c) 2012, Google Inc. All rights reserved
Copyright (c) 2011-2012,2014 Michihiro NAKAJIMA
(C) StringRef std::transform(constraints.begin(), constraints.end(), Constraints, [&](StringRef Constraint) { return
copyIntoContext(C, Constraint);

```

```

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Copyright (C) 1998 - 2018, Daniel Stenberg, <daniel@haxx.se>, et al.
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Copyright line of expected format endif()else() message(STATUS "SKIPPED: CMAKE_VERSION does not know the year: ${CMAKE_VERSION}")
Copyright (C) 2010-2015 Free Software Foundation, Inc.
copyright-file", KEY_STR, iso9660->copyright_file_identifier.s); if (iso9660->opt.iso_level != OPT_ISO_LEVEL_DEFAULT)
set_option_info(&info, &opt, "iso-level", KEY_INT, iso9660->opt.iso
Copyright (c) 2011-2012 Michihiro NAKAJIMA * All rights reserved
Copyright (c) 2014 - 2019 Apple Inc. and the Swift project authors
Copyright (c) 2018-present, Facebook, Inc.
Copyright 2008, Google Inc.All rights reserved
(C) Stmt memcpy(SubExprs, Exprs.data(), sizeof(Expr *) * Exprs.size());}
Copyright (C) 2008 Free Software Foundation, Inc.
(C) C->setSelectionKind(llvm::Comdat::Largest); } VFTable = llvm::GlobalAlias::create(CGM.Int8PtrTy,
/*AddressSpace=*/0, VFTableLinkage,
Copyright File", 0, D_CHAR); if (r != ARCHIVE_OK) return (r); /* Abstract File Identifier */ r = set_file_identifier(bp, 740, 776, vdc, a, vdd,
&(iso9660->abstract_file_identifier), "Ab
Copyright (c) 2013, Kenneth MacKay
Copyright (c) 2006 Rudolf Marek SYSGO s.r.o.
(C) Stmt std::copy(exprs, exprs + NumExprs, Exprs);
Copyright (c) 2012 Ondrej Holy * All rights reserved
Copyright (C) 2016, Yann Collet.
(C) byteReader_t reader; // Opaque value passed to the reader const void* readerArg; // The address of the next byte to read via the reader uint64_t
readerCursor;
(C) for (int i=0; i<100; i#pragma omp target#pragma omp teams distribute parallel for simd num_teams(T) // expected-error {{'T' does not refer to a
value}}) for (int i=0; i<100; i++) foo();#prag
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Copyright (C) 2014, International Business Machines* Corporation and others. All Rights Reserved
Copyright (c) 2000, 2003, 2004, 2008 Red Hat, Inc. SPARC 64-bit Foreign Function Interface
copyright) + KMP_VERSION_MAGIC_LEN, 0}, // Skip magic prefix.
(C) are both contained within Group(D). const uint64_t ResourceMask;
(C) CXXConstCastExpr(T, VK, Op, WrittenTy, L, RParenLoc, AngleBrackets);}
Copyright (c) 2008 Anthony Green SuperH SHmedia Foreign Function Interface
(C) MemberSpecializationInfo(RD, TSK); MSI->setPointOfInstantiation(POI); D->TemplateOrInstantiation = MSI; mergeRedeclarable(D, Redecl);
break; }
(C) for (int i=0; i<100; i#pragma omp target teams distribute parallel for simd num_teams(T) // expected-error {{'T' does not refer to a value}} for (int
i=0; i<100; i++) foo();#pragma omp targe
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Copyright (c) 2002 Bo Thorsen <bo@suse.de>
Copyright (c) 2002, 2009 Free Software Foundation, Inc. based on darwin.S by John Hornkvist
(C) /// MA_preload = load MA; /// use MA_preload Value *preloadInvariantLoad(const MemoryAccess &MA, __isl_take isl_set
*Domain);
Copyright (c) 1996, 1998 Red Hat, Inc. Predefined ffi_types needed by libffi.
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Copyright (C) 2010, Howard Chu, <hyc@openldap.org>
Copyright (C) 2016 - 2017, Steve Holme, <steve_holme@hotmail.com>.
(C) and X < abs(C) Constant *PosDivisorC = ConstantInt::get(Ty, C->abs()); Constant *NegDivisorC = ConstantInt::get(Ty, -C->abs()); if
(isICmpTrue(CmpInst::ICMP_SGT, X, NegDivisorC,

```

```

    COPYRIGHT MATCHES "ActiveState")      set (${_PYTHON_PREFIX}_INTERPRETER_ID "ActivePython")      endif()      endif()      endif()      else()
set (${_PYTHON_PREFIX}_INTERPRETER_ID
Copyright (C) 2013-2015 Free Software Foundation, Inc.  Written by Ian Lance Taylor, Google.
Copyright (c) 2007 Joerg Sonnenberger
(C) A  a[0].setValue(13);#ifndef SUPPRESSED // expected-warning@-2 {{Called C++ object pointer is null}}#endif
Copyright (C) 1995-2003 Mark Adler * For conditions of distribution and use, see copyright notice in zlib.h */
copyright; please read the individual source files for details:
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Copyright 2019 Peter Dimov
(C)      SimplifiedInputValues.push_back(      }
copyright \xC2xA9", "copyright \xC2xA9" { "form-feed \f", "form-feed [NON-XML-CHAR-0xC]", { "angles <>", "angles &lt;&gt;", { "ampersand &",
"ampersand &amp;", { "bad-byte \x80", "bad-
(C)// expected-warning@7// expected-warning@9 {"custom message"}#elif defined(D)// expected-error@7 {{use of old-style cast}}// expected-
error@9 {"custom message"}#elif defined(E)// expecte
(C) " << R.getName() << "Attr(getLocation(), C"; for (auto const &ai : Args) {      OS << ", ";      ai->writeCloneArgs(OS);      }      OS << ",
getSpellingListIndex());\n";      OS << " A->Inher
Copyright (c) 2013, Sony Mobile Communications AB
Copyright 2006, Dean Edwards
(C) const APInt if (!match(Cmp.getOperand(1), m_APInt(C)) ||      !Bitcast->getType()->isIntegerTy() ||      !Bitcast->getSrcTy()-
>isIntOrIntVectorTy())      return nullptr;
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Copyright (C) 2010, Hoi-Ho Chan, <hoiho.chan@gmail.com>
copyright comment if present.  comments := file.Comments      if len(comments) > 0 && strings.HasPrefix(comments[0].Text(), "Copyright") {
comments = comments[1:]}
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Copyright 2018 Google Inc. All rights reserved
(C) if (      // (1 << Y) < 30 -> Y <= 4      // (1 << Y) <= 30 -> Y <= 4      // (1 << Y) >= 30 -> Y > 4      // (1 << Y) > 30 -> Y > 4      if (Pred
== ICmpInst::ICMP_ULT)      Pred
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(C)!b go      :more \./!{      N      s\n# //      b more
(C)!b go      :more \./!{      N      s\n# ||      b more
copyright */      0x00a9,      /* 161 exclamdown */      0x00a1,      /* 162 cent */      0x00a2,      /* 163 sterling */      0x00a3,      /* 164
fraction */      0x2044,      /* 165 yen */      0x00a5,
Copyright (c) 2013 Imagination Technologies Ltd.
Copyright (c) 1986 by University of Toronto.
Copyright (c) 2009 Bradley Smith <brad@brad-smith.co.uk>
(C) indirect case ProtocolType(Any) indirect case CEnumType(Simple) indirect case ADTEnumType(ADT) indirect case Recursive(GuineaPig) }
var GP_StructType = GuineaPig.StructType(12)
Copyright (C) 1998 - 2014, Daniel Stenberg, <daniel@haxx.se>, et al.
Copyright 2016, 2017 Tobias Grosser. All rights reserved
(C) thread_limit(d// CHECK-NEXT: foo())// CHECK-NEXT: #pragma omp target teams allocate(f) reduction(^: e,f) reduction(&&: g)// CHECK-NEXT:
foo())// CHECK: template<> int tmain<int, 5>(int argc, int
(C) SEHTryStmt(IsCXXTry, TryLoc, TryBlock, Handler);}
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(C)!b go      :more \.!\{ N      s/\n# //      b more
(C)<<2)      (__v8df)_mm512_setzero_pd(), \      (__mmask8)(U), \
_MM_FROUND_C
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Copyright (c) 20011 Anthony Green
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copyright = True
(C) for (int j=0; j<100; j#pragma omp target#pragma omp teams distribute parallel for thread_limit(T) // expected-error {{'T' does not refer to a
value}} for (int j=0; j<100; j++) foo());#pragma
Copyright (C) 1996-2014 Free Software Foundation, Inc.
Copyright (c) 2013 The Written Word, Inc.
(C) ParmVarDecl std::copy(NewParamInfo.begin(), NewParamInfo.end(), ParamInfo); }
(C) UnaryOperator(M.makeIntegerLiteral(0, C.LongTy), UO_Not, C.LongTy, VK_RValue, OK_Ordinary, SourceLocation(),
/*CanOverflow*/false);
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Copyright (c) 2009-2013 Apple Inc. All rights reserved
(C)<<2)      (__v8sf)_mm256_setzero_ps(), \      (__mmask8)(U)
Copyright (c) 2003-2010 Tim Kientzle * All rights reserved
Copyright (C) 1996-2017 Free Software Foundation, Inc.
Copyright (C) 2011 Free Software Foundation, Inc. This config.lt script is free software; the Free Software Foundation gives unlimited permission to copy,
distribute and modify it."
(C)# Look for a source tree left by enable_language internal checks.if(NOT EXISTS
${CMAKE_CURRENT_BINARY_DIR}/CMakeFiles/CMakeTmp/CMakeLists.txt) message(FATAL_ERROR "--debug-trycompile should lea
Copyright (C) 2009-2013, International Business Machines* Corporation and others. All Rights Reserved
(C) MemberSpecializationInfo(FD, TSK); MSInfo->setPointOfInstantiation(POI); }
(C) C->finalize(TheSema); }
Copyright (C) 2007, 2008, 2010 Free Software Foundation, Inc
Copyright 2012 The Go Authors. All rights reserved
(C) num_threads(s) proc_bind(close) reduction( foo());#pragma omp target parallel if (target:argc > 0) foo();#pragma omp target parallel if
(parallel:argc > 0) foo();
(C)<<2)      (__v4df)_mm256_setzero_pd(), \      (__mmask8)-1)
Copyright (c) 1998, 2008, 2011 Red Hat, Inc.
copyright line was found error(filename, 0, 'legal/copyright', 5,
Copyright 2010 Zencoder, Inc.
Copyright (c) 2006, Alexander Neundorf, <neundorf@kde.org>
Copyright (c) 2012 Tiler Corp.
Copyright (C) 2010-2015 Free Software Foundation, Inc.
(C) // expected-warning@end}
Copyright (C) 2000-2012, International Business Machines* Corporation and others. All Rights Reserved
Copyright (c) 2017-2019 by the contributors listed in CREDITS.TXT
copyright * * The following files need to be updated as well, which can be done * by running the UNIX makefile target 'update-windows-makefiles' in

```

```

icu/source. *
(C) StringLiteral std::copy(clobbers, clobbers + NumClobbers, Clobbers);}
Copyright (c) 1998, 2001, 2007, 2008 Red Hat, Inc.
(C) copyin(S<T>::TS, thrp) proc_bind(master) reduction( foo());
Copyright (c) 2009 Joerg Sonnenberger
COPYRIGHT// CHECK: Copyright
Copyright (c) 2014 Red Hat, Inc.
Copyright (C) Microsoft Corporation\.. ( $i < @bulk and $bulk[ $i ] =~ m{^s*$} ) or parse_error( $tool, @bulk, $i ); ++
$; ( $i < @bulk and $bulk[ $i
Copyright 2014 The Go Authors. All rights reserved
Copyright (C) 2011 Nokia Corporation and/or its subsidiary(-ies). ** All rights reserved
(C) StringLiteral std::copy(constraints, constraints + NumConstraints, Constraints);
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COPYRIGHT: * Copyright (c) 1997-2011, International Business Machines Corporation and * others. All Rights Reserved
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Copyright (C) 2016 InfoTeCS JSC. All rights reserved
Copyright (c) 1999-2007 Apple Inc. All rights reserved
© the copyright symbol// CHECK-NEXT: CommentAST=[// CHECK-NEXT: (CXComment_FullComment// CHECK-NEXT: (CXComment_Paragraph//
CHECK-NEXT: (CXComment_Text Text=[ ] IsWhitespace)
(C) * 13; return Result;}
(C) Token std::copy(asmtoks.begin(), asmtoks.end(), AsmToks);
Copyright (c) 2013 Marek Kubica * All rights reserved
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Copyright (c) 2009-2011 Sean Purcell * All rights reserved
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Gordon Matzigkeit, 1996
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{{0, 0, 0, 0}}, // NOT SIGN {'-', 0, 0, 0}}, // SOFT HY
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Copyright (c) 1996-2008, PostgreSQL Global Development Group * * IDENTIFICATION * $PostgreSQL: pgsq/src/port/win32error.c,v 1.4
2008/01/01 19:46:00 momjian Exp $
Copyright 2002 Niels Provos <provos@citi.umich.edu> * All rights reserved
Copyright symbol
Copyright (C) 2001, 2002, 2003, 2005, 2008, 2010 Free Software# Foundation, Inc.
(C) TemplateArgumentLoc(DefArg));}
Copyright (c) 2016-present, Przemyslaw Skibinski, Yann Collet, Facebook, Inc.
Copyright The Go Authors.\n\n" "package build\n\n" + " // shouldBuild checks tags given by lines of the form\n" + " // +build tag\n"
+ "func shouldBuild(content []byte)\n" want3 := map[string]b
Copyright (c) 2016-2018, Apple Inc. and the Swift project authors
copyright Niels Provos. Two clause BSD license.
(C) foo();#pragma omp target#pragma omp teams thread_limit(T) // expected-error {{'T' does not refer to a value}} foo();#pragma omp
target#pragma omp teams thread_limit // expected-error {{exp
Copyright (c) 2014 Michihiro NAKAJIMA * All rights reserved
Copyright (c) 2003, 2004, 2006, 2008 Kaz Kojima SuperH SHmedia Foreign Function Interface
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(C)<<2)      (__v16sf)(__m512)(W), \      (__mmask16)(U), \
_MM_FROUND_CUR_DIRECT
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mach_port rights for CF types that make use of: de
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Copyright (c) 2004-2018, Apple Inc. All rights reserved
Copyright (c) 2000-2018, Apple Inc.and the Swift project authors
(C) copyin(S<T>::TS,thrp) proc_bind(master) reduction(// CHECK-NEXT: foo())
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(C) g()func (*C) h()
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(C) SEHExceptStmt(Loc,FilterExpr,Block);}
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(C) Chinese (traditional).      "\u4ED6\u5011\u7232\u4EC0\u9EBD\u4E0D\u8AAA\u4E2D\u6587",      "ihqwctvzc91f659drss3x8bo0yb", },
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copyright - __kmp_version_build_time;
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copyright = STR(COPYRIGHT); // no-warning
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(C) AC_LINK_IFELSE( AC_LANG_POP]) if test x"${lt_cv_cc_needs_belf}" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need
for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
Copyright 2008, Google Inc.// All rights reserved
copyright symbol// CHECK-NEXT: (CXComment_Text Text=[ ] IsWhitespace)// CHECK-NEXT: (CXComment_Text Text=[™])// CHECK-NEXT:
(CXComment_Text Text=[ the trade mark symbol] Has
Copyright (C) 1996-2014, International Business Machines Corporation and * others. All Rights Reserved
(C) endif() mark_as_advanced(CMAKE_C_COMPILER)
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Copyright 2015, Google Inc.// All rights reserved
(C) * sizeof(T)#else#define STACK_BUFFER_DECL(T, N, C) T N[C]#endif
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```

```

(C) for (int j=0; j<100; j#pragma omp target#pragma omp teams distribute simd thread_limit(T) // expected-error {{'T' does not refer to a value}} for
(int j=0; j<100; j++) foo());#pragma omp tar
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(C)<<2) (__v2df)(__m128d)(W), \ (__mmask8)(U)
(C) serialization::DeclID *Result = IDs.size(); std::copy(IDs.begin(), IDs.end(), Result + 1);
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(C) ((C) ==#elif defined(UNIX_PATH_SEMANTICS)
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(C) FloatingLiteral(C, Empty);}
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Copyright (c) 2009, 2010 Martin Hedenfalk <martin@bzero.se> * * Permission to use, copy, modify, and distribute this software for any * purpose with
or without fee is hereby granted, provided that
(C) return {C->Offset, C->Length, 4, DWARF32}}; return None;
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Foundation, Inc.# Written by Gordon Matzigke
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(C)<<2) (__v16sf)_mm512_undefined_ps(), \ (__mmask16)-1, (int)(R))
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(C) CachedEntries return SymRef;}
(C) num_threads(s) proc_bind(close) reduction( foo(); return 0;}
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Copyright (c) 2002, 2003, 2004, 2006, 2008 Kaz Kojima SuperH Foreign Function Interface
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(C) C->initialize(TheSema); }
Copyright (c) 1998-2018, Apple Inc. and the Swift project authors All of the functions in this file exist to orchestrate the exact time/circumstances we
decrement the port references.

```



```

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Copyright (c) 2009-2011 Michihiro NAKAJIMA * All rights reserved
(C) for (int i=0; i<100; i#pragma omp target teams distribute num_teams(T) // expected-error {{'T' does not refer to a value}} for (int i=0; i<100;
i++) foo();#pragma omp target teams distribute
Copyright (C) 2010 - 2011, Hoi-Ho Chan, <hoiho.chan@gmail.com>
(C) CXXCtorInitializer memcopy(ivarInitializers, initializers, numInitializers * sizeof(CXXCtorInitializer*)); IvarInitializers = ivarInitializers; }
(C) for (int i = 0; i < 100; i foo();#pragma omp target#pragma omp teams distribute parallel for simd num_threads(T(23)) for (int i = 0; i < 100;
i++) foo(); return 0; }
copyright: case CommandTraits::KCI_date: case CommandTraits::KCI_invariant:
Copyright (c) 2009, 2010, 2011, 2012 ARM Ltd.
Copyright (c) 2015 Apple Inc. and the Swift project authors
Copyright File Identifier r = set_file_identifier(bp, 703, 739, vdc, a, vdd, &(iso9660->copyright_file_identifier),
copyright is * claimed, and the software is hereby placed in the public domain. * In case this attempt to disclaim copyright and place the software in the
* public domain is deemed null and void, t
COPYRIGHT;char const __kmp_version_lib_ver KMP_VERSION_PREFIX "version: " stringer(KMP_VERSION_MAJOR) "." stringer(
KMP_VERSION_MINOR) "." stringer(KMP_VERSION_BUILD);char const __kmp_ve
Copyright (C) 2008 Red Hat, Inc
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(C)<<2) (__v8df)_mm512_setzero_pd(), \ (__mmask8)-1, \
_MM_FROUND_CU
(C) thread_limit(d foo());#pragma omp target#pragma omp teams reduction(^:e, f) reduction(&& : g) foo(); return 0; }
Copyright 2015 INRIA Paris-Rocquencourt
Copyright (c) 1996, 2003-2004, 2007-2008 Red Hat, Inc. SPARC Foreign Function Interface
Copyright (c) 2011 Tim Kientzle * All rights reserved
(C) __compiler_check_default_language_standard(C 6.10 90 8.10 99) set(CMAKE_C_OUTPUT_EXTENSION ".r51")
(C) ObjCIVarRefExpr(const_cast<ObjCIVarDecl IVar->getType(), SourceLocation(),
SourceLocation(),
const_cast<Expr*>(Base),
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Copyright (c) 1993 * The Regents of the University of California. All rights reserved
(C) for (int i=0; i<100; i#pragma omp target teams distribute simd num_teams(T) // expected-error {{'T' does not refer to a value}} for (int i=0;
i<100; i++) foo();#pragma omp target teams distr
(C) Designator std::copy(Designators, Designators + Idx, NewDesignators); std::copy(First, Last, NewDesignators + Idx); std::copy(Designators + Idx
+ 1, Designators + NumDesignators,
(C) ASSERT_CONST_TYPE((C), C99_UNSUUF_DEC_TYPE((C)))#define CHECK_UNSUUF_OCTHEX_TYPE(C) \
ASSERT_CONST_TYPE((C), C99_UNSUUF_OCTHEX_TYPE((C)))
(C) copyin(S < T > ::TS) proc_bind(master) reduction( { foo();
Copyright: 2008-2012 Aleksey Kravchenko <rhash.admin@gmail.com>
(C) C->atTemplateBegin(TheSema, Inst); }
(C)<<2) (__v4df)(__m256d)(W), \ (__mmask8)(U)
(C) __compiler_check_default_language_standard(C 1.10 90 5.10 99) set(CMAKE_C_OUTPUT_EXTENSION ".r43")
(C) ASSERT_CONST_TYPE((C), C99_SUFU_TYPE((C)))
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Copyright 2009 The Go Authors. All rights reserved
(C) Designator NumDesignators = NumDesigs; for (unsigned I = 0; I != NumDesigs; ++I) Designators[I] = Desigs[I]; }
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  Copyright (C) 2012 - 2014, Linus Nielsen Feltzing, <linus@haxx.se>
  (C) for (int i = 0; i < 100; i  foo());#pragma omp target#pragma omp teams#pragma omp distribute parallel for num_threads(T(23)) for (int i = 0; i <
  100; i++)  foo()); return 0;}
  Copyright (C) 2019, Daniel Stenberg, <daniel@haxx.se>, et al.
  Copyright (c) 2012 Tiler Corp.
  Copyright (c) 2013 Tensilica, Inc.  XTENSA Foreign Function Interface
  (C) instruction. We cannot use immediate  // value for this offset because it cannot be determined accurately when  // the program has inline
  assembly statements. We therefore use the
  Copyright (c) 1996-1999 by Internet Software Consortium. * * Permission to use, copy, modify, and distribute this software for any * purpose with or
  without fee is hereby granted, provided that the
  Copyright (c) 1996, 2003-2004, 2007-2008 Red Hat, Inc.
  copyright-file=<value> * Type : string, max 37 bytes * Default: Not specified
  Copyright (c) 2008 Benjamin Kosnik <bkoz@redhat.com>
  Copyright libuv project contributors. All rights reserved
  Copyright (c) 2012 Zack Weinberg <zackw@panix.com>
  Copyright (C) 2009-2013, International Business Machines// Corporation and others. All Rights Reserved
  Copyright (c) 1996-2015, International Business Machines Corporation and others.* All Rights Reserved
  (C) to a UErrorCode * first test if(U_FAILURE(errorCode)) { return immediately; } * so that in a chain of such functions the first one that sets an error
  code * causes the following ones to not per
  (C) UnaryOperator(const_cast<Expr  VK_LValue, OK_Ordinary, SourceLocation(), /*CanOverflow*/ false);}
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  Copyright (C) 2013 IBM
  Copyright (c) 2005 Axis Communications AB
  Copyright (c) 1998, 2008, 2011 Red Hat, Inc.
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  Copyright (c) 2005-2018, Apple Inc. and the Swift project authors
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  Copyright (C) 2002-2007 Michael J. Fromberger, All Rights Reserved
  Copyright (C) 1998 - 2012, Daniel Stenberg, <daniel@haxx.se>, et al. * * Thanks for code and inspiration!
  Copyright 2000-2018 The OpenLDAP Foundation.
  (C) should return false since we want to be able to join// A and C.
  (C) StringLiteral std::copy(Constraints, Constraints + NumConstraints, this->Constraints);
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(C) thread_limit(d foo());#pragma omp target teams allocate(f) reduction(^:e, f) reduction(&& : g) foo(); return 0;}
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copyright string. * @stable ICU 2.4 */#define U_COPYRIGHT_STRING_LENGTH 128
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(C) AC_TRY_LINK( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS="$SAVE_CFLAGS" fi ;;sp
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"warranty; not even for MERCHANTABILITY or FITNESS
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(C) AC_TRY_LINK( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS="$SAVE_CFLAGS" fi ;;sp
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belf CFLAGS="$SAVE_CFLAGS" fi ;;sp
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syslog-ng 3.23.1 fedora:syslog-ng/3.23.1-2.el8/aarch64
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -

```

belf CFLAGS=\$SAVE\_CFLAGS fi ;:~\*~\*so  
(C)/!b go :more \./!{ N s|\n# | | b more  
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"../json\_tokener.h"int main(void){ json\_object \*json;  
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 copyright-err.log"  
 copyright-format/1.0/Upstream-Name: syslog-ngUpstream-Contact: Syslog-ng users' and developers' mailing list <syslog-ng@lists.balabit.hu>Source:  
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copyright-err.log"
copyright-format/1.0/Upstream-Name: syslog-ngUpstream-Contact: Syslog-ng users' and developers' mailing list <syslog-ng@lists.balabit.hu>Source:
git://github.com/syslog-ng/syslog-ng.git
copyright information, homepage and other important information about the module const gchar *description; /* git sha that identifies the core revision
that this was compiled for */ const gchar
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syslog-ng 3.25.1 opensuse:syslog-ng/3.25.1-3.11/s390x
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;:*-*so
(C)!b go :more /\.!{ N s|\n# | | b more
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"../json\_tokener.h"int main(void){ json\_object \*json;  
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copyright() { echo " grep --quiet --extended-regexp "\^\  
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git://github.com/syslog-ng/syslog-ng.git
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that this was compiled for */ const gchar
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}ex || # Otherwise
Copyright\ (C)\ print ' ' x $space if length($+{end}); print "${end}\n"; $replaced = 1; }ex ||
Copyright (C) year name of author
Copyright: Dave Gandy
copyright entries like
copyright{float:right
copyright-format/1.0/Upstream-Name: freetypeUpstream-Contact: freetype-devel@nongnu.orgSource: https://www.freetype.org/download.html
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underline_position; FT_Fixed un
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copyright{max-width:75
Copyright: None
copyright or release year data, are taken from fonts available to the developers.
copyright' script; it is a more portable replacement for# the shebang, using the first `perl' program in the shell's path instead.]
Copyright (?<space1>(\ | \ +(C)\ +)) (?<first>[12][0-9][0-9][0-9]) (?<middle>.+ ) (?<last>[12][0-9][0-9][0-9])
(?<space2>\ +) by
Copyright (?<space1>(\ | \ +(C)\ +)) (?<first>[12][0-9][0-9][0-9]) (?<space2>\ +) by (?<space3>\ *) (?
<end>.*) }
copyright string in the executable of your product. */
Copyright' string /* that simply doesn't begin with an opening parenthesis, even */ /* though they have a closing one! E.g. "amuncial.pfb"
*/ /*
copyright# to be compiled by windres and tagged into DLL usually.
Copyright? Version
copyright was not recognized as a valid string token /* and made the old token parser commit errors. */
copyright \ | xargs
copyright-year
copyright-year file1
copyright year. s { (?<begin>.* )
copyright years. s { (?<begin>.* )
copyright-year# taking care of exceptions stored in file `no-copyright'.
copyright © <year> The FreeType Project (www.freetype.org). All rights reserved
The FreeType Project 2.10.1 unknown:undefined
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;;*-so
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(C)!b go :more /\.!{ N s|\n# | | b more
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copyright;00A9copyrightsans;F8E9copyrightserif;F6D9cornerbracketleft;300Ccornerbracketlefthalfwidth;FF62cornerbracketleftvertical;FE41cornerbracketri
ght;300Dcornerbracketrighthalfwidth;FF63cor
copyright = 0xFFFFFU; top->full_name = 0xFFFFFU; top->family_name = 0xFFFFFU; top->weight = 0xFFFFFU; top-
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copyright = 0xFFFFFU; top->full_name = 0xFFFFFU; top->family_name = 0xFFFFFU; top->weight = 0xFFFFFU; top-
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Copyright 2000, 2001, 2004-2007 by# /* foobar */## and replaces them uniformly with
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if there is non-whitespace afte */## (assuming that the current year is 2015). As can be seen, the line length# is retained
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Copyright %2 2016-2019<br>" " by Werner Lemberg <tt>&lt;wl@gnu.org&gt;</tt></p>" "" "<p><b>ftinspect</b> shows how a font
gets rendered" " by FreeType, allowing control
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Copyright\ (C)\ # If $space is negative this inserts nothing.    print ' ' x $space if length(${end});    print "${end}\n";    $replaced = 1;
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Copyright\ (C)\ # If $space is negative this inserts nothing.    print ' ' x $space if length(${end});    print "${end}\n";    $replaced = 1;
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copyright; FT_UInt full_name; FT_UInt family_name; FT_UInt weight; FT_Bool is_fixed_pitch; FT_Fixed italic_angle; FT_Fixed
underline_position; FT_Fix
copyright; FT_UInt full_name; FT_UInt family_name; FT_UInt weight; FT_Bool is_fixed_pitch; FT_Fixed italic_angle; FT_Fixed
underline_position; FT_Fixed un
copyright info, notices, etc.) (body). ** This is _not_ used to retrieve glyph names!
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copyright{margin:0 .6rem;padding:.4rem 0;color:hsla(0,0
copyright{max-width:75
copyright or release year data, are taken from fonts available to the developers.
copyright or release year data, are taken from fonts available to the developers. Note that later versions of the fonts might be no longer
tricky; for example
copyright' script; it is a more portable replacement for# the shebang, using the first `perl' program in the shell's path instead.]
copyright' script; it is a more portable replacement for# the shebang, using the first `perl' program in the shell's path instead.]# Usage:#
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Copyright (?<space1>(\ | \ +\ (C)\ \ +)) (?<first>[12][0-9][0-9][0-9]) (?<space2>\ +) by (?<space3>\ *) (?
<end>.* ) }
Copyright (?<space1>(\ | \ +\ (C)\ \ +)) (?<first>[12][0-9][0-9][0-9]) (?<space2>\ +) by (?<space3>\ *) (?
<end>.* ) }
copyright string in the executable of your product. */
copyright string in the executable of your product. */
Copyright' string /* that simply doesn't begin with an opening parenthesis, even */ /* though they have a closing one! E.g. "amuncial.pfb"
*/ /*
Copyright' string /* that simply doesn't begin with an opening parenthesis, even */ /* though they have a closing one! E.g. "amuncial.pfb"
*/ /*
copyright# to be compiled by windres and tagged into DLL usually.
copyright# to be compiled by windres and tagged into DLL usually.#ifneq ($(RC),) FTVER_SRC := $(BASE_DIR)/ftver.rc FTVER_OBJ :=
$(OBJ_DIR)/ftver.$O
Copyright? Version printf(" family: %s\n", face->family_name ); printf(" style: %s\n", face->style_name );
copyright was not recognized as a valid string token /* and made the old token parser commit errors. */
copyright was not recognized as a valid string token /* and made the old token parser commit errors. */ while ( cur < limit && ( *cur
== ' | *cur == '\t' ) )
copyright \ | xargs
copyright \ | xargs
copyright-year
copyright-year file1
copyright-year file1
copyright year. s { (?<begin>.* )
copyright year. s { (?<begin>.* )
copyright years. s { (?<begin>.* )
copyright years. s { (?<begin>.* )
copyright-year# taking care of exceptions stored in file `no-copyright'.
copyright-year# taking care of exceptions stored in file `no-copyright'.topdir=`git rev-parse --show-toplevel`toolsdir=$topdir/src/tools
(C)# Source file extension for C test sources.ac_ext=c# Object file extension for compiled C test sources.objext=o_LT_TAGVAR(object, $1)=$objext
traceroute 2.1.0 unknown:undefined
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 putc(\*c, stderr); putc('\n', stderr); }  
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(C) AC\_LINK\_IFELSE( AC\_LANG\_POP) if test x"\$lt\_cv\_cc\_needs\_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need  
for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;  
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 for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;  
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<inttypes.h>#include <ctype.h>#include <errno.h>#include <
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<newline>      nex
COPYRIGHT that * forms part of this distribution. * * Adaption to Linux by Peter MacDonald.
copyright));          write_all(STDOUT_FILENO, help,          strlen(help));          goto
newcmd;          }          case 'n':
util-linux 2.35.1 debian:util-linux/2.35.1-5
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(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$!t_cv_cc_needs_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS=$SAVE_CFLAGS fi ;*-*so
(C)/!b go :more \./!{ N s|\n# | | b more
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 <inttypes.h>#include <ctype.h>#include <errno.h>#include <  
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copyright)); write_all(STDOUT_FILENO, help, strlen(help)); goto
newcmd; } case 'n':
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copyright line and a pointer to where the full notice is found.\\n"); my_strcat(inbuf, "\\n"); my_strcat(inbuf, " <one line to give the program's name
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xmlns:fo="http://www.w3.org/1999/XSL/Format" text-align="center" font-s  
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copyright issues connected with their potential copying out of the header file. */#include <sys/syscall.h>#include <sys/trap.h>
copyright issues, vki-solaris.h follows the same approach as vki-darwin.h (not copying anything from kernel header files but instead just including
them). */
copyright line and a pointer to where the full notice is found.\n"); my_strcat(inbuf, "\n"); my_strcat(inbuf, " <one line to give the program's name
and a brief idea of what it does.>\n");
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copyright Mozilla Foundation,
copyright\n");
copyright string in the executable of your product. */
copyright(void);
copyright(void){#if (LZO_OS_DOS16 && LZO_CC_TURBOC)
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 copyright issues connected with their potential copying out of the header file. \*/#include <sys/syscall.h>#include <sys/trap.h>  
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them). */
copyright line and a pointer to where the full notice is found.\n"); my_strcat(inbuf, "\n"); my_strcat(inbuf, " <one line to give the program's name
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volume_key 0.3.9 opensuse:volume_key/0.3.9-2.1/armv7hl
(C) AC_LINK_IFELSE( AC_LANG_POP]) if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need
for -belf CFLAGS="$SAVE_CFLAGS" fi ;;
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volume-key 0.3.12 ubuntu:volume-key/0.3.12-2

(C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test yes != "\$lt\_cv\_cc\_needs\_belf"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -belf CFLAGS=\$SAVE\_CFLAGS fi ;:\*-\*so

(C)!b go :more /\.!{ N s|\n# | | b more

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(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_PATH_WITH_DIR(P) \ (strchr (P, '/') != NULL ||
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(C) AC_TRY_LINK( AC_LANG_POP ] if test x"$lt_cv_cc_needs_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -
belf CFLAGS="$SAVE_CFLAGS" fi ;;sp
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(C) tests whether C is a directory separator character. IS_PATH_WITH_DIR(P) tests whether P contains a directory specification. */#if defined
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* *
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
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Wget 1.20.3 debian:wget/1.20.3-1
(C)# B. (c)
(C) __btowc (C)# else
(C) btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# ifdef _LIBC# define MEMPCPY(D, S, N) __mempcpy (D, S,
N)# else# if HAVE_MEMPCPY# define MEMPCPY(D
(C) ((C) ==# define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':')# define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) ==# define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0])# define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL)# define

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FILE_SYSTEM_PREFIX_LEN(P) 0#endif
(C) ((C) ==#else# define FILE_SYSTEM_PREFIX_LEN(Filename) 0
(C) ((C) ==#else /* Unix */
(C) ((C) ==#endif
(C) (C)# ifdef _LIBC# define STRLEN(S) __wcslen (S)# define STRCAT(D, S) __wcscat (D, S)# define MEMPCPY(D, S, N) __wmempcpy (D, S,
N)# else# define STRLEN(S) wcslen (S)# defin
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s/\@pounds{\}/\#/g; s/\@mi
copyright character (circle-c) should be used in preference to "(C)". */ if (printf _("\
Copyright \{C\} in_vc_files=$(sample-test)
copyright{\}|\\|\{co
copyright: # Code from module useless-if-before-free: # Code from module utime:
Copyright &copy; 90,2005,2007-2009# * Free Software Foundation, Inc.# */
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\definedummyword\equiv \definedummyword\error \definedummyword\euro
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Copyright \ $(export VC_LIST_EXCEPT_DEFAULT=COPYING
copyright{\} \gdefchar \gdefchar^^ab{\guillemetleft{}} \gdefchar^^ac{\ensuremath\lnot} \gdefchar^^ad{\~} \gdefchar^^ae{\registeredsymbol{}}
\gdefchar^^af{\=}{}} % \gdefchar^^b0{\textdeg
copyright in $_sc_search_regexp)
copyright in $_sc_search_regexp)
copyright issues, or so I was informed. Dr. Marcus Hennecke distributed with phttpd, is excellent, but we cannot use it because it is not assigned to
the FSF. So I stuck it with strptime.
copyright line is 72.# Set UPDATE_COPYRIGHT_MAX_LINE_LENGTH to use a different length.# 5. Set UPDATE_COPYRIGHT HOLDER if the
copyright holder is other# than "Free Software Foundation,
copyright: prereq# By default, exclude all variants of COPYING; you can also# add exemptions (such as ChangeLog.* for rotated change logs)# in the
file .x-update-copyright.
copyright target rule# in maint.mk from gnulib's maintainer-makefile module.
copyrightupdate-copyright:
copyright \# utime \# utime-h \# utimens \# vasprintf \# vsnprintf \# wwidth \# write \# xmemdup0 \# xstrndup
copyright year intervals in a reformatted FSF copyright statement# are expanded instead.# If UPDATE_COPYRIGHT_USE_INTERVALS=2, convert
a sequence with gaps# to the minimal containing
copyright year intervals. (See "Environment variables"# below.)
copyright year is 2 or 4 digits, and years are separated by# commas or dashes. Whitespace may appear after commas.
copyright year lists in your project.# If you have an additional project-specific rule,
copyright year list to include the current year.
copyright years (such as 90, 1991, 1992-2007, 2008) in a# reformatted FSF copyright statement is collapsed to a single# interval (such as 1990-
2008). If unset or set to 0, all existing
(C)" takes one of the following forms or is omitted# entirely:
(C) tests whether C is a directory separator character. */#if defined _WIN32 || defined __CYGWIN__ || defined __EMX__ || defined __DJGPP__
/* Native Windows, Cygwin, OS/2, DOS */
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct

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(C) (((unsigned int) (C)
(Filename)[1] == ':' ? 2 : 0) # ifndef _
©);my$holder ||= 'Free Software Foundation, Inc. ';my $prefix_max = 5;my $margin = $ENV{UPDATE_COPYRIGHT_MAX_LINE_LENGTH};!$margin ||
$margin !~ m/^\d+$/ and $margin = 72;
Wget 1.20.3 openssl:wget/1.20.3-lp151.5.3.1/x86_64
(C) # B. (c)
(C) __btowc (C) # else
(C) btowc (C) # endif # define STRLEN(S) strlen (S) # define STRCAT(D, S) strcat (D, S) # ifdef _LIBC # define MEMPCPY(D, S, N) __mempcpy (D, S,
N) # else # if HAVE_MEMPCPY # define MEMPCPY(D
(C) ((C) == # define HAS_DEVICE(P) \ (((P)[0] >= 'A' && (P)[0] <= 'Z') || ((P)[0] >= 'a' && (P)[0] <= 'z')) \ && (P)[1] == ':') # define
IS_ABSOLUTE_PATH(P) (ISLASH ((P)[0]) || HAS_DEVICE (
(C) ((C) == # define IS_ABSOLUTE_PATH(P) ISLASH ((P)[0]) # define IS_PATH_WITH_DIR(P) (strchr (P, '/') != NULL) # define
FILE_SYSTEM_PREFIX_LEN(P) 0 # endif
(C) ((C) == # else # define FILE_SYSTEM_PREFIX_LEN(Filename) 0
(C) ((C) == # else /* Unix */
(C) ((C) == # endif
(C) (C) # ifdef _LIBC # define STRLEN(S) __wcslen (S) # define STRCAT(D, S) __wscat (D, S) # define MEMPCPY(D, S, N) __wmempcpy (D, S,
N) # else # define STRLEN(S) wcslen (S) # defin
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copyright issues, or so I was informed. Dr. Marcus Hennecke distributed with phttpd, is excellent, but we cannot use it because it is not assigned to the FSF. So I stuck it with strptime.  
copyright line is 72.# Set UPDATE\_COPYRIGHT\_MAX\_LINE\_LENGTH to use a different length.# 5. Set UPDATE\_COPYRIGHT HOLDER if the  
copyright holder is other# than "Free Software Foundation,  
copyright: prereq# By default, exclude all variants of COPYING; you can also# add exemptions (such as ChangeLog..\* for rotated change logs)# in the  
file .x-update-copyright.  
copyright target rule# in maint.mk from gnulib's maintainer-makefile module.  
copyrightupdate-copyright:  
copyright \# utime \# utime-h \# utimens \# vasprintf \# vsnprintf \# wwidth \# write \# xmemdup0 \# xstrndup  
copyright year intervals in a reformatted FSF copyright statement# are expanded instead.# If UPDATE\_COPYRIGHT\_USE\_INTERVALS=2, convert  
a sequence with gaps# to the minimal containing

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copyright year intervals. (See "Environment variables"# below.)
copyright year is 2 or 4 digits, and years are separated by# commas or dashes. Whitespace may appear after commas.
copyright year lists in your project.# If you have an additional project-specific rule,
copyright year list to include the current year.
copyright years (such as 90, 1991, 1992-2007, 2008) in a# reformatted FSF copyright statement is collapsed to a single# interval (such as 1990-
2008). If unset or set to 0, all existing
(C)" takes one of the following forms or is omitted# entirely:
(C) tests whether C is a directory separator character. */#if defined _WIN32 || defined __CYGWIN__ || defined __EMX__ || defined __DJGPP__
/* Native Windows, Cygwin, OS/2, DOS */
(C) tests whether C is a directory separator character. IS_ABSOLUTE_PATH(P) tests whether P is an absolute path. If it is not,
it may be concatenated to a direct
(C) (((unsigned int) (C) <= 'z' - 'a')# define FILE_SYSTEM_PREFIX_LEN(FileName) \ (_IS_DRIVE_LETTER ((FileName)[0]) &&
(FileName)[1] == ':' ? 2 : 0)# ifndef _
©);my$holder ||= 'Free Software Foundation, Inc. ';my $prefix_max = 5;my $margin = $ENV{UPDATE_COPYRIGHT_MAX_LINE_LENGTH};!$margin ||
$margin !~ m/^\d+$/ and $margin = 72;
Wget 1.20.3 opensuse:wget-debugsource/1.20.3-lp152.2.3.1/armv7hl
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(C) ((C) ==#endif
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 (C) tests whether C is a directory separator character. \*/#if defined \_WIN32 || defined \_\_CYGWIN\_\_ || defined \_\_EMX\_\_ || defined \_\_DJGPP\_\_  
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 copyright to help \* - Send help to stdout instead of stderr \* - Add some preemptive typecasts \* - Add L to constants in lseek() calls  
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2012 Mark Adler */
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(abseil 0~20210324.2, ADAL 1.2.2, aiohttp v3.6.2, aiohttp v3.6.3, android-sdk-common 24.3.3+1, Apache HTTP Server 2.2.16, Apache HTTP Server 2.4.23, Apache HTTP Server 2.4.52, Apache HTTP Server 2.4.53, Apache HTTP Server 2.4.54, Apache Portable Runtime 1.6.1, Apache Portable Runtime 1.7.0, apache/xerces-c 3.2.3+debian, apr-1 1.5.2.BT2, apr-debugsource 1.7.0, apr-util 1.6.1, argcomplete v1.10.0, argon2 0~20190702, async-timeout 3.0.1, async\_timeout 3.0.1, awslabs/aws-checksums v0.1.2, azure-cosmosdb-nspkg 2.0.2, azure-cosmosdb-table 1.0.6, bcrypt 3.1.3, bcrypt 3.1.7, BPF Compiler Collection v0.11.0, Cython 0.29.14, erlang-otp 22.3.3, Go programming language 20150630-snapshot, google-api-core 1.23.0, google-api-python-client 1.12.8, google-auth-library-python 1.24.0, google-auth-library-python v1.21.2, google-auth-library-python v1.22.1, google-auth-library-python v1.23.0, googleapis-common-protos 1.52.0, googleapis/python-api-core v1.23.0, grpc 1.38.0, grpcio 1.38.0, jffi - Java Foreign Function Interface 1.3.4, jsonrpclib-pelix 0.4.1, librust-utf-parser-dev 0.3.0, py3-google-api-core 1.17.0, py3-google-auth 1.19.2, py3-google-auth 1.22.1, py3-googleapis-common-protos 1.52.0, pyca/cryptography 38.0.3, pyca/pynacl 1.3.0, python-argcomplete 1.10.0, python-bcrypt 3.1.7, python-cryptography 38.0.4, python-google-api-core 1.22.4, python-google-api-core 1.24.1, python3-google-api-core 1.24.1, python3-google-auth-httpplib2 0.1.0, python3-google-auth-oauthlib 0.4.1, python3-google-auth-oauthlib 0.4.2, ruby-grpc-tools 1.30.2, ruby-grpc-tools 1.44.0, rust-bzip2-sys 0.1.6, rust-bzip2-sys 0.1.9+1.0.8, rust-ed25519 2.2.2+ds, rust-lzma-sys 0.1.13, sigs.k8s.io/json 0.0~git20211208.9f7c6b3, swift-lang 5.2.2, uritemplate-py 3.0.1, xml-log-filter-conformance 1.0.0, yarll-python v1.6.3)

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(EV 4.33, JSON 2.90, libhtml-parser-perl 3.72, libtext-template-perl 1.46, Perl extension for Berkeley DB 0.50, XML-Parser 2.46)

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(CGI.pm 4.28, gcc-9 9.3.0, gcc-riscv64-unknown-elf 9.3.0, libberkeleydb-perl 0.49, libhtml-tagset-perl 3.20, libstdc++ 12.2.0, Perl 2.12, Sort::Naturally 1.02)

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There are two mailing lists related to FreeType:

- \* [freetype@nongnu.org](mailto:freetype@nongnu.org)

Discusses general use and applications of FreeType, as well as future and wanted additions to the library and distribution. If you are looking for support, start in this list if you haven't found anything to help you in the documentation.

\* devel@nongnu.org

Discusses bugs, as well as engine internals, design issues, specific licenses, porting, etc.

Our home page can be found at

<http://www.freetype.org>

--- end of FTL.TXT ---

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(libpgp-error 1.4, Linux Unified Key Setup 2.1.0)

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Although their code does not appear in gd, the authors wish to thank David Koblas, David Rowley, and Hutchison Avenue Software Corporation for their prior contributions.

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(gSOAP gSOAP 2.7.1 stable (upd))

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\_\_\_\_\_, a \_\_\_\_\_  
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"Source Code" means computer programming code in human readable form that is

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## EXHIBIT A

### 1. Genivia gSOAP Source Code Products.

Original Source Code files suitable for compilation into Run-Time Modules for integration into a Target Application:

dom.h  
dom++.h  
dom.c  
dom++.cpp  
dom.cpp  
soapdoc2.pdf  
soapdoc2.html  
stdsoap2.h  
stdsoap2.c  
stdsoap2.cpp  
custom/\* (all files included in the package under 'custom')  
import/\* (all files included in the package under 'import')  
plugin/\* (all files included in the package under 'plugin')  
samples/\* (all files included in the package under 'samples')  
uddi2/\* (all of the UDDI v2 support files included in the package under 'uddi2')  
WS/\* (all of the files included in the package under 'WS' )

Updates to any of the Original Source Code files listed above and distributed by Genivia are also covered under this Agreement.

Original Source Code files of the Software with development functionality not suitable for compilation and integration into Target Applications:

src/error2.c  
src/error2.h  
src/init2.c  
src/soapcpp2.c  
src/soapcpp2.h  
src/soapcpp2\_lex.l  
src/soapcpp2\_yacc.y  
src/symbol2.c  
wsdl/dime.h  
wsdl/gwsdl.h  
wsdl/http.h  
wsdl/imports.h  
wsdl/includes.h  
wsdl/mime.h  
wsdl/schema.cpp  
wsdl/schema.h  
wsdl/service.cpp  
wsdl/service.h  
wsdl/soap.cpp  
wsdl/soap.h  
wsdl/typemap.dat  
wsdl/types.cpp  
wsdl/types.h  
wsdl/wsdl.cpp  
wsdl/wsdl.h  
wsdl/wsdl2h.cpp

The source codes above are part of the software development toolkit. The development toolkit generates source code that is suitable for compilation and integration into the Target Application as set forth by Sections 2 and 3.

2. Approved Software Download Site  
<http://sourceforge.net/projects/gsoap2>

3. Brief Description of the Customer's Project and Target Application  
(Confidential, for Internal Identification Purposes Only).

---

License Fee: \$195.00 USD

Authorized Site (address and building identification):  
\_\_\_\_\_

IN WITNESS WHEREOF, the parties' authorized representatives have executed this Agreement and Exhibit as of the Effective Date.

GENIVIA

By: Robert van Engelen

Title: President

Date: \_\_\_\_\_

CUSTOMER \_\_\_\_\_

By: \_\_\_\_\_

Title: \_\_\_\_\_

Date: \_\_\_\_\_

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Tallahassee, FL32309, USA

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Jan 1, 2004, revised June 27, Aug 1/14, Oct 19, 04; Mar 31, 05, May 10/23, 05, July 1, 05, April 17, 06, May 23, 06.

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(GNU Autoconf Archive 2019.01.06, GNU nano 4.0, gnuilib 20180621~6979c25)

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Version 3, 19 November 2007

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(autoconf-archive 20190106, AutoGen: The Automated Program Generator 5.18.16, Bash 5.0, Bash 5.0.16, Bash 5.0.17, Bash 5.0.18, bison-doc 3.5.1+repack, bison-doc 3.5.4, ccache 3.7.7, CGI.pm 4.28, crash 7.2.9, dosfstools 4.1, dosfstools-doc 4.1, eCryptfs 111, findutils 4.6.0+git+20190209, findutils 4.6.0+git+20190510, findutils v4.7.0, Gawk 5.0.1, gcc-9 9.3.0, gcc-9-doc 9.3.0, gcc-riscv64-unknown-elf 9.3.0, GDB 7.6, GDB 7.6.50.20130731, GDB 9.1, GDB 9.2, gerbv a Gerber (RS-274X) viewer 2.6.2, gerbv a Gerber (RS-274X) viewer 2.7.0, gettext 0.20.1, GNU Autoconf Archive 2019.01.06, GNU bc 1.07.1, GNU Binutils 2.22, GNU Binutils 2.23.1, GNU Bison 3.5.4, GNU Bison v3.5.3, GNU Core Utilities v8.30, GNU Core Utilities v8.31, GNU Diff Utilities 3.7, GNU Libtool 2.4.6, GNU nano 4.0, GNU sed 4.7, GNU tar 1.32, GNU troff 1.22.4, gnulib 20180621~6979c25, GnuPG 1.4.10, GnuPG Made Easy (GPGME) 1.13.1, GnuTLS 3.6.7, gperf 3.1, groff 1.22.4, guile-gnutls 3.7.9, help2man 1.48.5, itstool 2.0.6, Less 551, libassuan 2.5.3, libgcrypt 1.8.5, libgdbm 1.18.1, Libidn 2.3.0, LibreOffice 6.4.3, libstdc++6 12.2.0, Libtasn1 4.16.0, Linux kernel crash utility 7.0.3, linux-firmware 20201218, nongnu - dmidecode 3.2, Readline 6.2, Readline 8.0, sbsigntools 0.9.4, Smartmontools - S.M.A.R.T. Disk Monitoring Tools 7.1, sqliteodbc 0.91, util-linux 2.35.1, x.org\_lib 1.0.7, XZ Utils 5.2.4, Zile (Zile is Lossy Emacs) 2.4.15)

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Version 3, 29 June 2007

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Version 3, 29 June 2007

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Version 2.1, February 1999

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Version 3, 29 June 2007

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Version 3, 29 June 2007

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Also, for each distributor's protection, we want to make certain that everyone understands that there is no warranty for this free library. If the library is modified by someone else and passed on, we want its recipients to know that what they have is not the original version, so that any problems introduced by others will not reflect on the original authors' reputations.

Finally, any free program is threatened constantly by software patents. We wish to avoid the danger that companies distributing free software will individually obtain patent licenses, thus in effect transforming the program into proprietary software. To prevent this, we have made it clear that any patent must be licensed for everyone's free use or not licensed at all.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License, which was designed for utility programs. This license, the GNU Library General Public License, applies to certain designated libraries. This license is quite different from the ordinary one; be sure to read it in full, and don't assume that anything in it is the same as in the ordinary license.

The reason we have a separate public license for some libraries is that they blur

the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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- d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

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Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as

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- a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)
- b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.
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Version 2, June 1991

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Preamble

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To protect your rights, we need to make restrictions that forbid anyone to deny you these rights or to ask you to surrender the rights. These restrictions translate to certain responsibilities for you if you distribute copies of the library, or if you modify it.

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```
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Version 1.1 - 26 February 2007

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(ccrypt 1.10, gitlab.collabora.com/spurv/json-c json-c-0.13.1-20180305, GNU libiberty 20190907, gnulib 20180621~6979c25, libpgp-error 1.4, libpthread-stubs0 0.4, libX11 1.6.9, libxext-dev 1.3.4, popt 1.6.2, popt 1.7, sgml-data 1.5.5, x11proto-bigreqs 1.0.2, x11proto-fonts 2.0.2, x11proto-xcmisc 1.1.2, x11proto-xcmisc 1.2.2, x11proto-xf86bigfont 1.1.2, x11proto-xf86vidmode 2.2.2, x11proto-xinerama 1.1.2, xorg-x11 1.0.9, xorg-x11 1.6.9, xtrans 1.4.0)

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(sgml-data 1.5.5)

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zlib License  
(CMake 3.16.4, CMake 3.17.0, cmake-debugsource 3.18.2, cmake3-debugsource 3.17.2, com.tagtraum:libz 4.0.0, lsof 4.91, zlib 1.2.11, zlib 1.2.3)

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